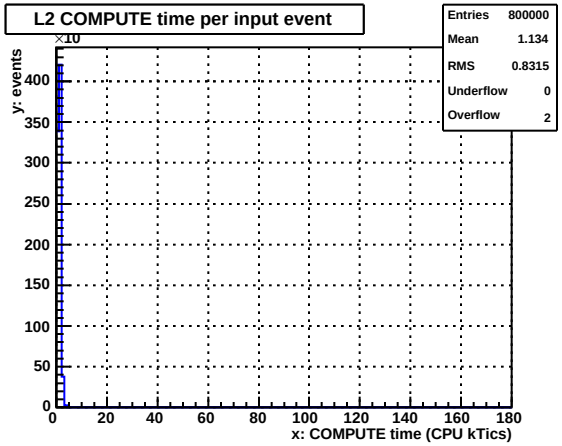
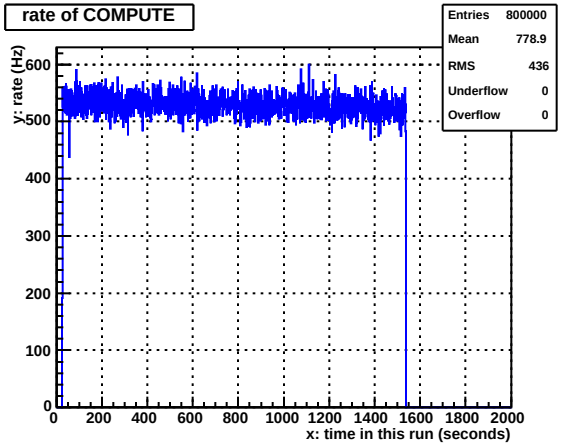
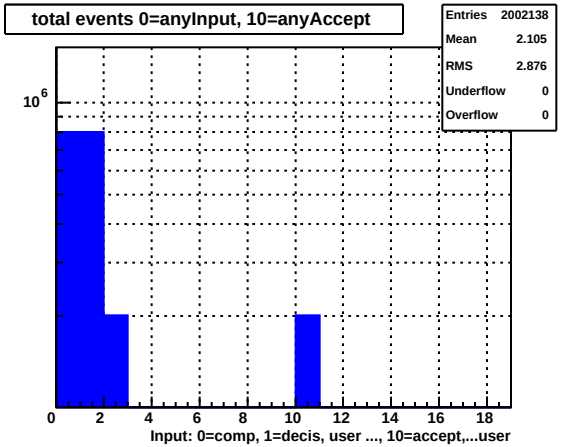
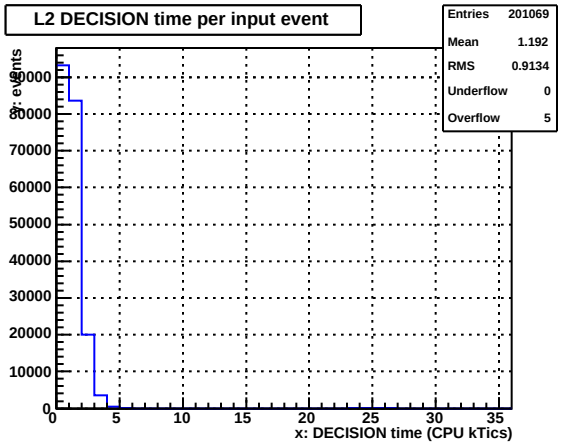
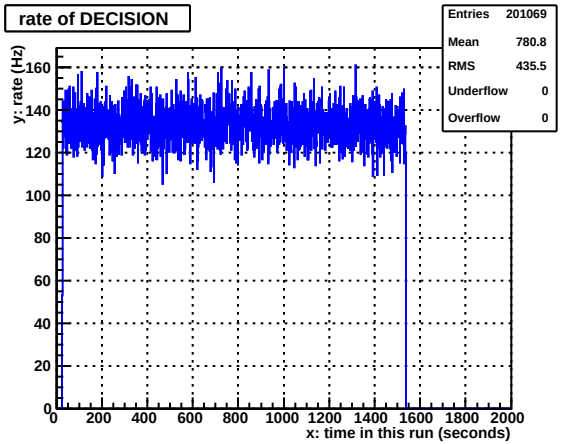
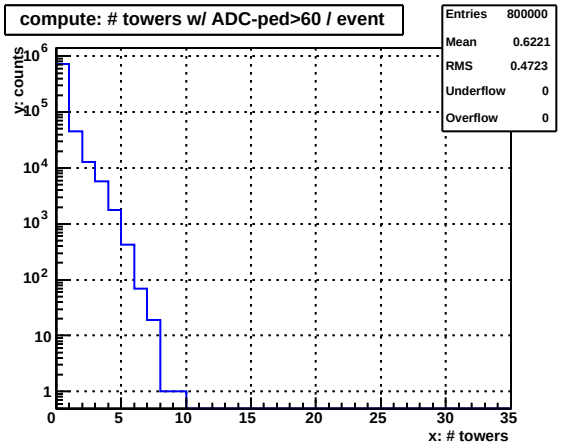

L2 COMPUTE time per input event
Entries 800000
Mean 1.134
RMS 0.8315
Underflow 0
Overflow 2
y: events
x: COMPUTE time (CPU kTics)

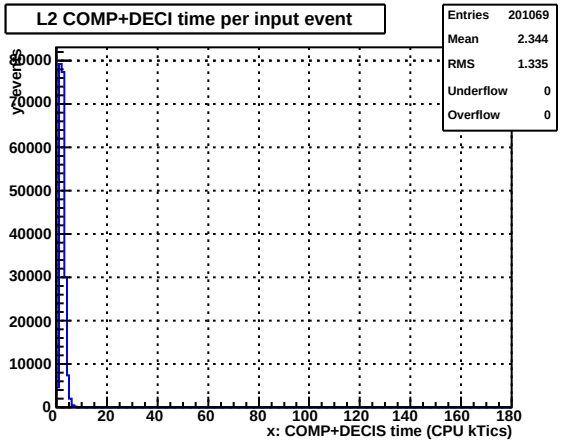

rate of COMPUTE
Entries 800000
Mean 778.9
RMS 436
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

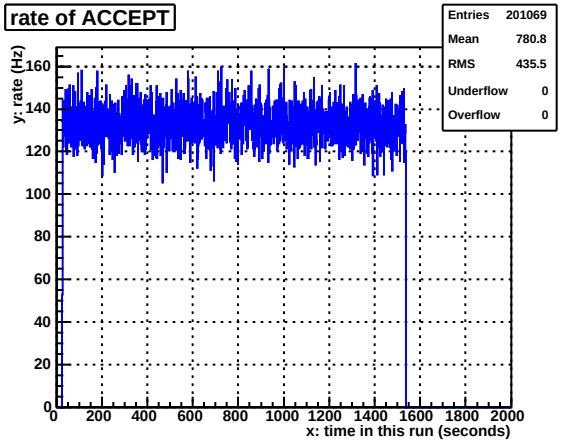

total events 0=anyInput, 10=anyAccept
Entries 2002138
Mean 2.105
RMS 2.876
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

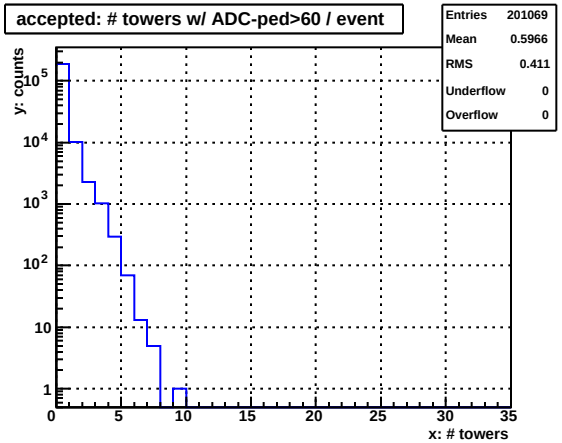

L2 DECISION time per input event
Entries 201069
Mean 1.192
RMS 0.9134
Underflow 0
Overflow 5
y: events
x: DECISION time (CPU kTics)

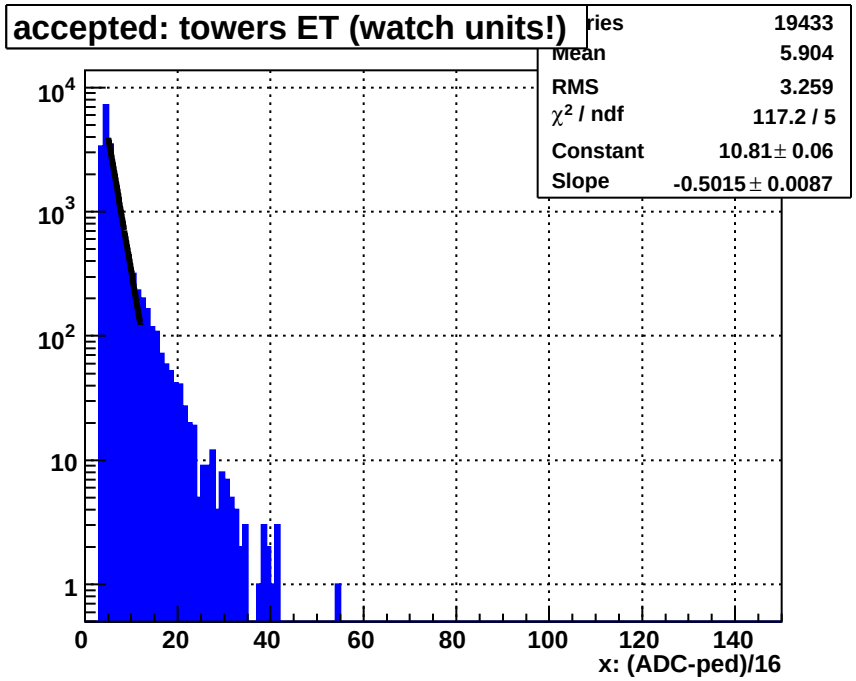

rate of DECISION
Entries 201069
Mean 780.8
RMS 435.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

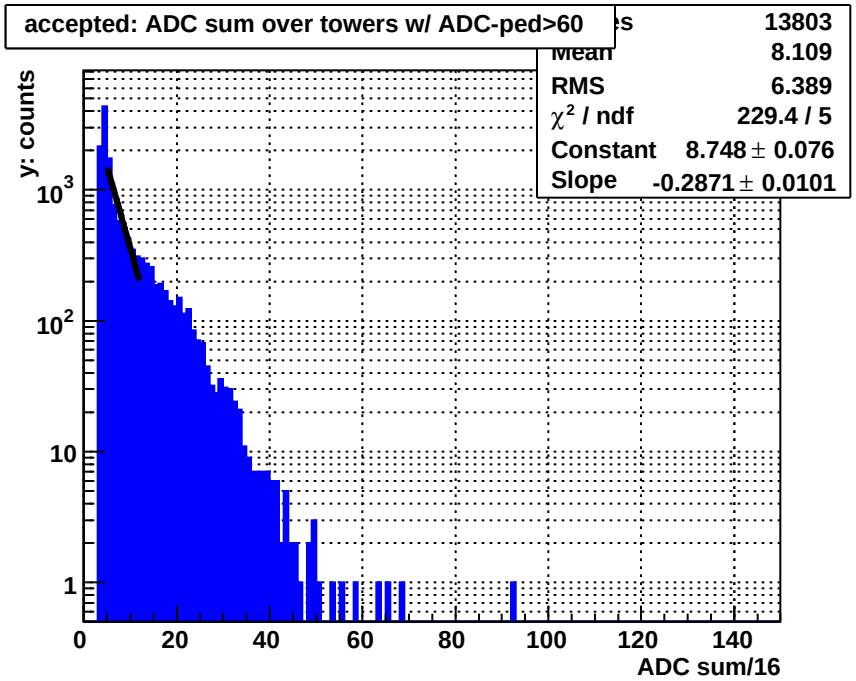

compute: # towers w/ ADC-ped>60 / event
Entries 800000
Mean 0.6221
RMS 0.4723
Underflow 0
Overflow 0
y: counts
x: # towers

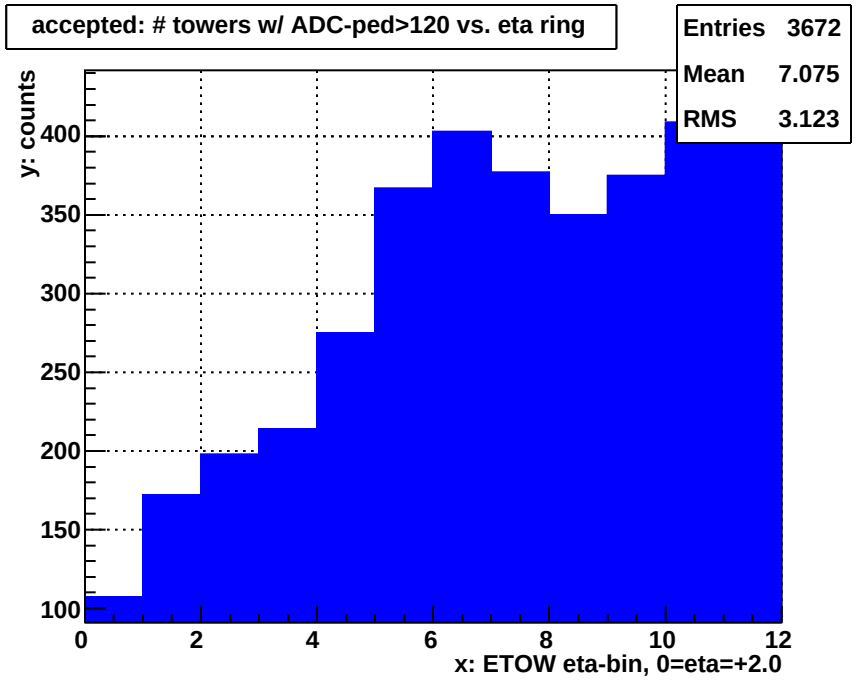

L2 COMP+DECI time per input event
Entries 201069
Mean 2.344
RMS 1.335
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

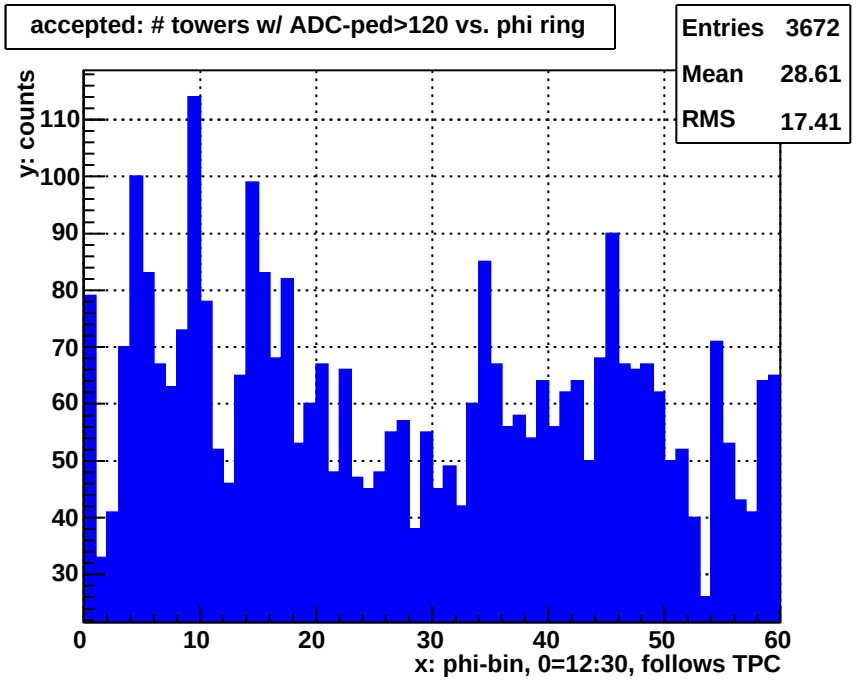

rate of ACCEPT
Entries 201069
Mean 780.8
RMS 435.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 201069
Mean 0.5966
RMS 0.411
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 19433
Mean 5.904
RMS 3.259
χ² / ndf 117.2 / 5
Constant 10.81 ± 0.06
Slope -0.5015 ± 0.0087
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 13803
Mean 8.109
RMS 6.389
χ² / ndf 229.4 / 5
Constant 8.748 ± 0.076
Slope -0.2871 ± 0.0101
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 3672
Mean 7.075
RMS 3.123
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 3672
Mean 28.61
RMS 17.41
y: counts
x: phi-bin, 0=12:30, follows TPC