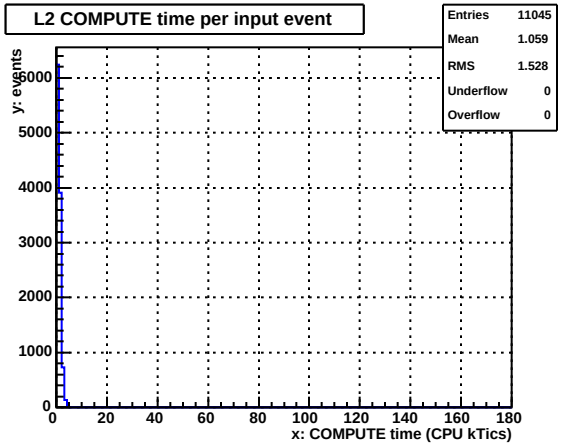
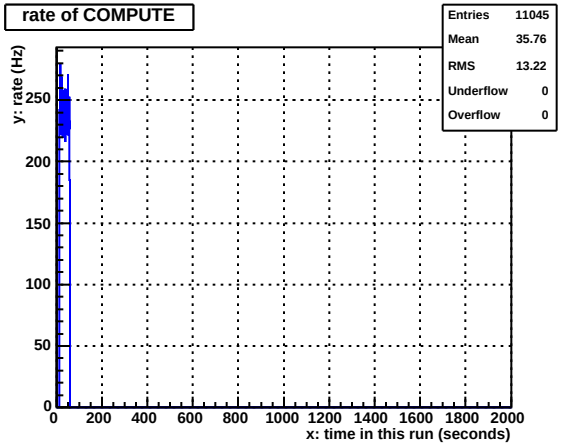
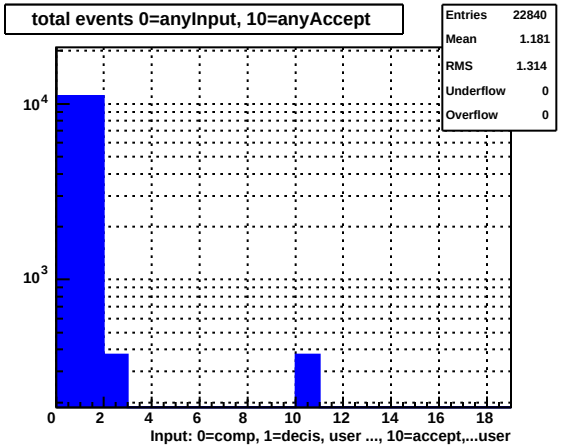
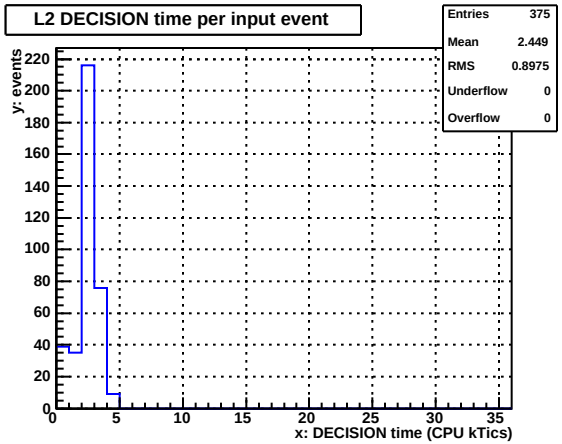
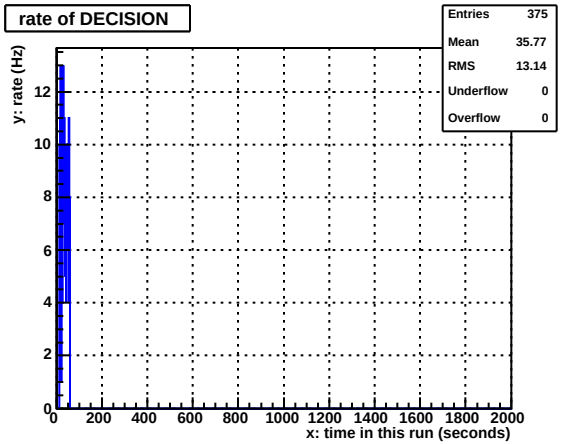
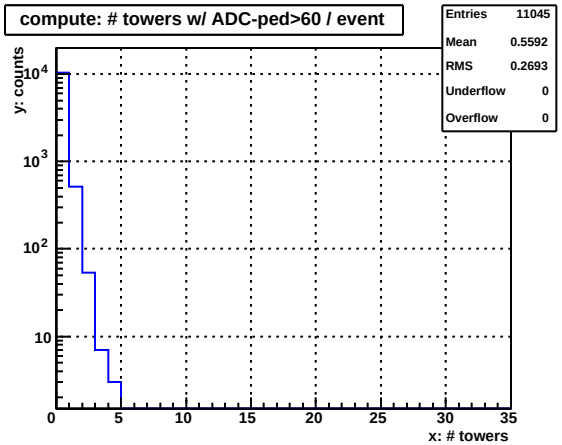

L2 COMPUTE time per input event
Entries 11045
Mean 1.059
RMS 1.528
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

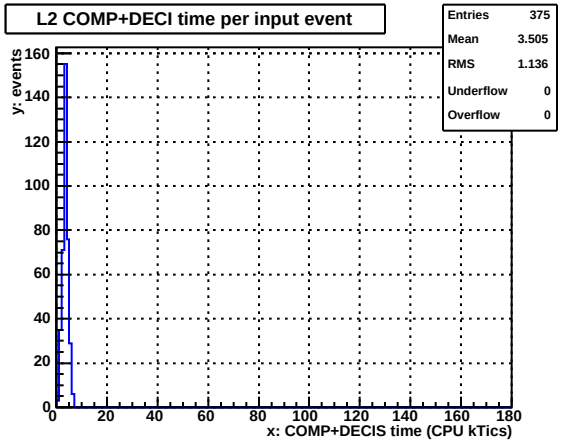

rate of COMPUTE
Entries 11045
Mean 35.76
RMS 13.22
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

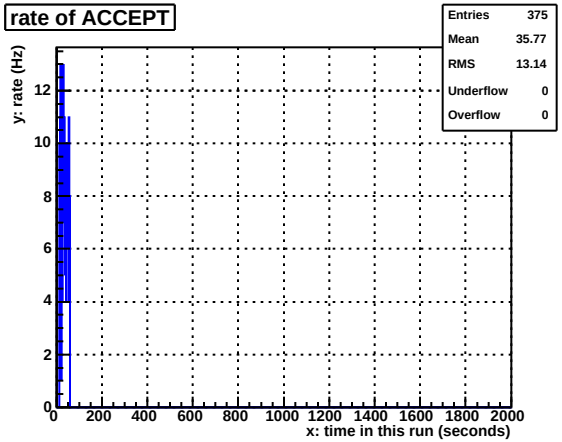

total events 0=anyInput, 10=anyAccept
Entries 22840
Mean 1.181
RMS 1.314
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

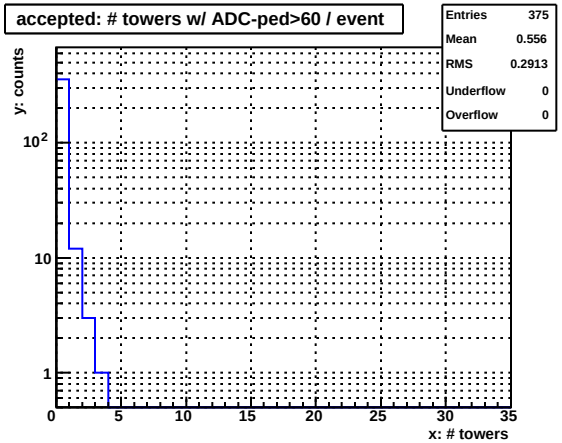

L2 DECISION time per input event
Entries 375
Mean 2.449
RMS 0.8975
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

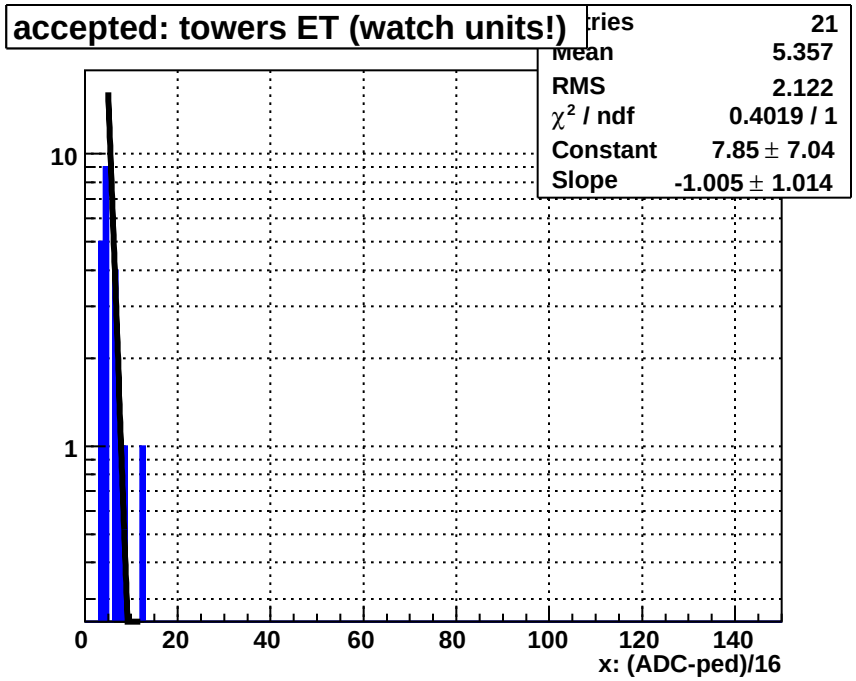

rate of DECISION
Entries 375
Mean 35.77
RMS 13.14
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

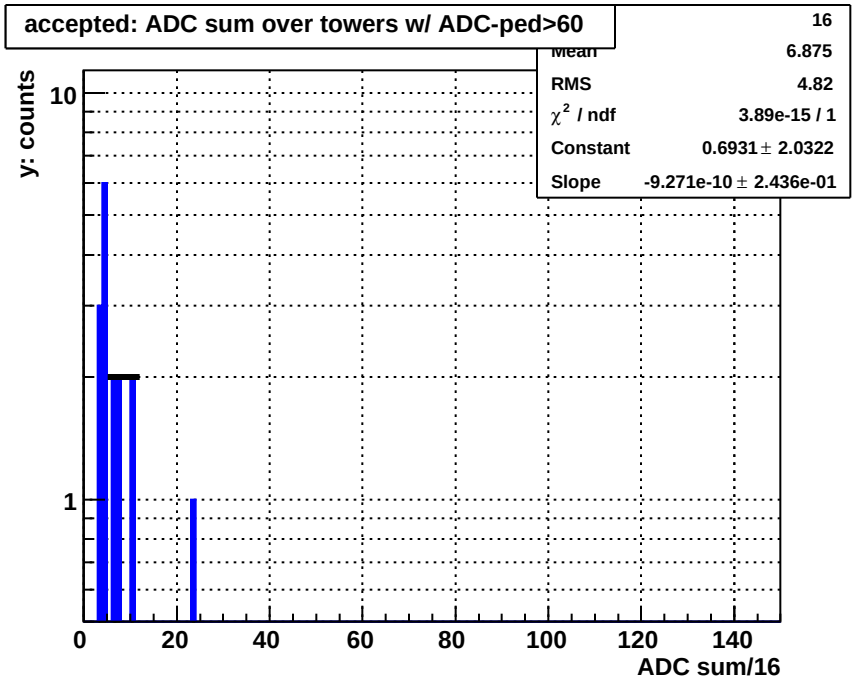

compute: # towers w/ ADC-ped>60 / event
Entries 11045
Mean 0.5592
RMS 0.2693
Underflow 0
Overflow 0
y: counts
x: # towers

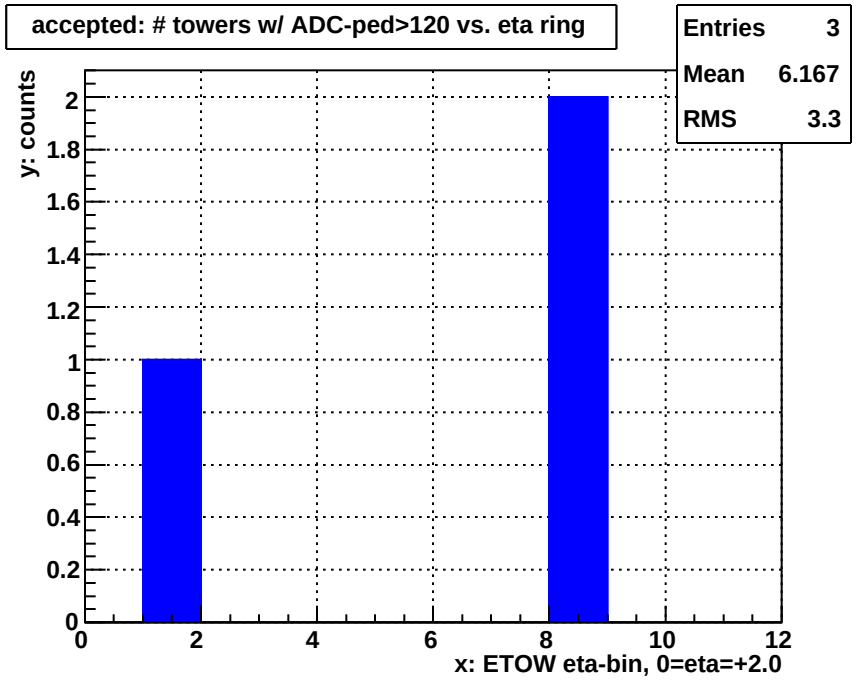

L2 COMP+DECI time per input event
Entries 375
Mean 3.505
RMS 1.136
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

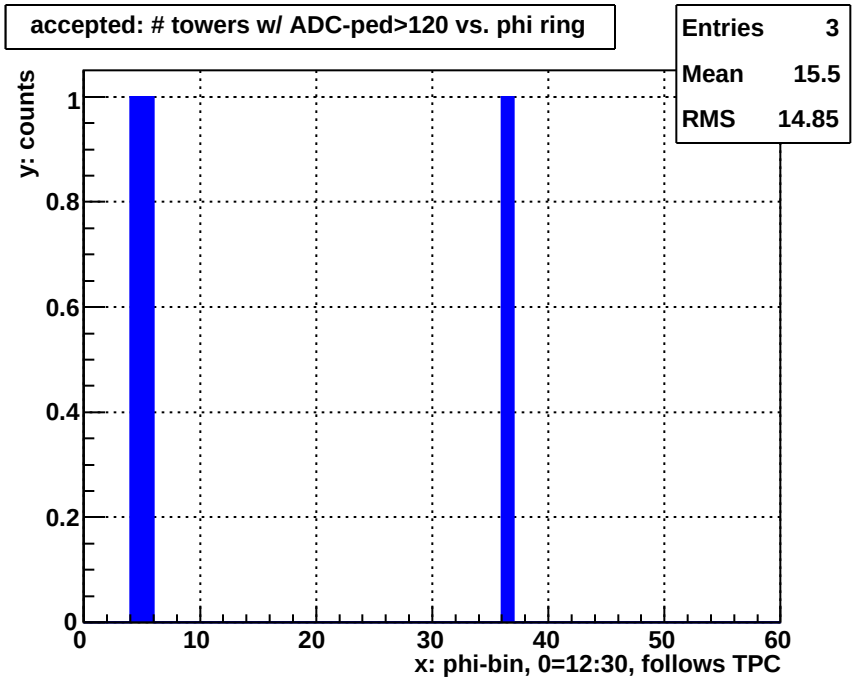

rate of ACCEPT
Entries 375
Mean 35.77
RMS 13.14
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 375
Mean 0.556
RMS 0.2913
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
Entries 21
Mean 5.357
RMS 2.122
χ² / ndf 0.4019 / 1
Constant 7.85 ± 7.04
Slope -1.005 ± 1.014
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
16
Mean 6.875
RMS 4.82
χ² / ndf 3.89e-15 / 1
Constant 0.6931 ± 2.0322
Slope -9.271e-10 ± 2.436e-01
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 3
Mean 6.167
RMS 3.3
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 3
Mean 15.5
RMS 14.85
y: counts
x: phi-bin, 0=12:30, follows TPC