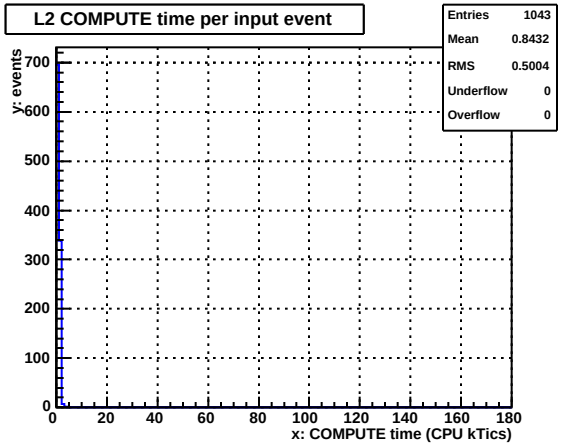
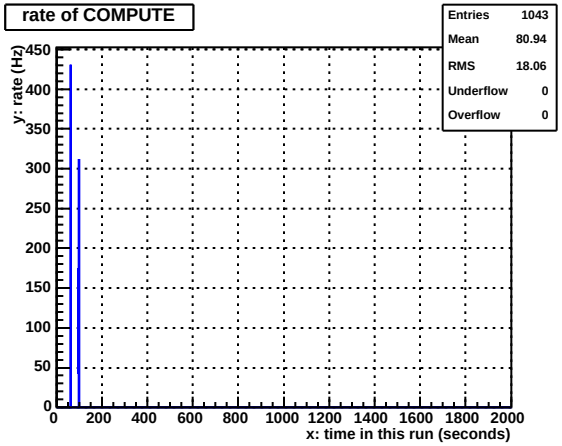
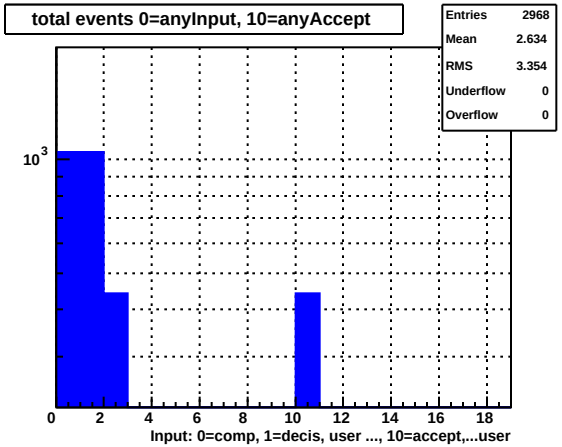
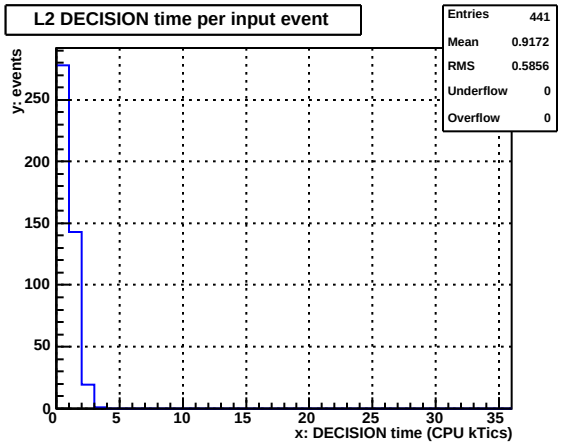
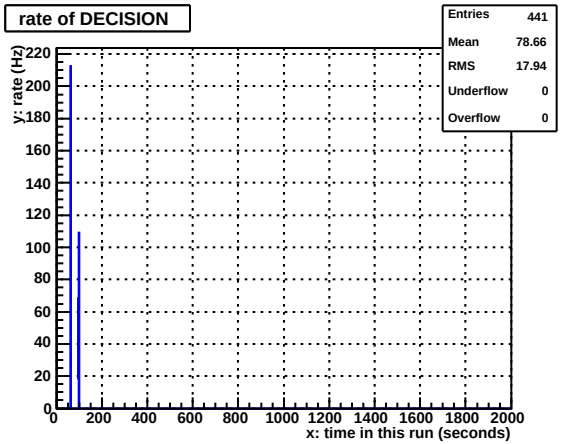
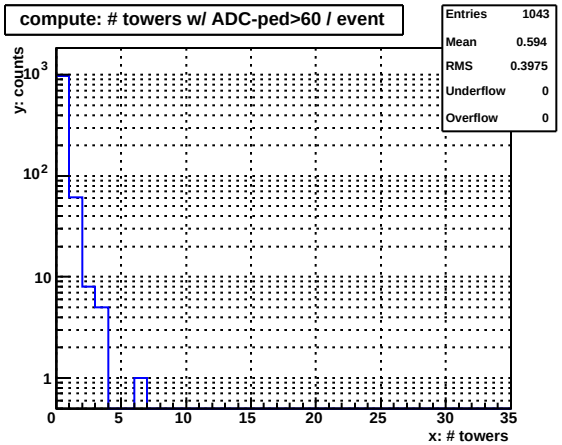

L2 COMPUTE time per input event
Entries 1043
Mean 0.8432
RMS 0.5004
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

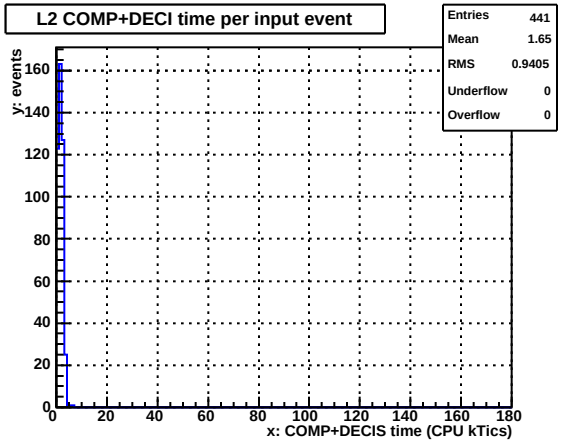

rate of COMPUTE
Entries 1043
Mean 80.94
RMS 18.06
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

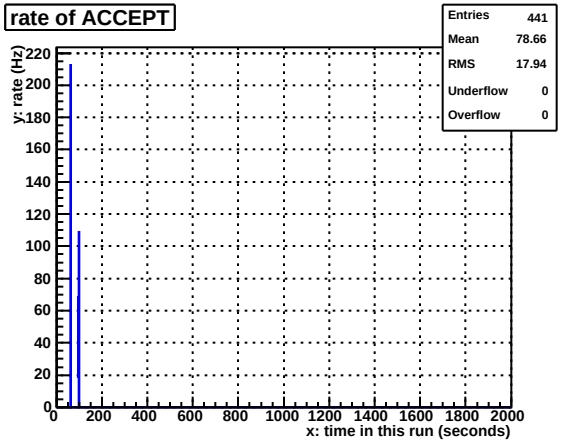

total events 0=anyInput, 10=anyAccept
Entries 2968
Mean 2.634
RMS 3.354
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

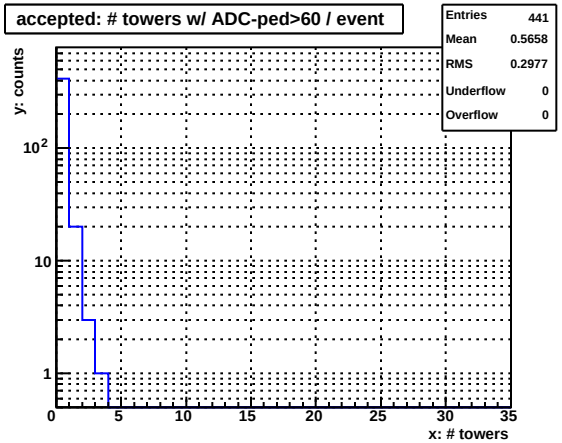

L2 DECISION time per input event
Entries 441
Mean 0.9172
RMS 0.5856
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

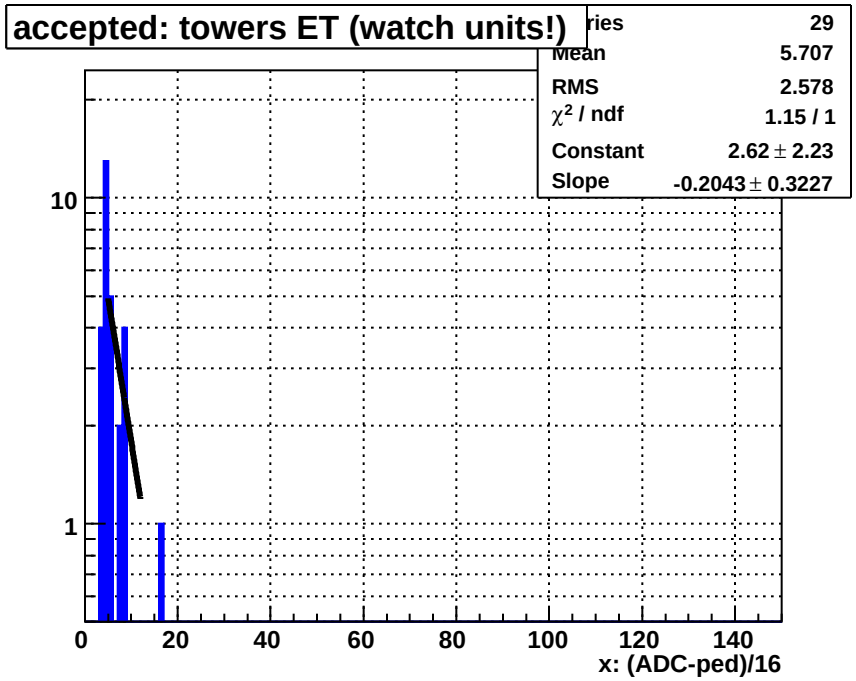

rate of DECISION
Entries 441
Mean 78.66
RMS 17.94
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

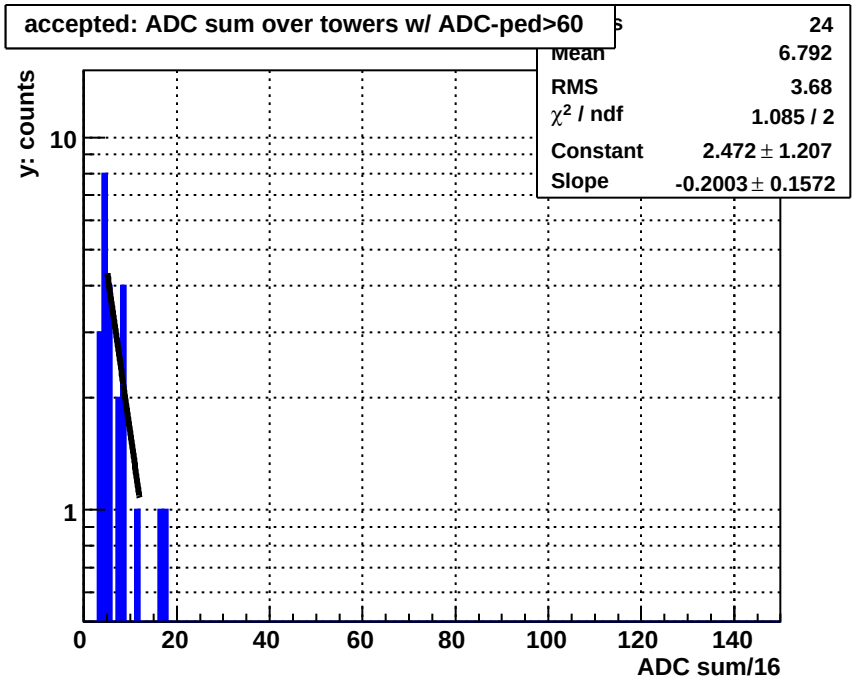

compute: # towers w/ ADC-ped>60 / event
Entries 1043
Mean 0.594
RMS 0.3975
Underflow 0
Overflow 0
y: counts
x: # towers

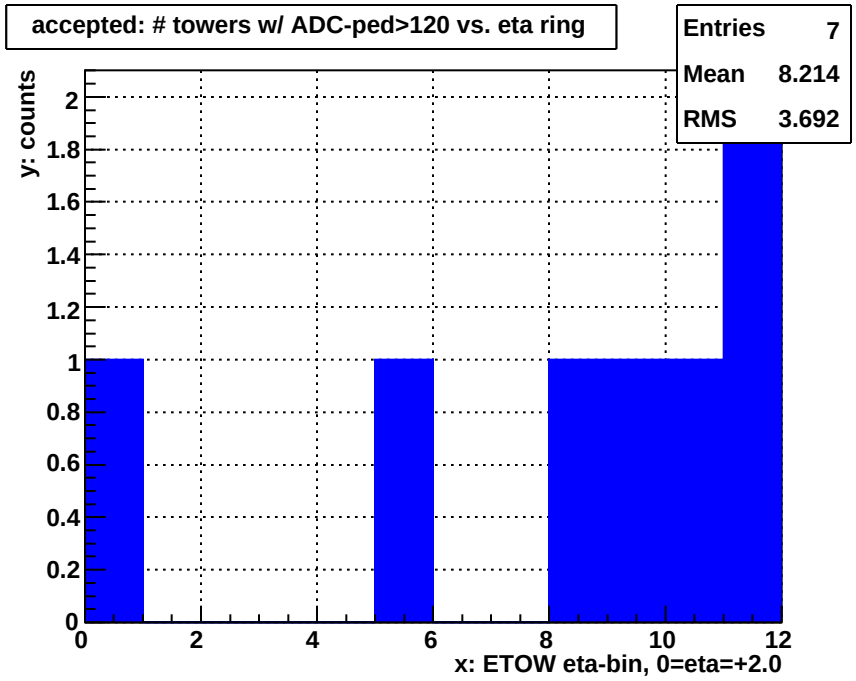

L2 COMP+DECI time per input event
Entries 441
Mean 1.65
RMS 0.9405
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

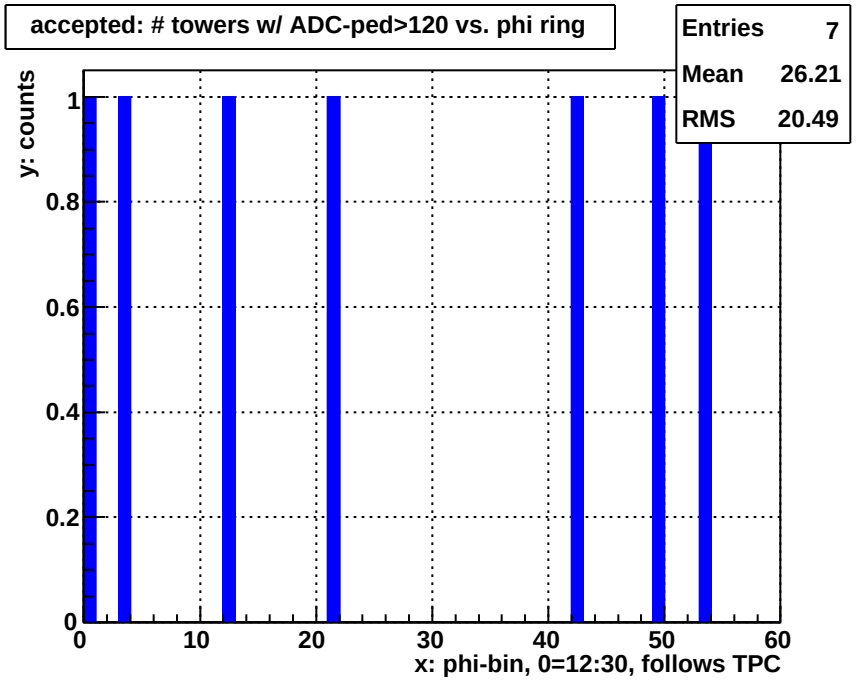

rate of ACCEPT
Entries 441
Mean 78.66
RMS 17.94
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 441
Mean 0.5658
RMS 0.2977
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 29
Mean 5.707
RMS 2.578
χ² / ndf 1.15 / 1
Constant 2.62 ± 2.23
Slope -0.2043 ± 0.3227
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 24
Mean 6.792
RMS 3.68
χ² / ndf 1.085 / 2
Constant 2.472 ± 1.207
Slope -0.2003 ± 0.1572
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 7
Mean 8.214
RMS 3.692
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 7
Mean 26.21
RMS 20.49
y: counts
x: phi-bin, 0=12:30, follows TPC