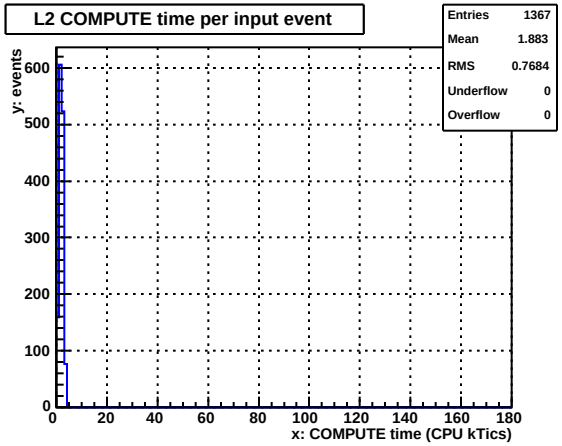
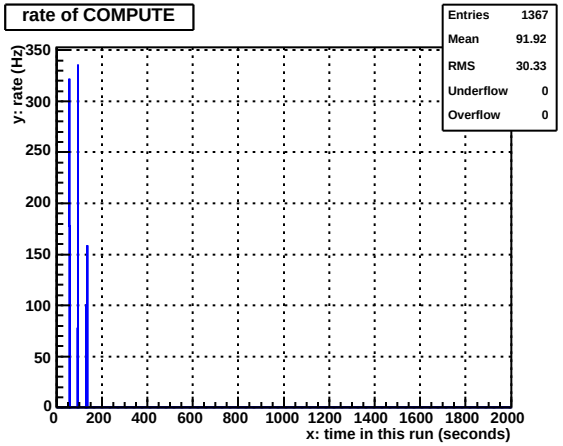
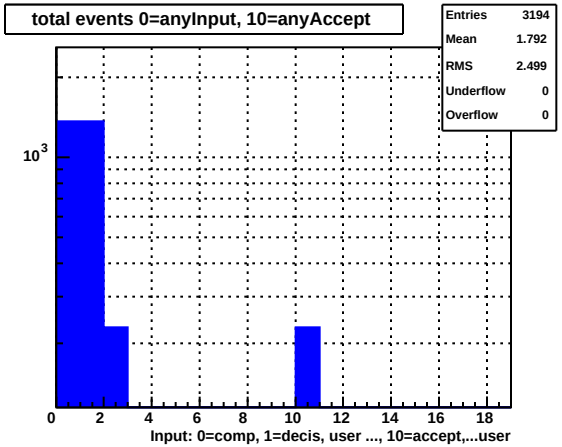
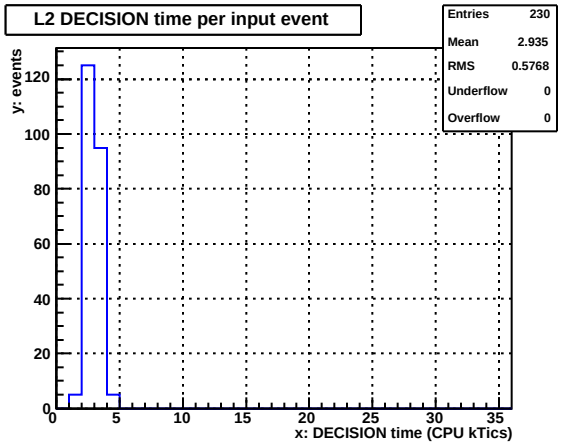
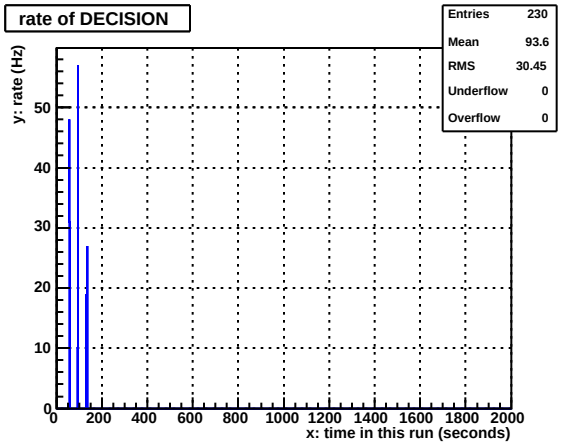
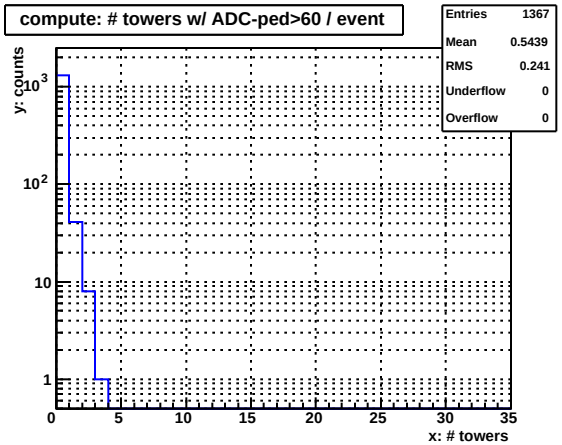

L2 COMPUTE time per input event
Entries 1367
Mean 1.883
RMS 0.7684
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

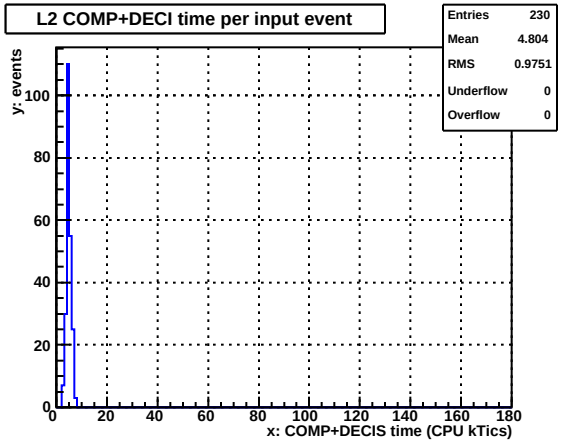

rate of COMPUTE
Entries 1367
Mean 91.92
RMS 30.33
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

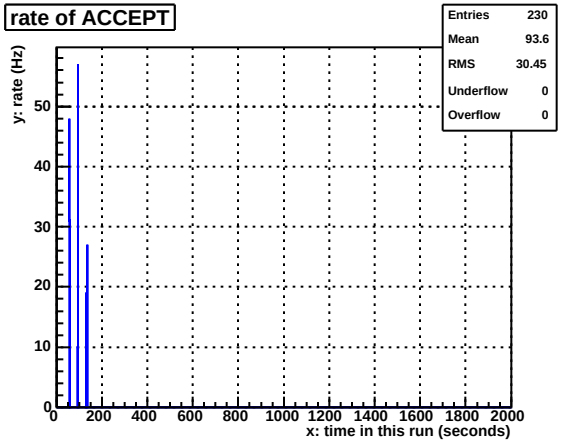

total events 0=anyInput, 10=anyAccept
Entries 3194
Mean 1.792
RMS 2.499
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

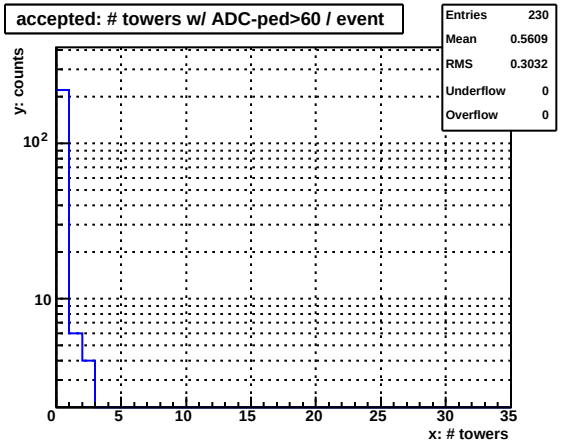

L2 DECISION time per input event
Entries 230
Mean 2.935
RMS 0.5768
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

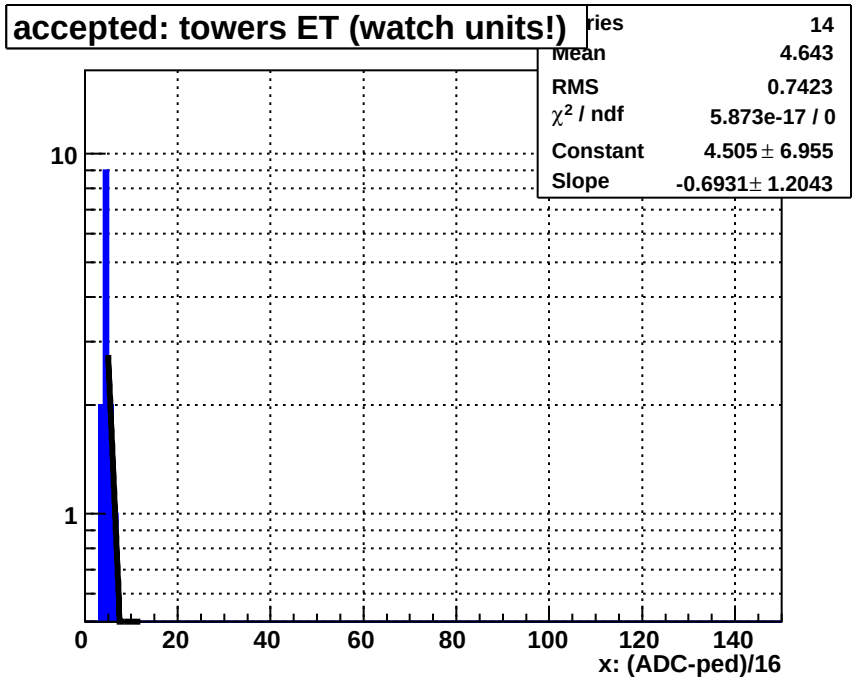

rate of DECISION
Entries 230
Mean 93.6
RMS 30.45
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

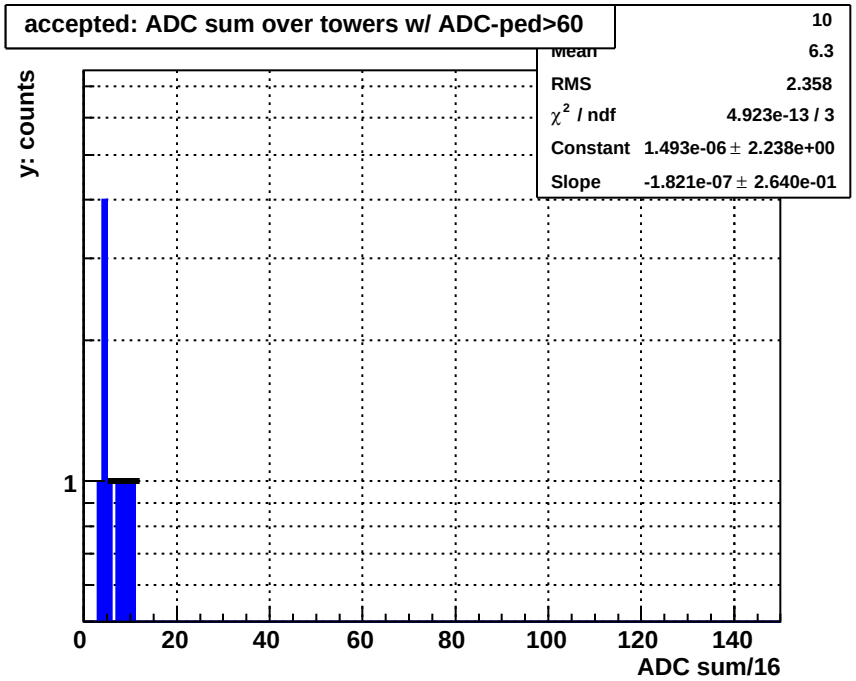

compute: # towers w/ ADC-ped>60 / event
Entries 1367
Mean 0.5439
RMS 0.241
Underflow 0
Overflow 0
y: counts
x: # towers

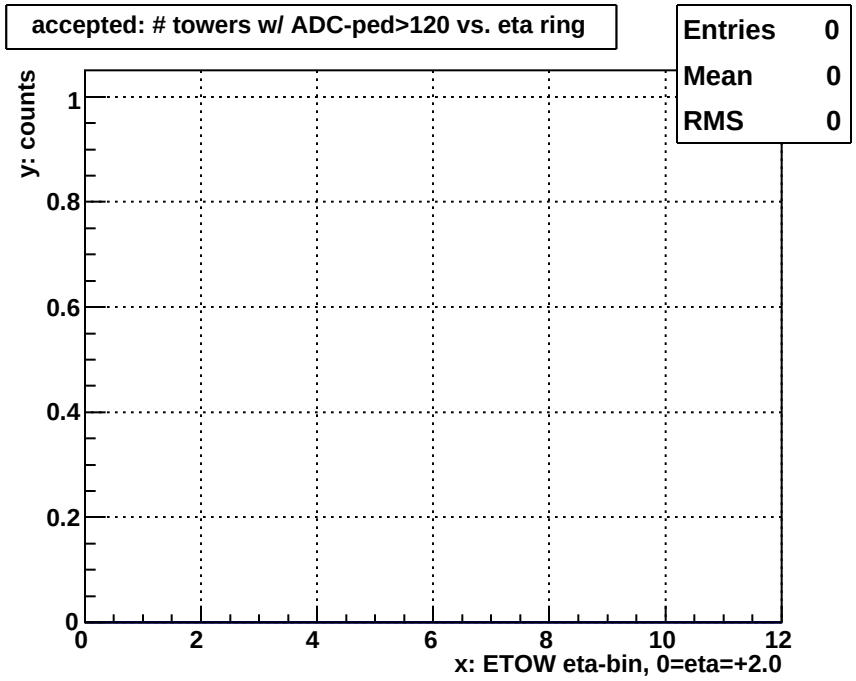

L2 COMP+DECI time per input event
Entries 230
Mean 4.804
RMS 0.9751
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

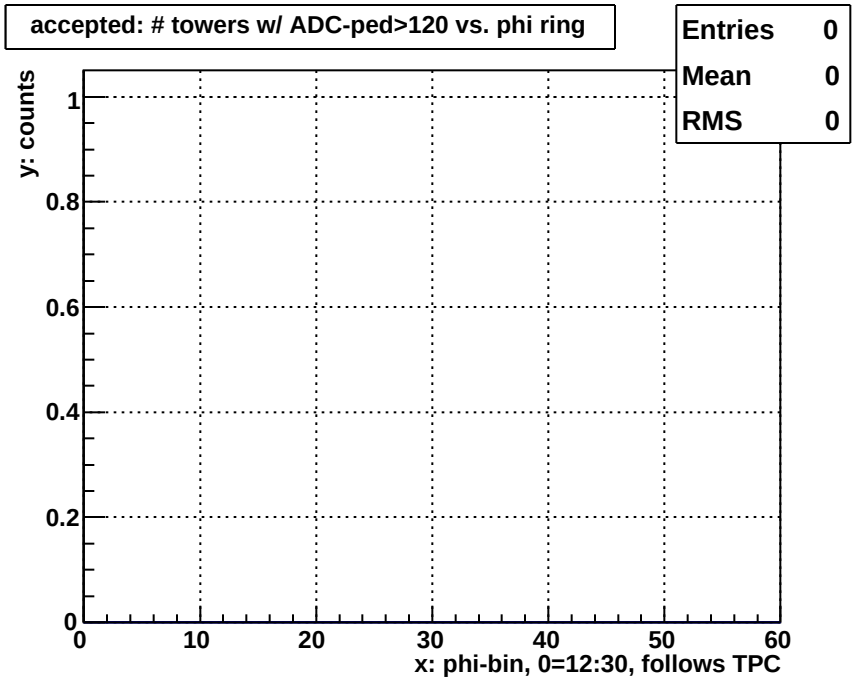

rate of ACCEPT
Entries 230
Mean 93.6
RMS 30.45
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 230
Mean 0.5609
RMS 0.3032
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
Entries 14
Mean 4.643
RMS 0.7423
χ² / ndf 5.873e-17 / 0
Constant 4.505 ± 6.955
Slope -0.6931 ± 1.2043
10
1
0 20 40 60 80 100 120 140
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
Entries 10
Mean 6.3
RMS 2.358
χ² / ndf 4.923e-13 / 3
Constant 1.493e-06 ± 2.238e+00
Slope -1.821e-07 ± 2.640e-01
y: counts
1
0 20 40 60 80 100 120 140
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 0
Mean 0
RMS 0
y: counts
1
0.8
0.6
0.4
0.2
0
0 2 4 6 8 10 12
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 0
Mean 0
RMS 0
y: counts
1
0.8
0.6
0.4
0.2
0
0 10 20 30 40 50 60
x: phi-bin, 0=12:30, follows TPC