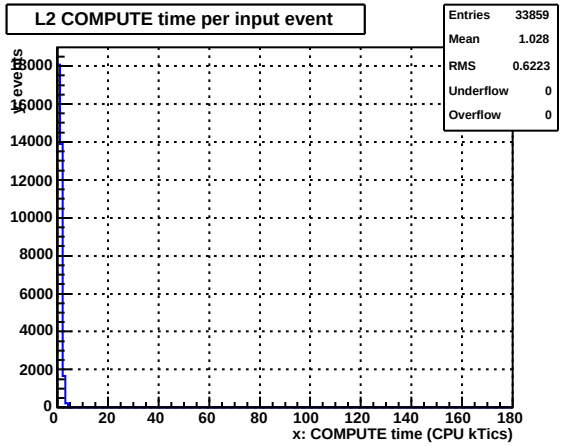
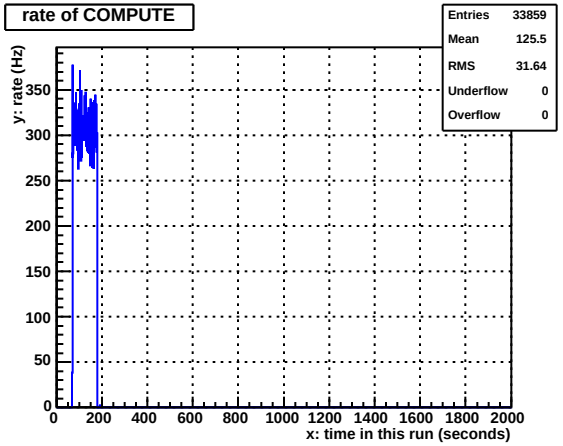
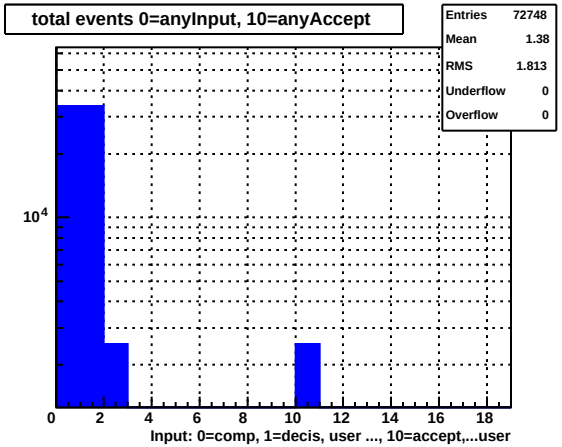
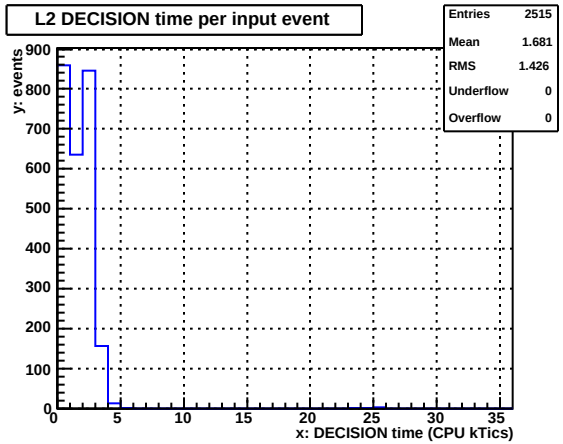
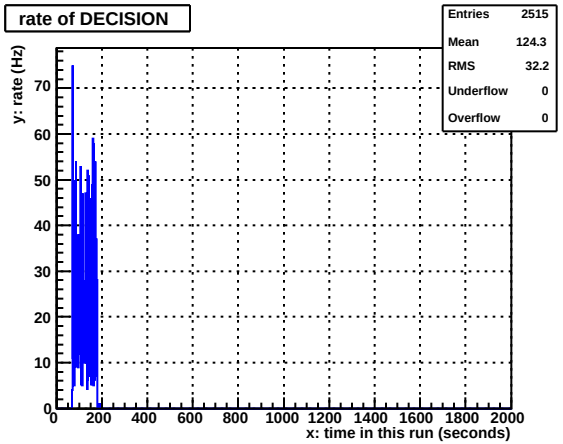
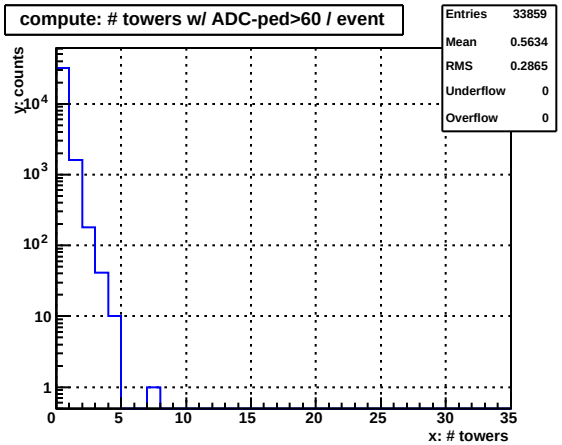

L2 COMPUTE time per input event
Entries 33859
Mean 1.028
RMS 0.6223
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

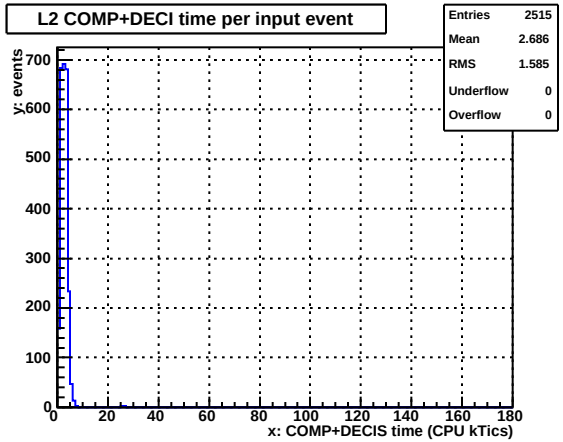

rate of COMPUTE
Entries 33859
Mean 125.5
RMS 31.64
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

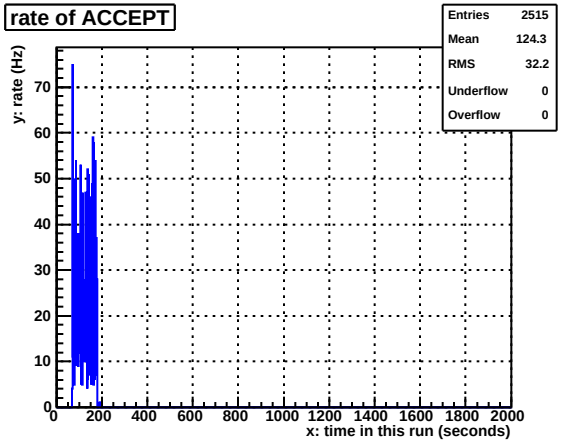

total events 0=anyInput, 10=anyAccept
Entries 72748
Mean 1.38
RMS 1.813
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

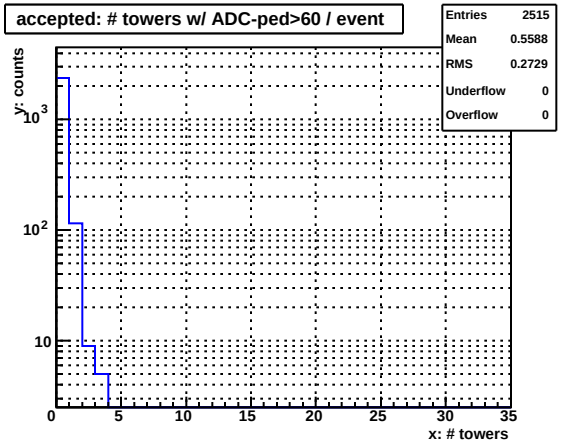

L2 DECISION time per input event
Entries 2515
Mean 1.681
RMS 1.426
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

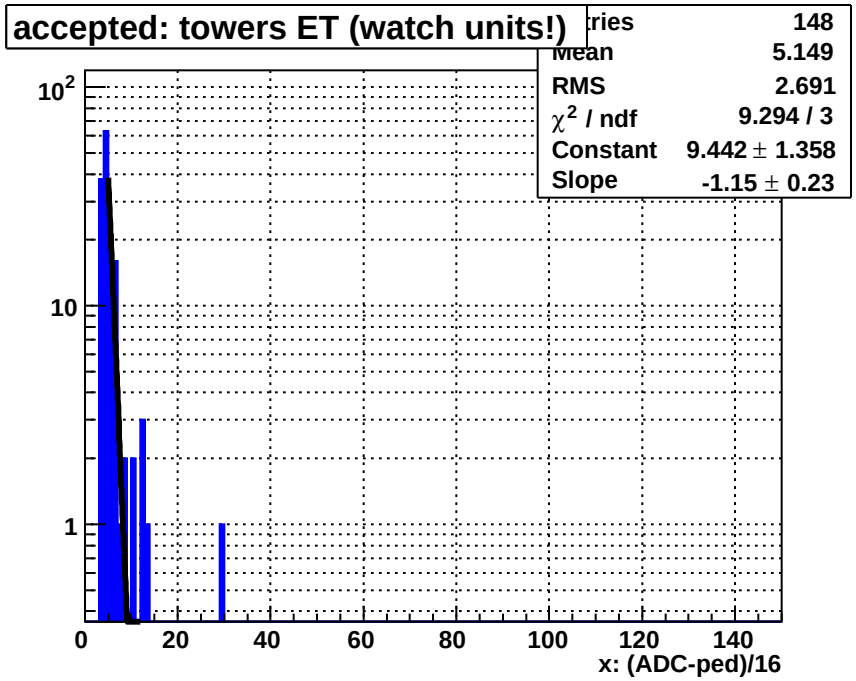

rate of DECISION
Entries 2515
Mean 124.3
RMS 32.2
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

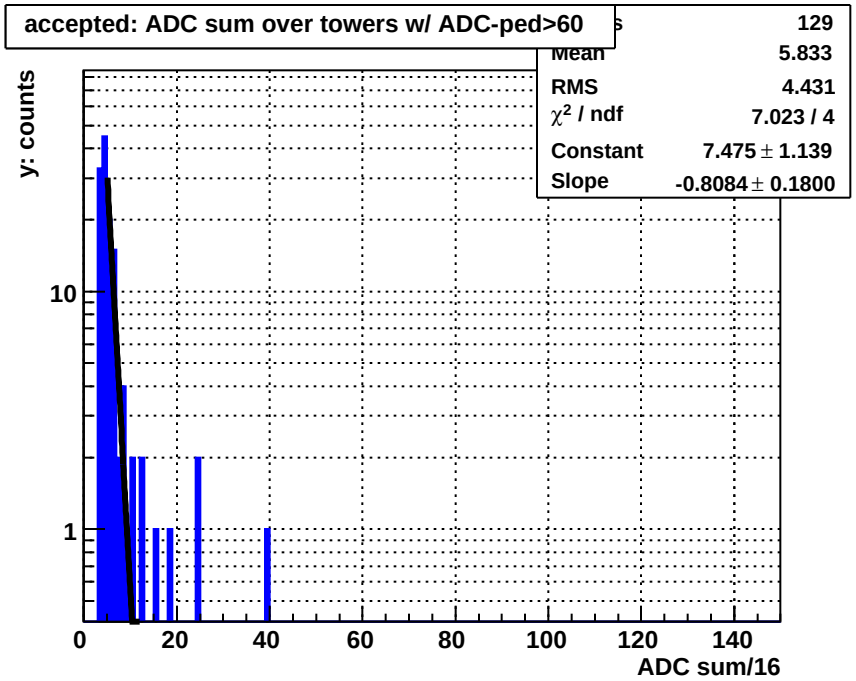

compute: # towers w/ ADC-ped>60 / event
Entries 33859
Mean 0.5634
RMS 0.2865
Underflow 0
Overflow 0
y: counts
x: # towers

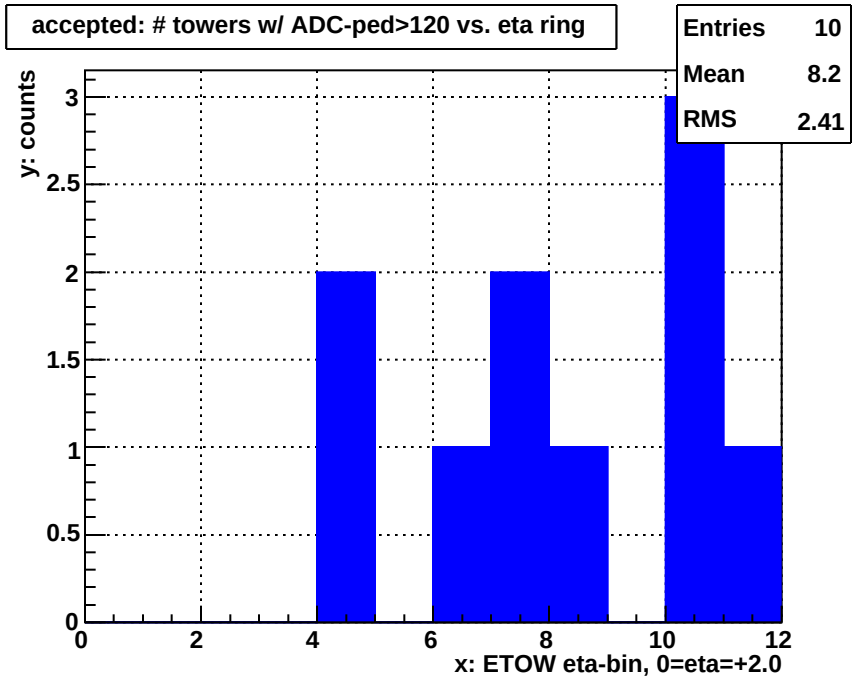

L2 COMP+DECI time per input event
Entries 2515
Mean 2.686
RMS 1.585
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

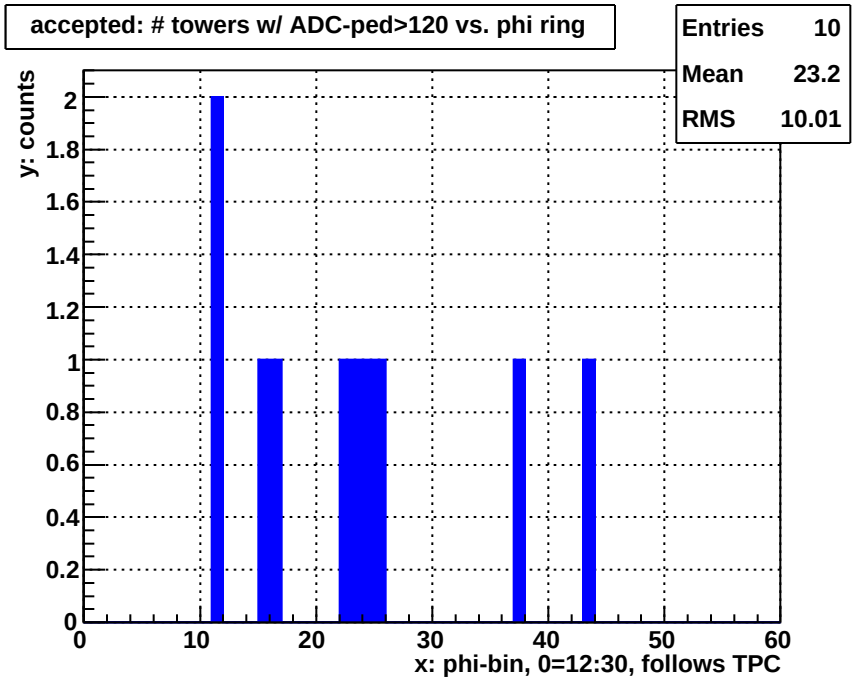

rate of ACCEPT
Entries 2515
Mean 124.3
RMS 32.2
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 2515
Mean 0.5588
RMS 0.2729
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
Entries 148
Mean 5.149
RMS 2.691
χ² / ndf 9.294 / 3
Constant 9.442 ± 1.358
Slope -1.15 ± 0.23
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
Mean 5.833
RMS 4.431
χ² / ndf 7.023 / 4
Constant 7.475 ± 1.139
Slope -0.8084 ± 0.1800
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 10
Mean 8.2
RMS 2.41
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 10
Mean 23.2
RMS 10.01
y: counts
x: phi-bin, 0=12:30, follows TPC