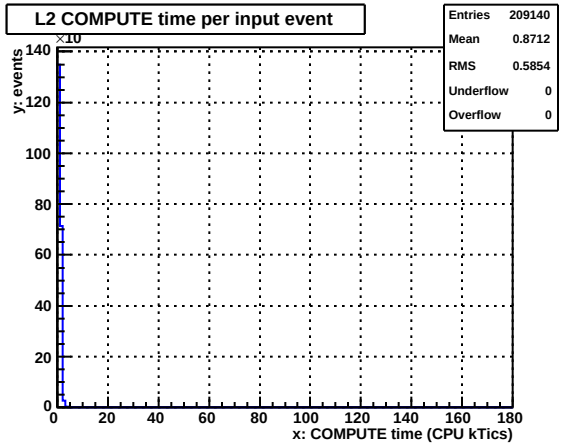
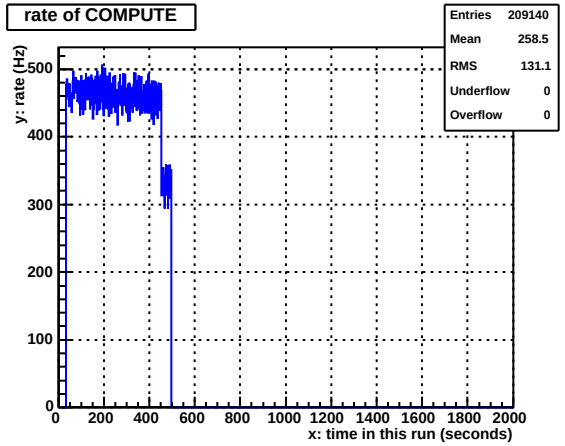
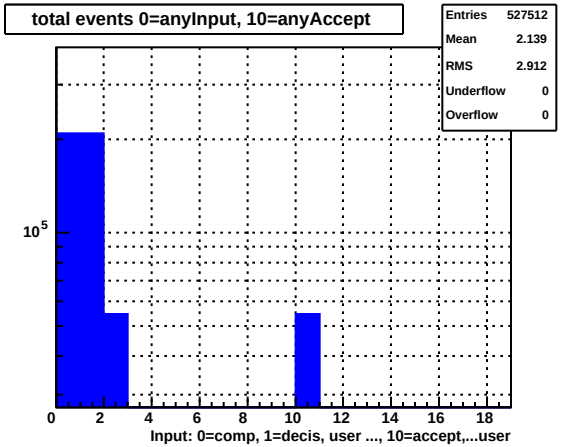
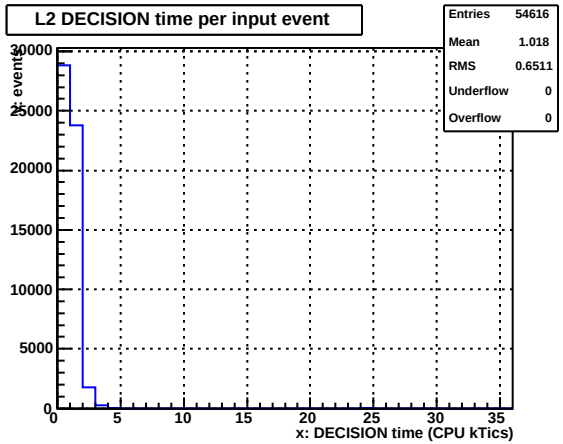
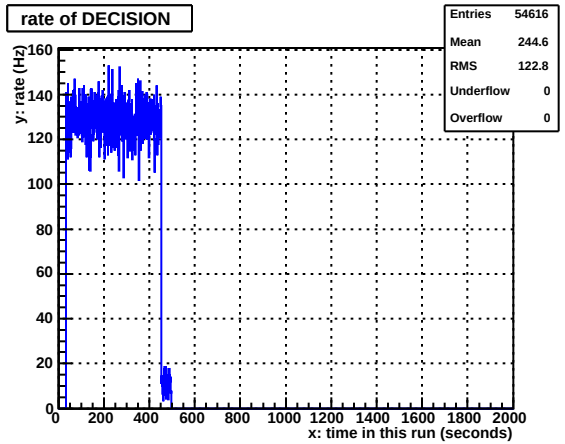
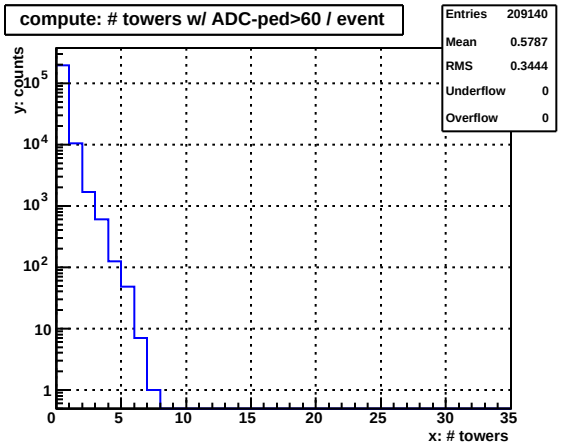

L2 COMPUTE time per input event
Entries 209140
Mean 0.8712
RMS 0.5854
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

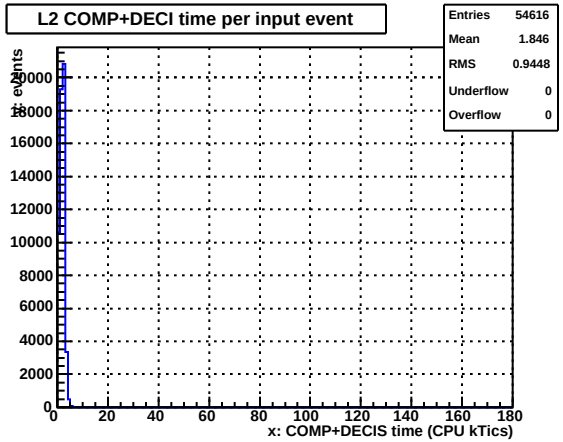

rate of COMPUTE
Entries 209140
Mean 258.5
RMS 131.1
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

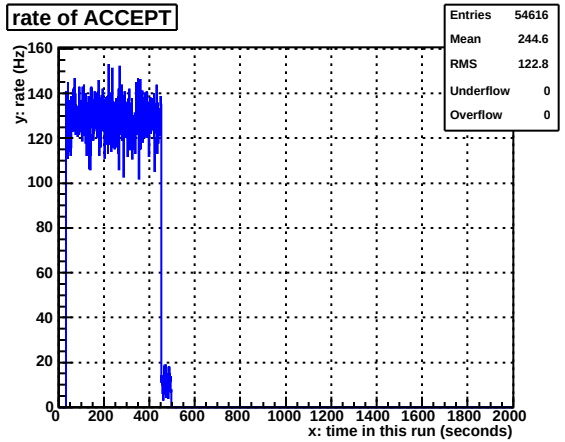

total events 0=anyInput, 10=anyAccept
Entries 527512
Mean 2.139
RMS 2.912
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

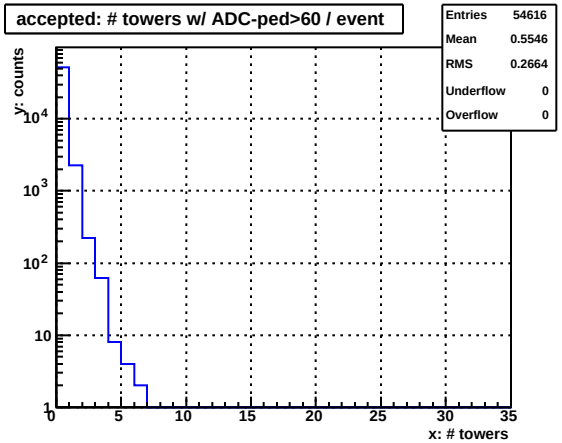

L2 DECISION time per input event
Entries 54616
Mean 1.018
RMS 0.6511
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

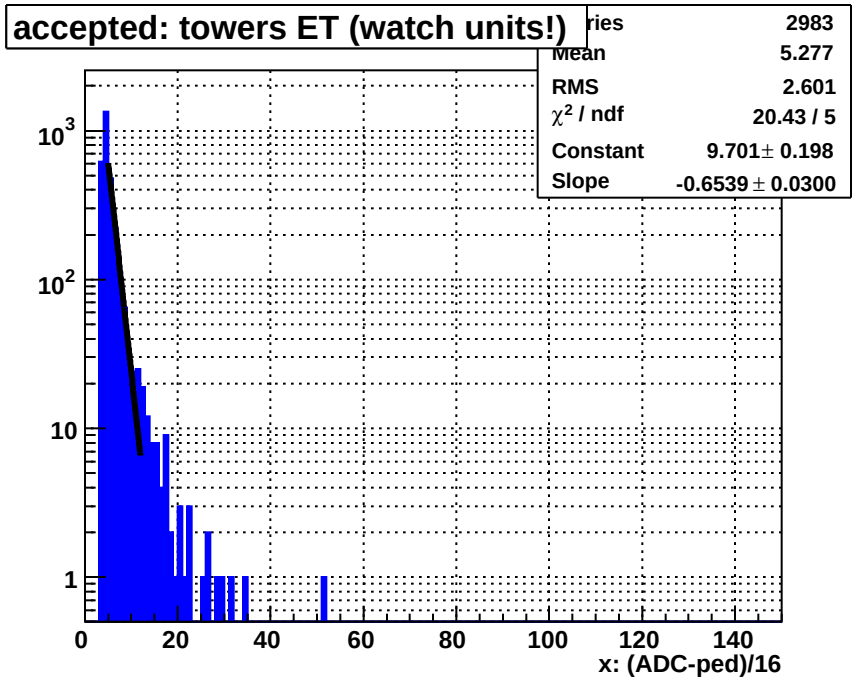

rate of DECISION
Entries 54616
Mean 244.6
RMS 122.8
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

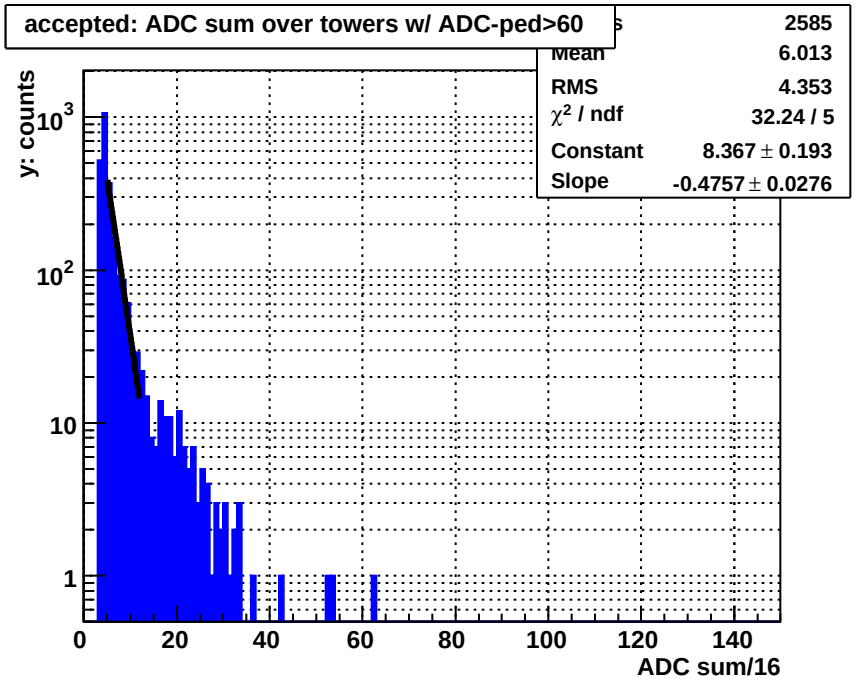

compute: # towers w/ ADC-ped>60 / event
Entries 209140
Mean 0.5787
RMS 0.3444
Underflow 0
Overflow 0
y: counts
x: # towers

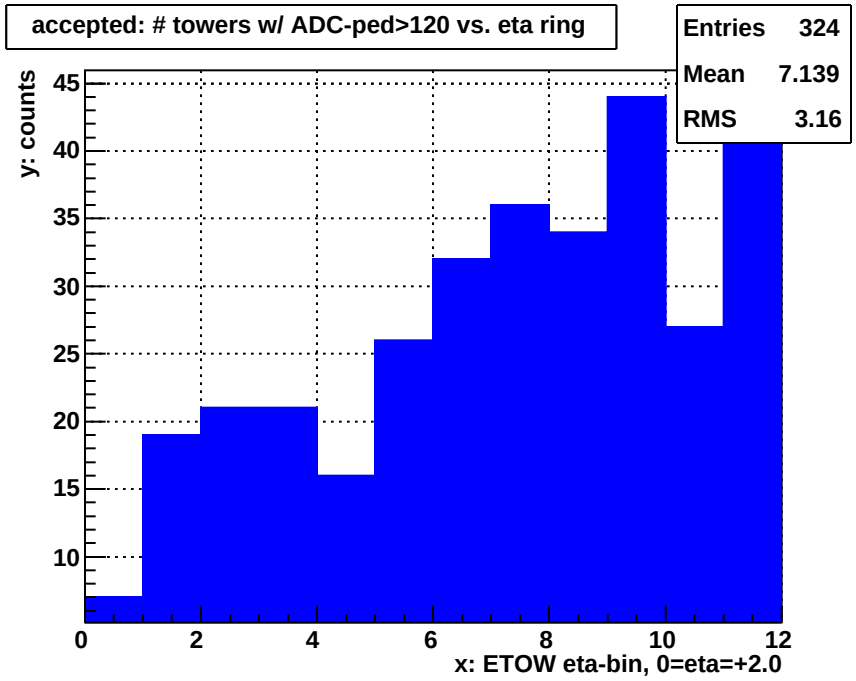

L2 COMP+DECI time per input event
Entries 54616
Mean 1.846
RMS 0.9448
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

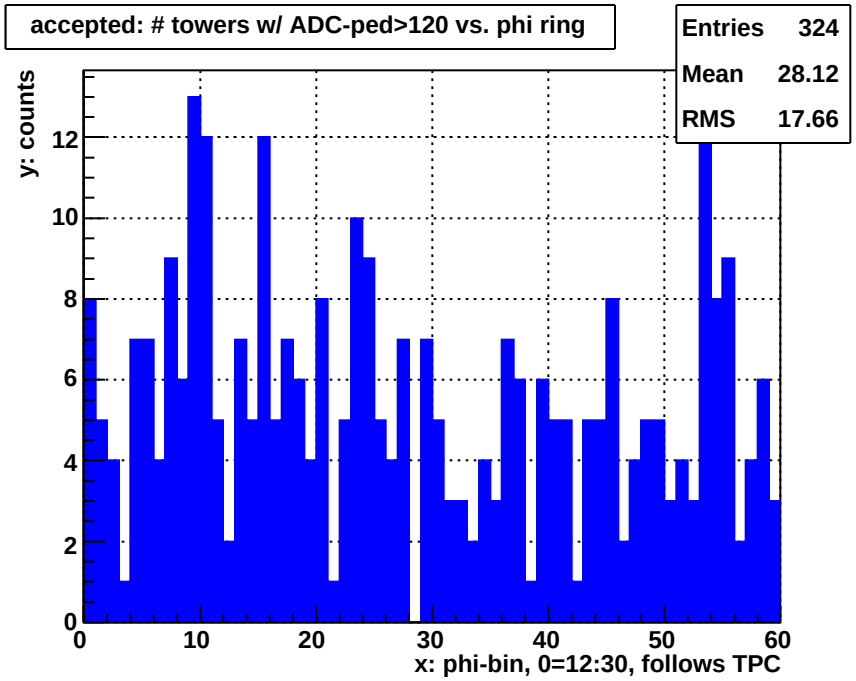

rate of ACCEPT
Entries 54616
Mean 244.6
RMS 122.8
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 54616
Mean 0.5546
RMS 0.2664
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 2983
Mean 5.277
RMS 2.601
χ² / ndf 20.43 / 5
Constant 9.701 ± 0.198
Slope -0.6539 ± 0.0300
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 2585
Mean 6.013
RMS 4.353
χ² / ndf 32.24 / 5
Constant 8.367 ± 0.193
Slope -0.4757 ± 0.0276
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 324
Mean 7.139
RMS 3.16
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 324
Mean 28.12
RMS 17.66
y: counts
x: phi-bin, 0=12:30, follows TPC