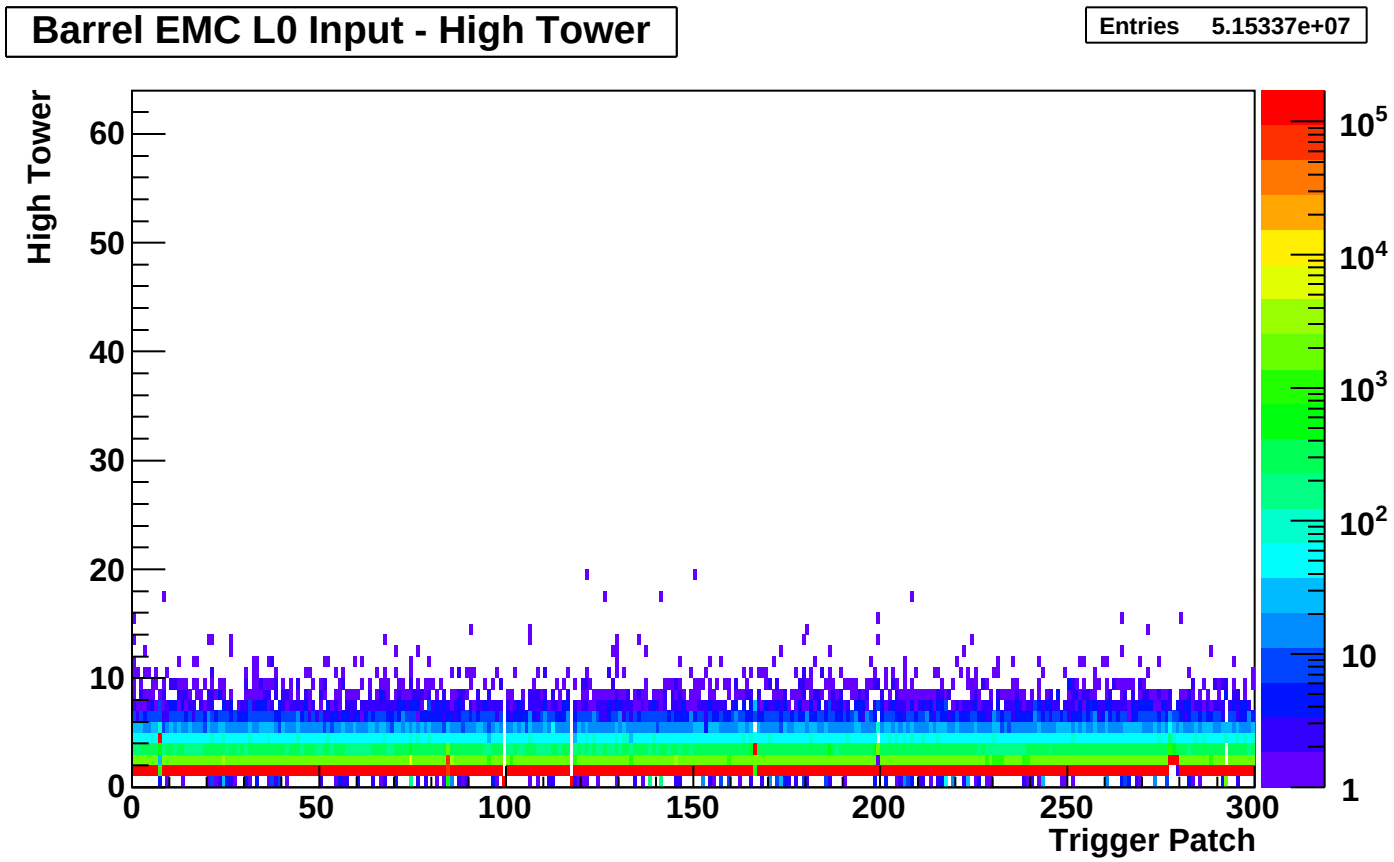
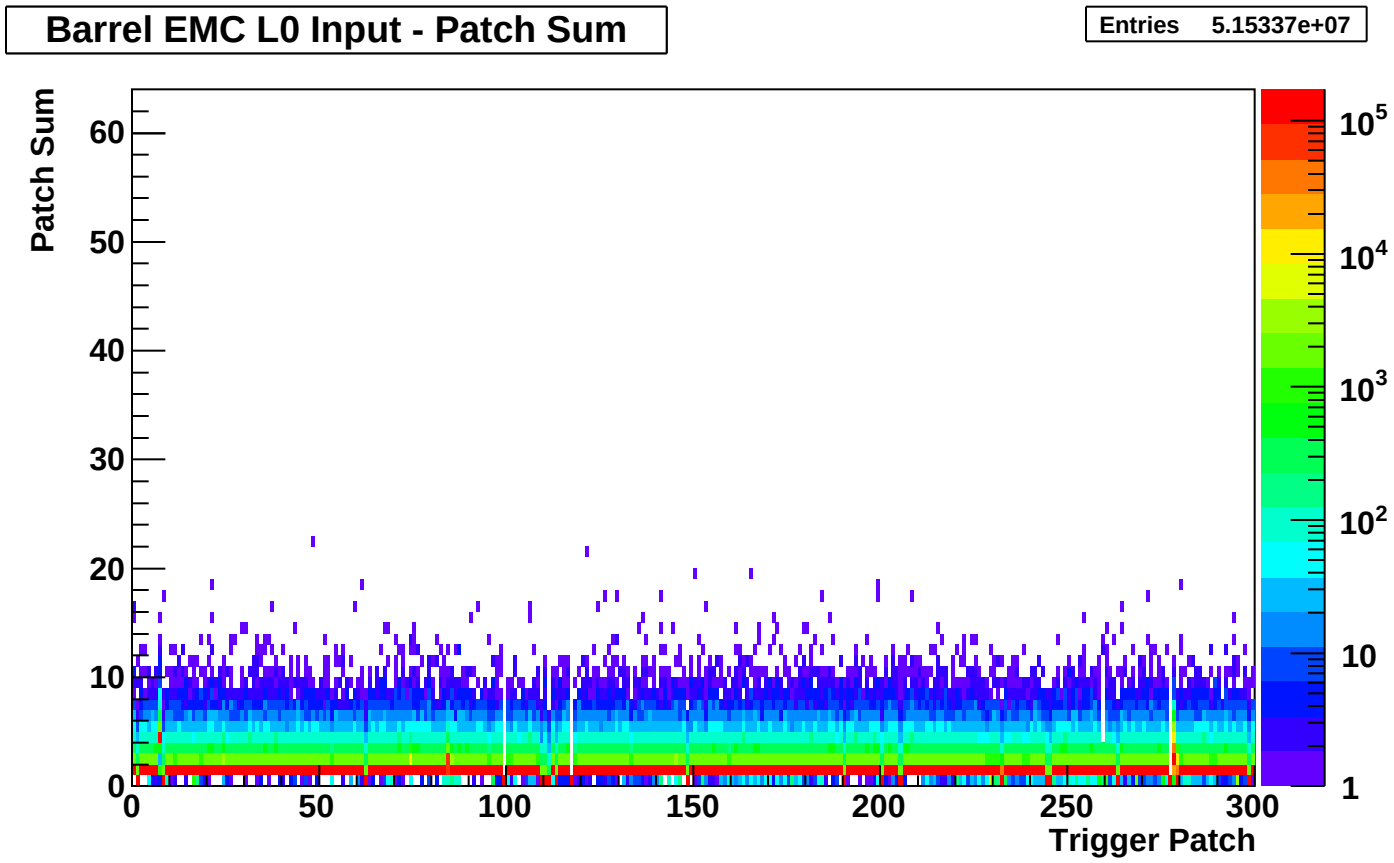
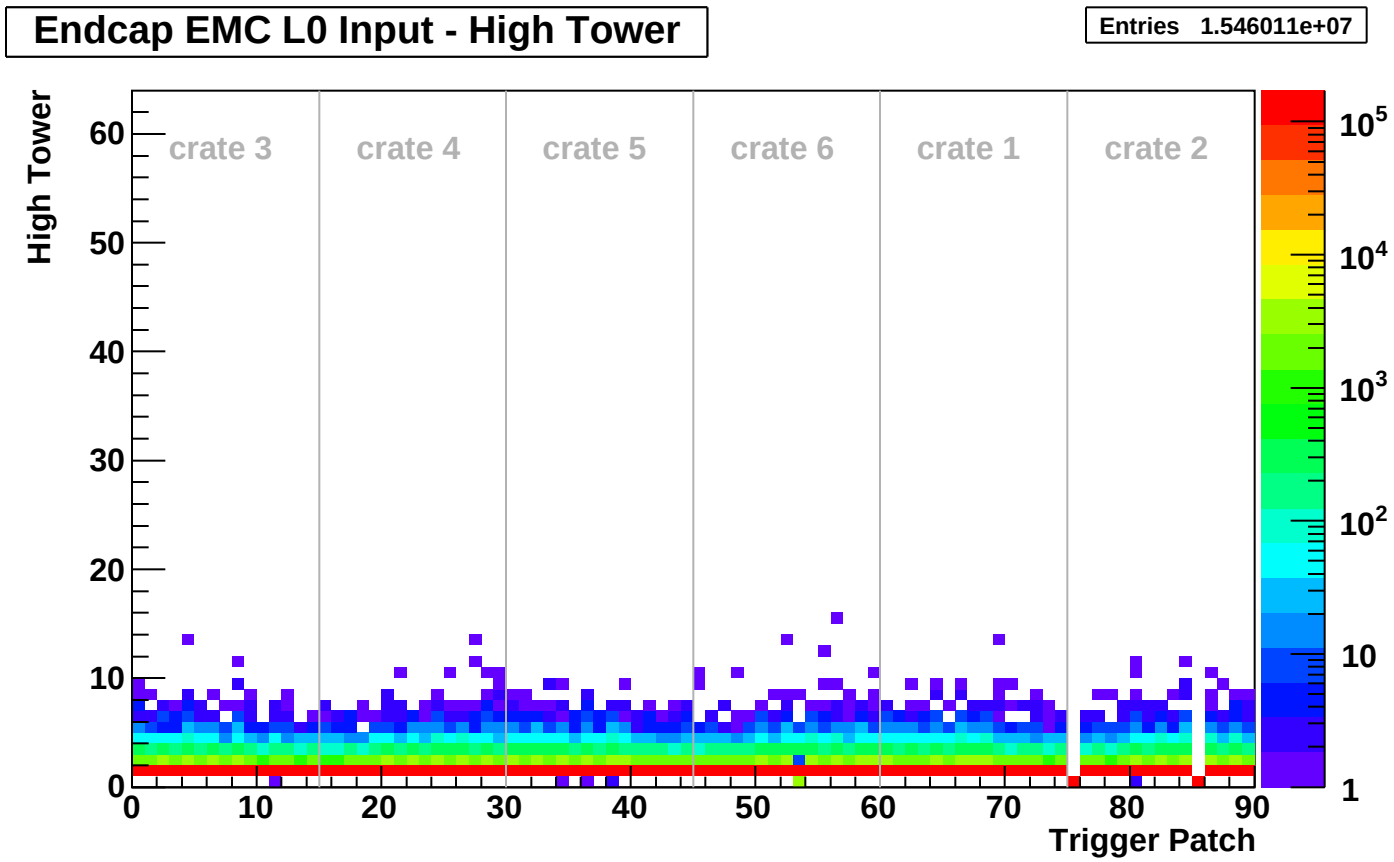
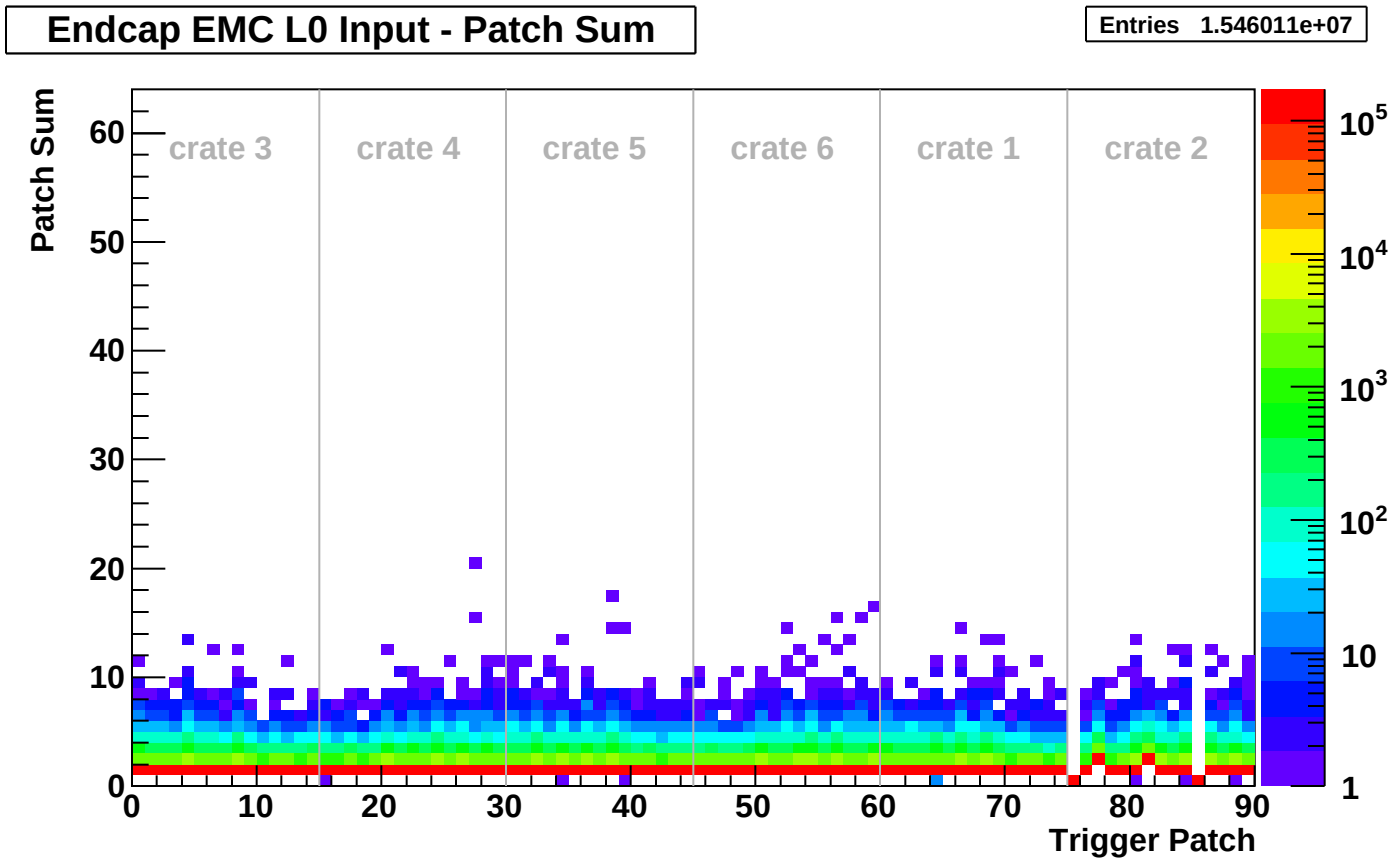
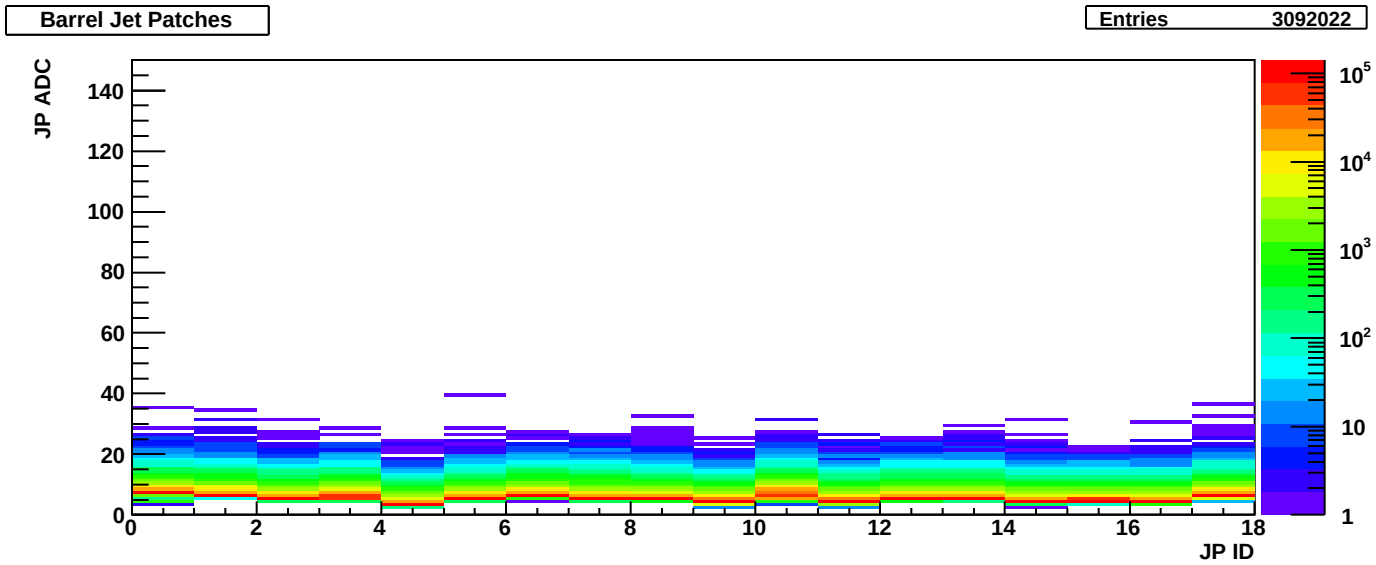
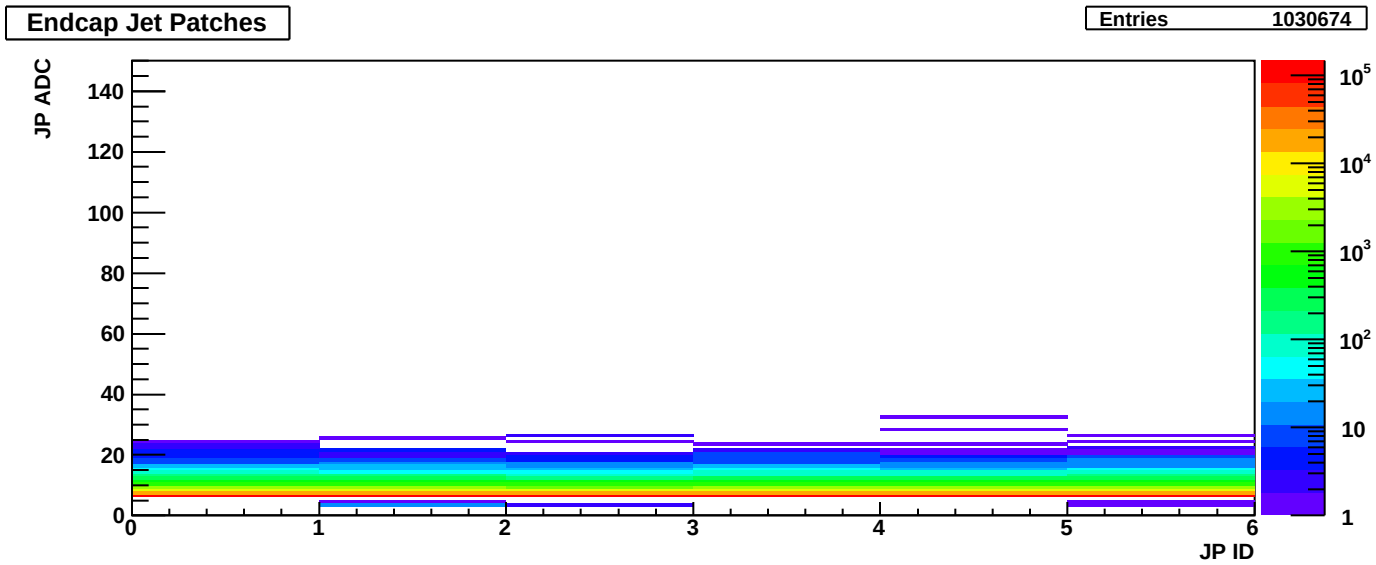

Barrel EMC L0 Input - High Tower
Entries 5.15337e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

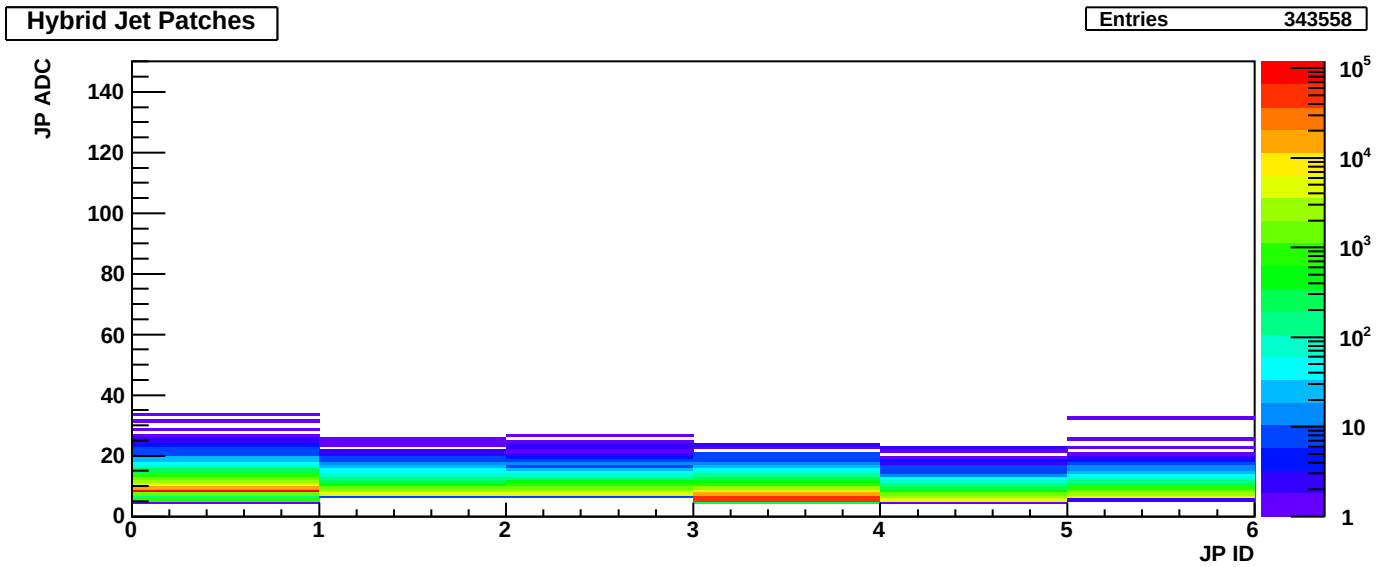

Barrel EMC L0 Input - Patch Sum
Entries 5.15337e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

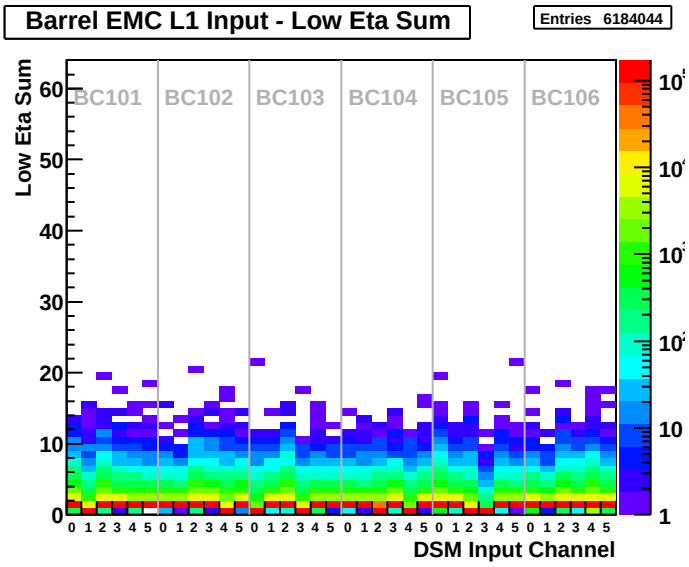

Endcap EMC L0 Input - High Tower
Entries 1.546011e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

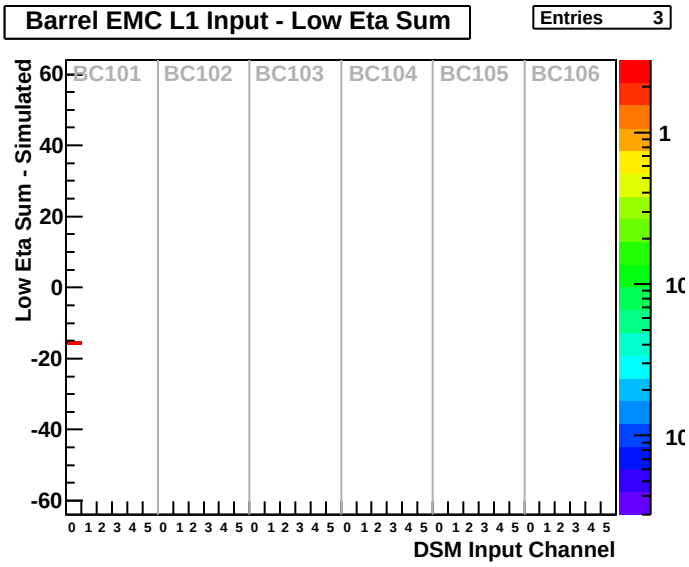

Endcap EMC L0 Input - Patch Sum
Entries 1.546011e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

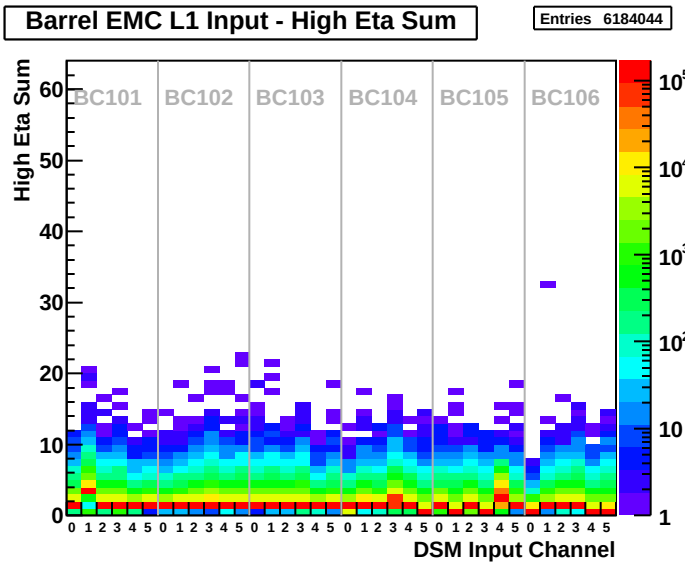

Barrel Jet Patches
Entries 3092022
JP ADC
JP ID

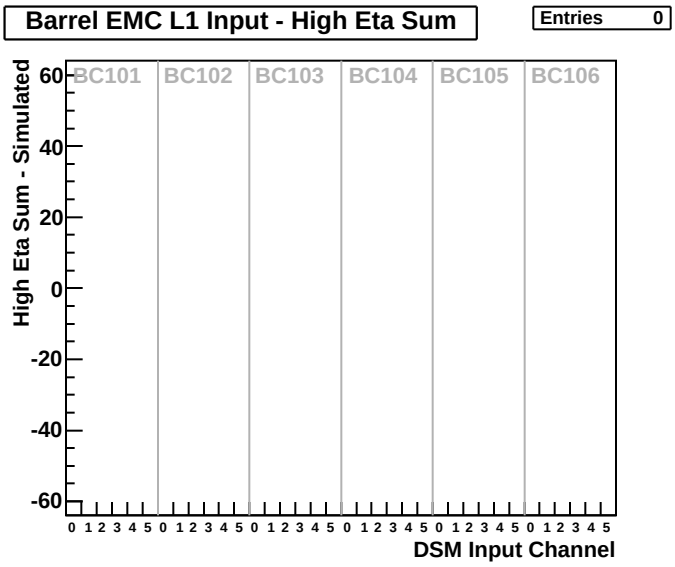

Endcap Jet Patches
Entries 1030674
JP ADC
JP ID

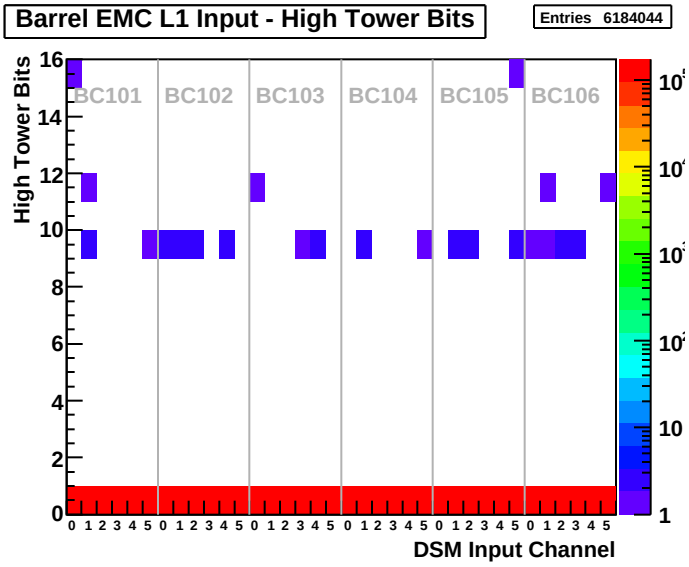

Hybrid Jet Patches
Entries 343558
JP ADC
JP ID

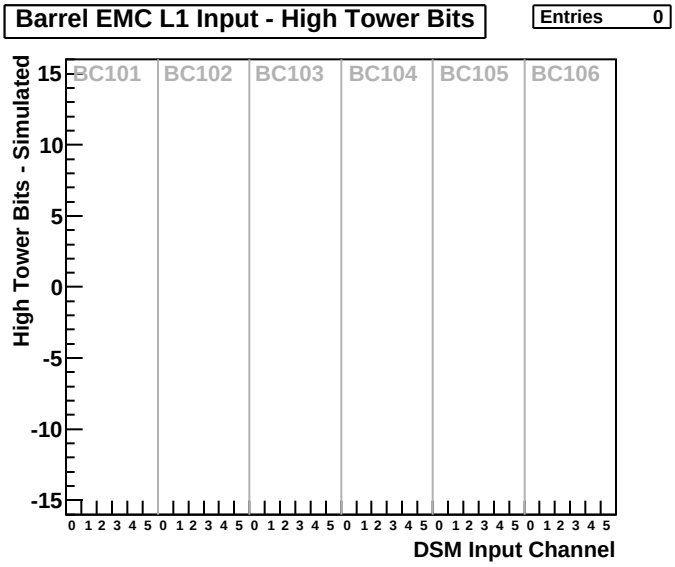

Barrel EMC L1 Input - Low Eta Sum
Entries 6184044
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

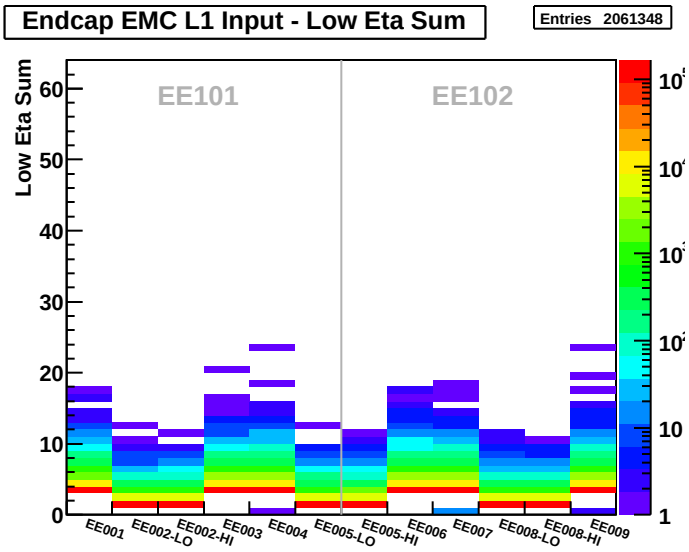

Barrel EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

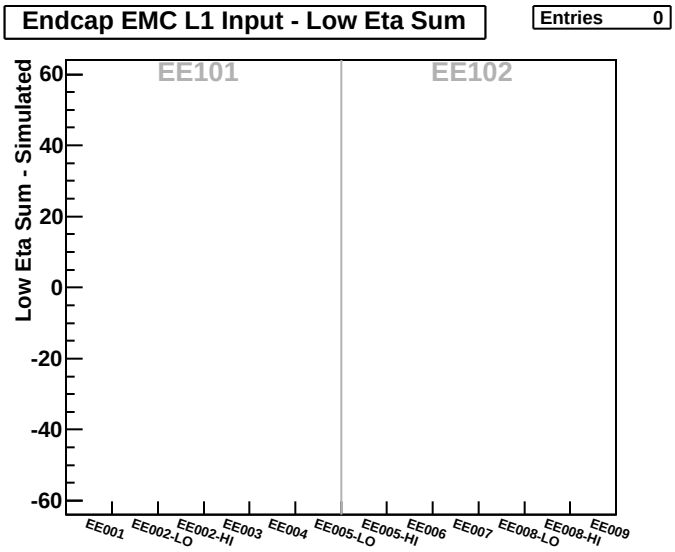

Barrel EMC L1 Input - High Eta Sum
Entries 6184044
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

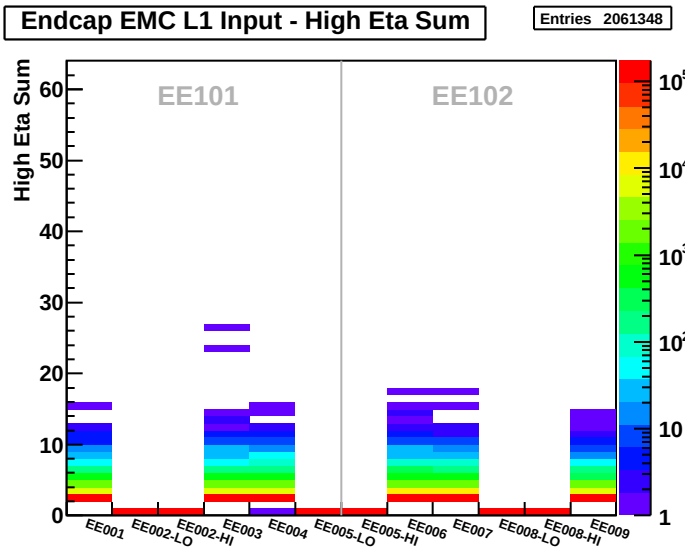

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

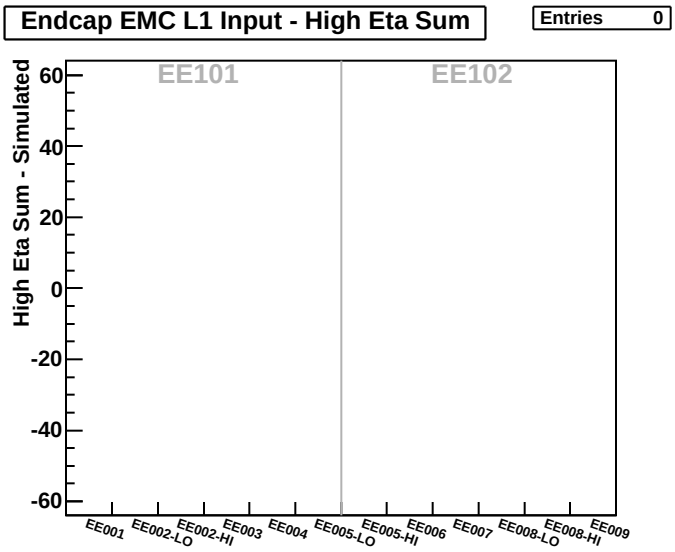

Barrel EMC L1 Input - High Tower Bits
Entries 6184044
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

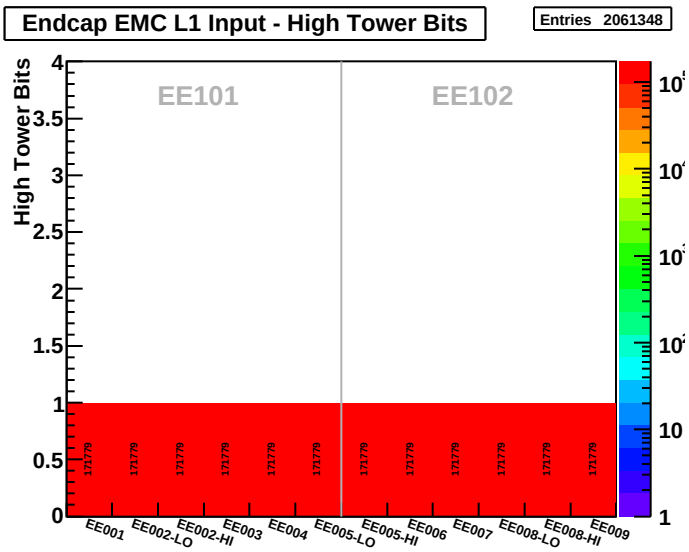

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

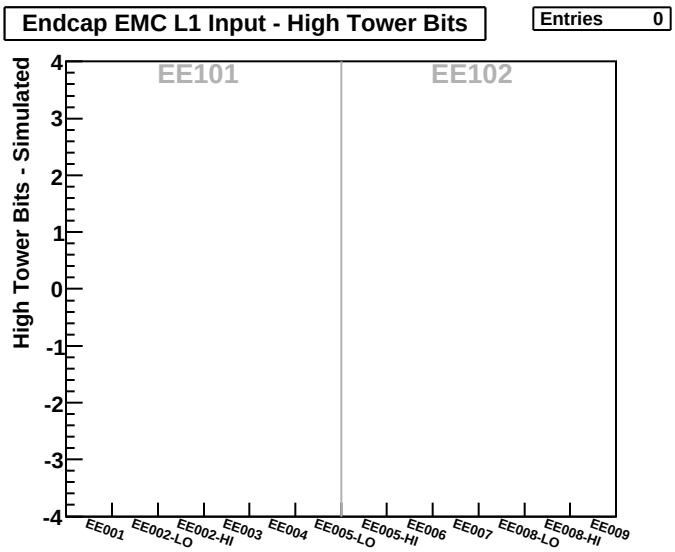

Endcap EMC L1 Input - Low Eta Sum
Entries 2061348
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

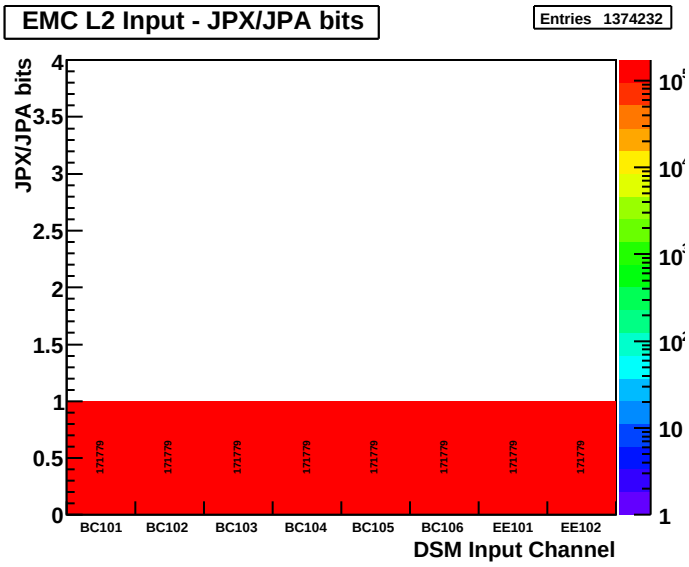

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

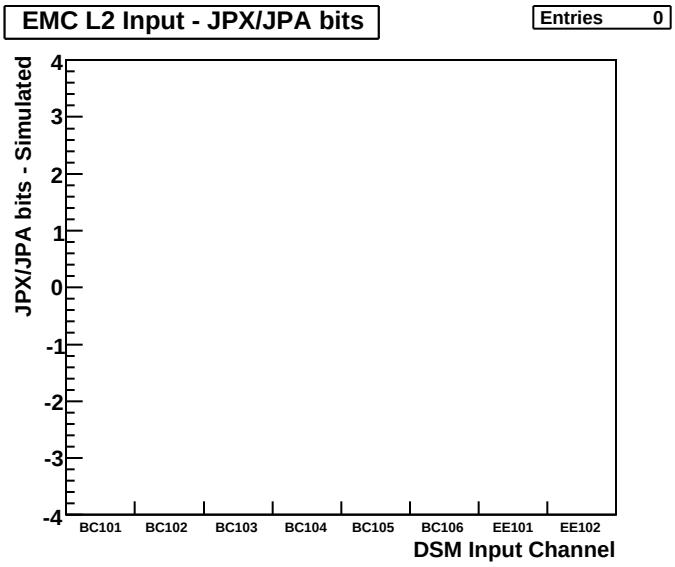

Endcap EMC L1 Input - High Eta Sum
Entries 2061348
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

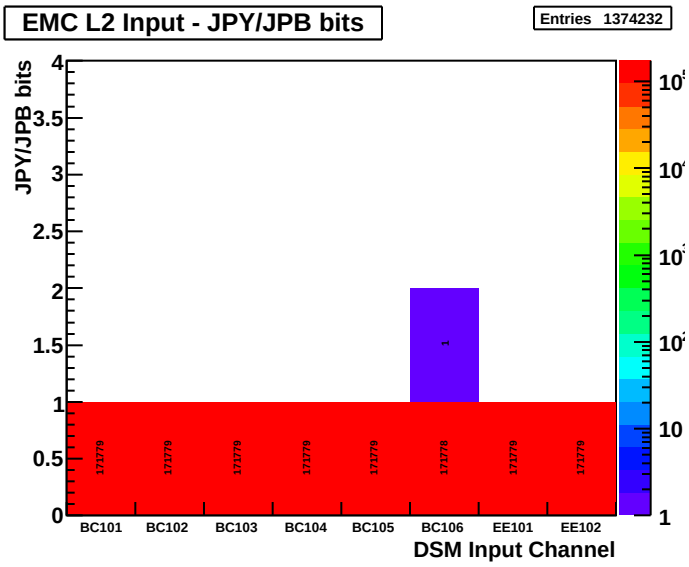

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

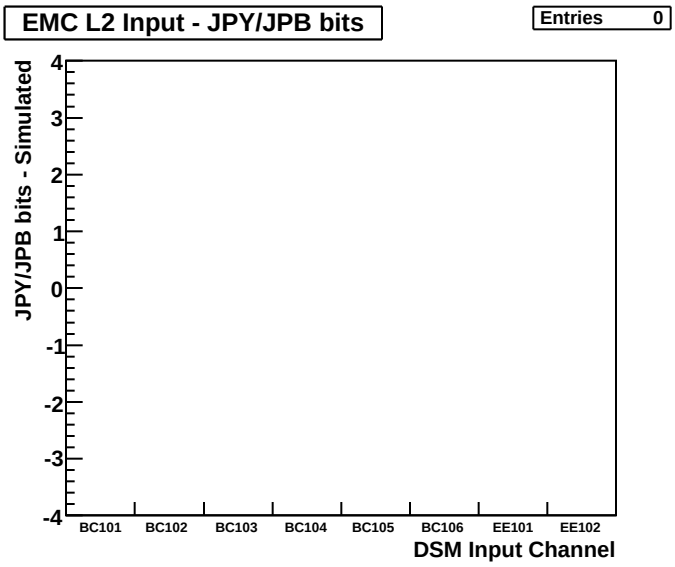

Endcap EMC L1 Input - High Tower Bits
Entries 2061348
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

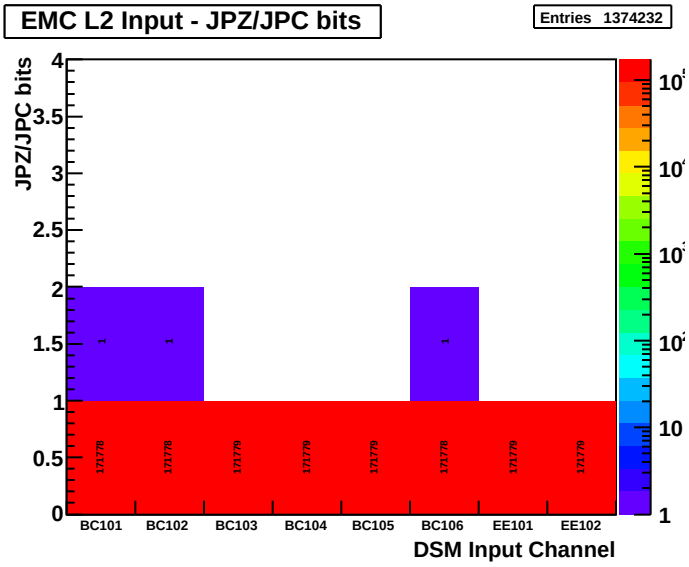

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

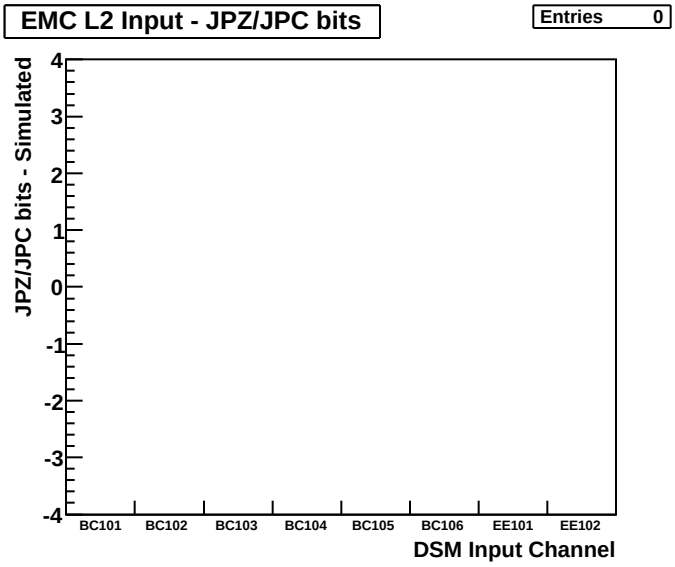

EMC L2 Input - JPX/JPA bits
Entries 1374232
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

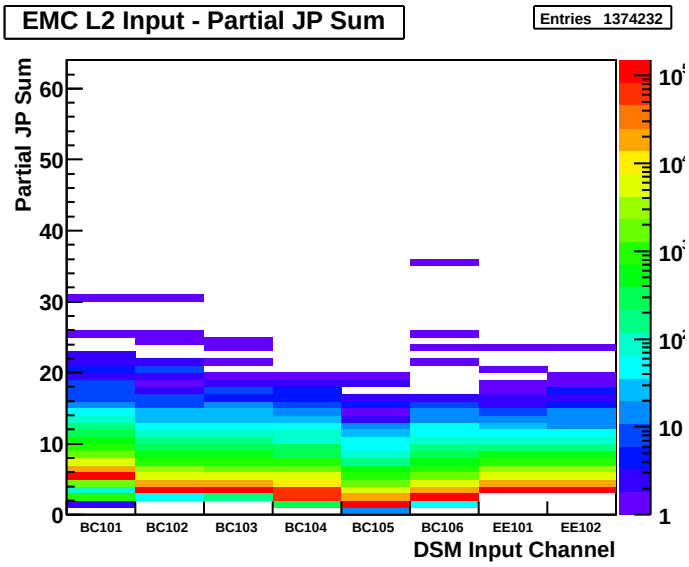

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

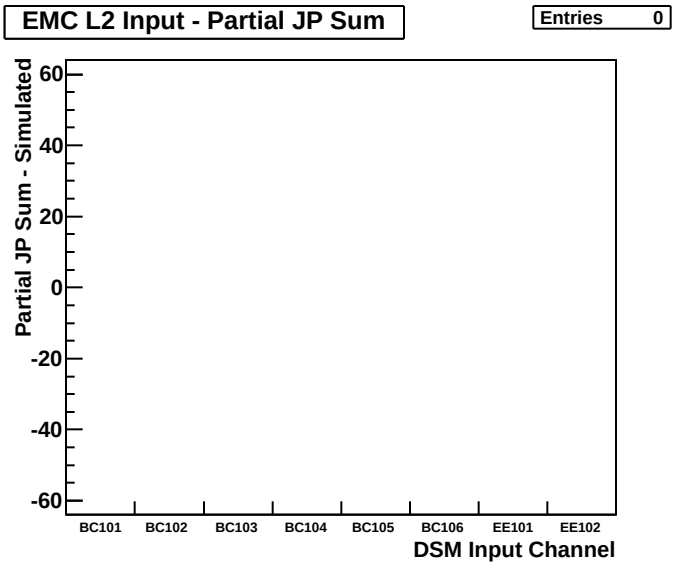

EMC L2 Input - JPY/JPB bits
Entries 1374232
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

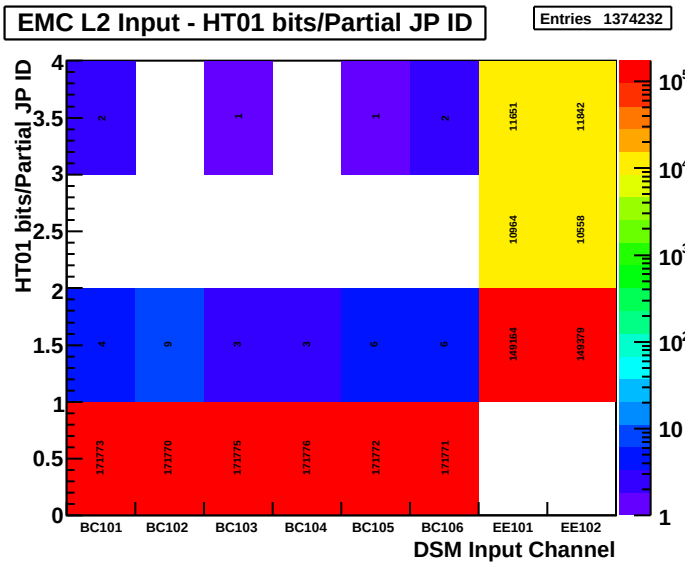

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

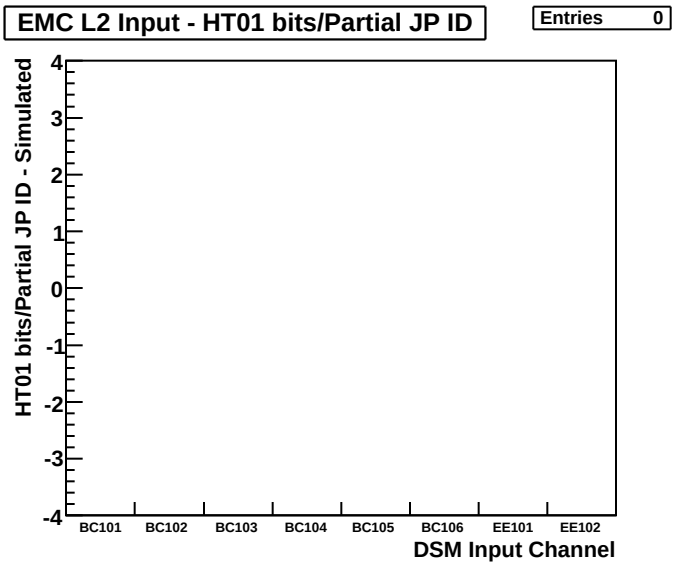

EMC L2 Input - JPZ/JPC bits
Entries 1374232
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

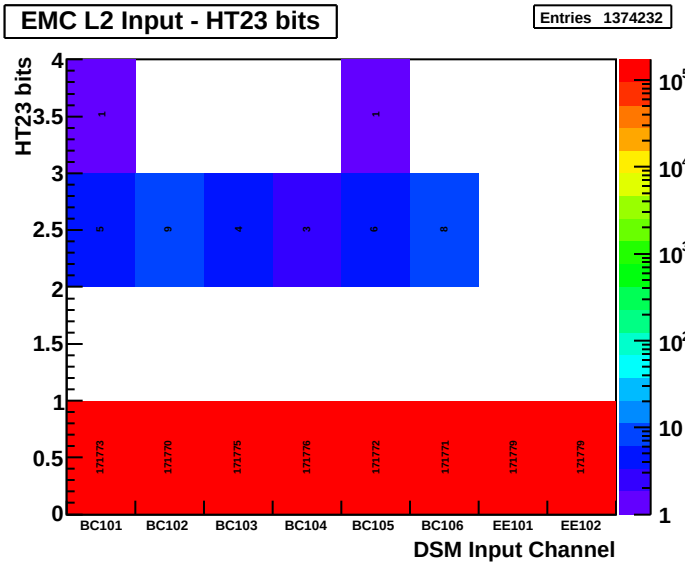

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

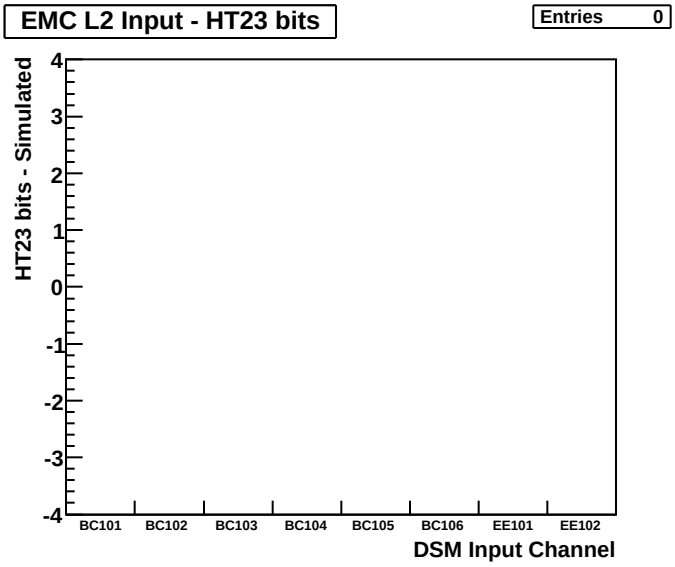

EMC L2 Input - Partial JP Sum
Entries 1374232
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

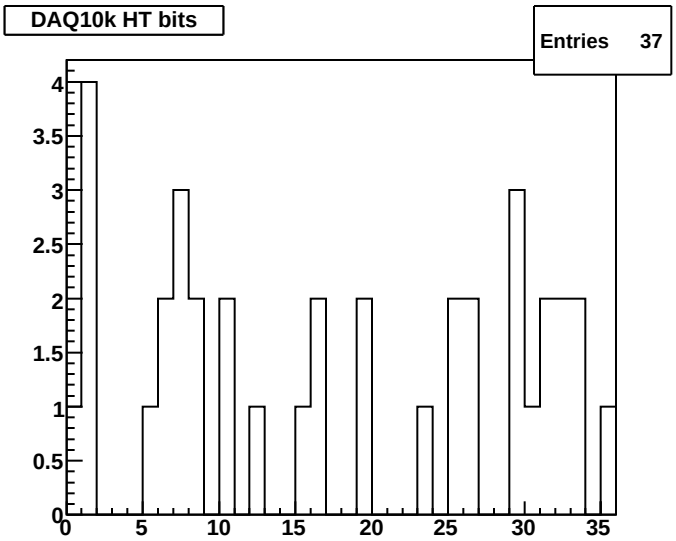

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

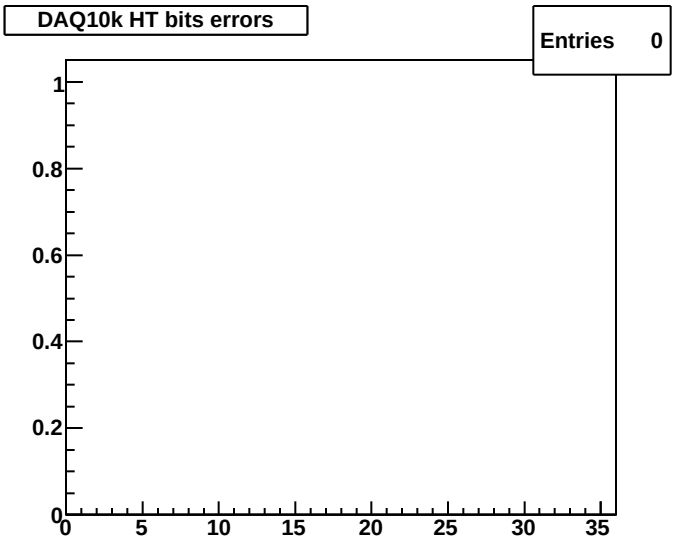

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1374232
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

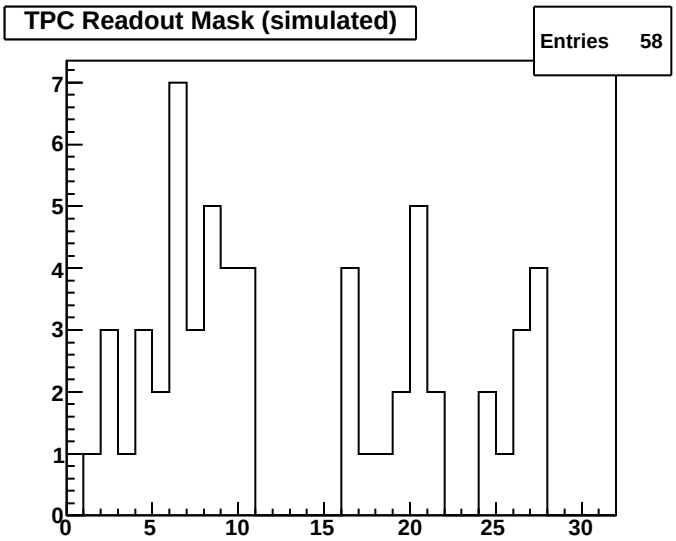

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

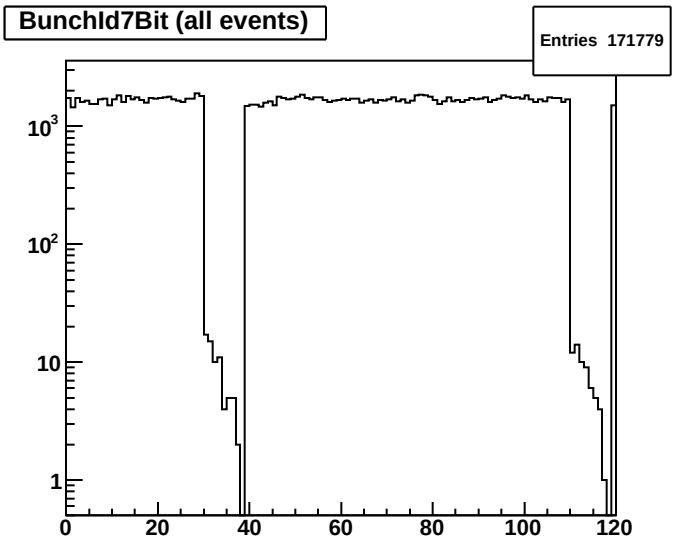

EMC L2 Input - HT23 bits
Entries 1374232
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

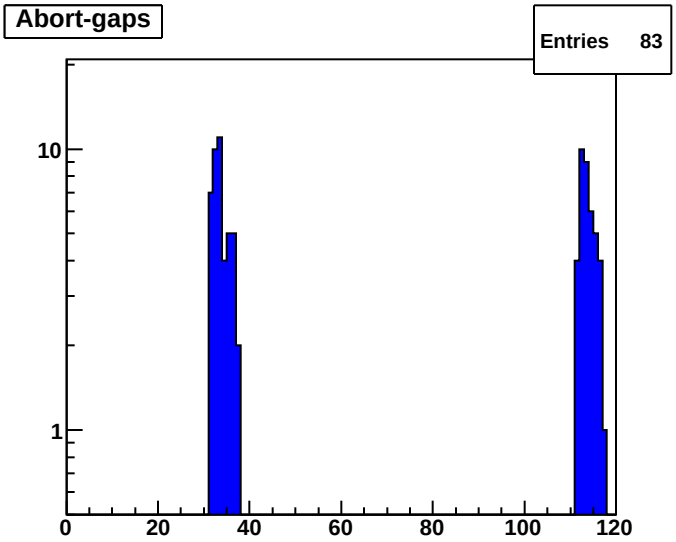

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

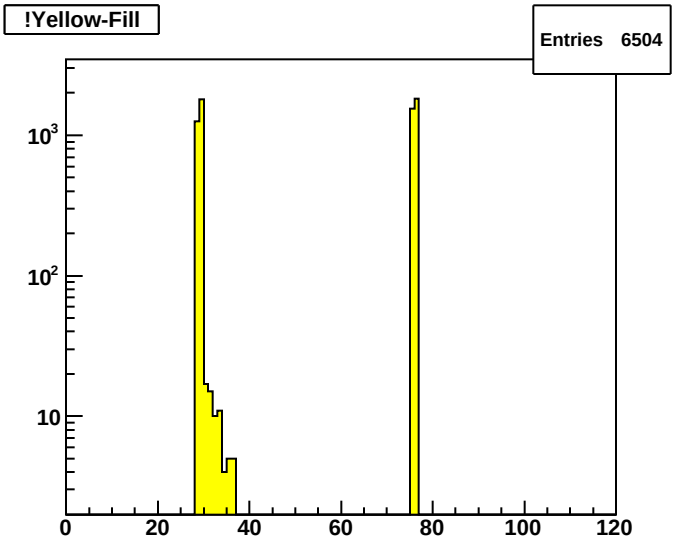

DAQ10k HT bits
Entries 37

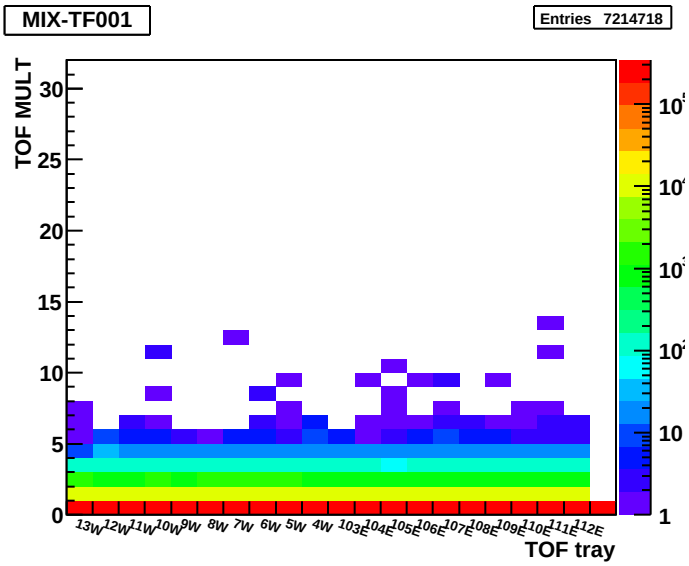

DAQ10k HT bits errors
Entries 0

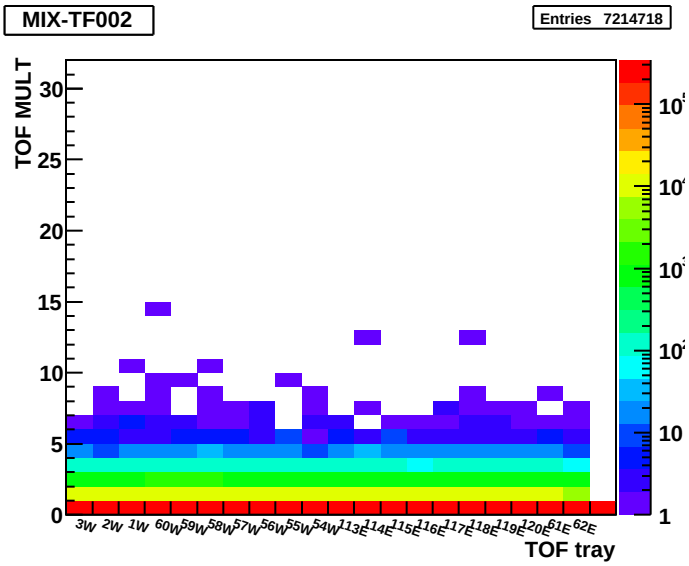

TPC Readout Mask (simulated)
Entries 58

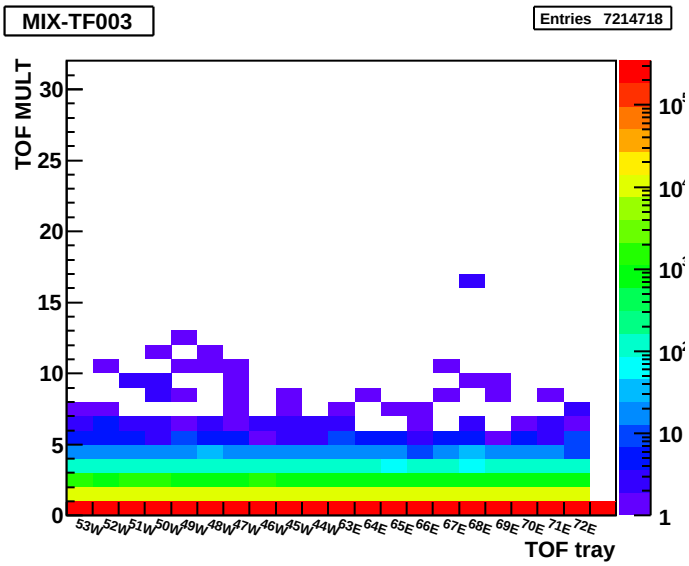

BunchId7Bit (all events)
Entries 171779

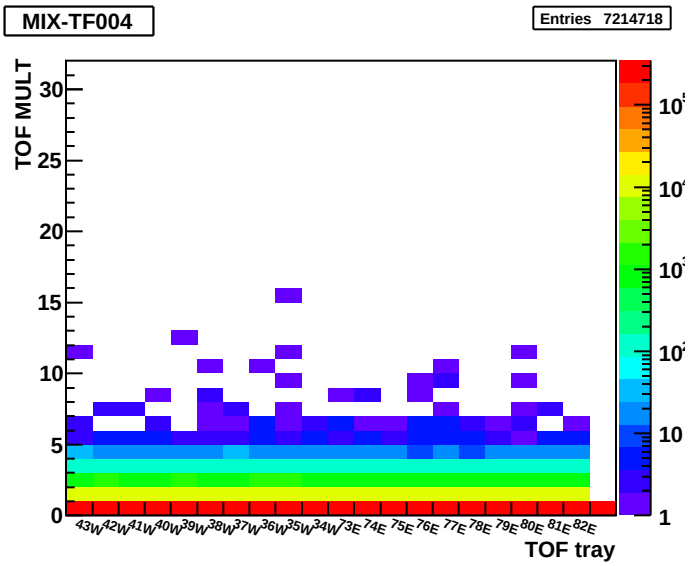

Abort-gaps
Entries 83

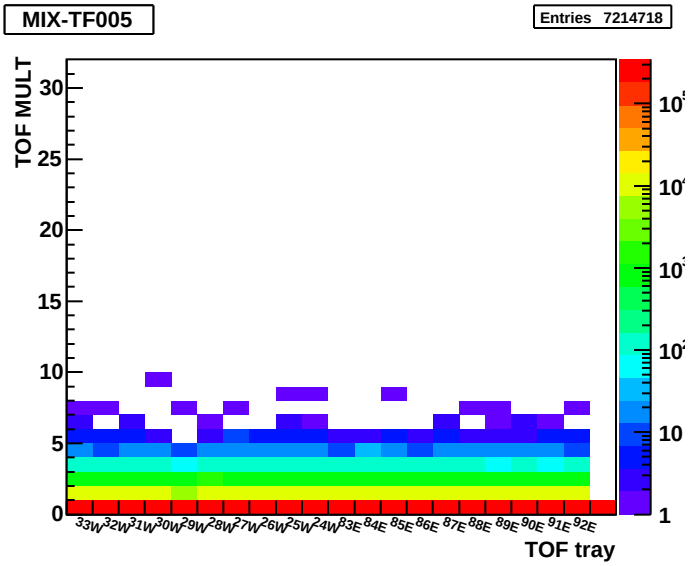

!Yellow-Fill
Entries 6504

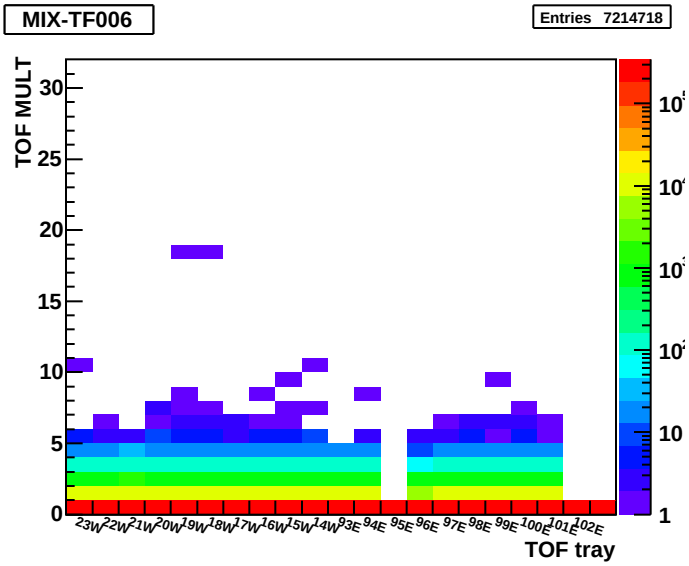

MIX-TF001
Entries 7214718
TOF MULT
TOF tray

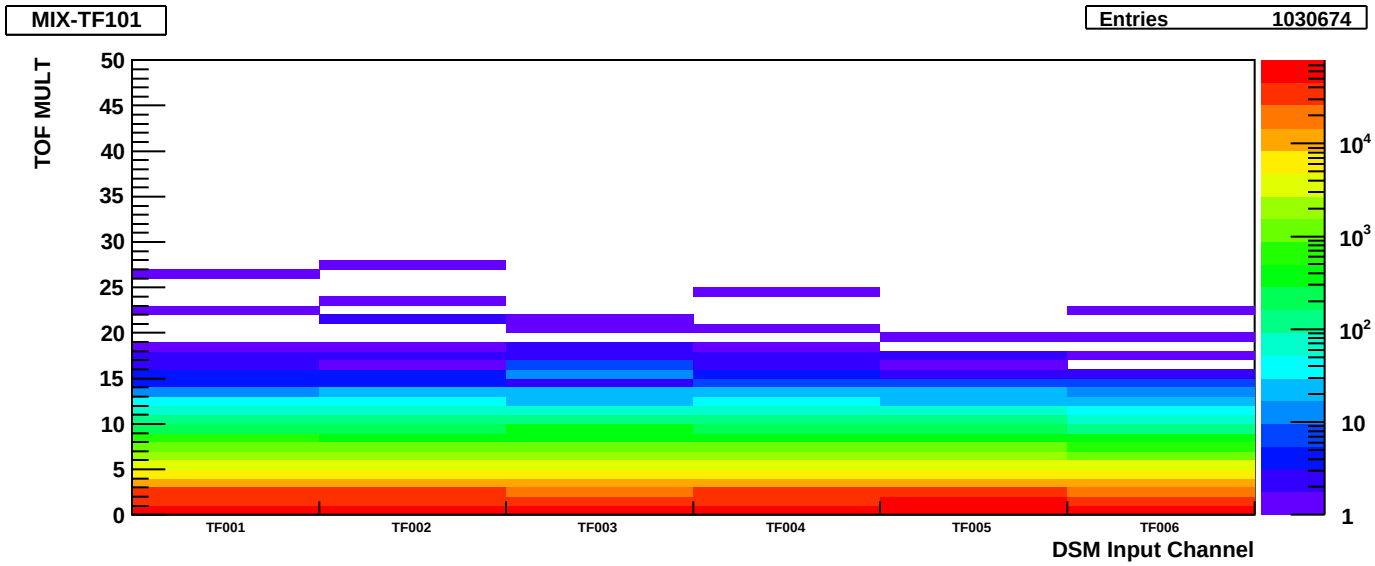

MIX-TF002
Entries 7214718
TOF MULT
TOF tray

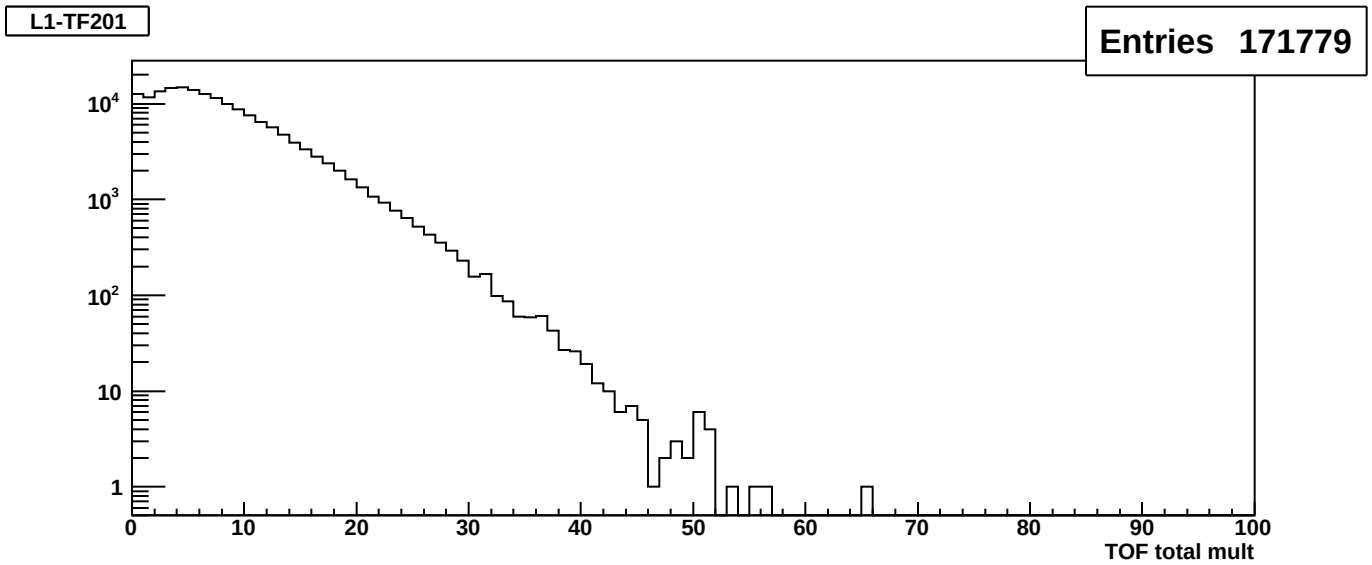

MIX-TF003
Entries 7214718
TOF MULT
TOF tray

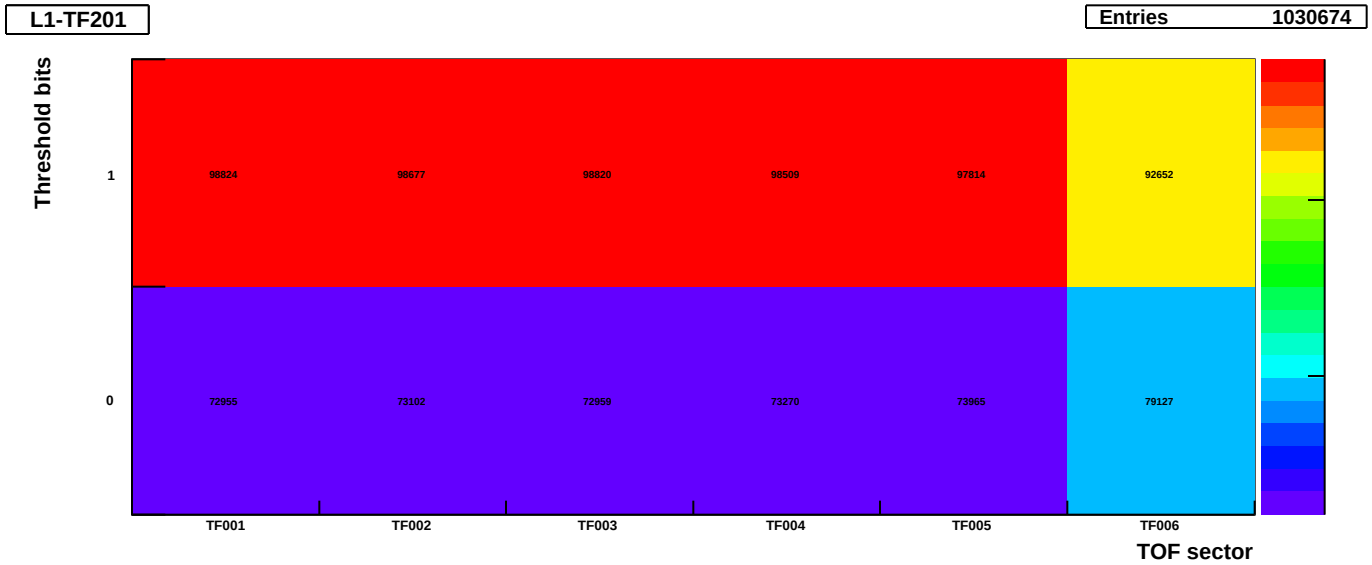

MIX-TF004
Entries 7214718
TOF MULT
TOF tray

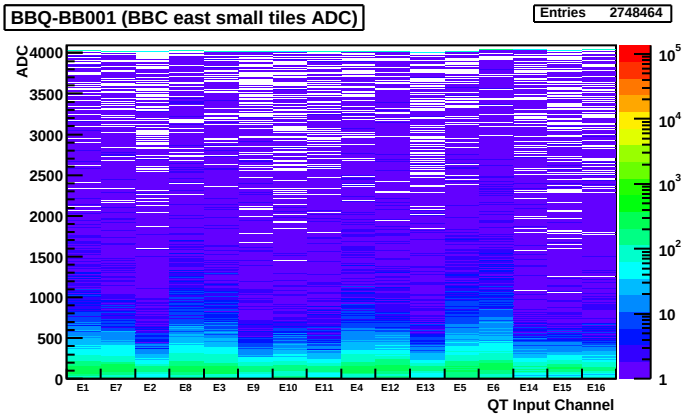

MIX-TF005
Entries 7214718
TOF MULT
TOF tray

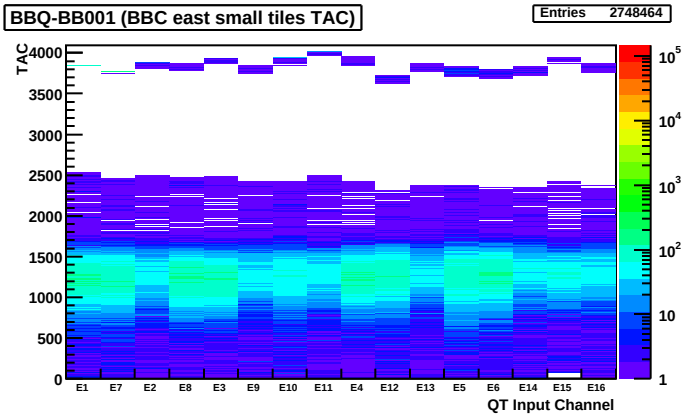

MIX-TF006
Entries 7214718
TOF MULT
TOF tray

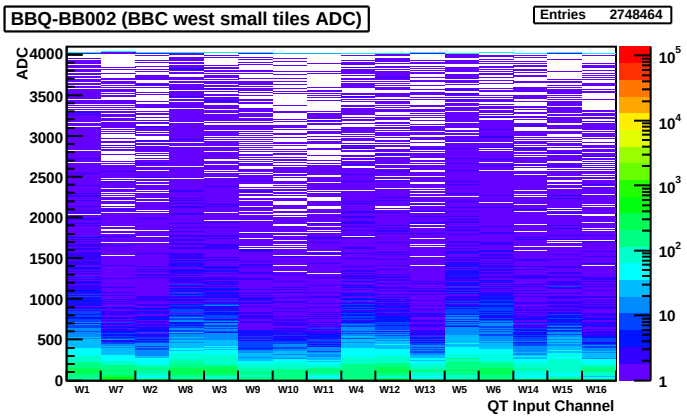

MIX-TF101
Entries 1030674
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

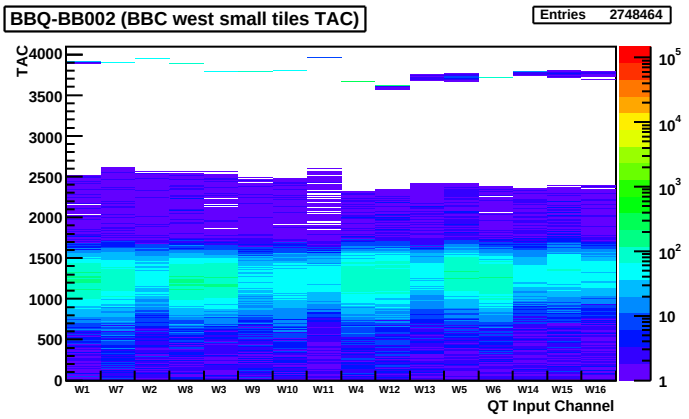

L1-TF201
Entries 171779
TOF total mult

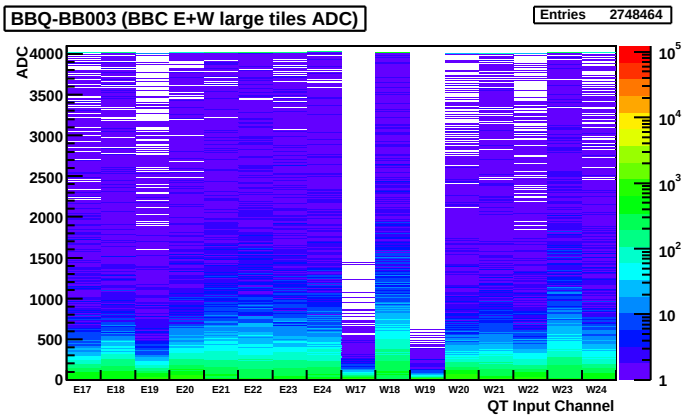

L1-TF201
Entries 1030674
Threshold bits
98824
98677
98820
98509
97814
92652
72955
73102
72959
73270
73965
79127
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

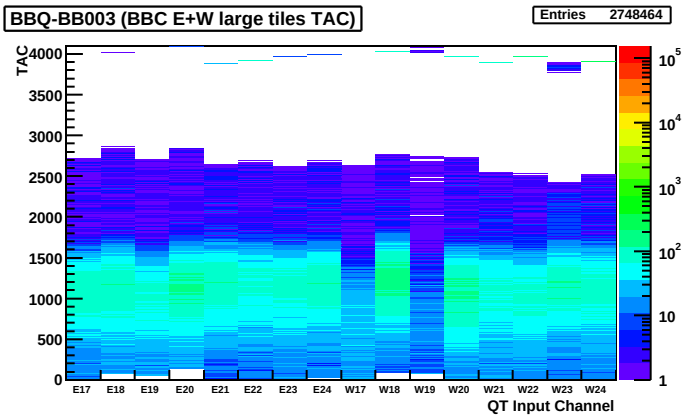

BBQ-BB001 (BBC east small tiles ADC)
Entries 2748464
ADC
QT Input Channel

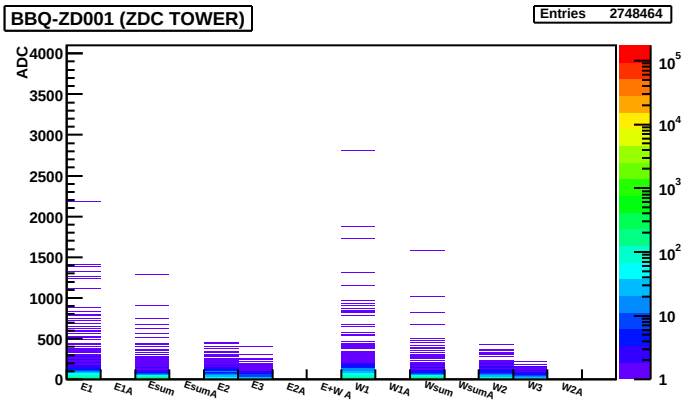

BBQ-BB001 (BBC east small tiles TAC)
Entries 2748464
TAC
QT Input Channel

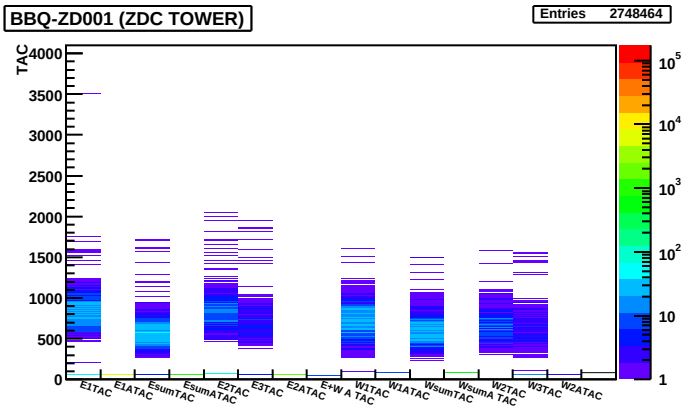

BBQ-BB002 (BBC west small tiles ADC)
Entries 2748464
ADC
QT Input Channel

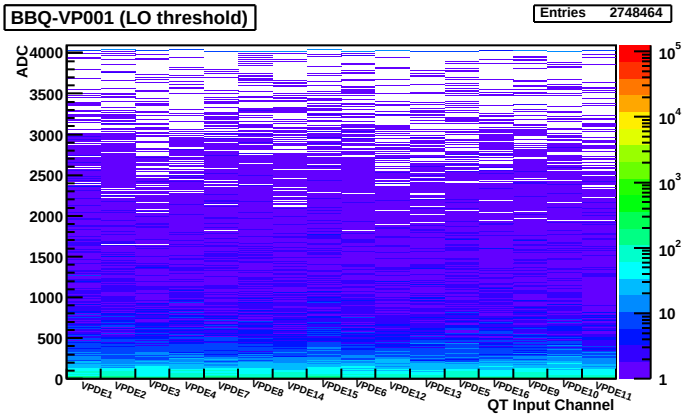

BBQ-BB002 (BBC west small tiles TAC)
Entries 2748464
TAC
QT Input Channel

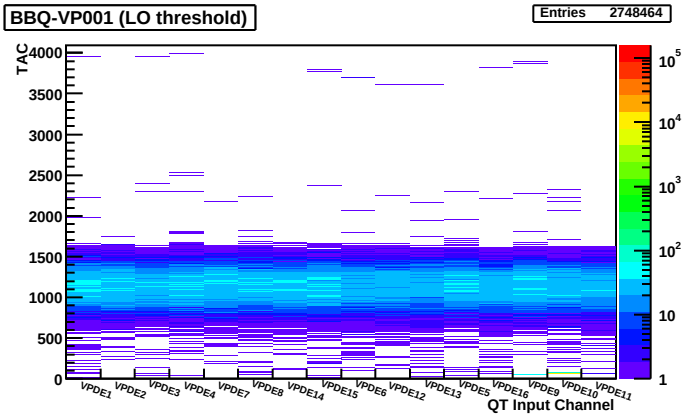

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2748464
ADC
QT Input Channel

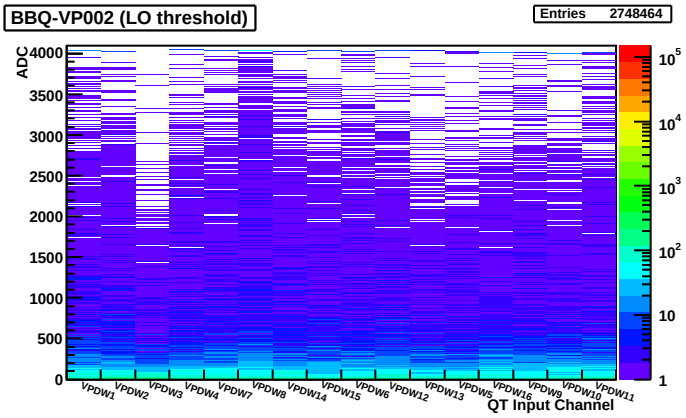

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2748464
TAC
QT Input Channel

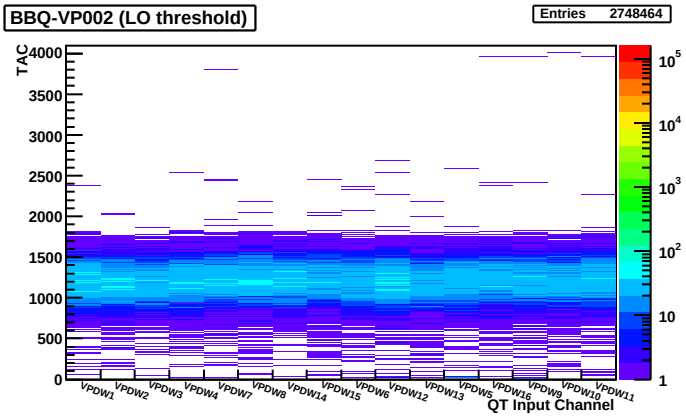

BBQ-ZD001 (ZDC TOWER)
Entries 2748464
ADC

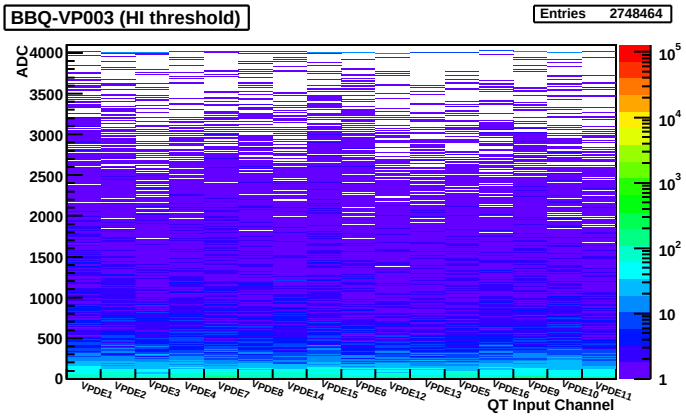

BBQ-ZD001 (ZDC TOWER)
Entries 2748464
TAC

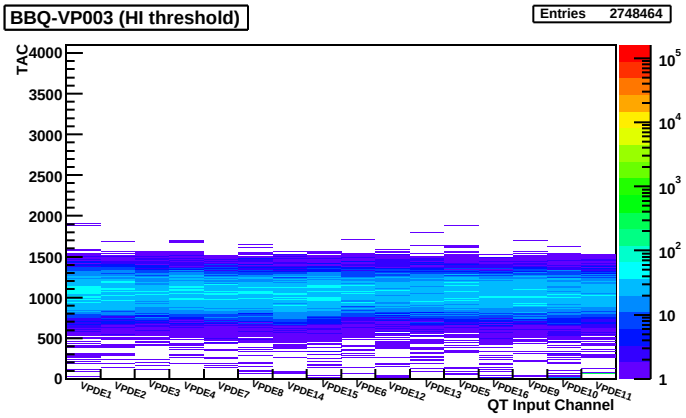

BBQ-VP001 (LO threshold)
Entries 2748464
ADC
QT Input Channel

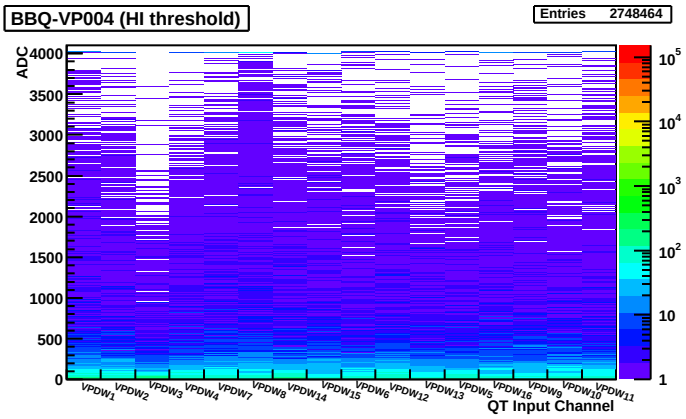

BBQ-VP001 (LO threshold)
Entries 2748464
TAC
QT Input Channel

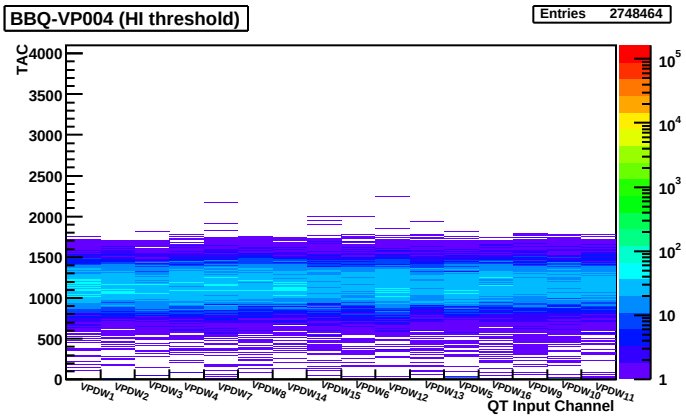

BBQ-VP002 (LO threshold)
Entries 2748464
ADC
QT Input Channel

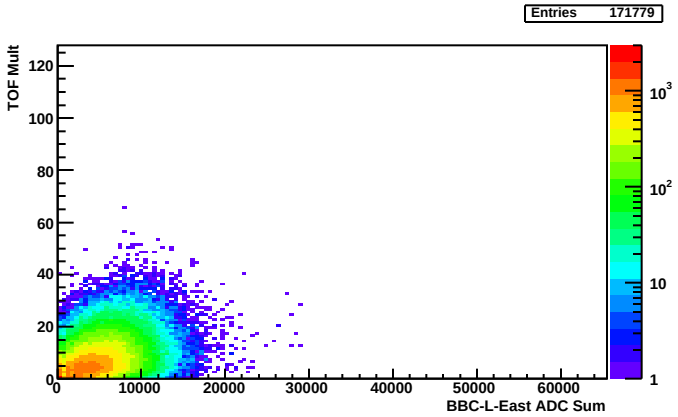

BBQ-VP002 (LO threshold)
Entries 2748464
TAC
QT Input Channel

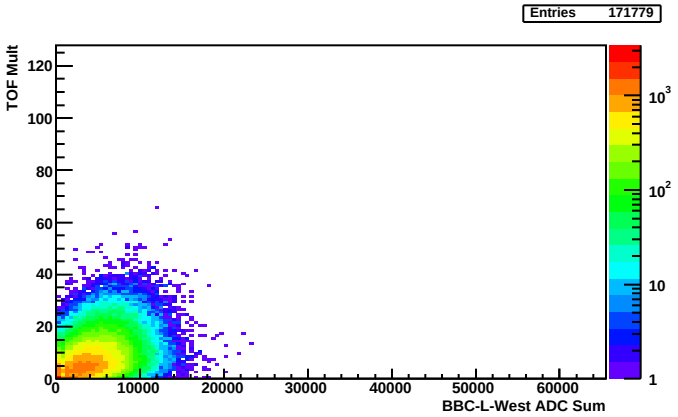

BBQ-VP003 (HI threshold)
Entries 2748464
ADC
QT Input Channel

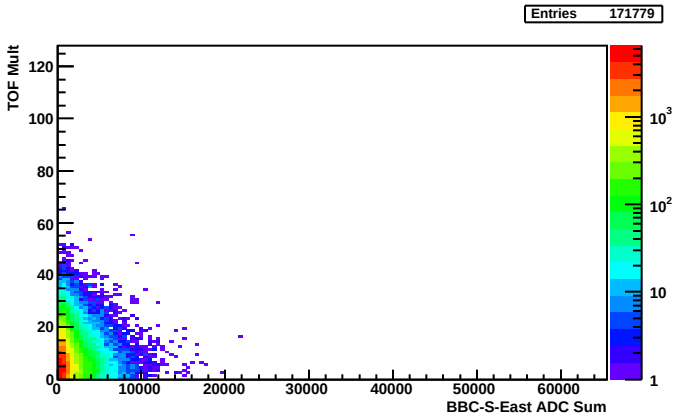

BBQ-VP003 (HI threshold)
Entries 2748464
TAC
QT Input Channel

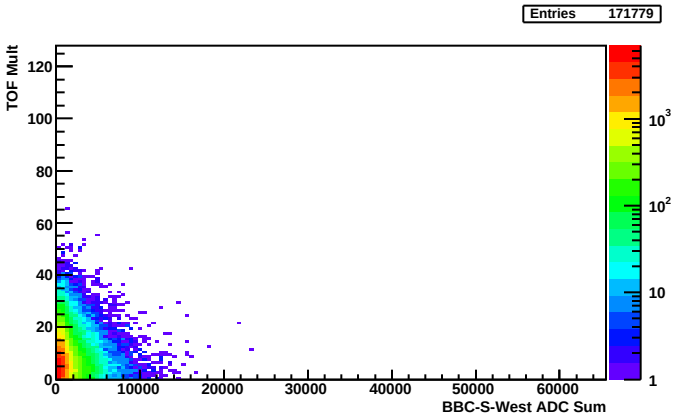

BBQ-VP004 (HI threshold)
Entries 2748464
ADC
QT Input Channel

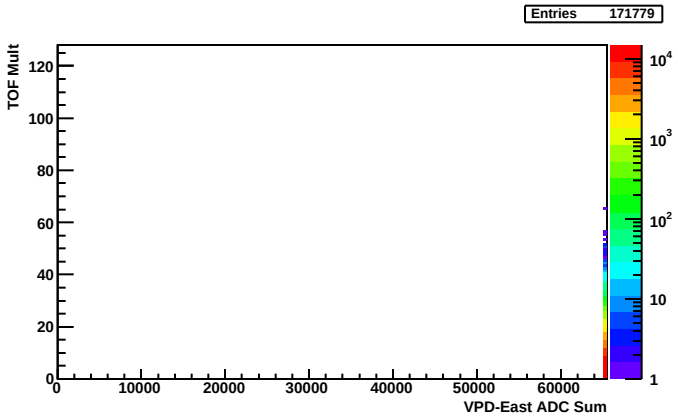

BBQ-VP004 (HI threshold)
Entries 2748464
TAC
QT Input Channel

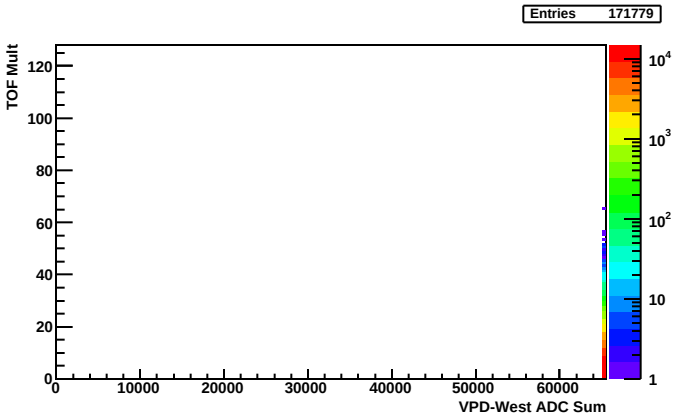

Entries 171779
TOF Mult
BBC-L-East ADC Sum

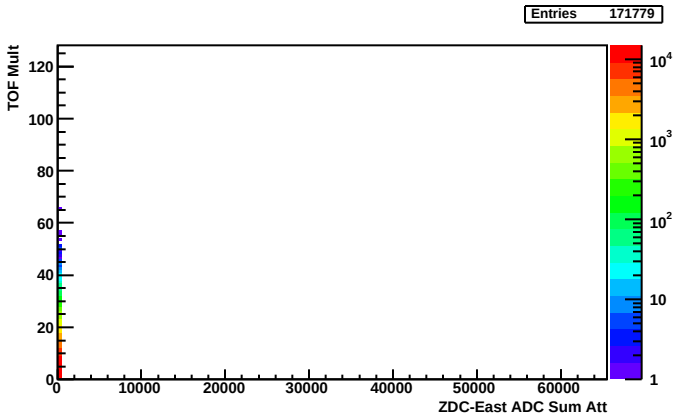

Entries 171779
TOF Mult
BBC-L-West ADC Sum

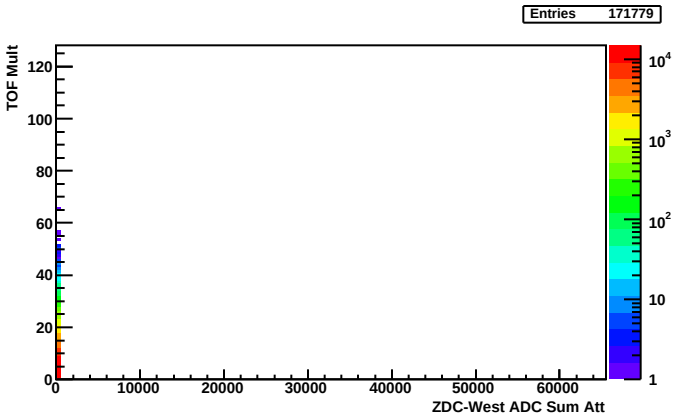

Entries 171779
TOF Mult
BBC-S-East ADC Sum

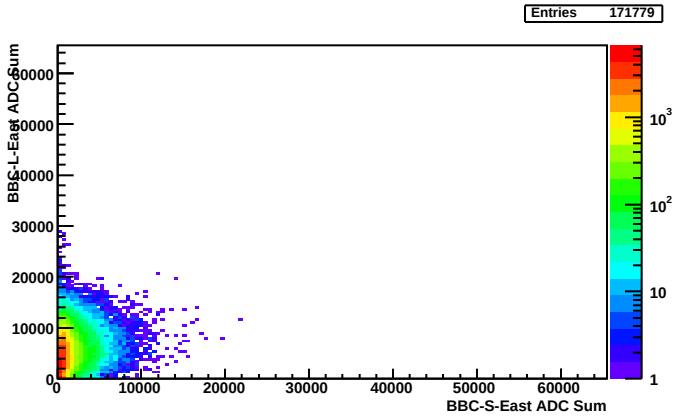

Entries 171779
TOF Mult
BBC-S-West ADC Sum

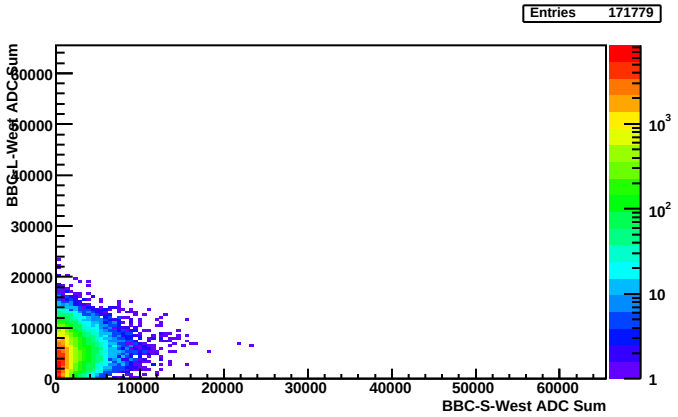

Entries 171779
TOF Mult
VPD-East ADC Sum

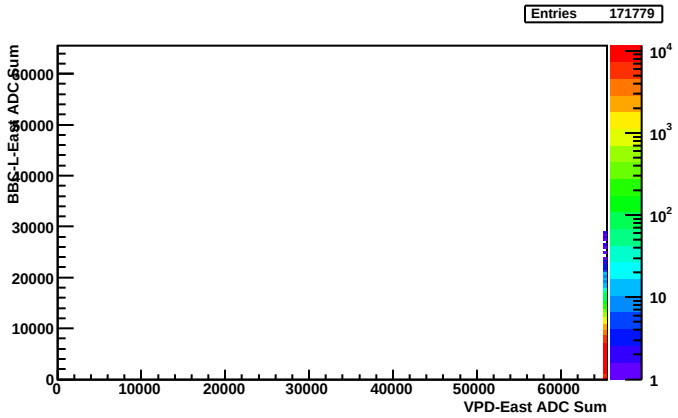

Entries 171779
TOF Mult
VPD-West ADC Sum

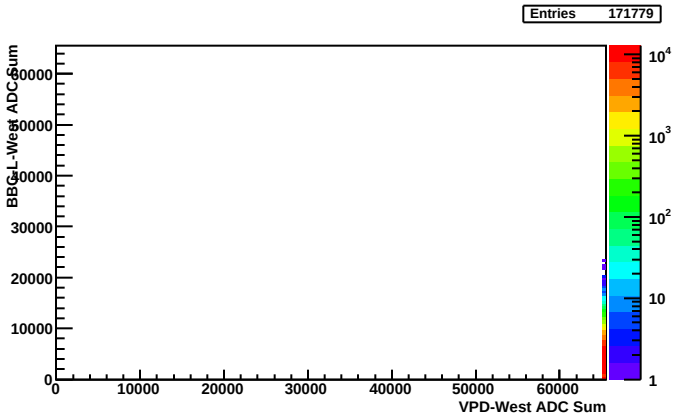

Entries 171779
TOF Mult
ZDC-East ADC Sum Att

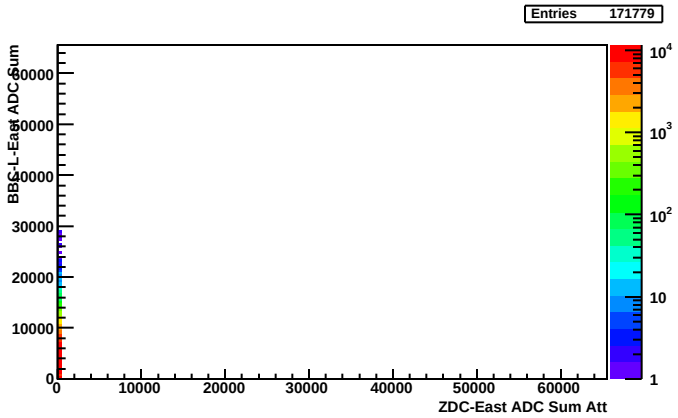

Entries 171779
TOF Mult
ZDC-West ADC Sum Att

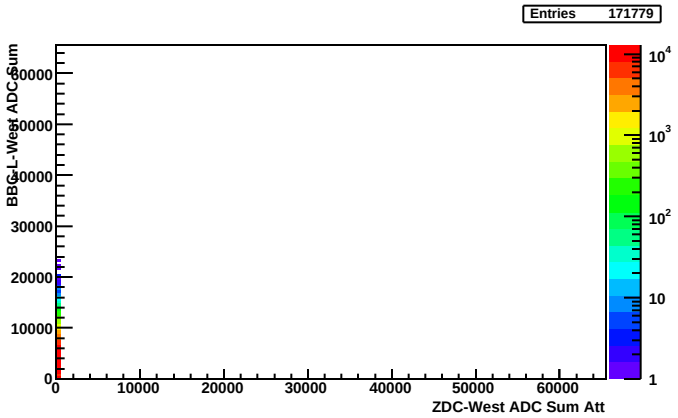

Entries 171779
BBC-L-East ADC Sum
BBC-S-East ADC Sum

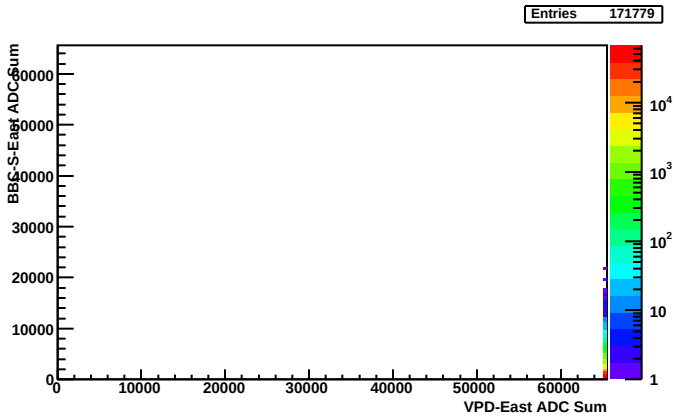

Entries 171779
BBC-L-West ADC Sum
BBC-S-West ADC Sum

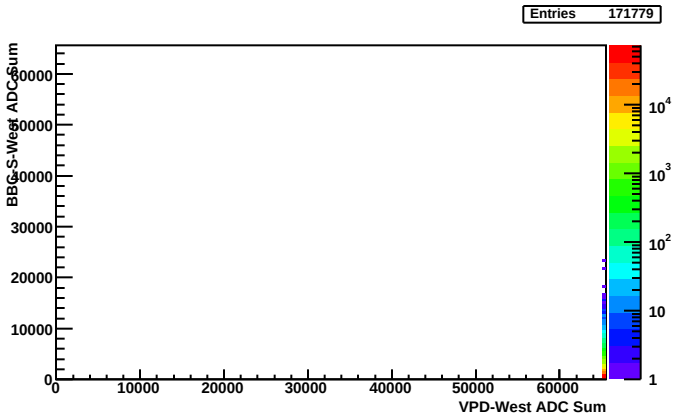

Entries 171779
BBC-L-East ADC Sum
VPD-East ADC Sum

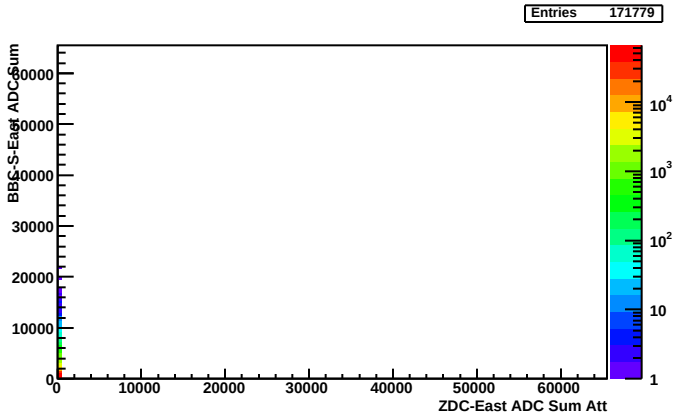

Entries 171779
BBC-L-West ADC Sum
VPD-West ADC Sum

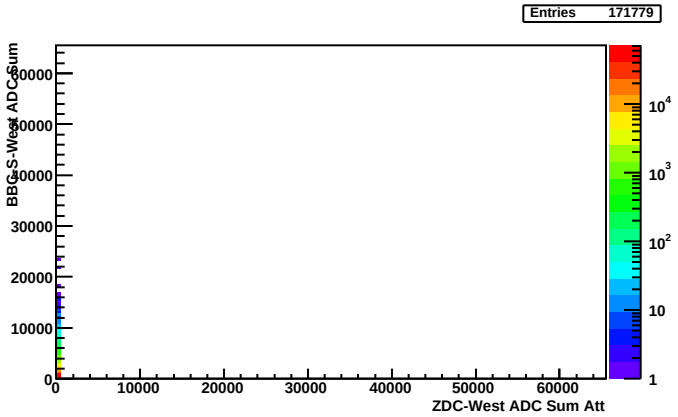

Entries 171779
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

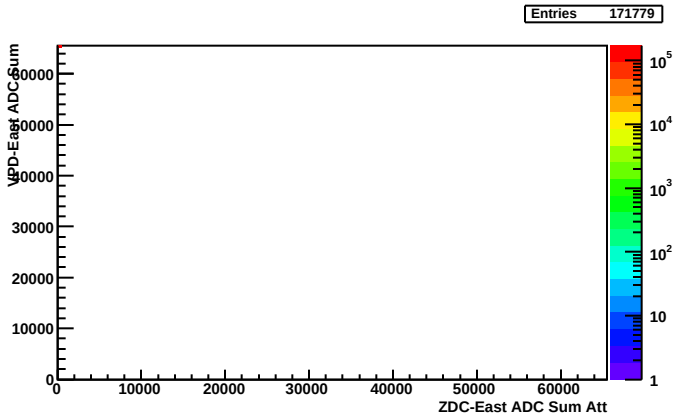

Entries 171779
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

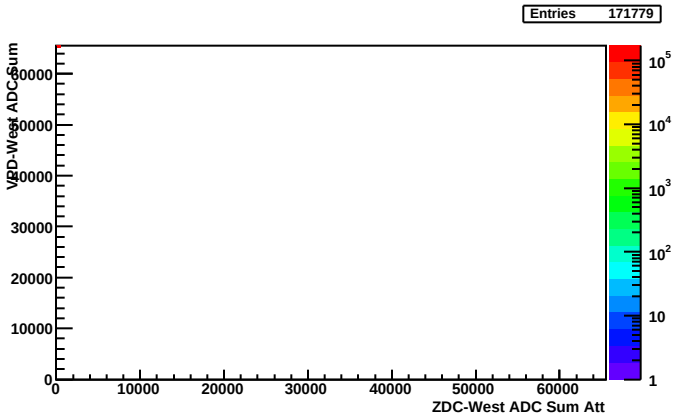

Entries 171779
BBC-S-East ADC Sum
VPD-East ADC Sum

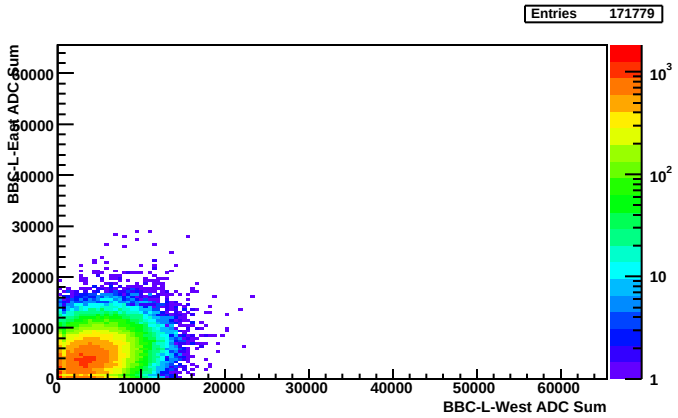

Entries 171779
BBC-S-West ADC Sum
VPD-West ADC Sum

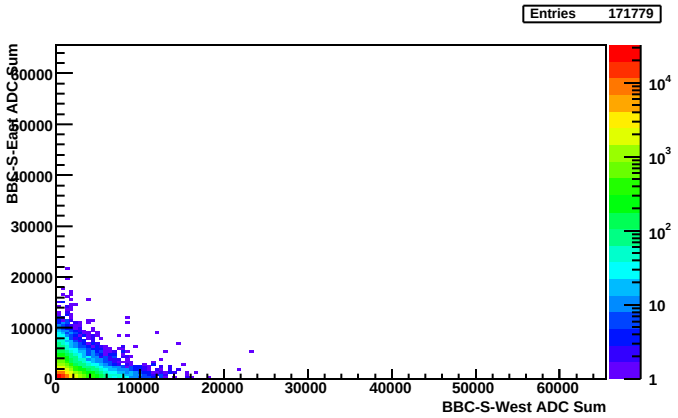

Entries 171779
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

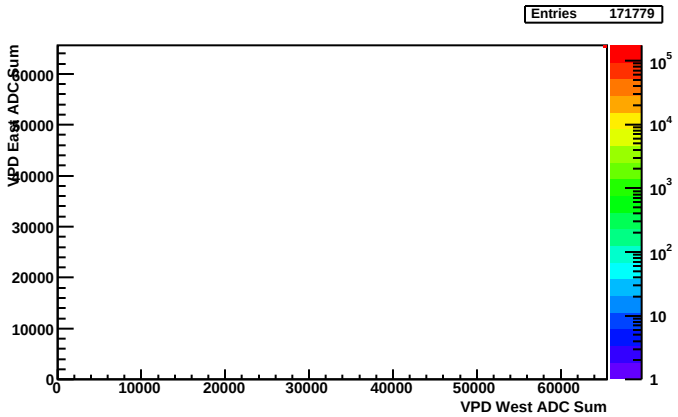

Entries 171779
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

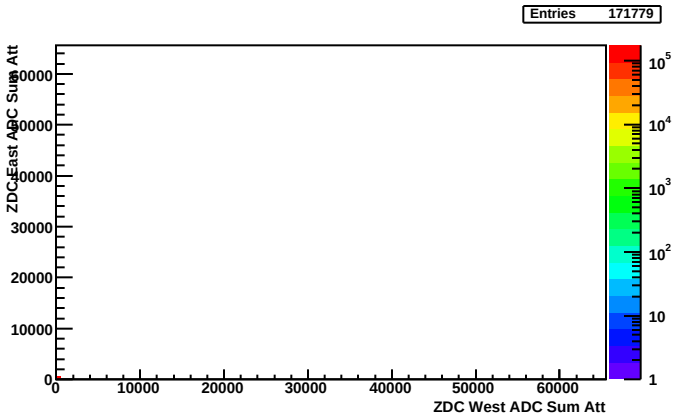

Entries 171779
VPD-East ADC Sum
ZDC-East ADC Sum Att

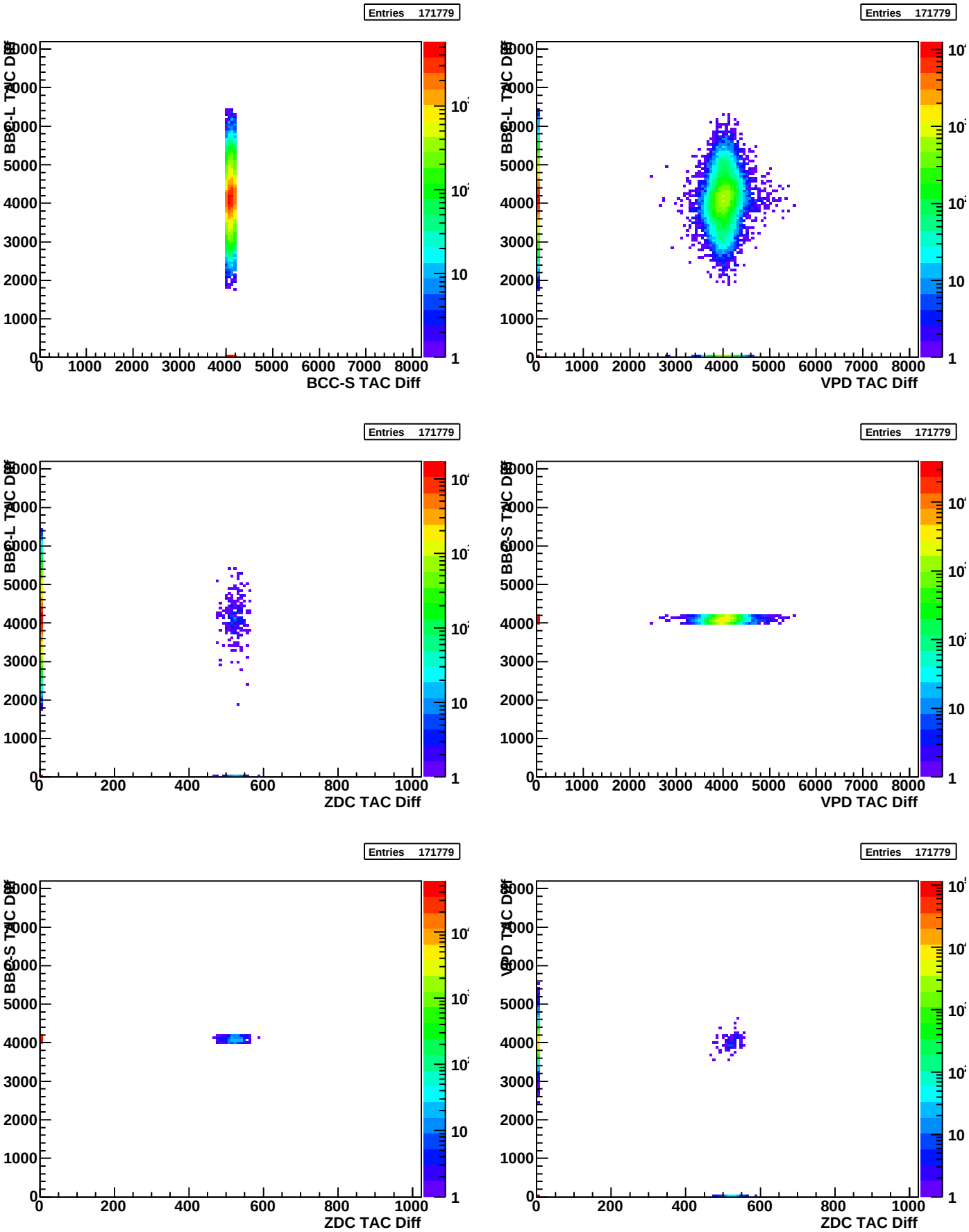

Entries 171779
VPD-West ADC Sum
ZDC-West ADC Sum Att

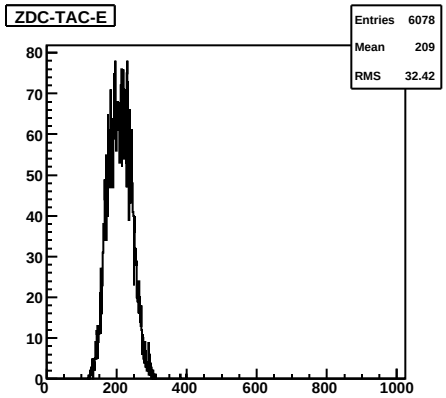

Entries 171779
BBC-L-East ADC Sum
BBC-L-West ADC Sum

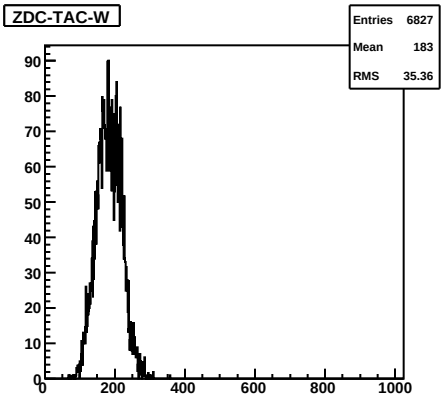

Entries 171779
BBC-S-East ADC Sum
BBC-S-West ADC Sum

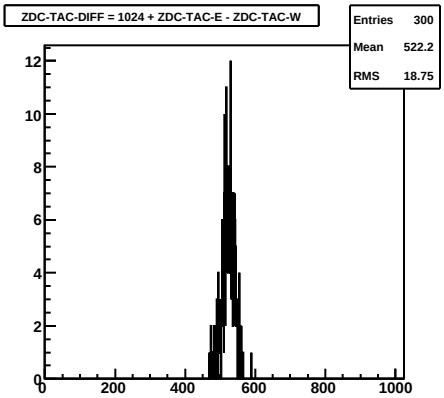

Entries 171779
VPD East ADC Sum
VPD West ADC Sum

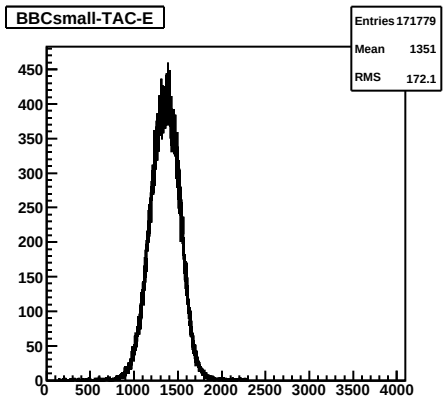

Entries 171779
ZDC East ADC Sum Att
ZDC West ADC Sum Att

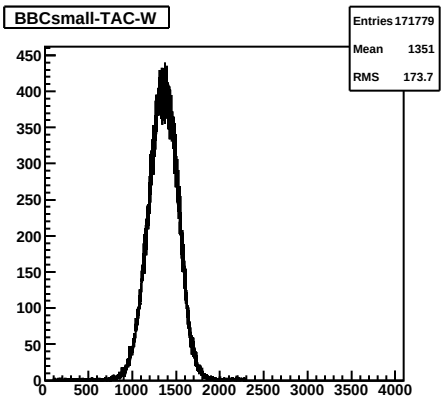

Entries 171779
BBC-L TAC Diff
BCC-S TAC Diff
Entries 171779
BBC-L TAC Diff
VPD TAC Diff
Entries 171779
BBC-L TAC Diff
ZDC TAC Diff
Entries 171779
BBC-S TAC Diff
VPD TAC Diff
Entries 171779
BBC-S TAC Diff
ZDC TAC Diff
Entries 171779
VPD TAC Diff
ZDC TAC Diff

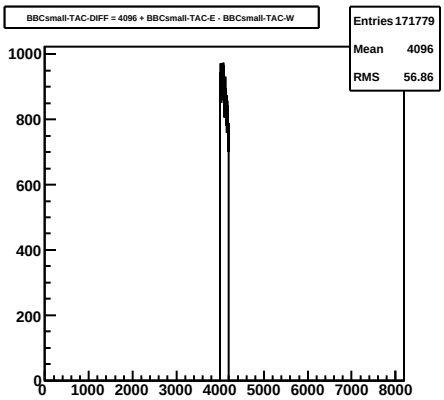

ZDC-TAC-E
Entries 6078
Mean 209
RMS 32.42

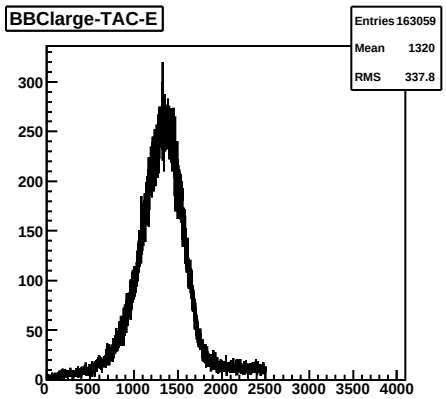

ZDC-TAC-W
Entries 6827
Mean 183
RMS 35.36

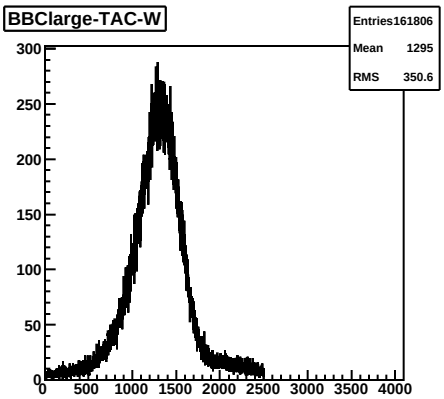

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 300
Mean 522.2
RMS 18.75

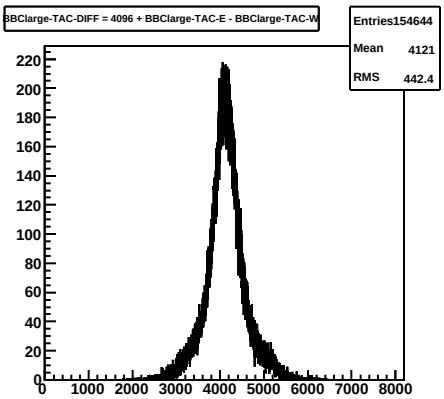

BBCsmall-TAC-E
Entries 171779
Mean 1351
RMS 172.1

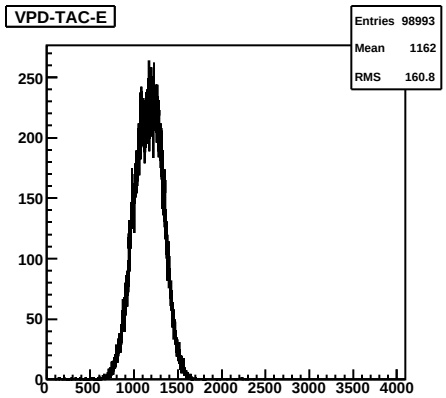

BBCsmall-TAC-W
Entries 171779
Mean 1351
RMS 173.7

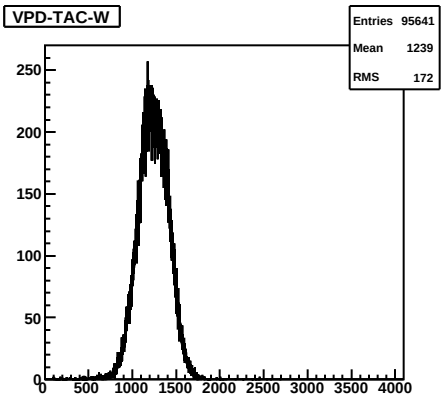

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 171779
Mean 4096
RMS 56.86

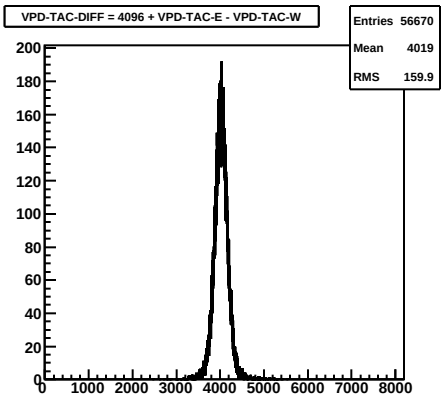

BBClarge-TAC-E
Entries 163059
Mean 1320
RMS 337.8

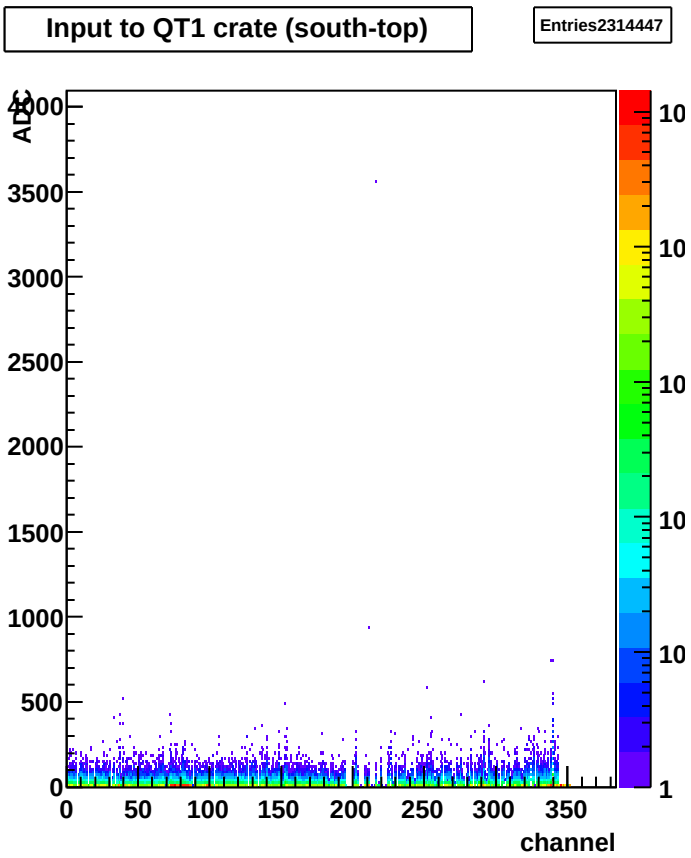

BBClarge-TAC-W
Entries 161806
Mean 1295
RMS 350.6

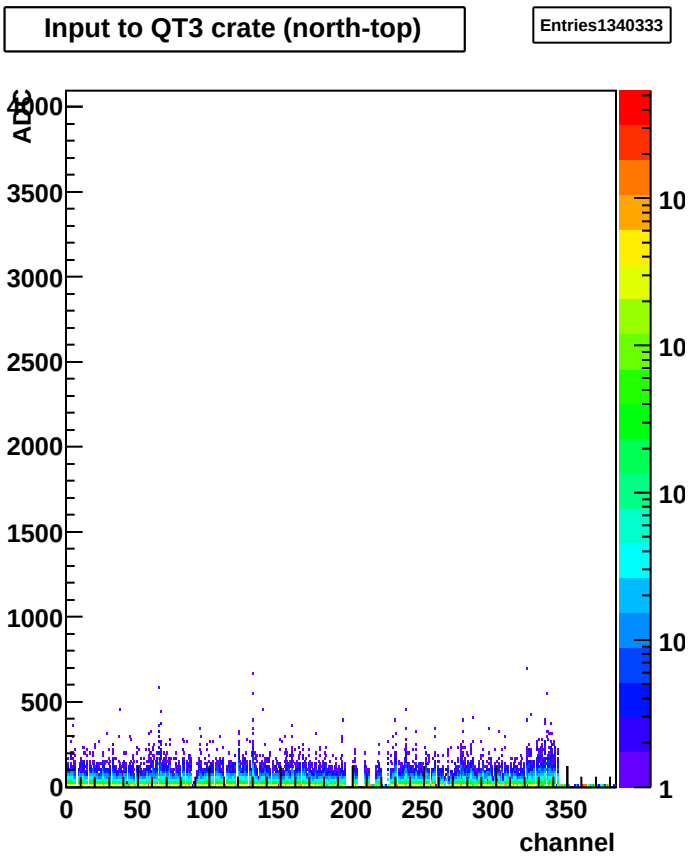

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 154644
Mean 4121
RMS 442.4

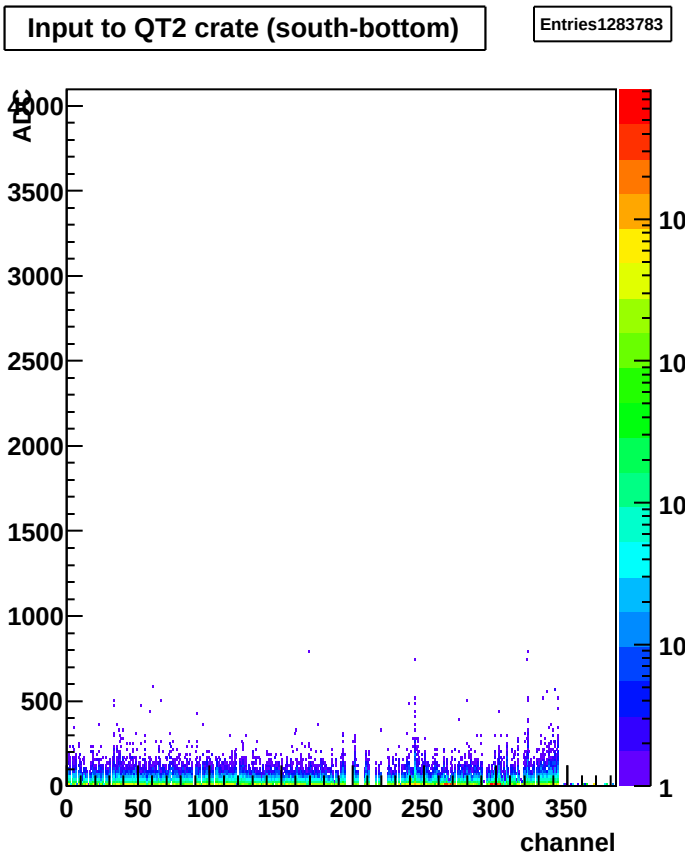

VPD-TAC-E
Entries 98993
Mean 1162
RMS 160.8

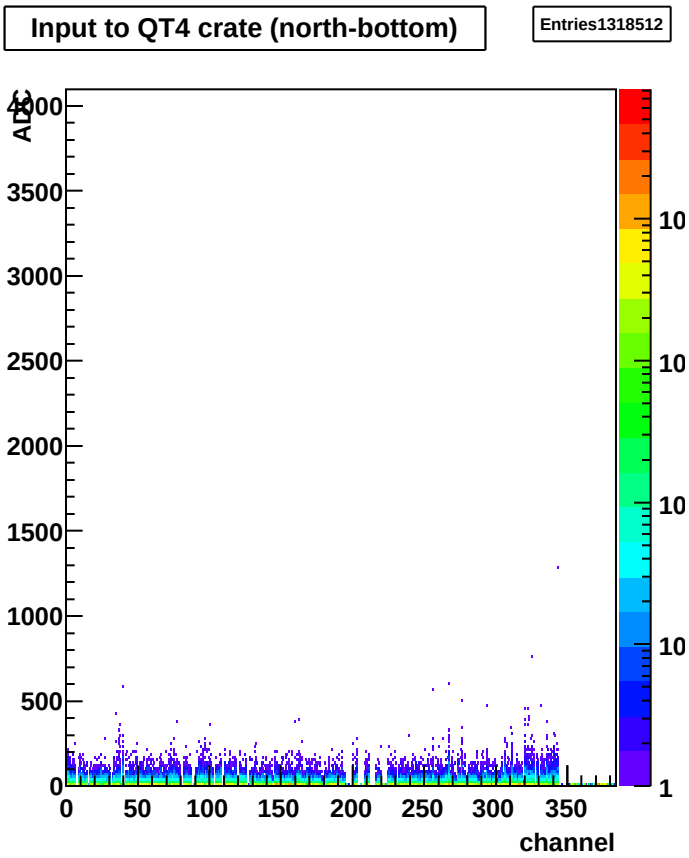

VPD-TAC-W
Entries 95641
Mean 1239
RMS 172

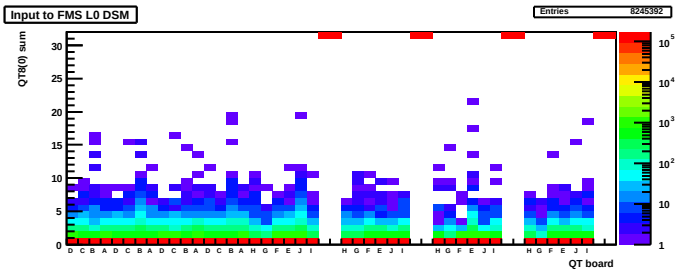

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 56670
Mean 4019
RMS 159.9

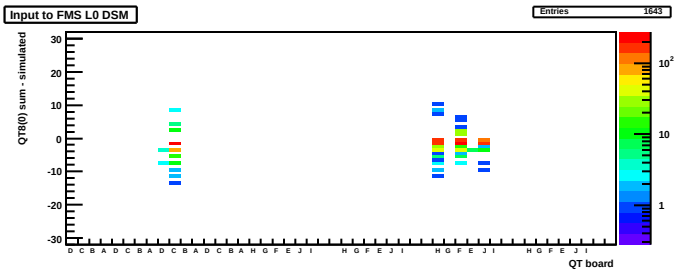

Input to QT1 crate (south-top)
Entries2314447
ADC
channel

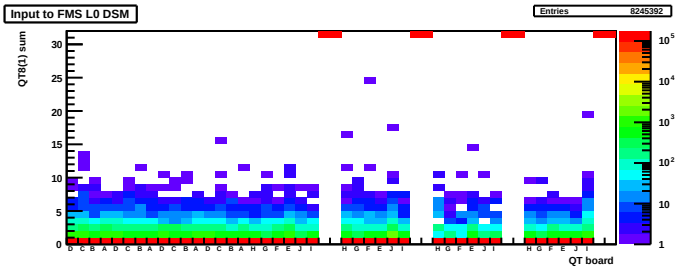

Input to QT3 crate (north-top)
Entries1340333
ADC
channel

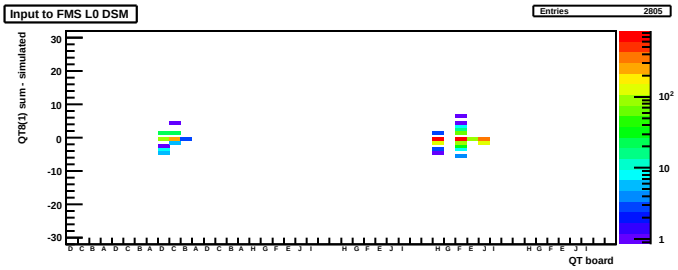

Input to QT2 crate (south-bottom)
Entries1283783
ADC
channel

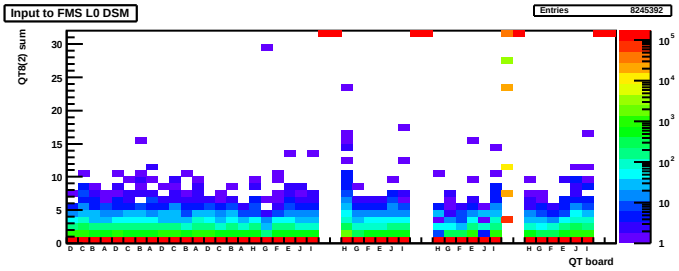

Input to QT4 crate (north-bottom)
Entries1318512
ADC
channel

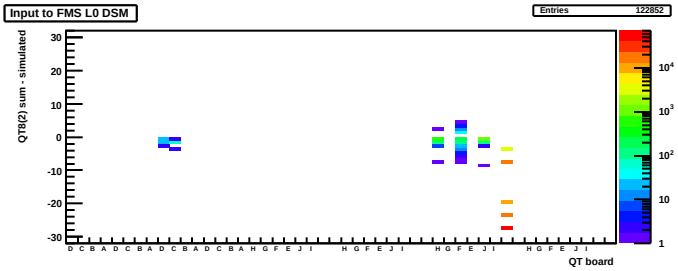

Input to FMS L0 DSM
Entries 8245392
QT8(0) sum
QT board

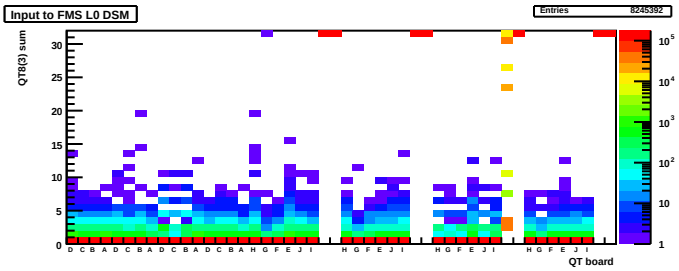

Input to FMS L0 DSM
Entries 1643
QT8(0) sum - simulated
QT board

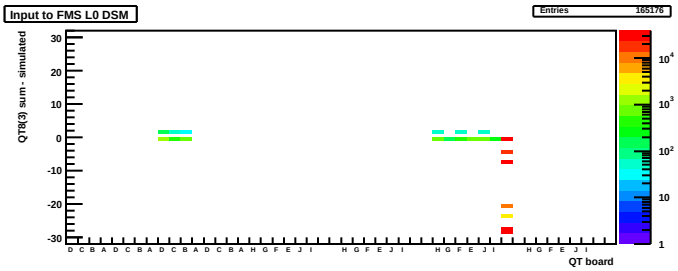

Input to FMS L0 DSM
Entries 8245392
QT8(1) sum
QT board

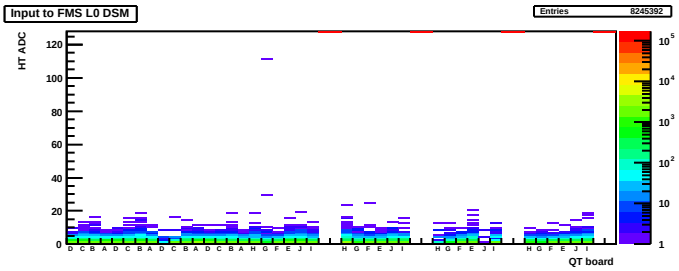

Input to FMS L0 DSM
Entries 2805
QT8(1) sum - simulated
QT board

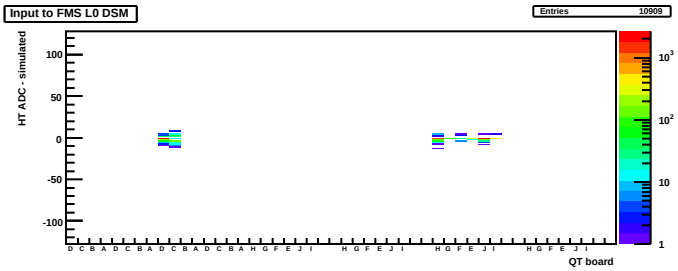

Input to FMS L0 DSM
Entries 8245392
QT8(2) sum
QT board

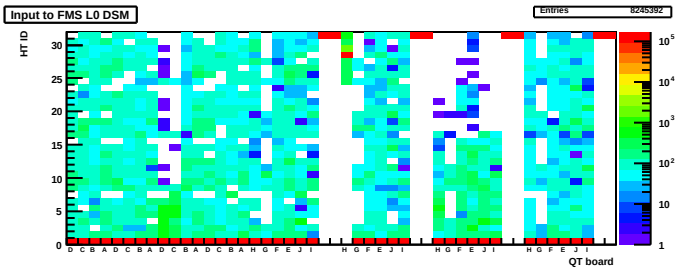

Input to FMS L0 DSM
Entries 122852
QT8(2) sum - simulated
QT board

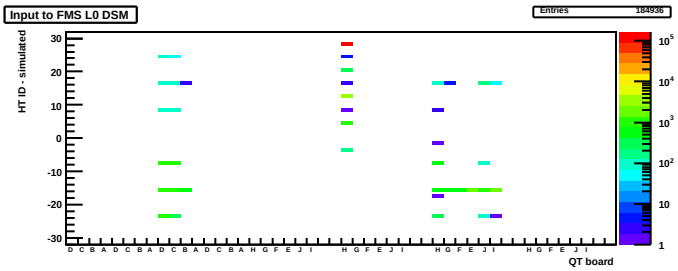

Input to FMS L0 DSM
Entries 8245392
QT8(3) sum
QT board

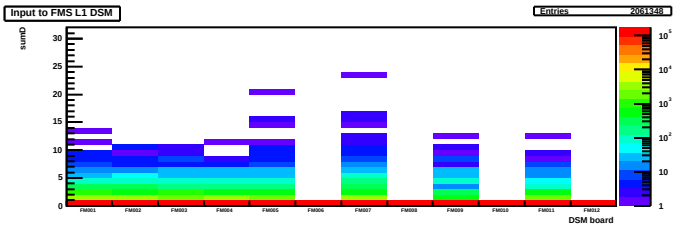

Input to FMS L0 DSM
Entries 165176
QT8(3) sum - simulated
QT board

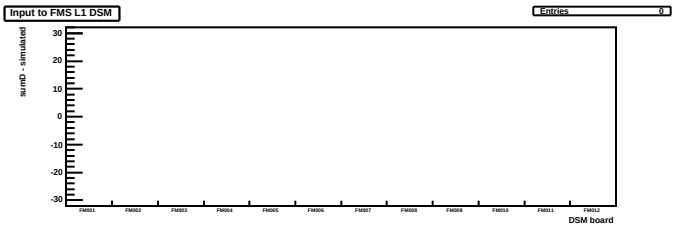

Input to FMS L0 DSM
Entries 8245392
HT ADC
QT board

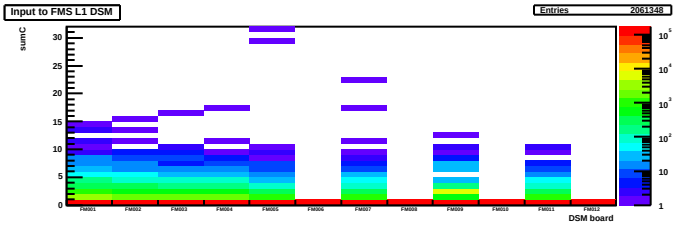

Input to FMS L0 DSM
Entries 10909
HT ADC - simulated
QT board

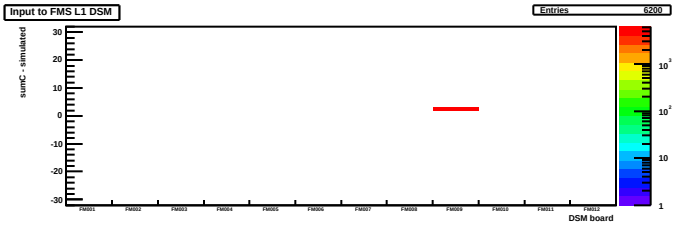

Input to FMS L0 DSM
Entries 8245392
HT ID
QT board

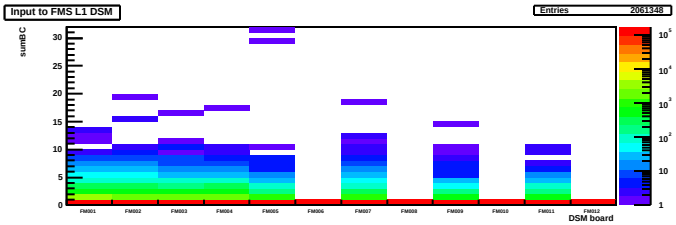

Input to FMS L0 DSM
Entries 184936
HT ID - simulated
QT board

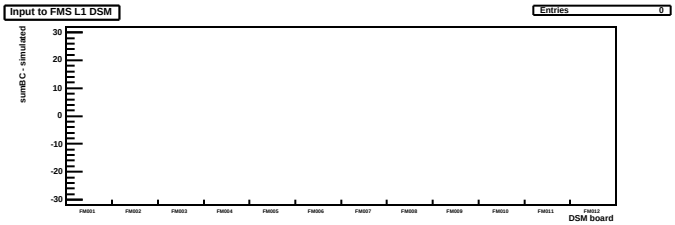

Input to FMS L1 DSM
Entries 2061348
sumD
DSM board

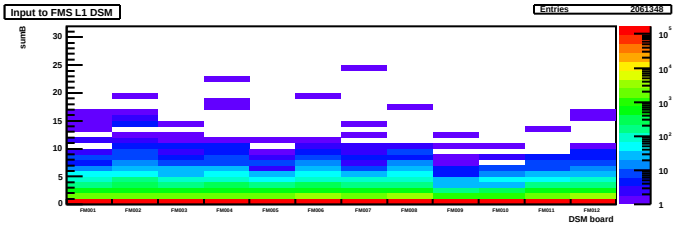

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

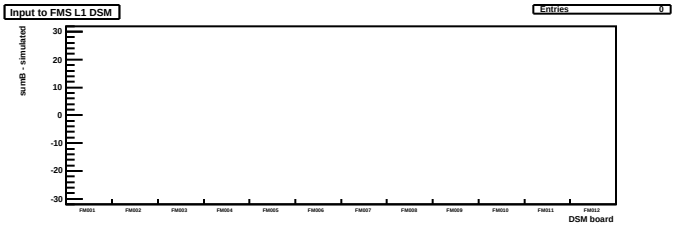

Input to FMS L1 DSM
Entries 2061348
sumC
DSM board

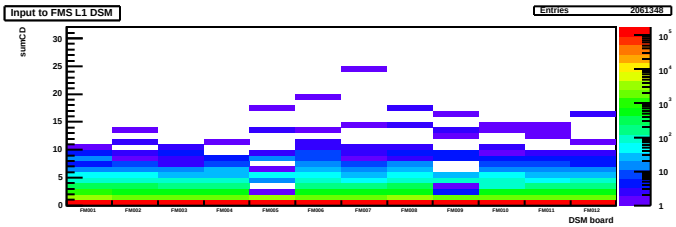

Input to FMS L1 DSM
Entries 6200
sumC - simulated
DSM board

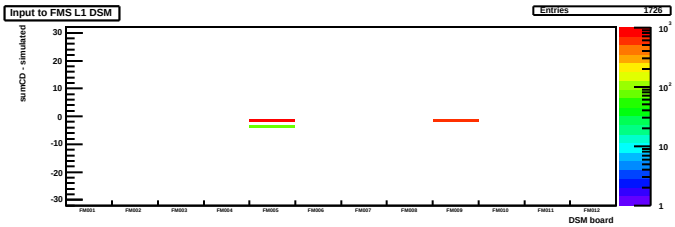

Input to FMS L1 DSM
Entries 2061348
sumBC
DSM board

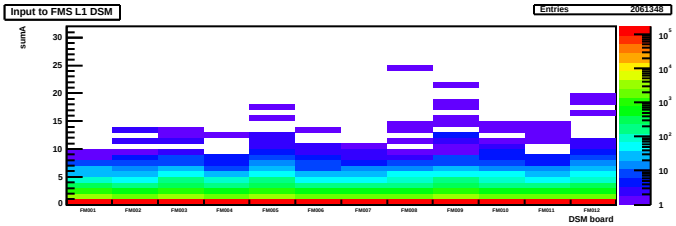

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

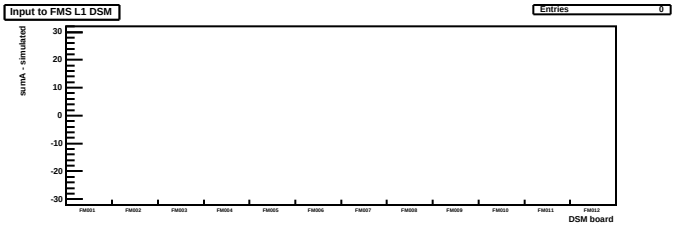

Input to FMS L1 DSM
Entries 2061348
sumB
DSM board

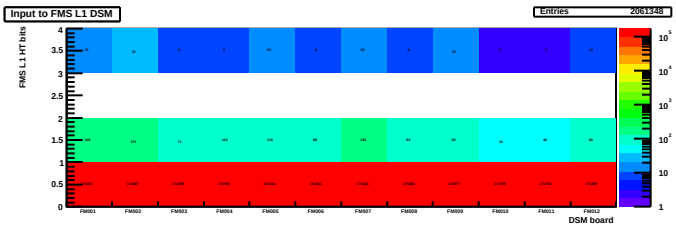

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

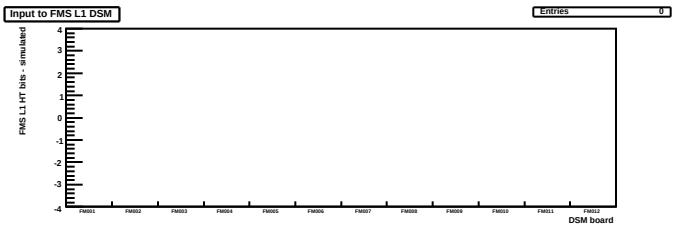

Input to FMS L1 DSM
Entries 2061348
sumCD
DSM board

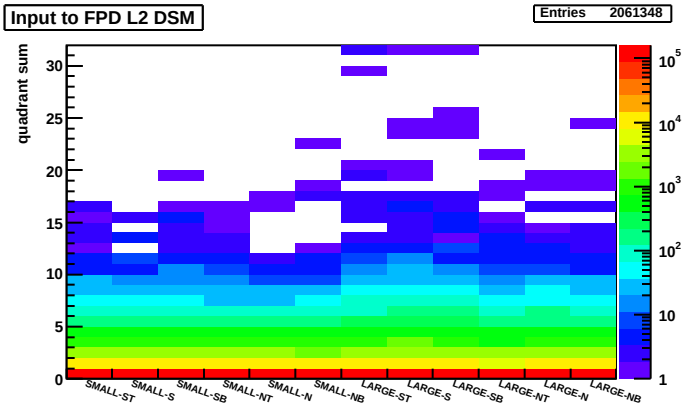

Input to FMS L1 DSM
Entries 1726
sumCD - simulated
DSM board

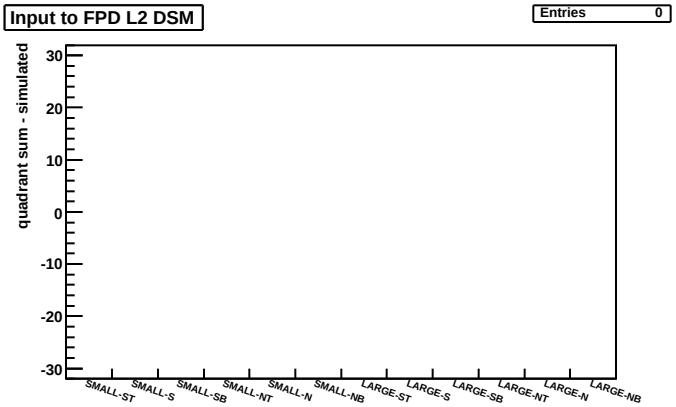

Input to FMS L1 DSM
Entries 2061348
sumA
DSM board

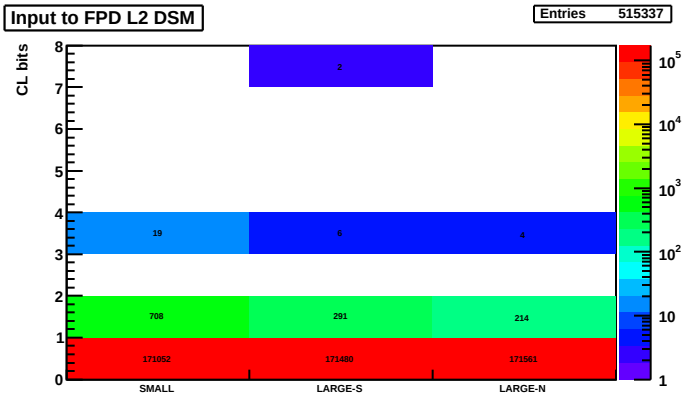

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

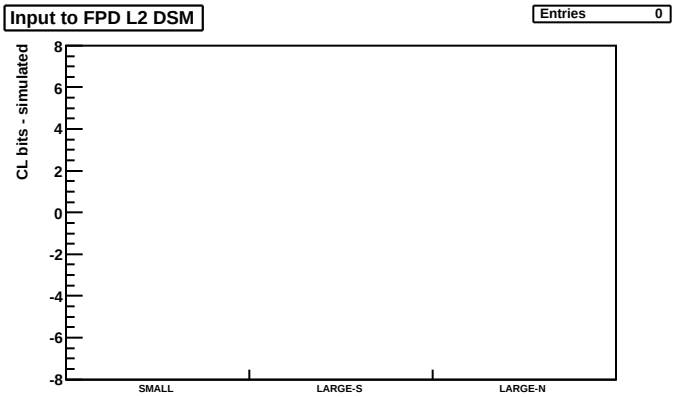

Input to FMS L1 DSM
Entries 2061348
FMS L1 HT bits
DSM board

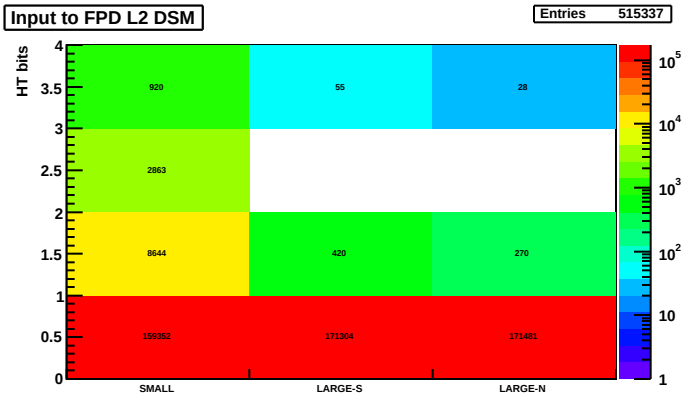

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

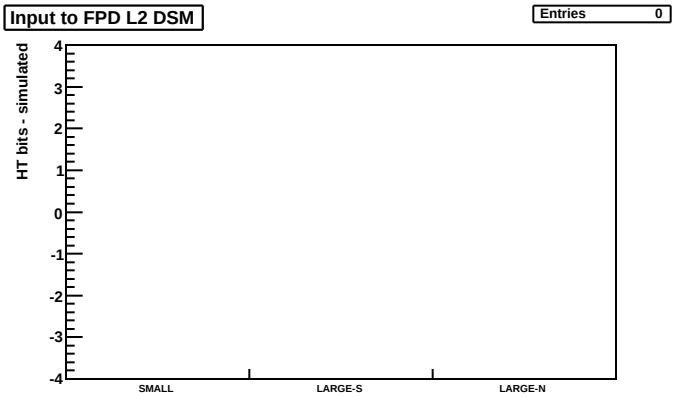

Input to FPD L2 DSM
Entries 2061348
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

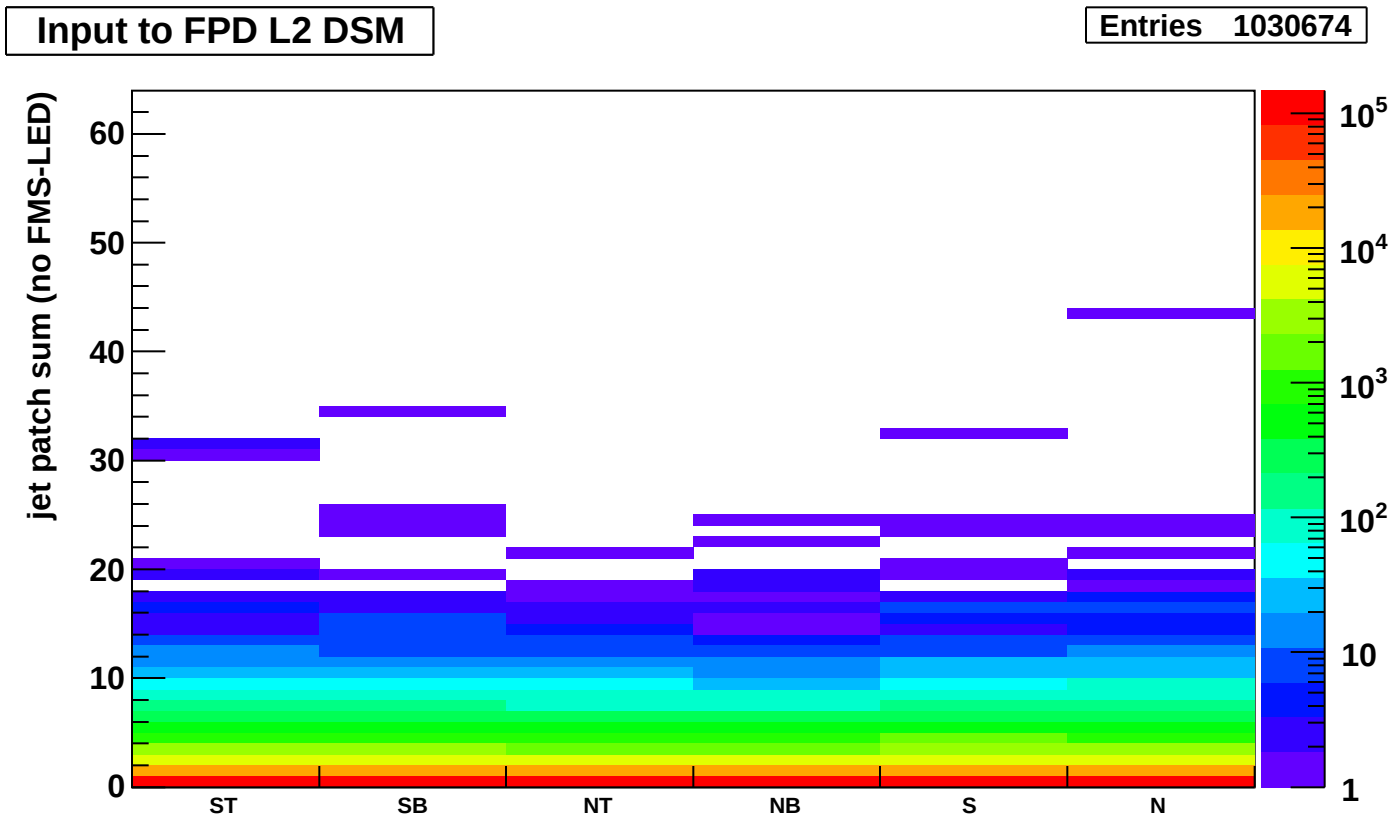

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

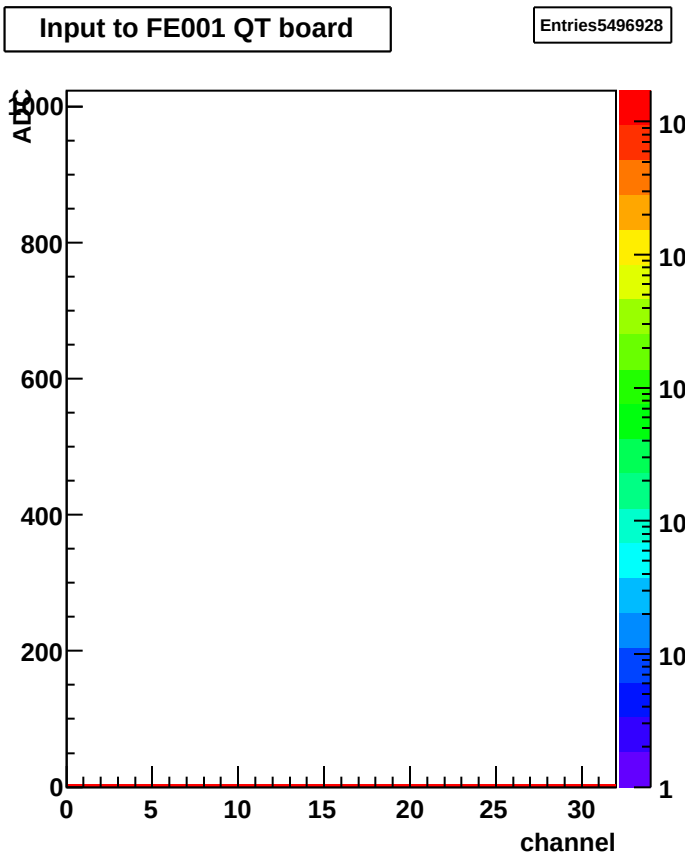

Input to FPD L2 DSM
Entries 515337
CL bits
2
19
6
4
708
291
214
171052
171480
171561
SMALL
LARGE-S
LARGE-N

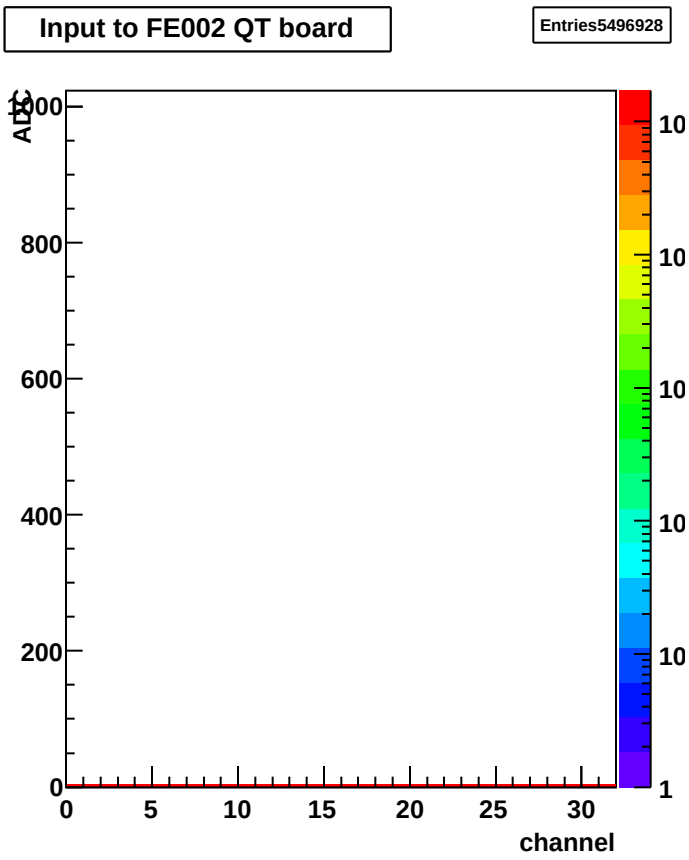

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

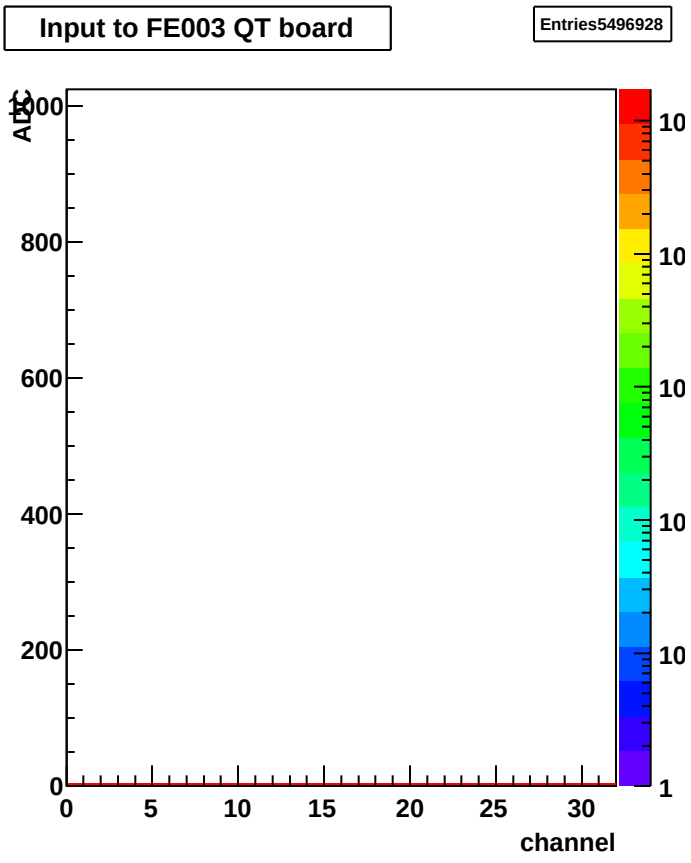

Input to FPD L2 DSM
Entries 515337
HT bits
920
55
28
2863
8644
420
270
159352
171304
171481
SMALL
LARGE-S
LARGE-N

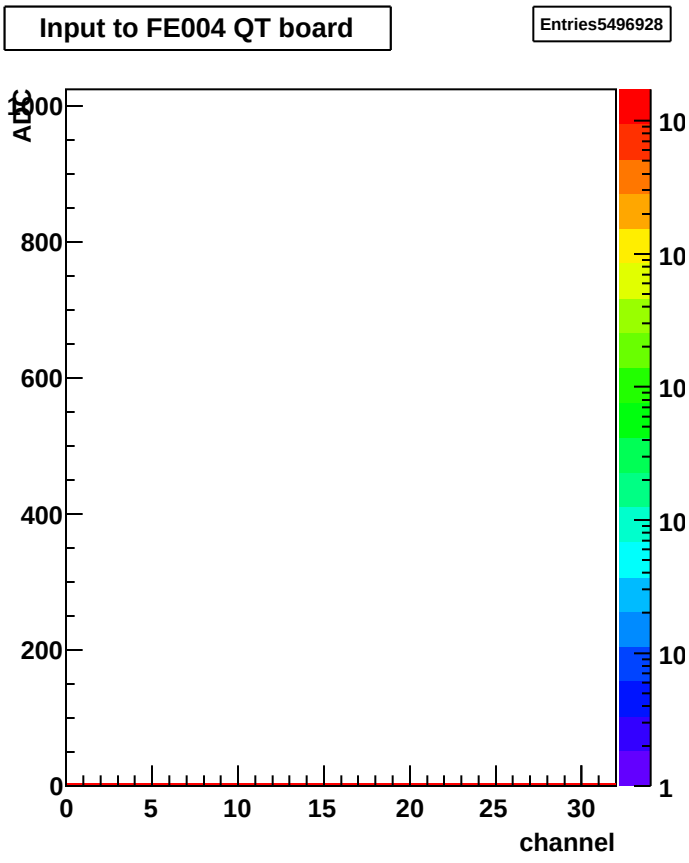

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

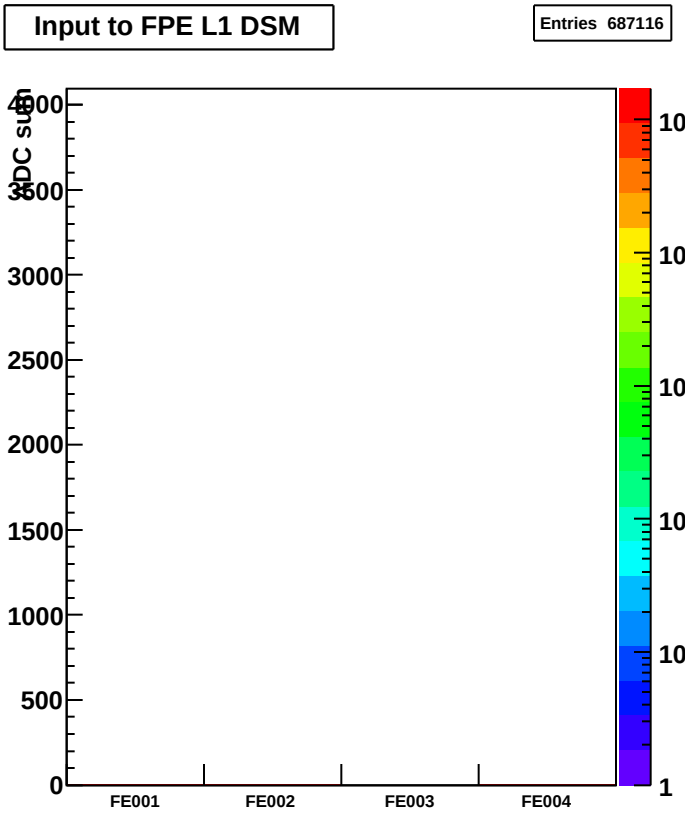

Input to FPD L2 DSM
Entries 1030674
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

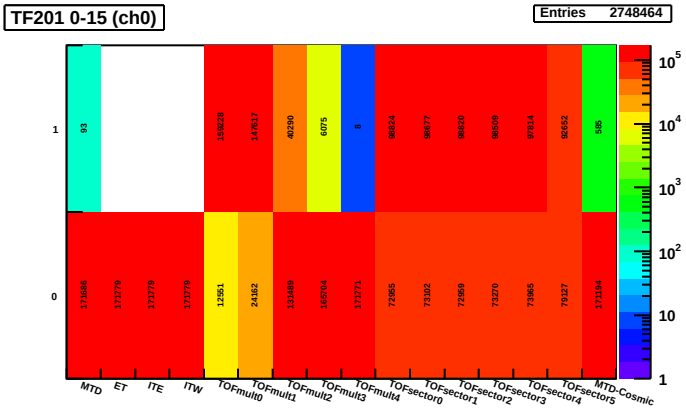

Input to FE001 QT board
Entries5496928
ADC
channel

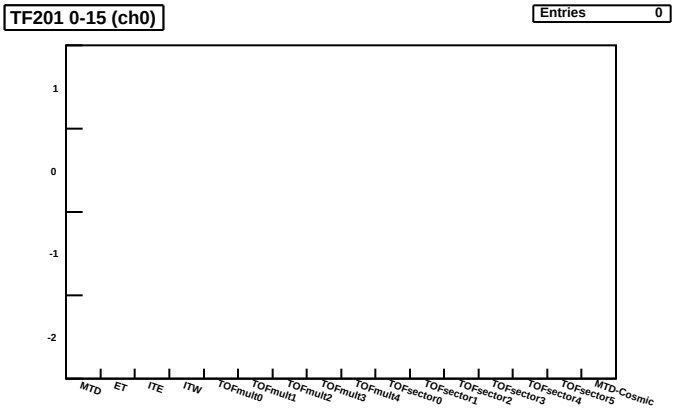

Input to FE002 QT board
Entries5496928
ADC
channel

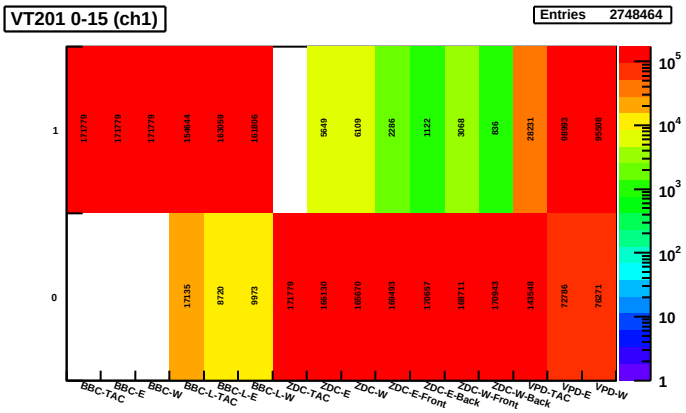

Input to FE003 QT board
Entries5496928
ADC
channel

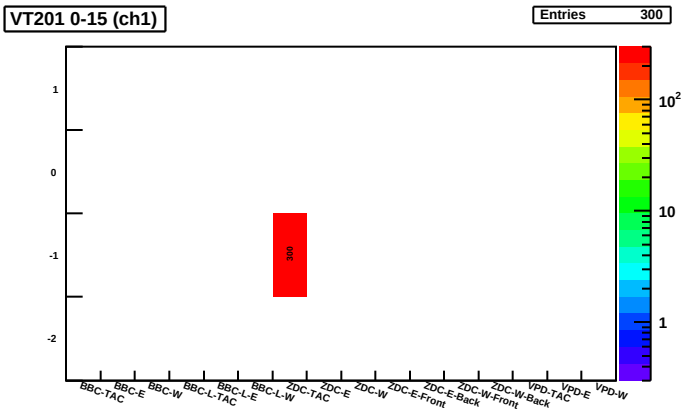

Input to FE004 QT board
Entries5496928
ADC
channel

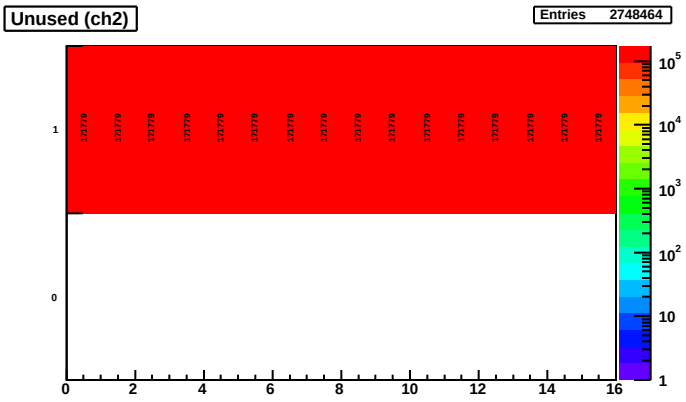

Input to FPE L1 DSM
Entries 687116
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

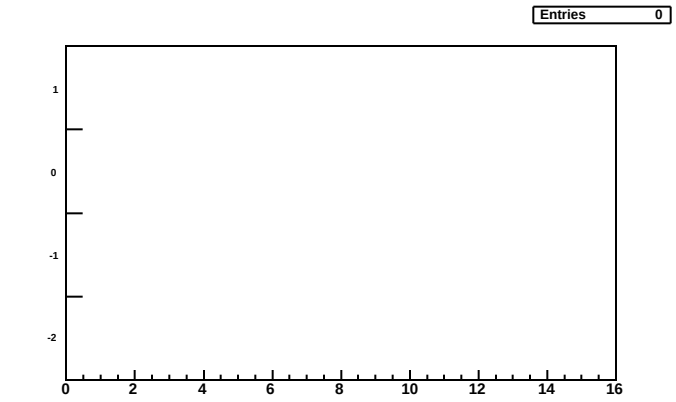

TF201 0-15 (ch0)
Entries 2748464
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

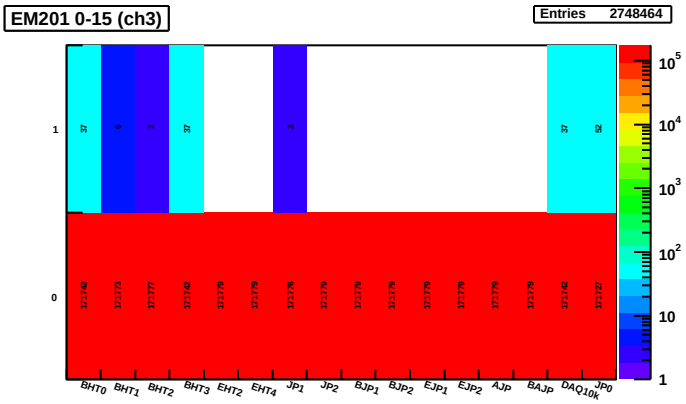

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

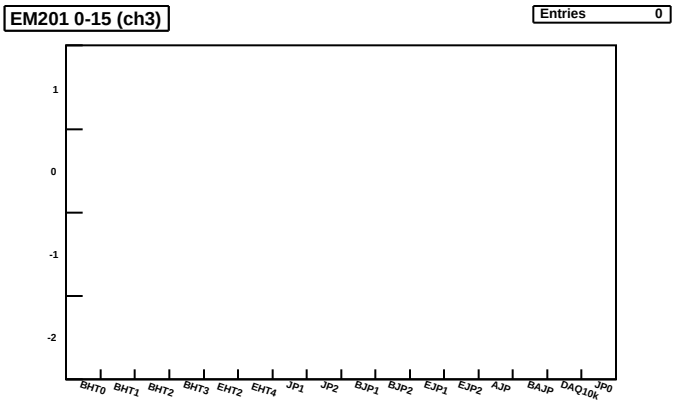

VT201 0-15 (ch1)
Entries 2748464
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 300
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

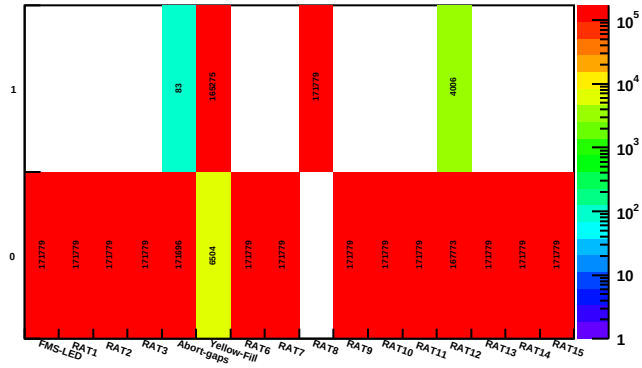

Unused (ch2)
Entries 2748464

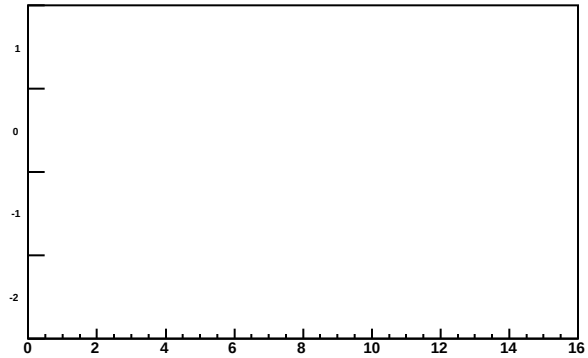

Entries 0


EM201 0-15 (ch3)
Entries 2748464
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

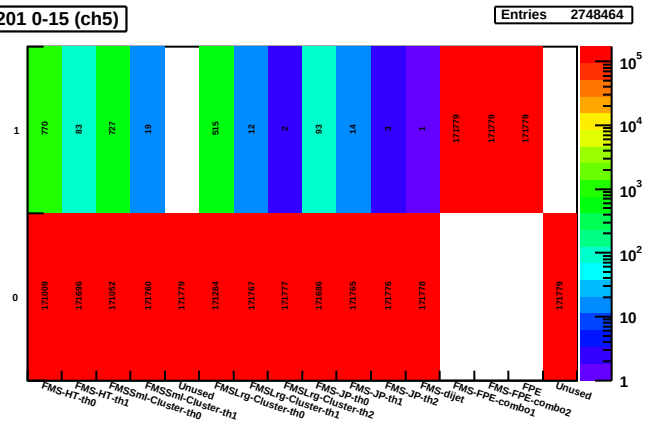

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

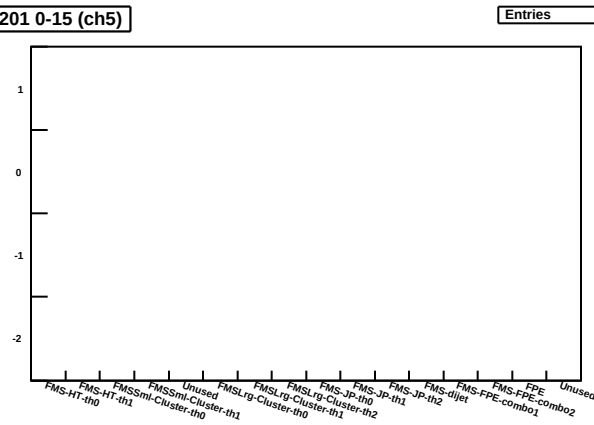




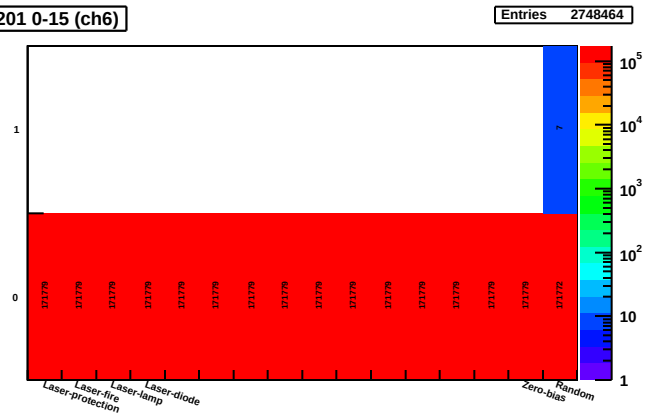
FP201 0-15 (ch5)

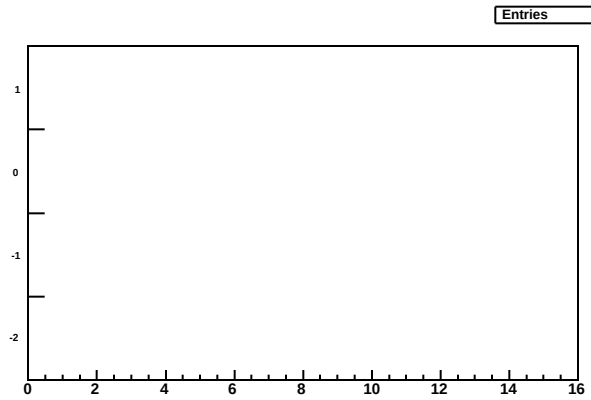
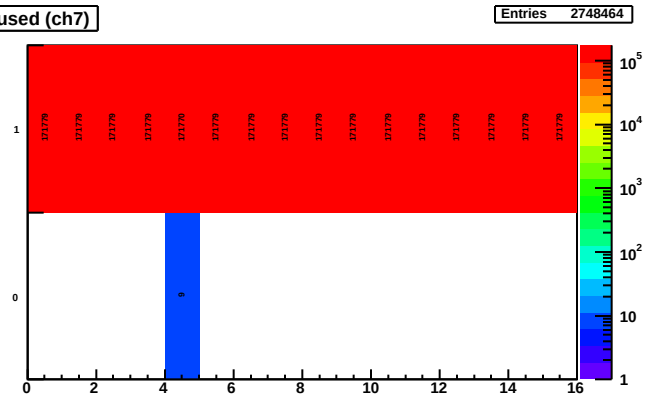
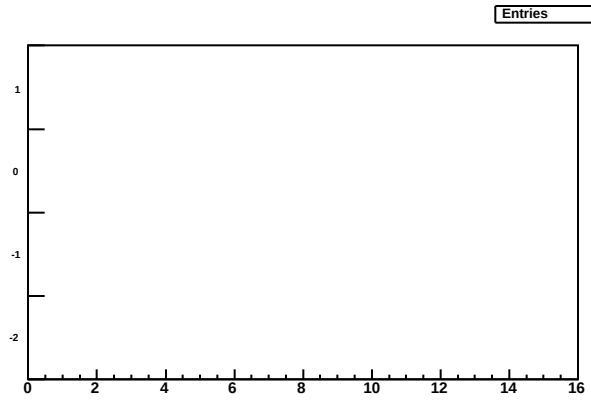


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)