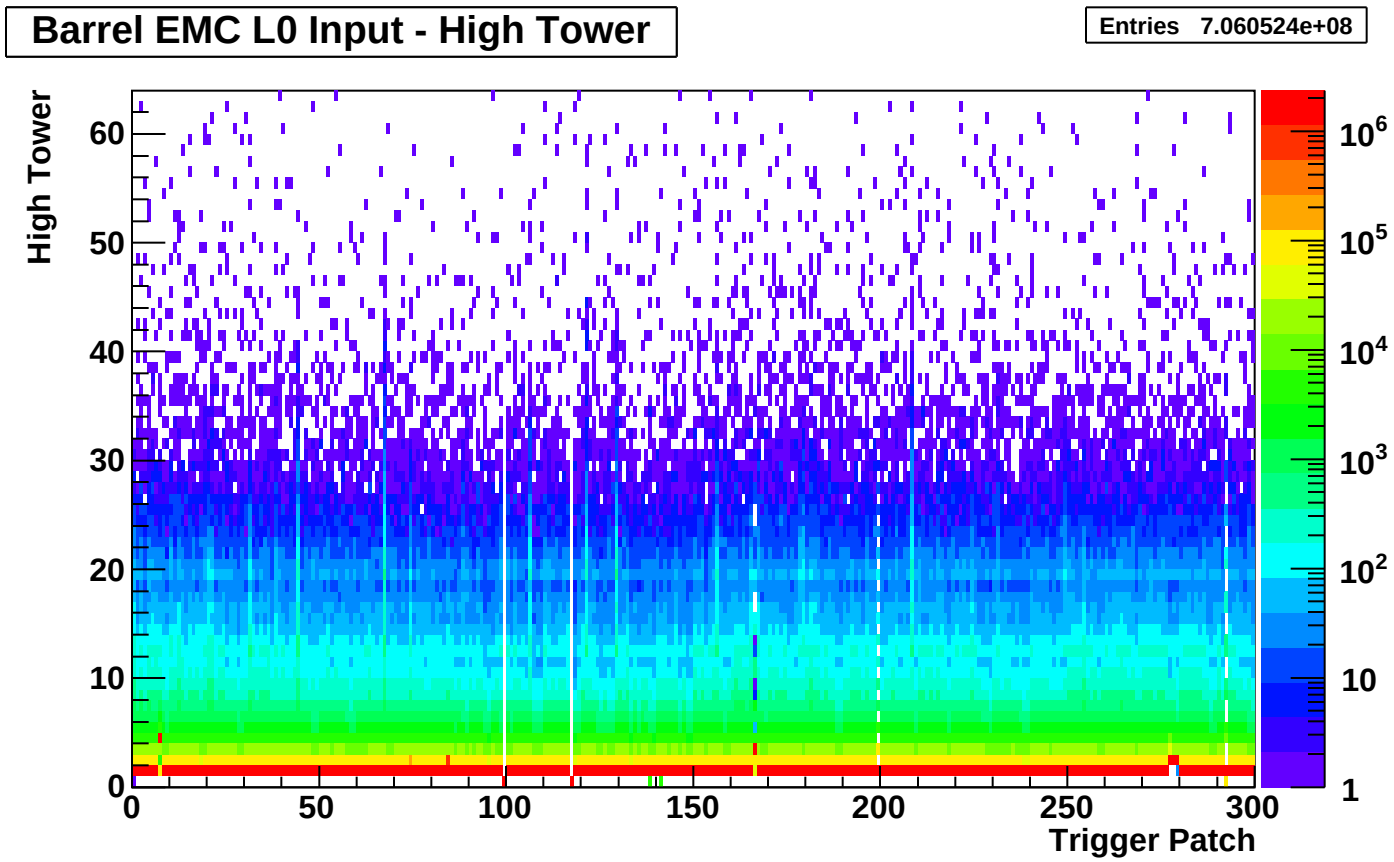
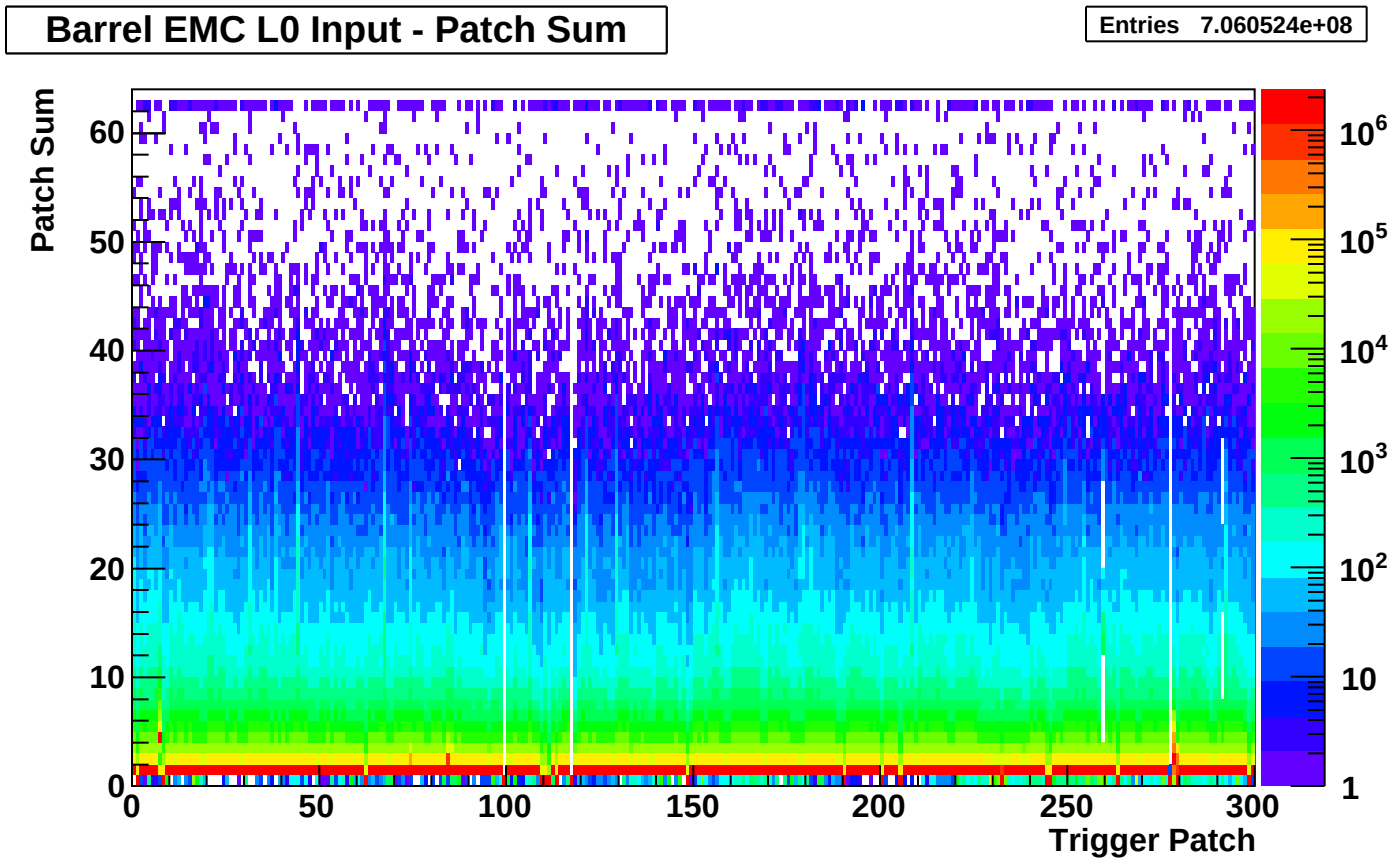
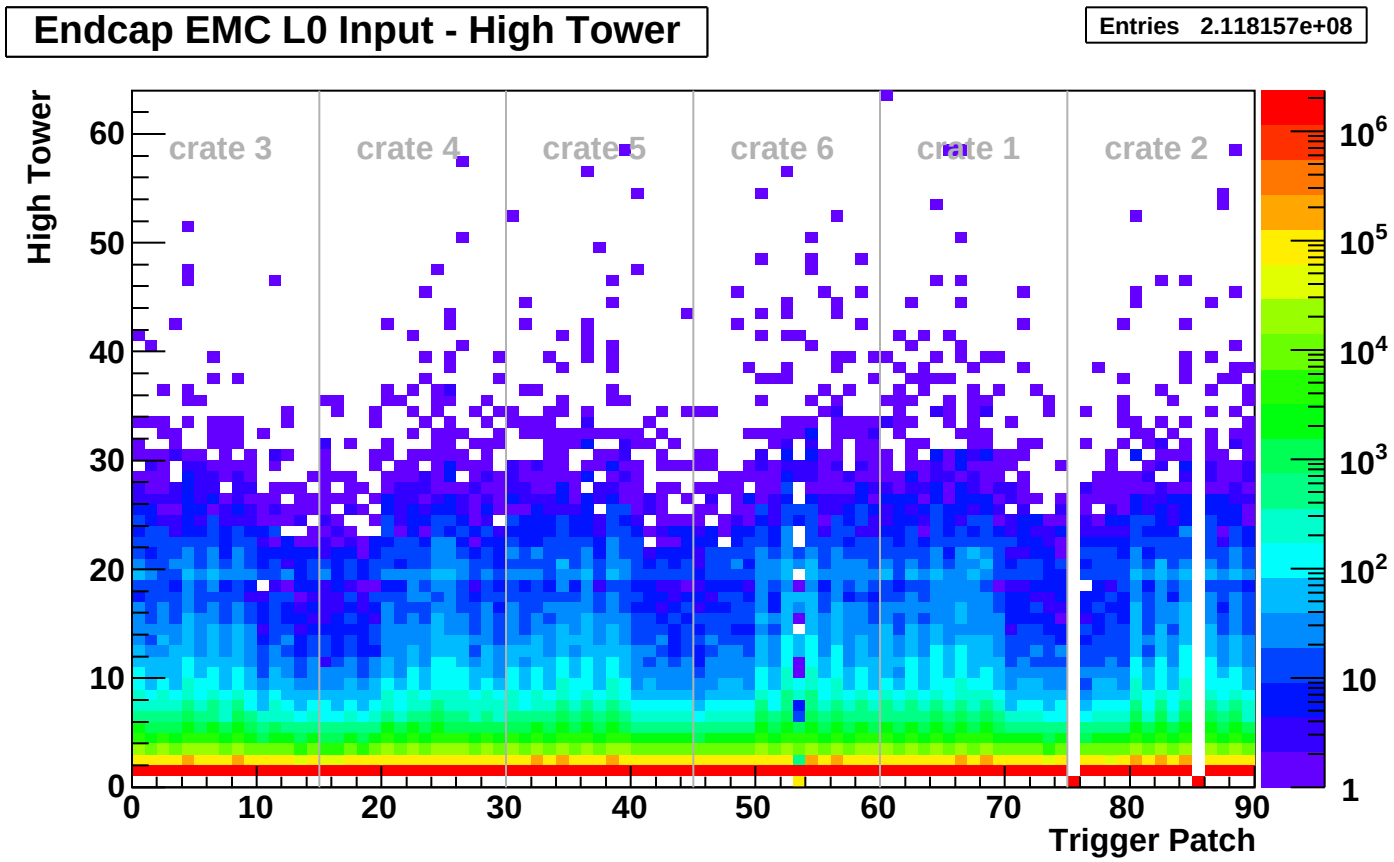
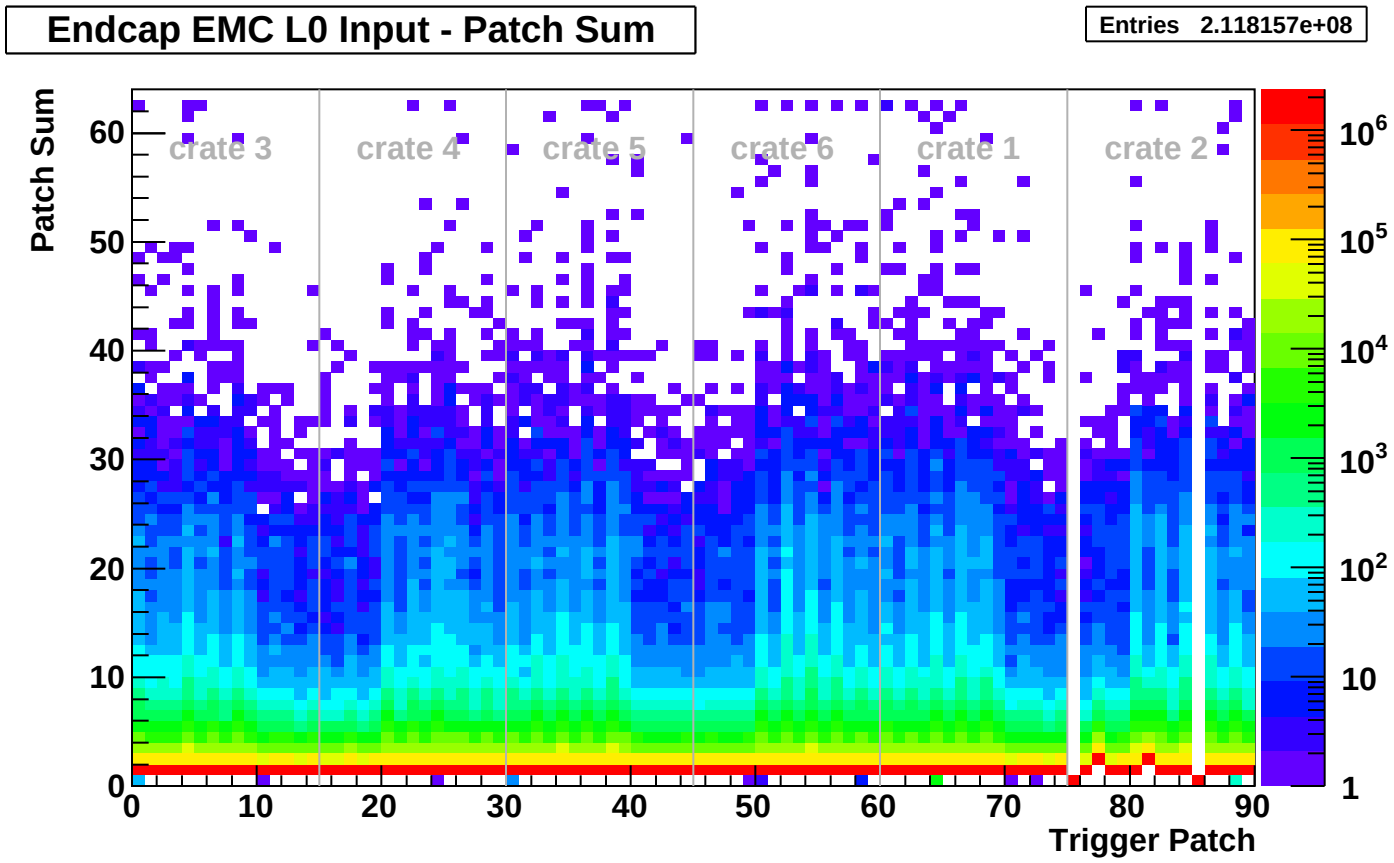
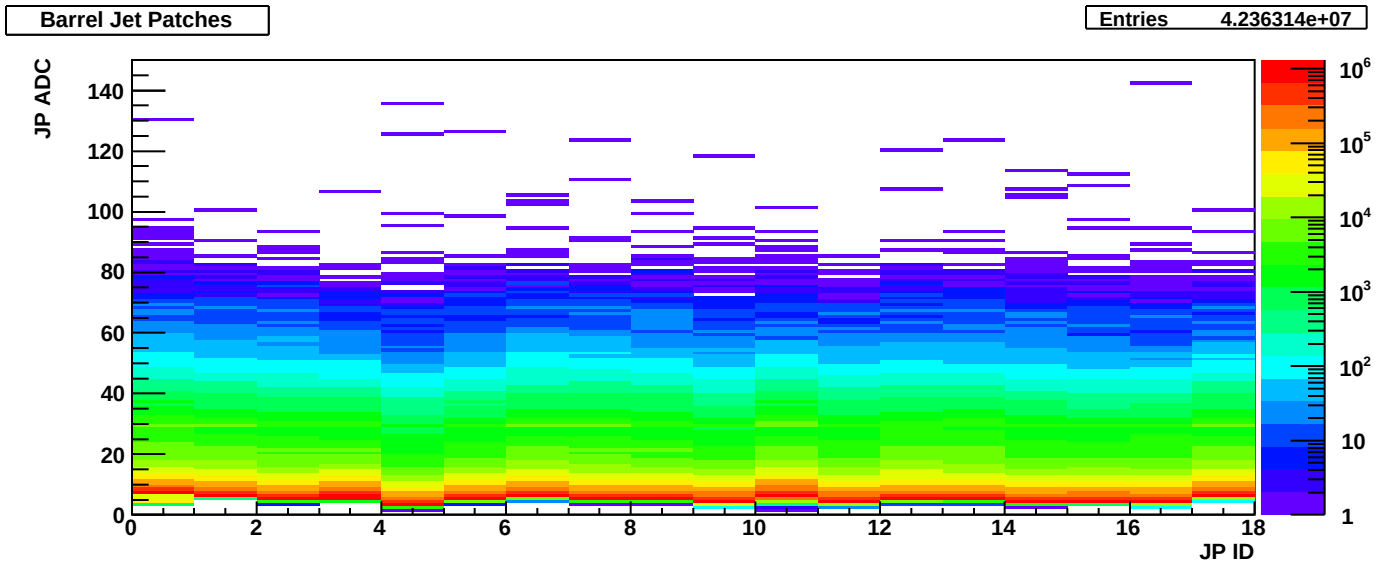
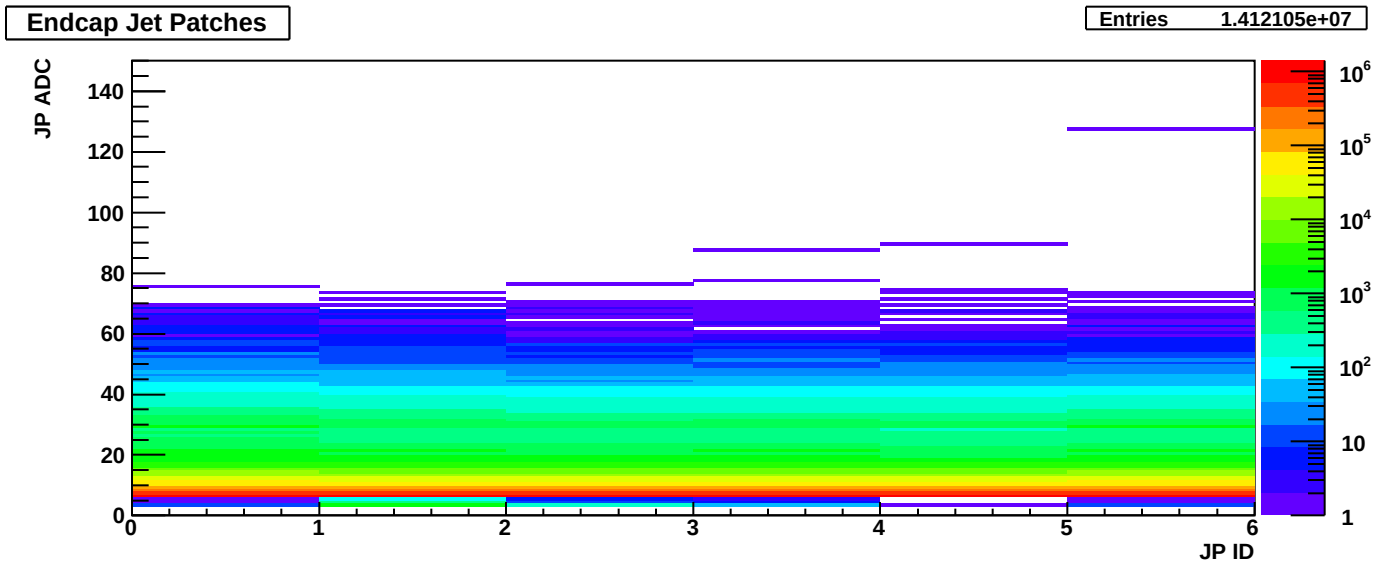

Barrel EMC L0 Input - High Tower
Entries 7.060524e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

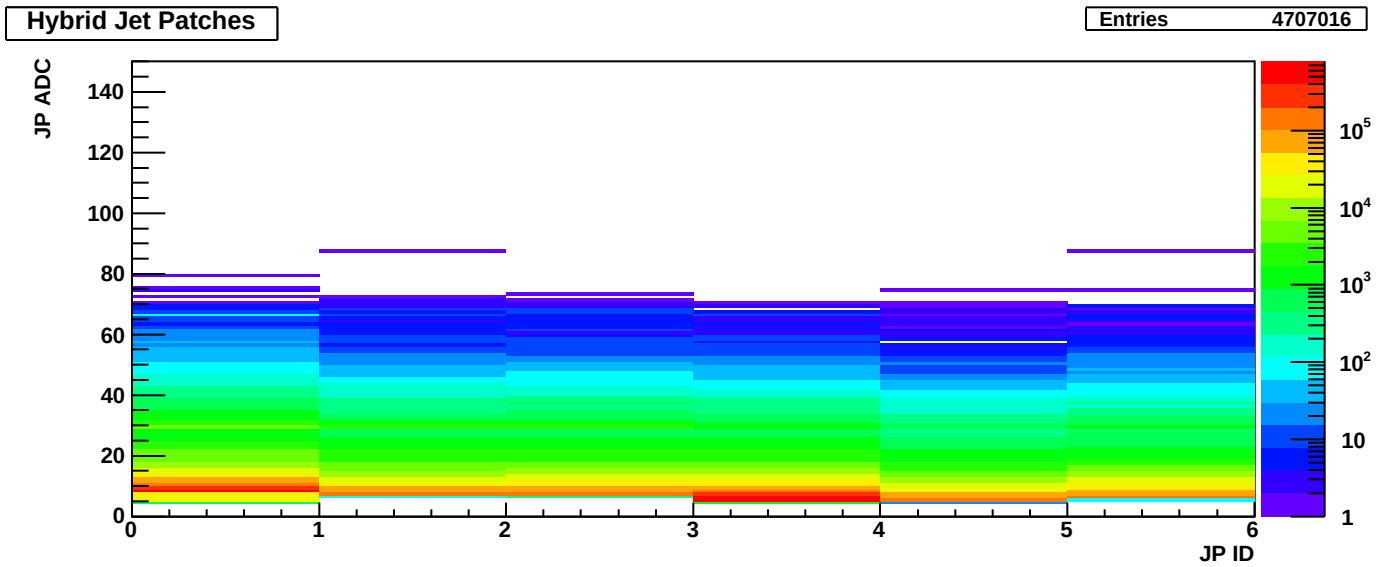

Barrel EMC L0 Input - Patch Sum
Entries 7.060524e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

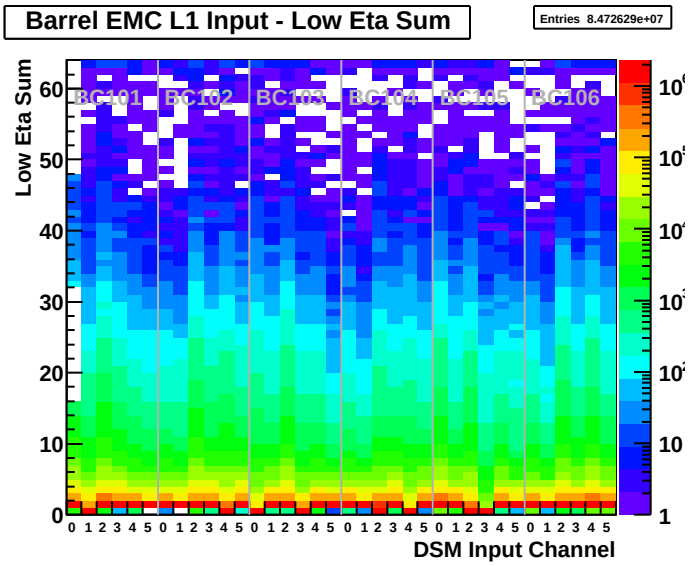

Endcap EMC L0 Input - High Tower
Entries 2.118157e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

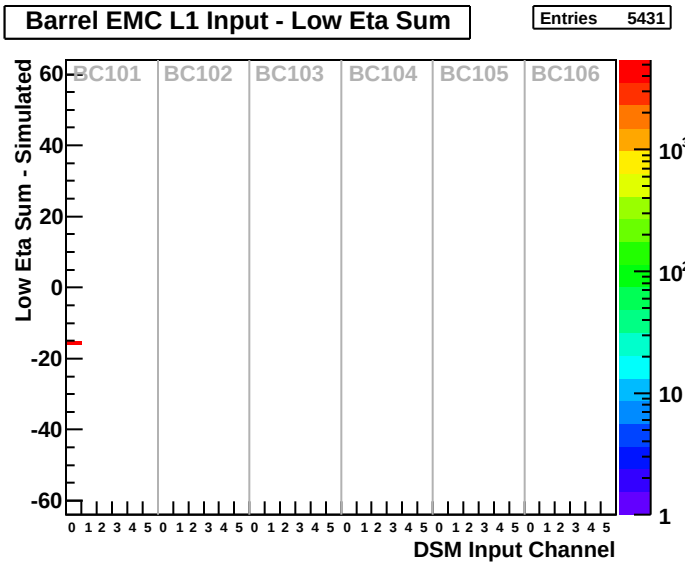

Endcap EMC L0 Input - Patch Sum
Entries 2.118157e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

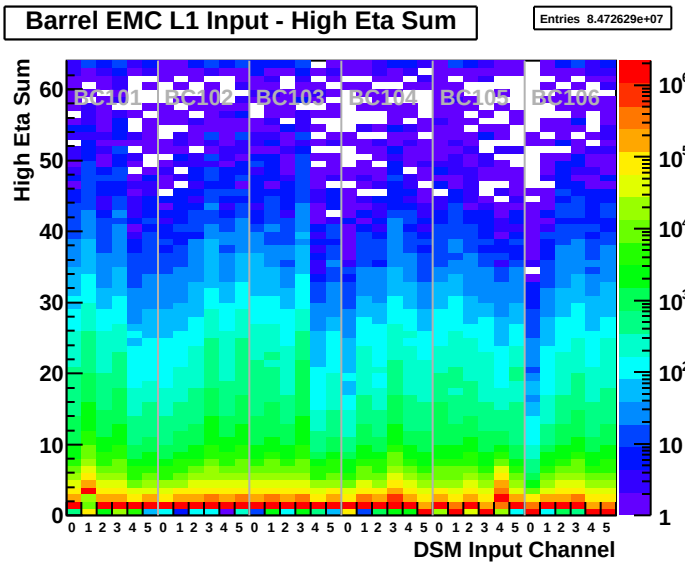

Barrel Jet Patches
Entries 4.236314e+07
JP ADC
JP ID

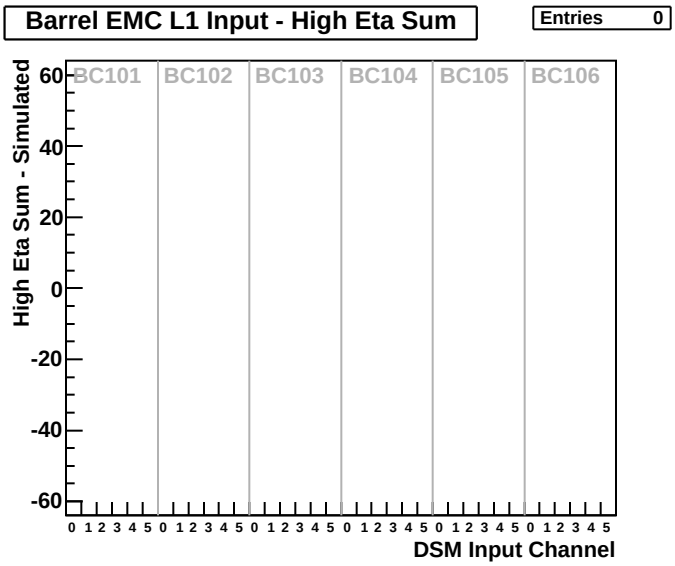

Endcap Jet Patches
Entries 1.412105e+07
JP ADC
JP ID

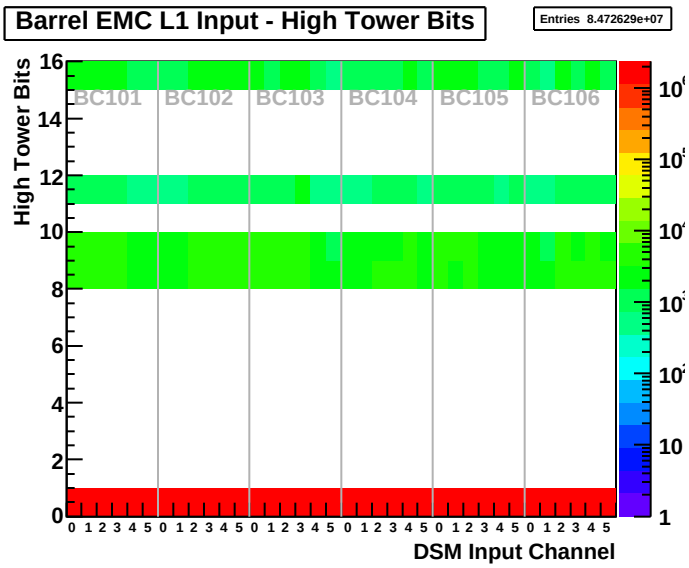

Hybrid Jet Patches
Entries 4707016
JP ADC
JP ID

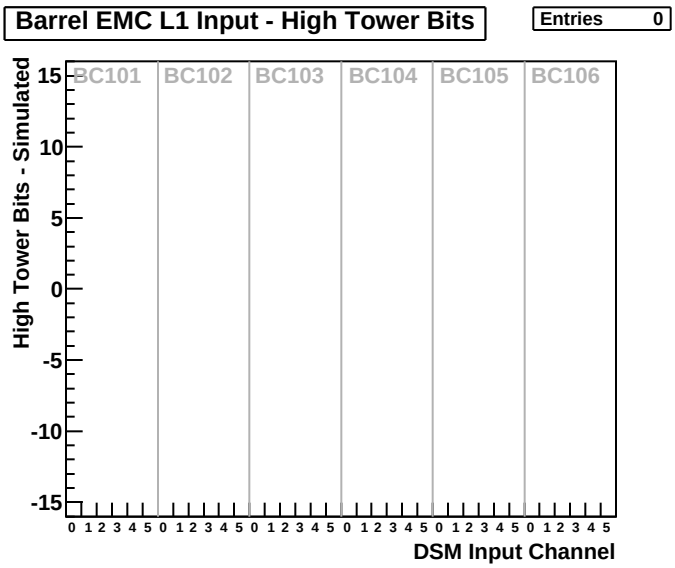

Barrel EMC L1 Input - Low Eta Sum
Entries 8.472629e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

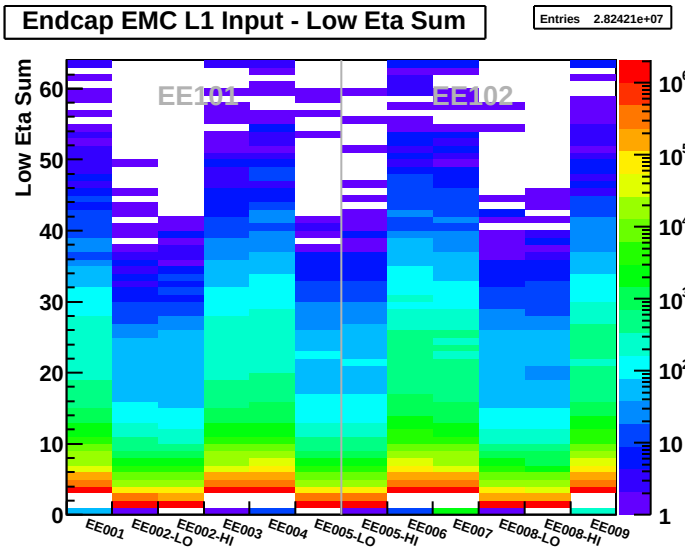

Barrel EMC L1 Input - Low Eta Sum
Entries 5431
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

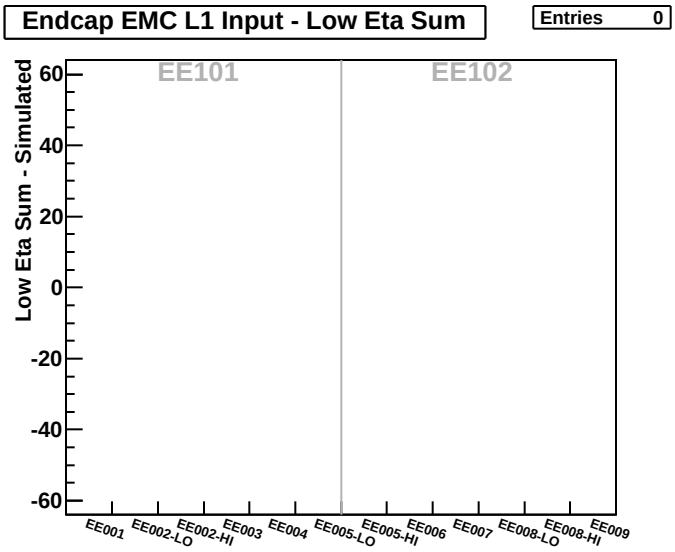

Barrel EMC L1 Input - High Eta Sum
Entries 8.472629e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

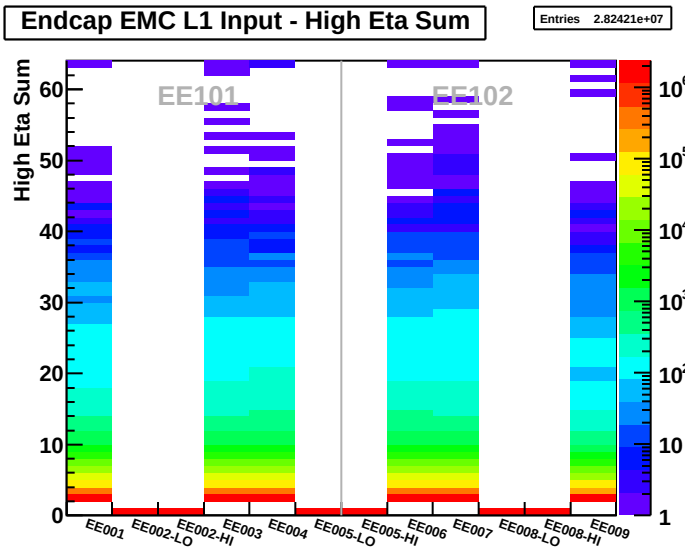

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

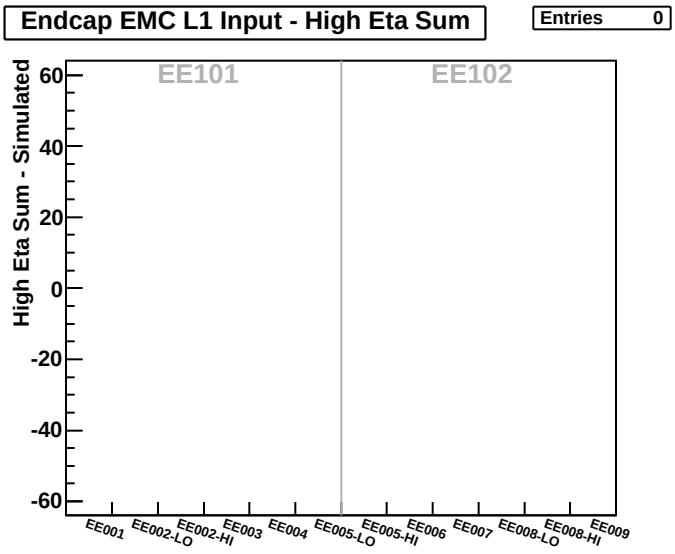

Barrel EMC L1 Input - High Tower Bits
Entries 8.472629e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

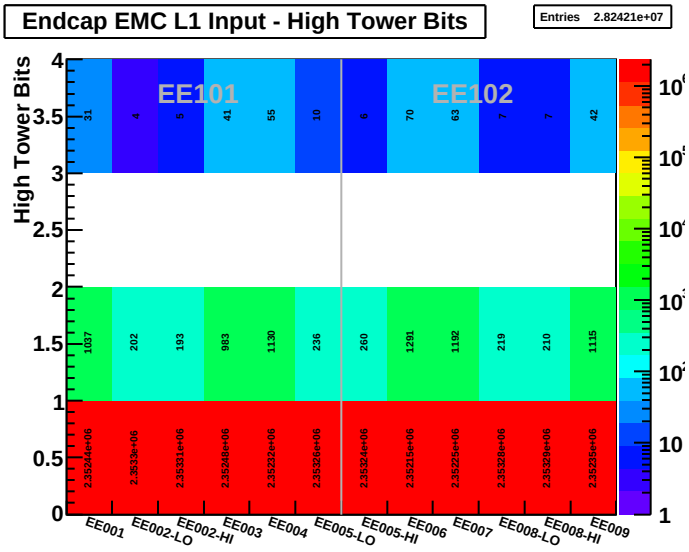

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

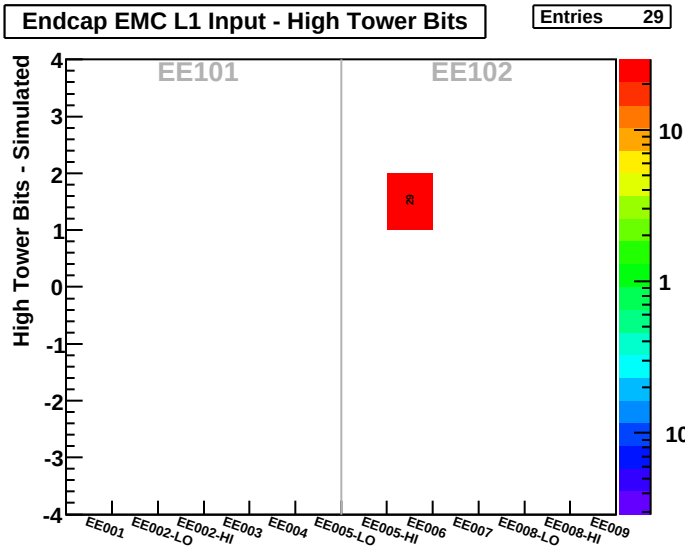

Endcap EMC L1 Input - Low Eta Sum
Entries 2.82421e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

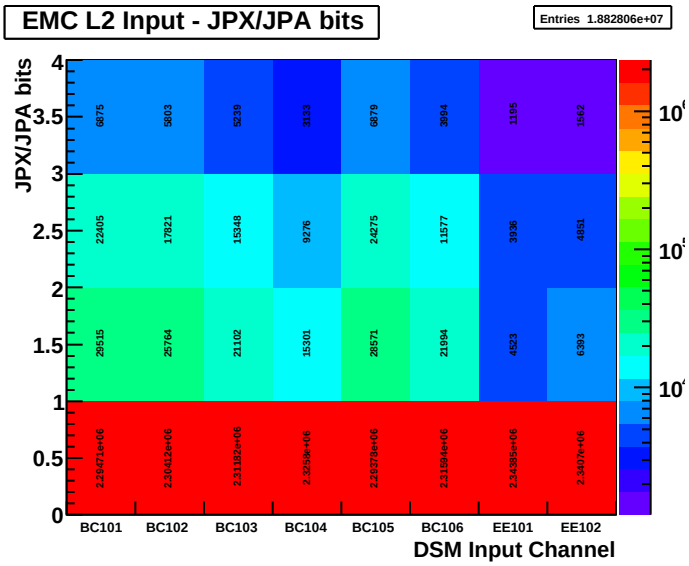

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

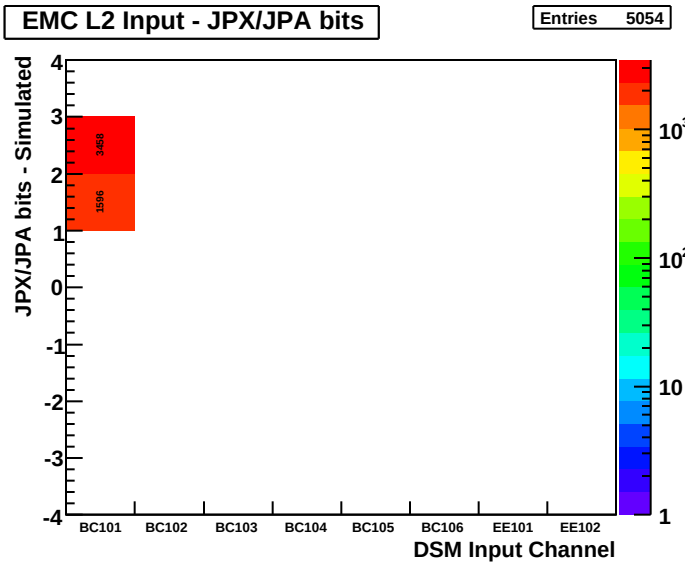

Endcap EMC L1 Input - High Eta Sum
Entries 2.82421e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

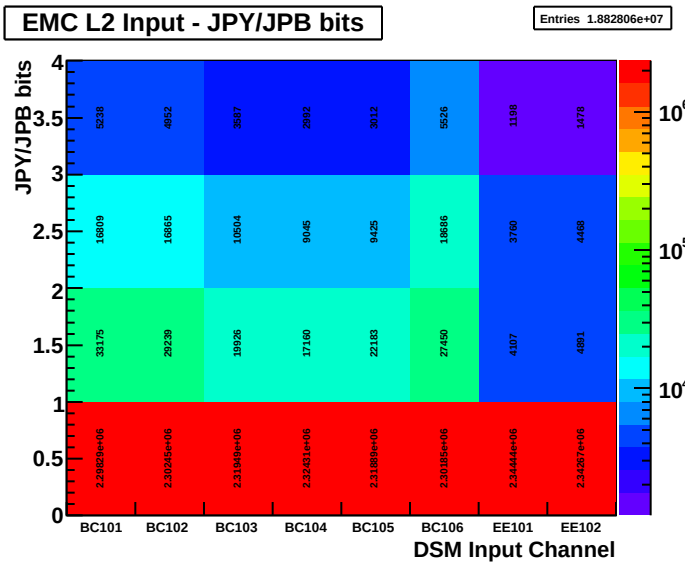

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

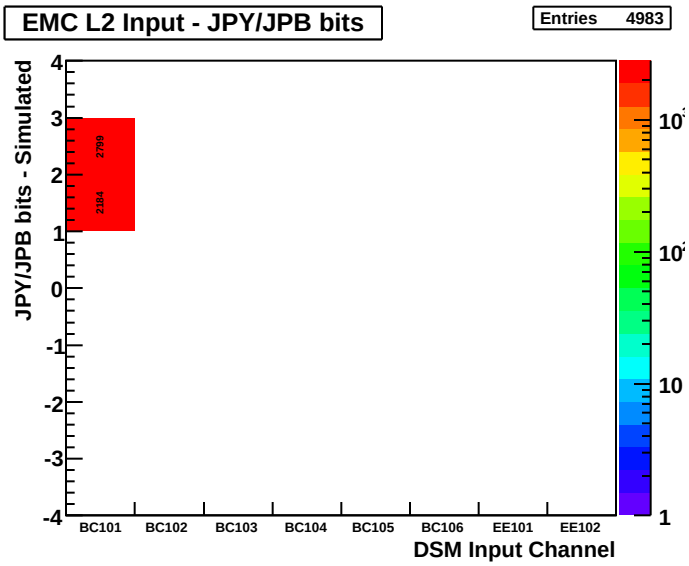

Endcap EMC L1 Input - High Tower Bits
Entries 2.82421e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

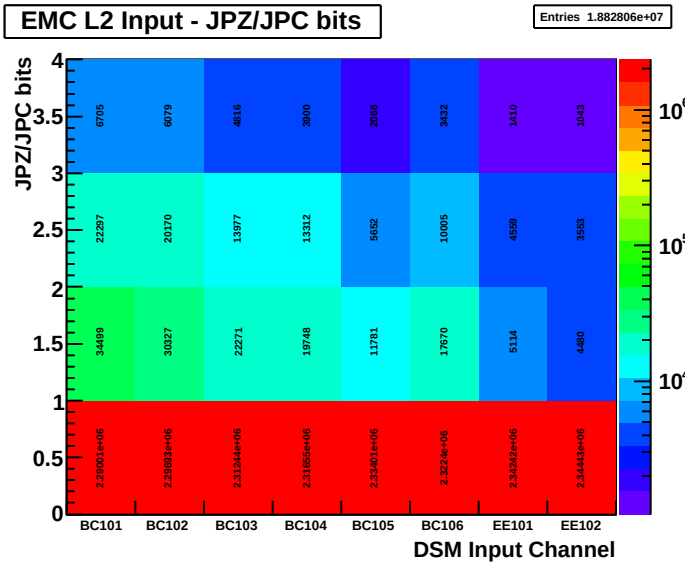

Endcap EMC L1 Input - High Tower Bits
Entries 29
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

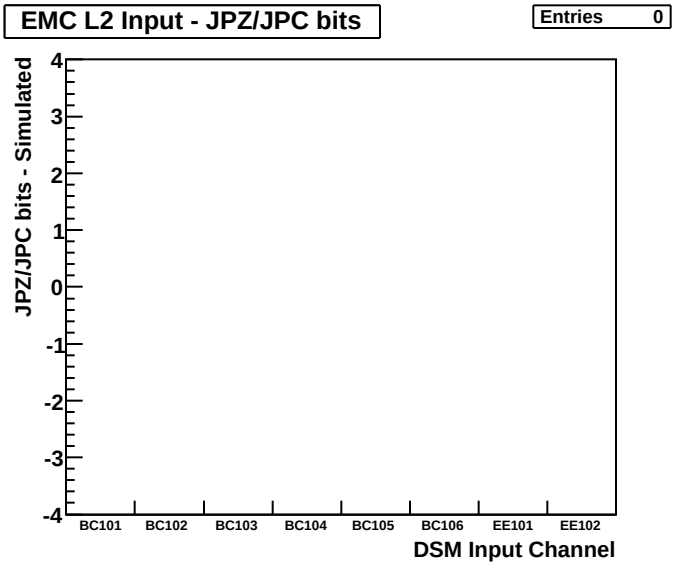

EMC L2 Input - JPX/JPA bits
Entries 1.882806e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6875 5803 5239 3133 6879 3994 1195 1562
22405 17821 15348 9276 24275 11577 3936 4851
29515 25764 21102 15301 29571 21994 4523 6393
2.29471e+06 2.30412e+06 2.31182e+06 2.32158e+06 2.29378e+06 2.31594e+06 2.34395e+06 2.3407e+06

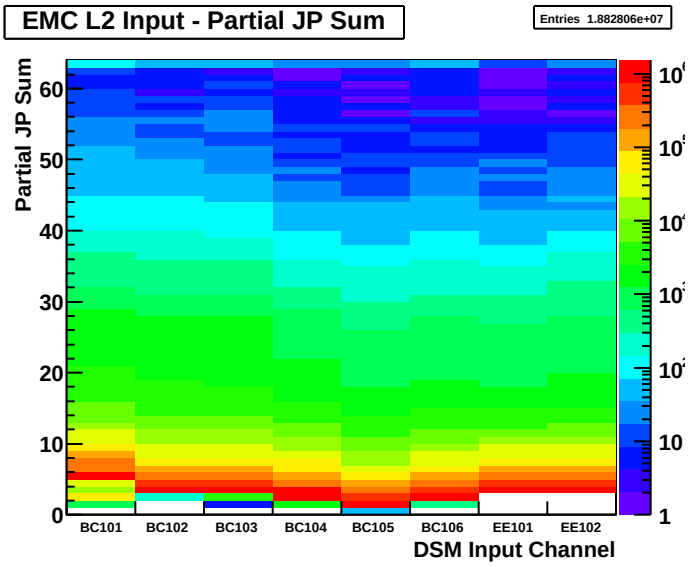

EMC L2 Input - JPX/JPA bits
Entries 5054
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3458
1596

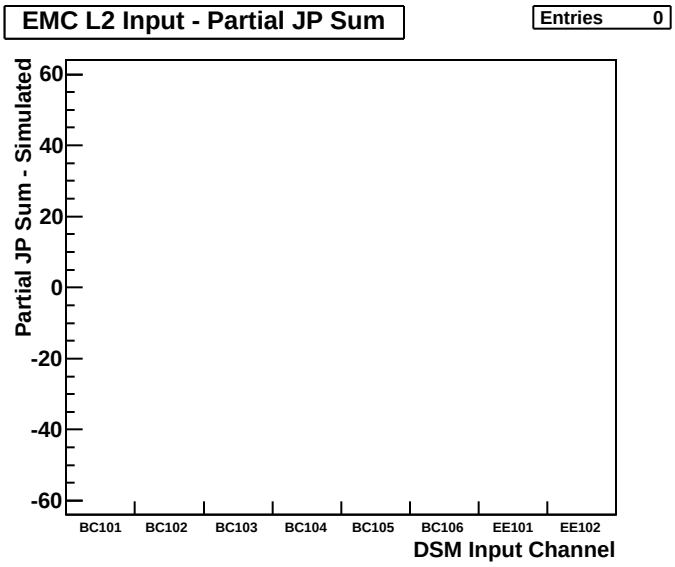

EMC L2 Input - JPY/JPB bits
Entries 1.882806e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5238 4952 3587 2992 3012 5526 1198 1478
18809 18665 10504 9045 9425 18686 3760 4468
33175 29239 19926 17160 22183 27450 4107 4891
2.29829e+06 2.30285e+06 2.31940e+06 2.32431e+06 2.31889e+06 2.30185e+06 2.34444e+06 2.34267e+06

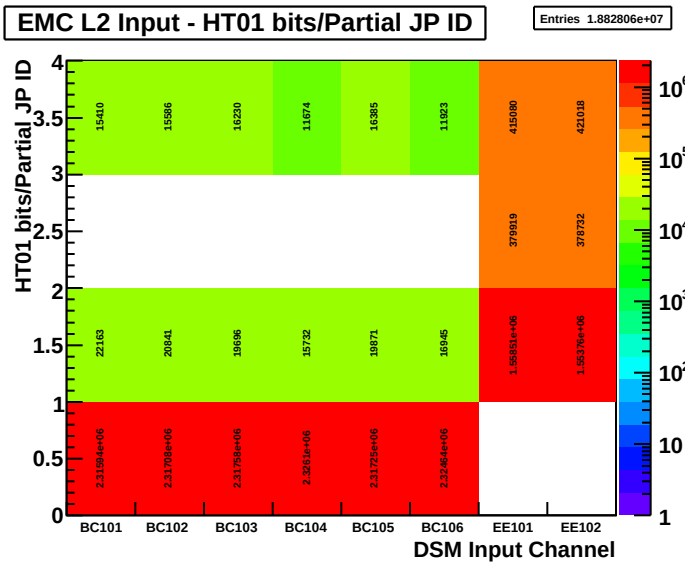

EMC L2 Input - JPY/JPB bits
Entries 4983
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2799
2184

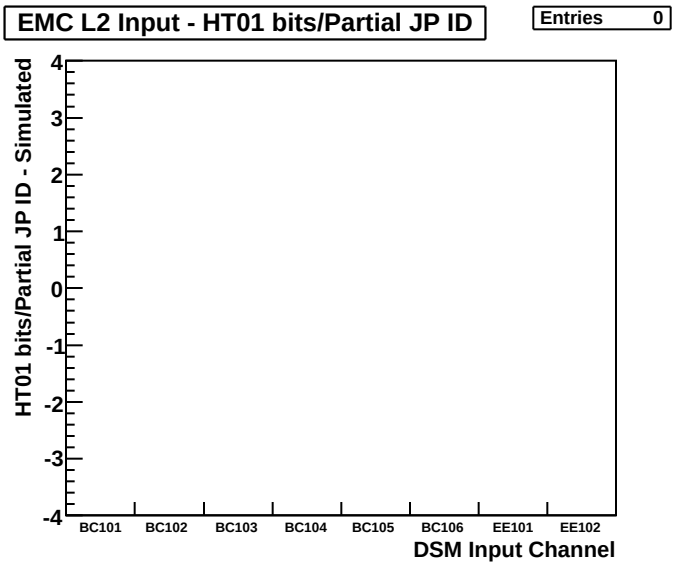

EMC L2 Input - JPZ/JPC bits
Entries 1.882806e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6705 6079 4816 3900 2068 3432 1410 1043
22297 20170 13977 13312 5652 10005 4599 3553
34499 30327 22271 19748 11781 17670 5114 4480
2.29001e+06 2.29693e+06 2.31244e+06 2.31655e+06 2.33401e+06 2.3224e+06 2.34252e+06 2.34443e+06

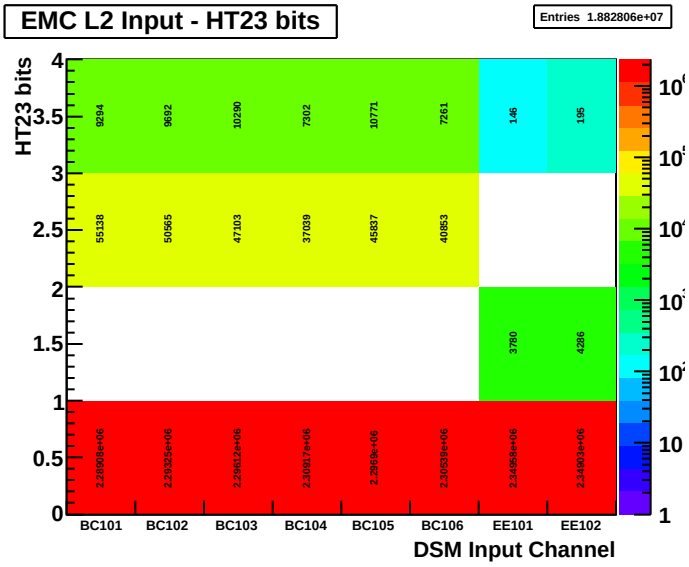

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

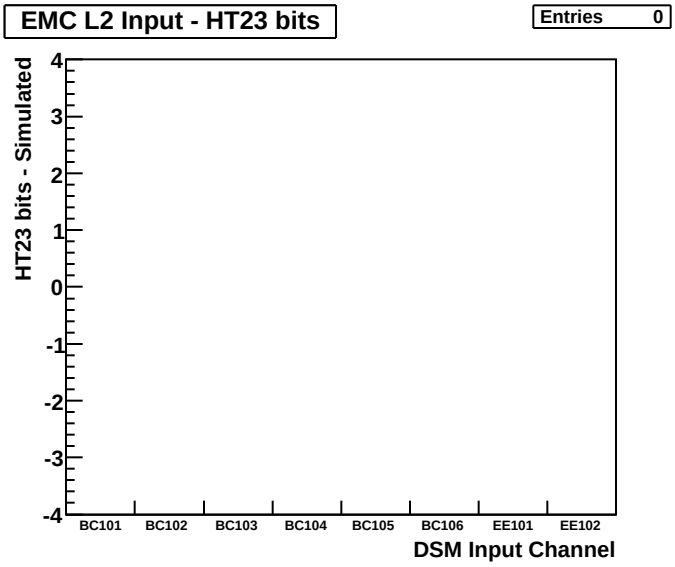

EMC L2 Input - Partial JP Sum
Entries 1.882806e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

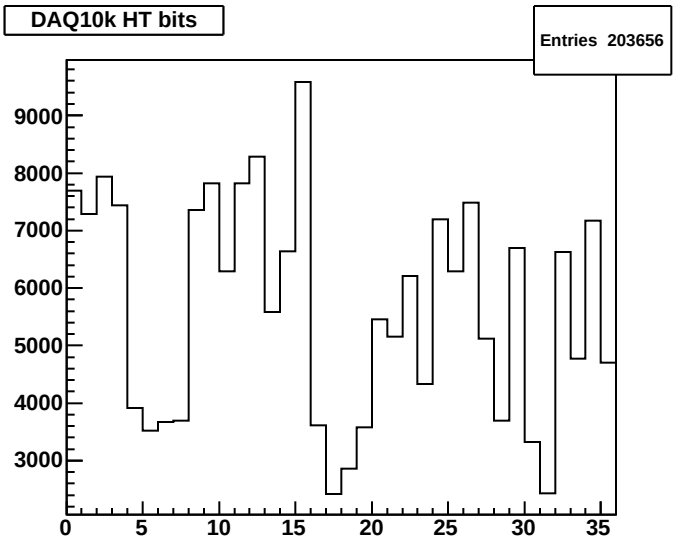

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

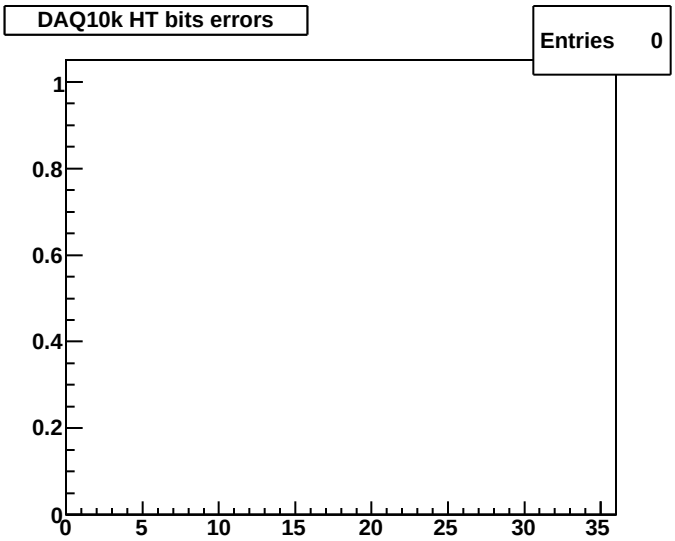

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.882806e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

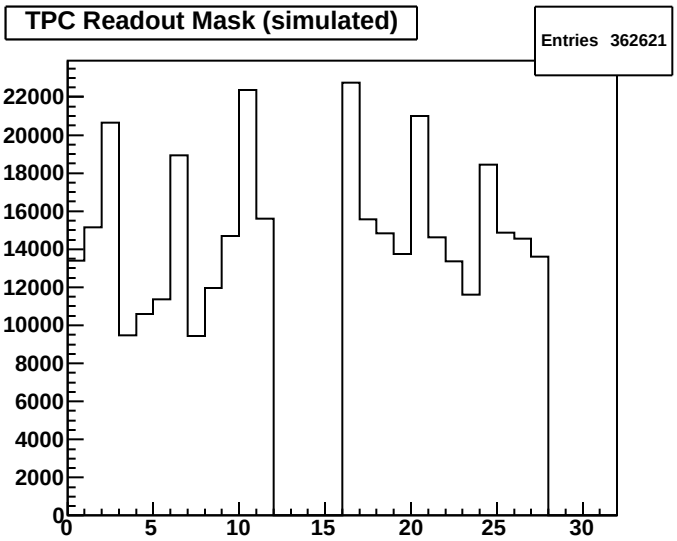

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

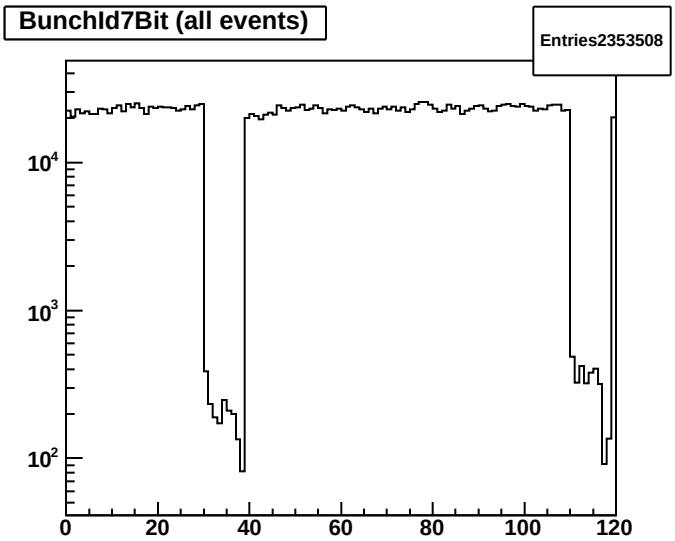

EMC L2 Input - HT23 bits
Entries 1.882806e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

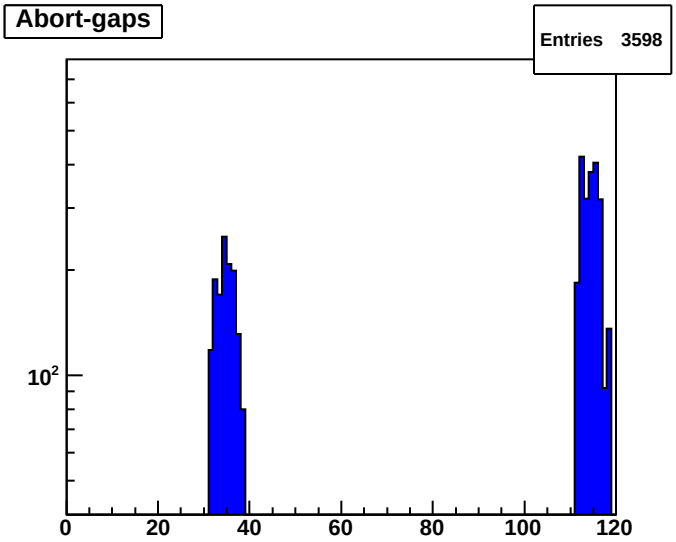

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

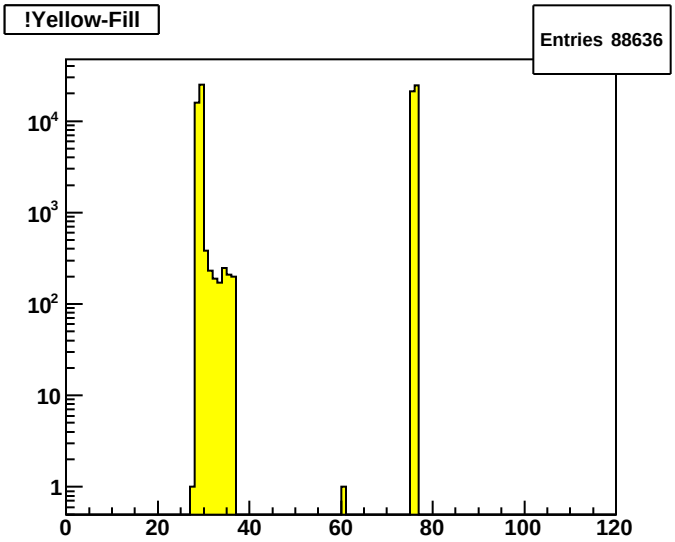

DAQ10k HT bits
Entries 203656

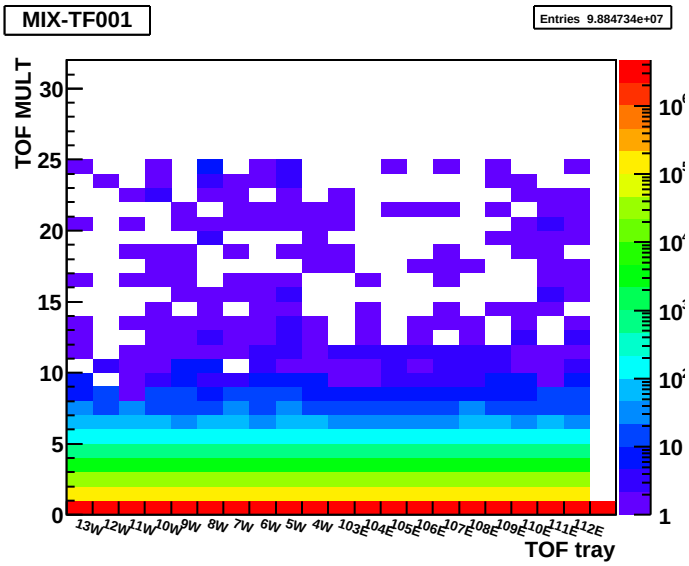

DAQ10k HT bits errors
Entries 0

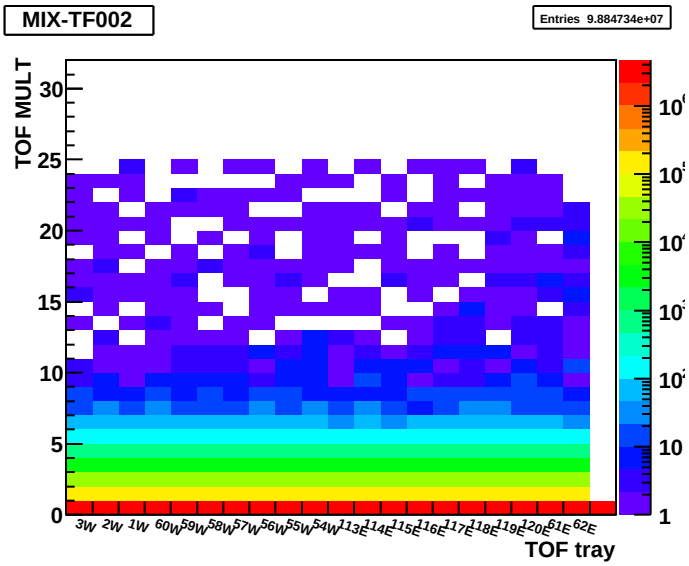

TPC Readout Mask (simulated)
Entries 362621

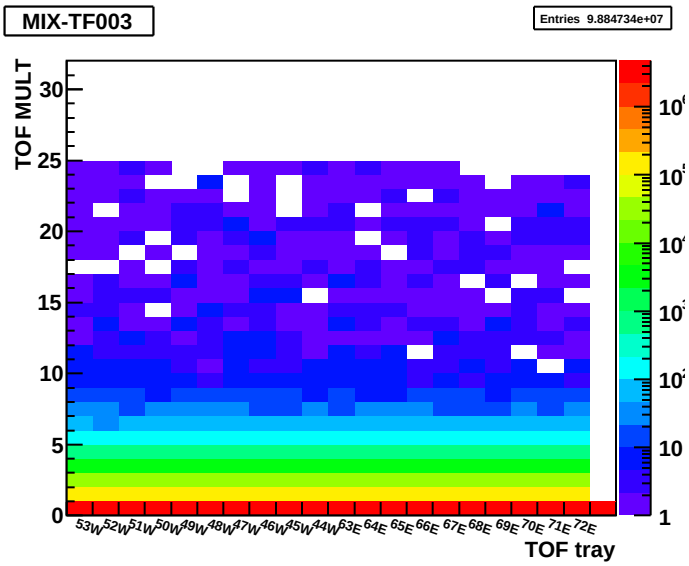

BunchId7Bit (all events)
Entries2353508

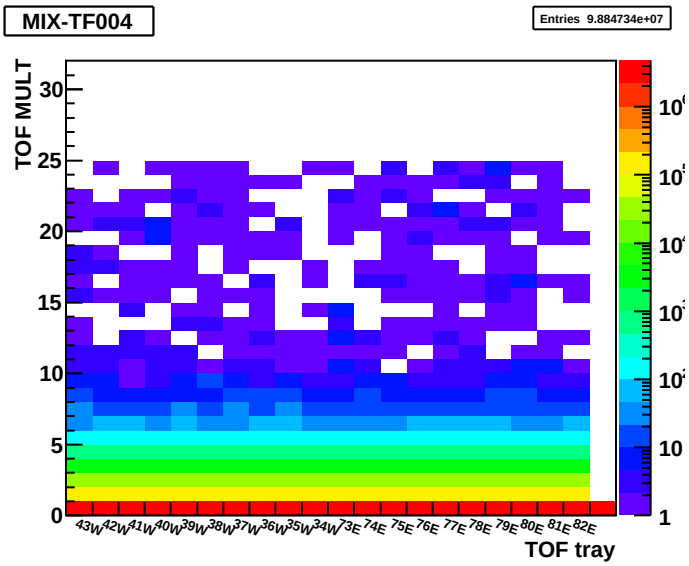

Abort-gaps
Entries 3598

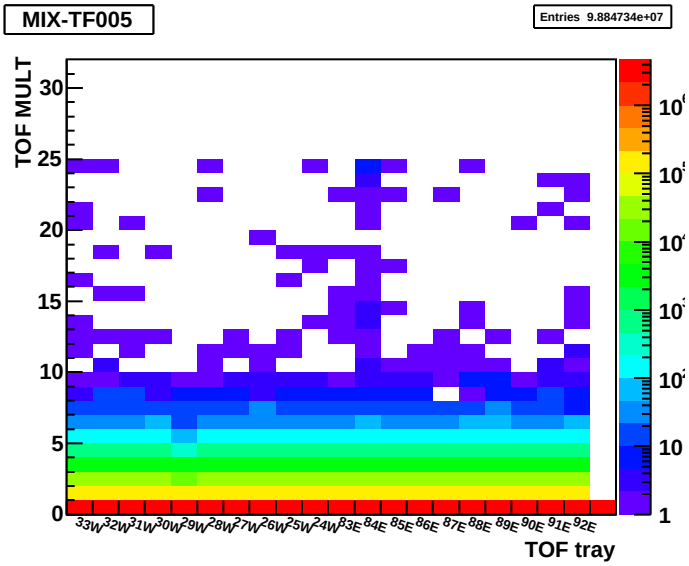

!Yellow-Fill
Entries 88636

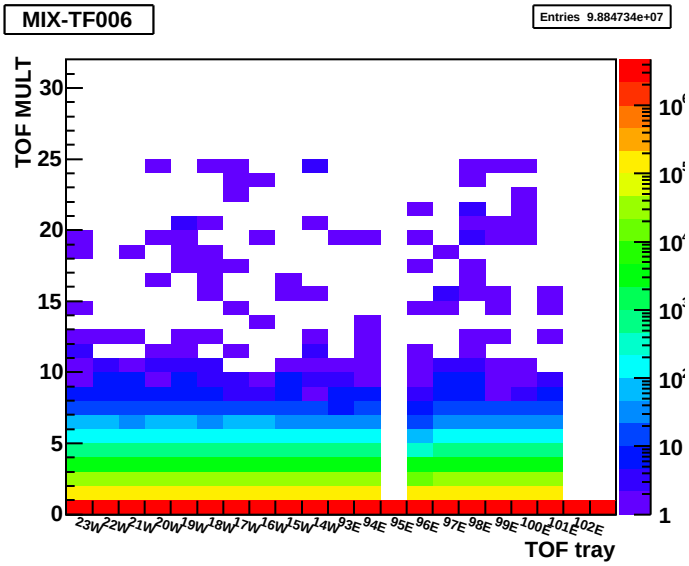

MIX-TF001
Entries 9.884734e+07
TOF MULT
TOF tray

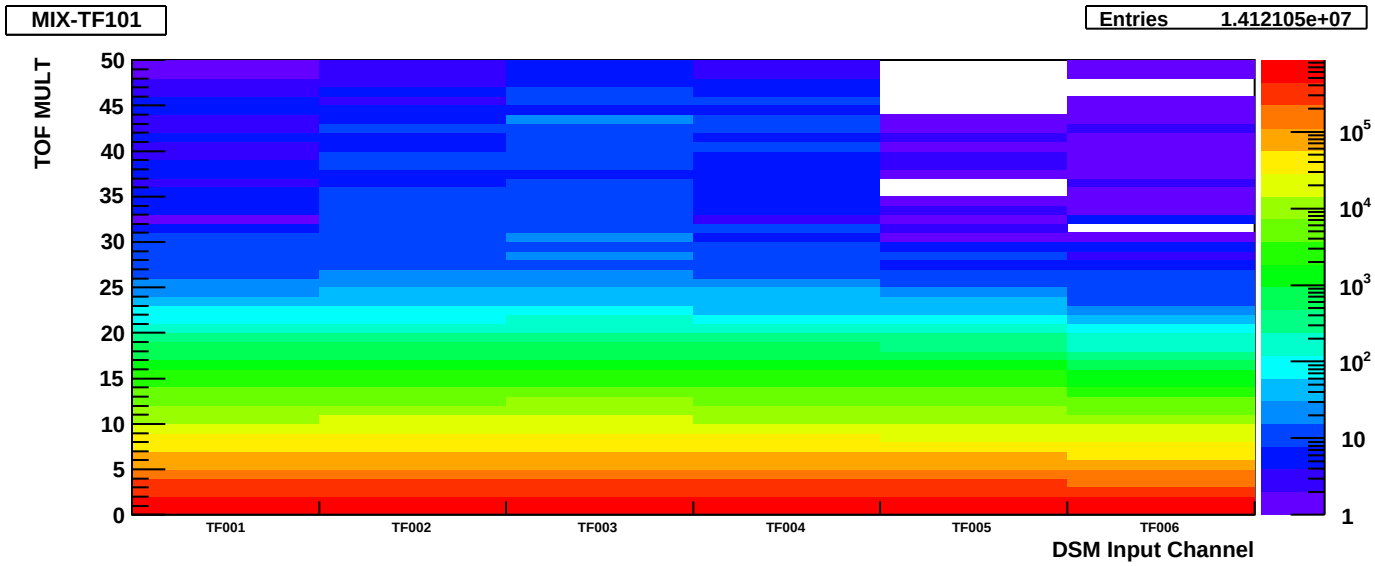

MIX-TF002
Entries 9.884734e+07
TOF MULT
TOF tray

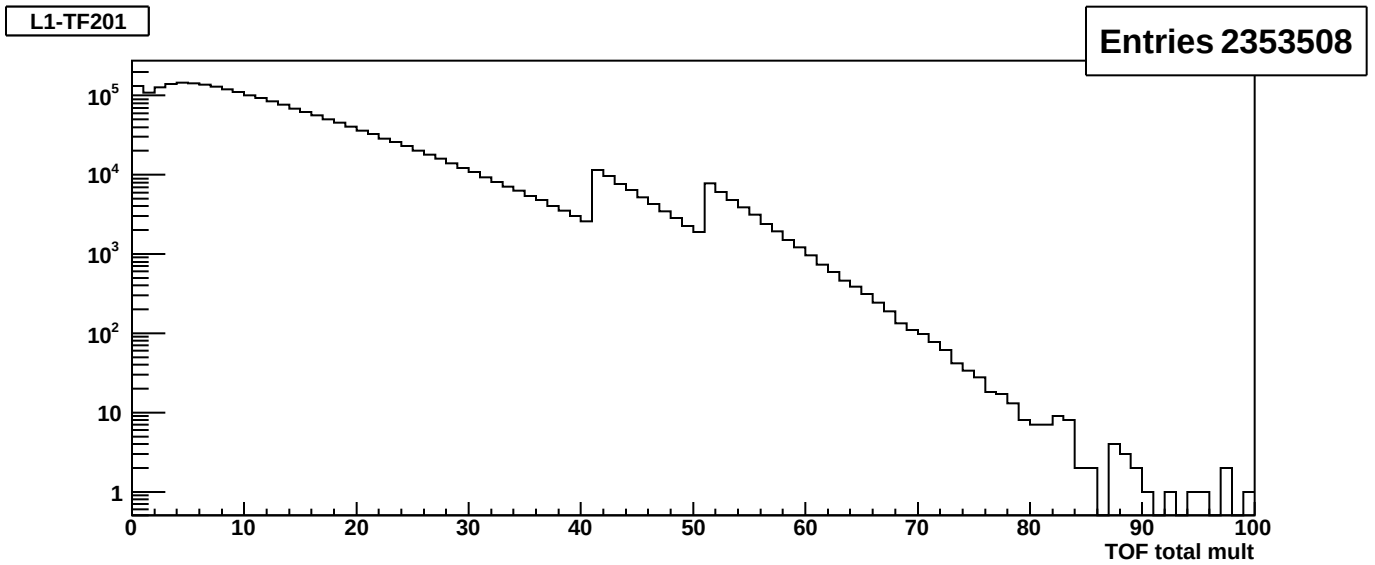

MIX-TF003
Entries 9.884734e+07
TOF MULT
TOF tray

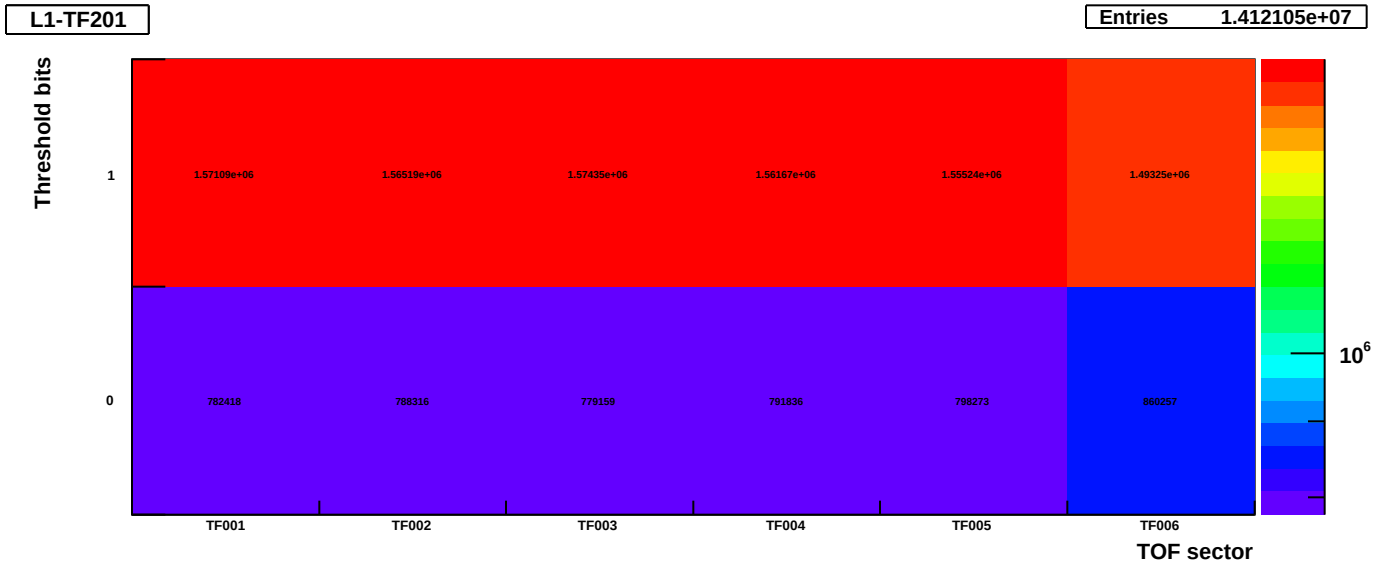

MIX-TF004
Entries 9.884734e+07
TOF MULT
TOF tray

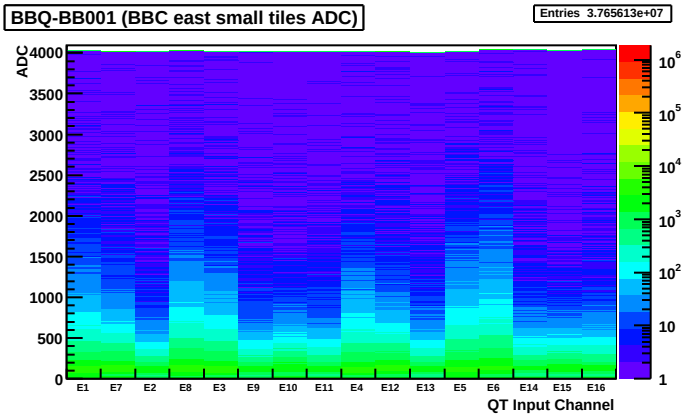

MIX-TF005
Entries 9.884734e+07
TOF MULT
TOF tray

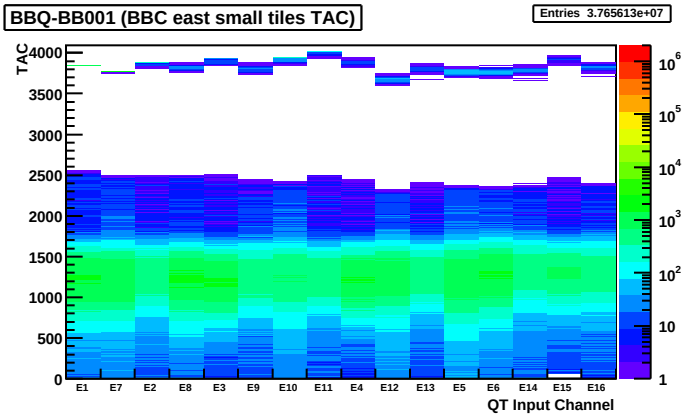

MIX-TF006
Entries 9.884734e+07
TOF MULT
TOF tray

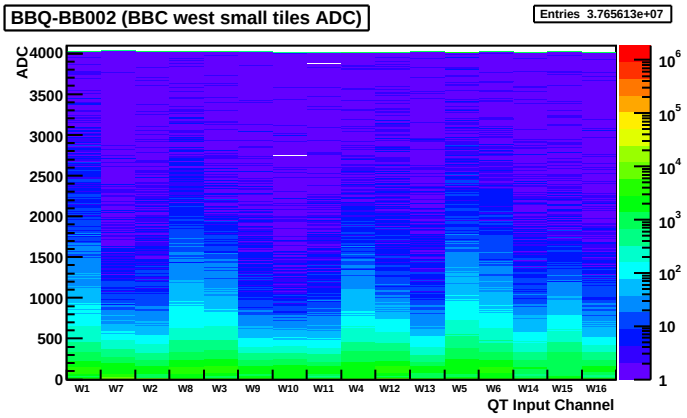

MIX-TF101
Entries 1.412105e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

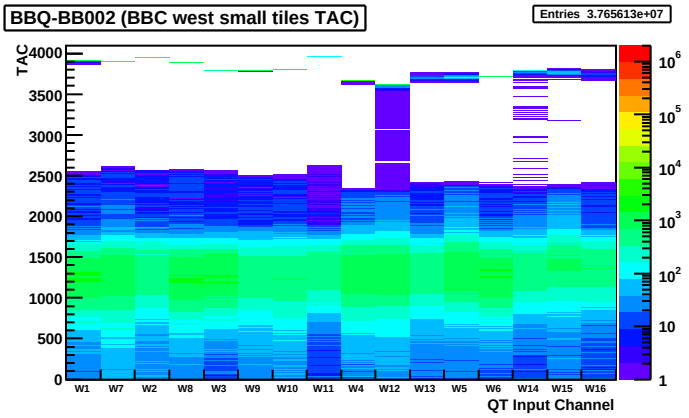

L1-TF201
Entries 2353508
TOF total mult

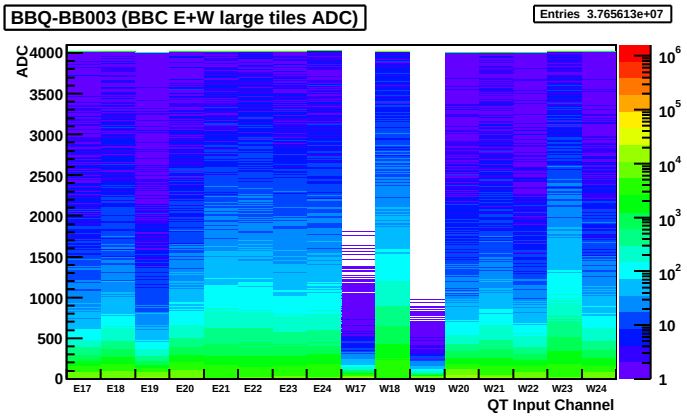

L1-TF201
Entries 1.412105e+07
Threshold bits
1.57109e+06
1.56519e+06
1.57435e+06
1.56167e+06
1.55524e+06
1.49325e+06
782418
788316
779159
791836
798273
860257
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

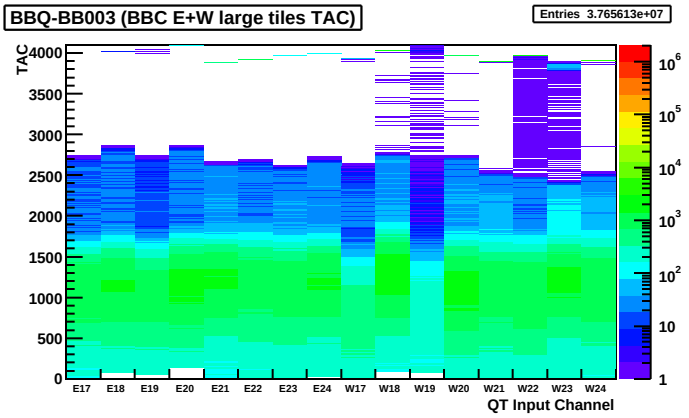

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.765613e+07
ADC
QT Input Channel

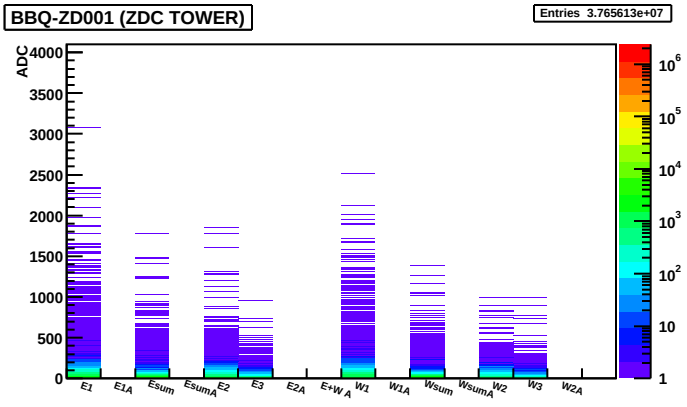

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.765613e+07
TAC
QT Input Channel

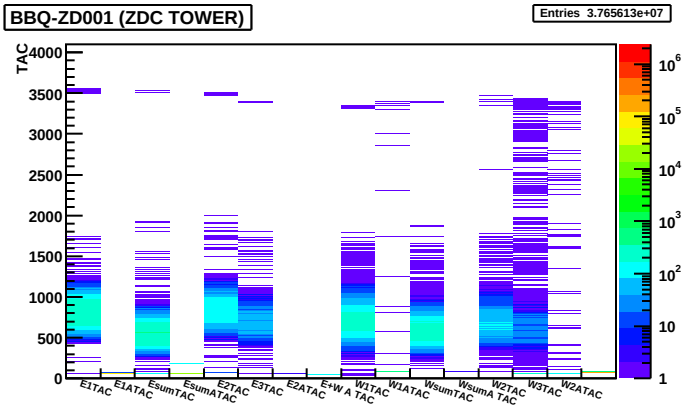

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.765613e+07
ADC
QT Input Channel

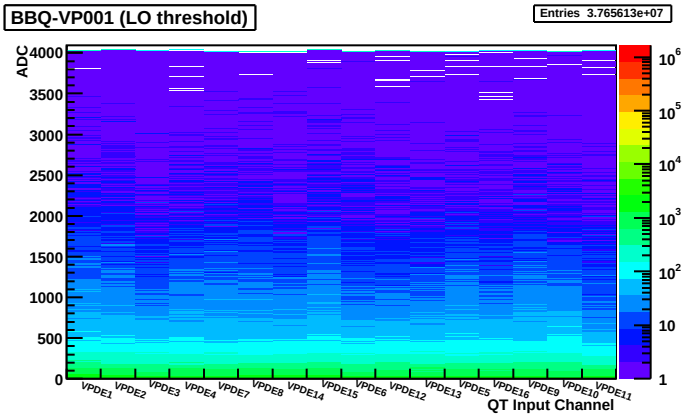

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.765613e+07
TAC
QT Input Channel

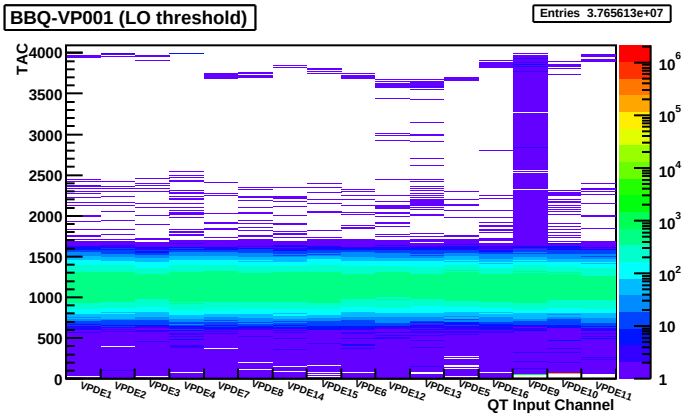

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.765613e+07
ADC
QT Input Channel

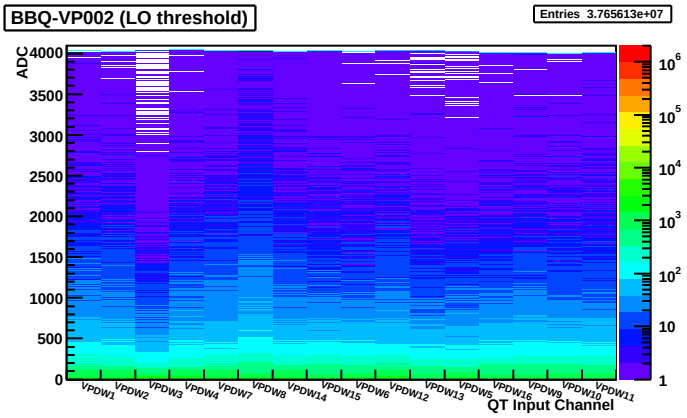

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.765613e+07
TAC
QT Input Channel

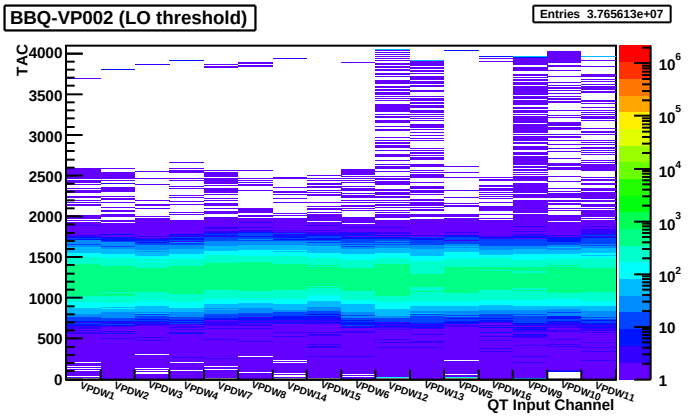

BBQ-ZD001 (ZDC TOWER)
Entries 3.765613e+07
ADC

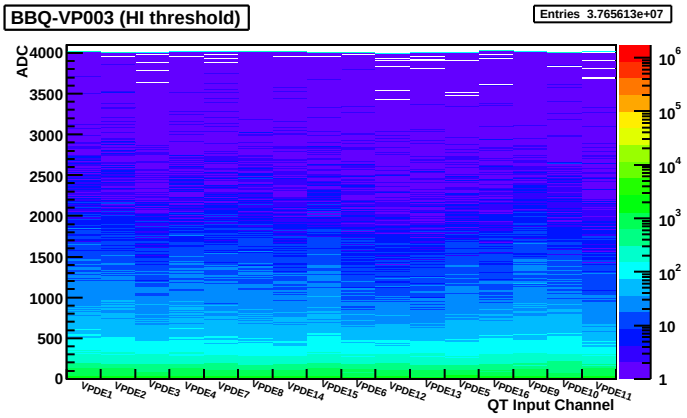

BBQ-ZD001 (ZDC TOWER)
Entries 3.765613e+07
TAC

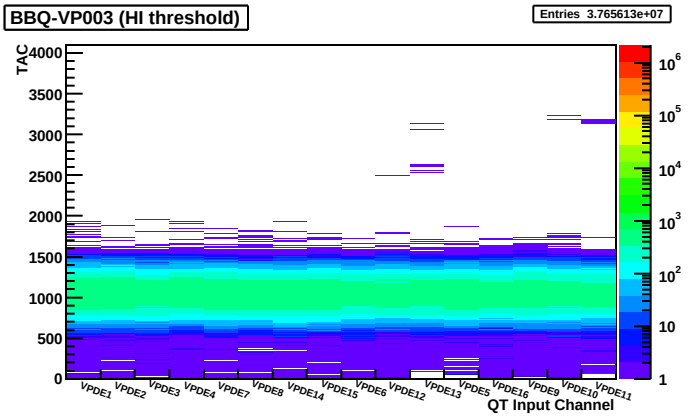

BBQ-VP001 (LO threshold)
Entries 3.765613e+07
ADC
QT Input Channel

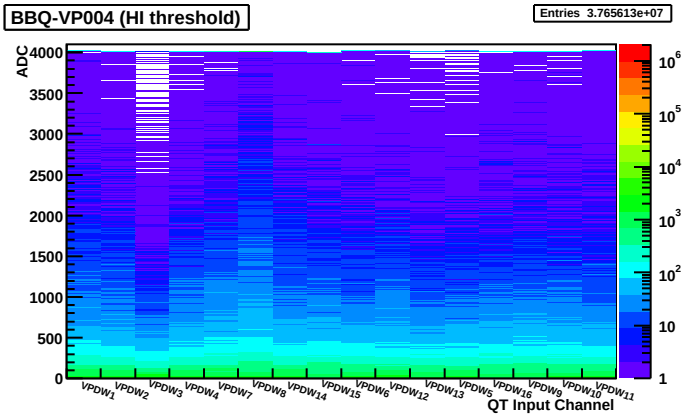

BBQ-VP001 (LO threshold)
Entries 3.765613e+07
TAC
QT Input Channel

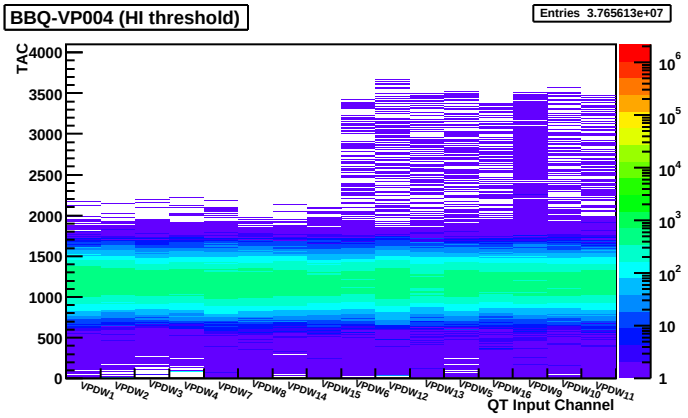

BBQ-VP002 (LO threshold)
Entries 3.765613e+07
ADC
QT Input Channel

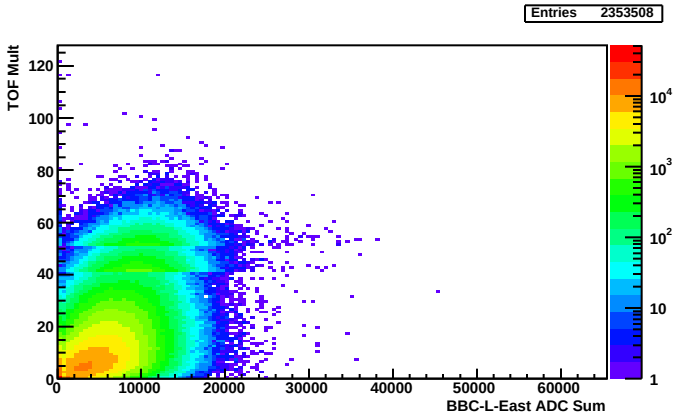

BBQ-VP002 (LO threshold)
Entries 3.765613e+07
TAC
QT Input Channel

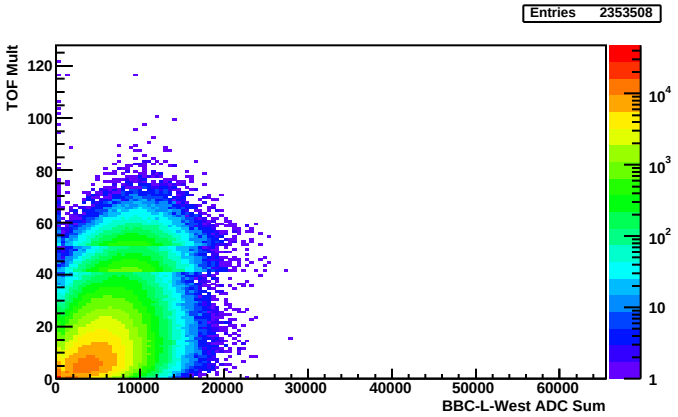

BBQ-VP003 (HI threshold)
Entries 3.765613e+07
ADC
QT Input Channel

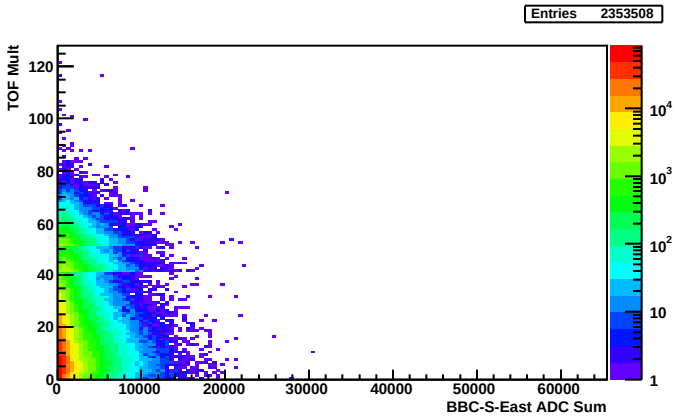

BBQ-VP003 (HI threshold)
Entries 3.765613e+07
TAC
QT Input Channel

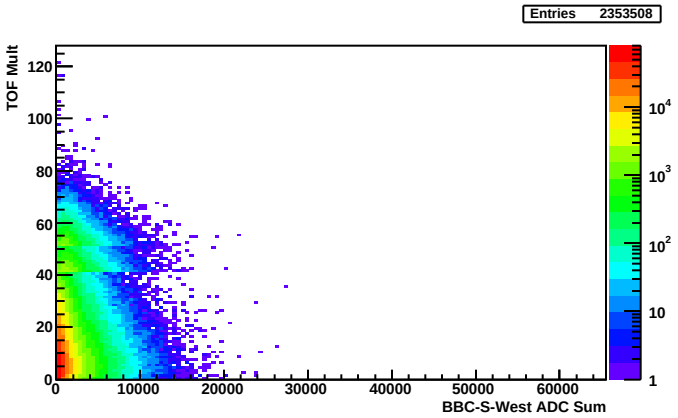

BBQ-VP004 (HI threshold)
Entries 3.765613e+07
ADC
QT Input Channel

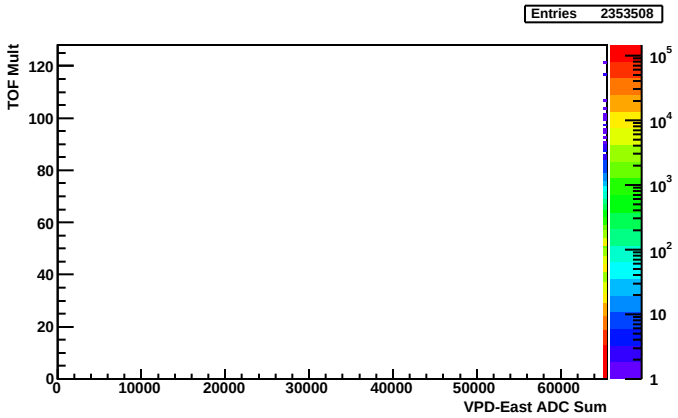

BBQ-VP004 (HI threshold)
Entries 3.765613e+07
TAC
QT Input Channel

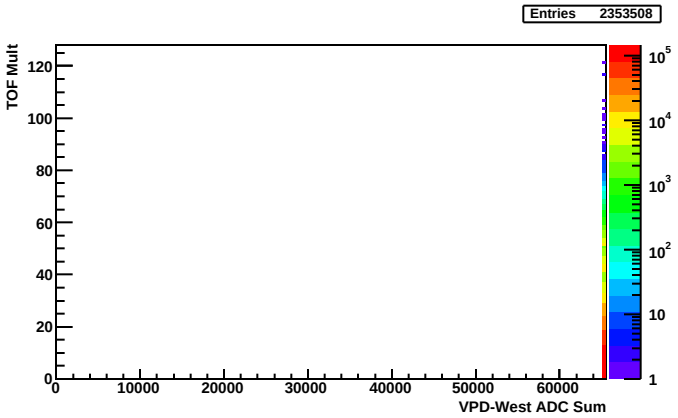

Entries 2353508
TOF Mult
BBC-L-East ADC Sum

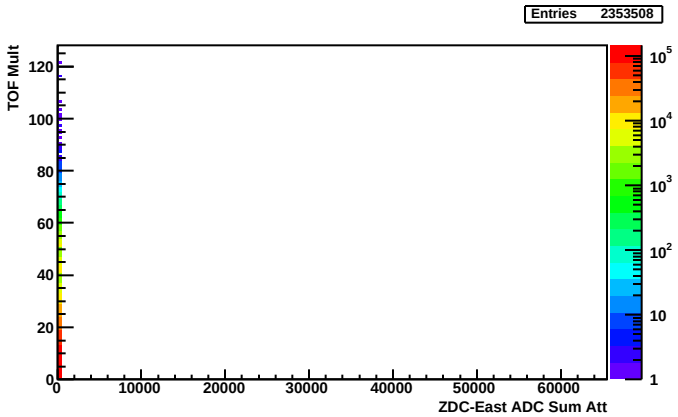

Entries 2353508
TOF Mult
BBC-L-West ADC Sum

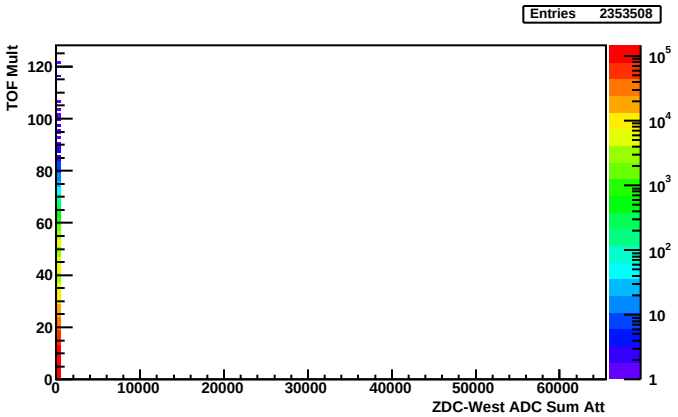

Entries 2353508
TOF Mult
BBC-S-East ADC Sum

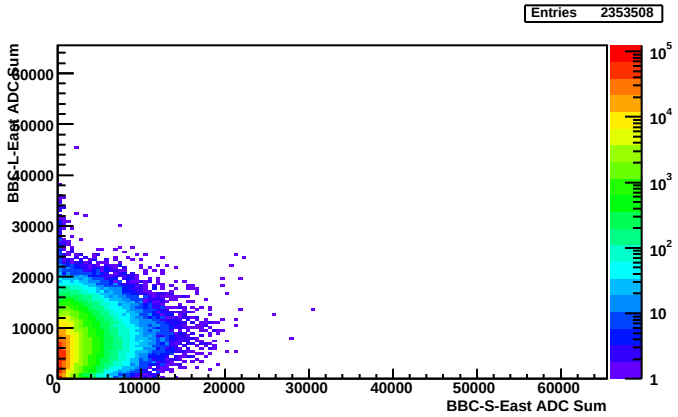

Entries 2353508
TOF Mult
BBC-S-West ADC Sum

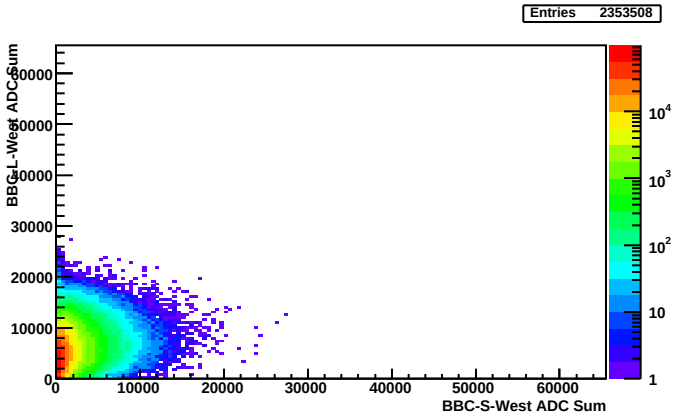

Entries 2353508
TOF Mult
VPD-East ADC Sum

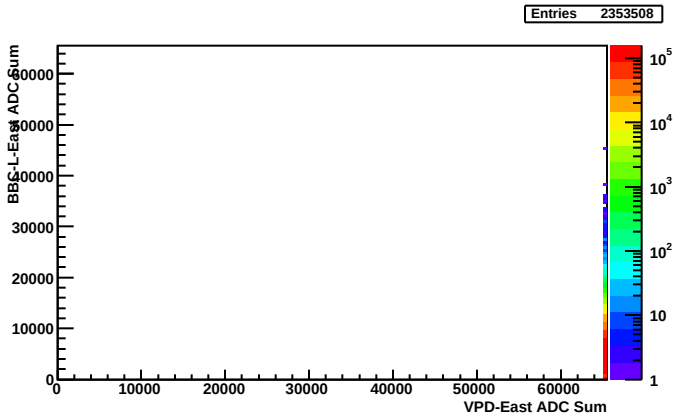

Entries 2353508
TOF Mult
VPD-West ADC Sum

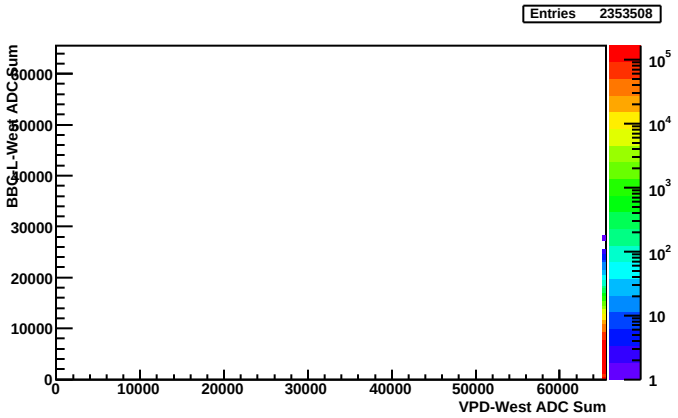

Entries 2353508
TOF Mult
ZDC-East ADC Sum Att

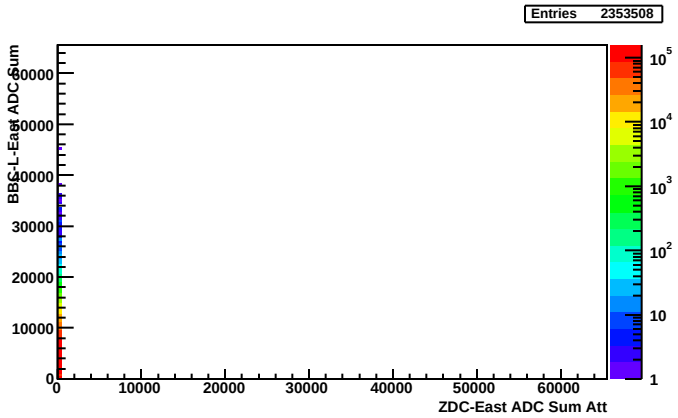

Entries 2353508
TOF Mult
ZDC-West ADC Sum Att

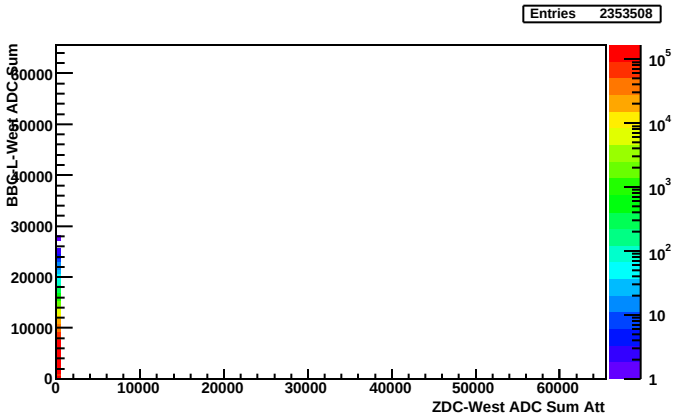

Entries 2353508
BBC-L-East ADC Sum
BBC-S-East ADC Sum

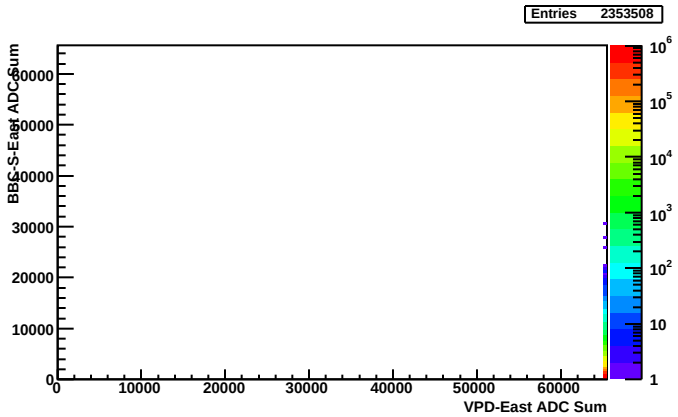

Entries 2353508
BBC-L-West ADC Sum
BBC-S-West ADC Sum

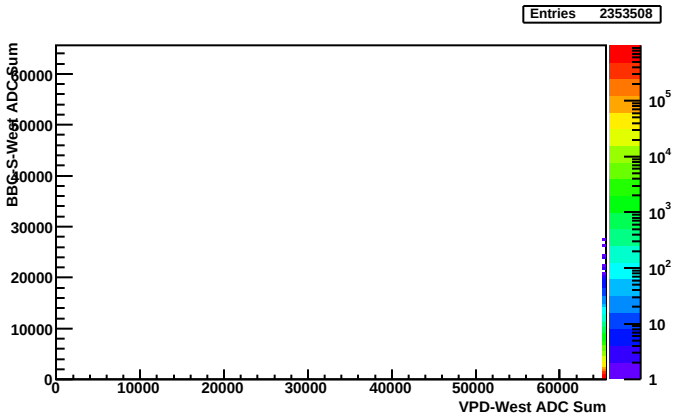

Entries 2353508
BBC-L-East ADC Sum
VPD-East ADC Sum

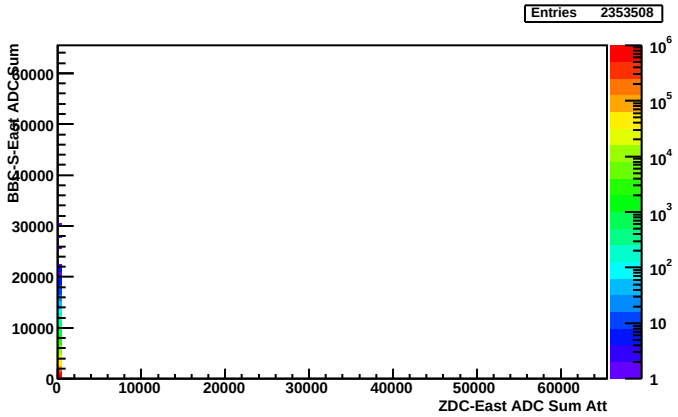

Entries 2353508
BBC-L-West ADC Sum
VPD-West ADC Sum

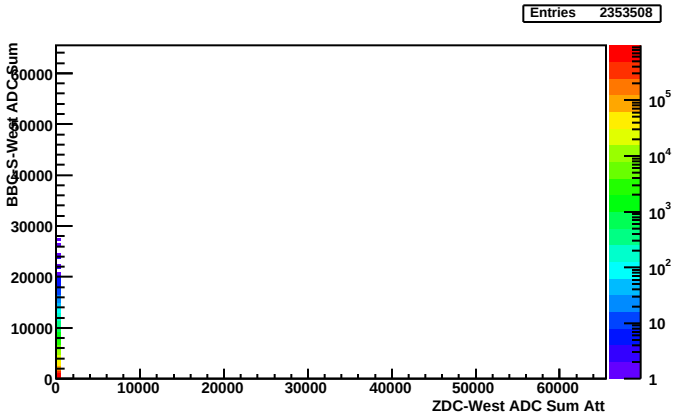

Entries 2353508
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

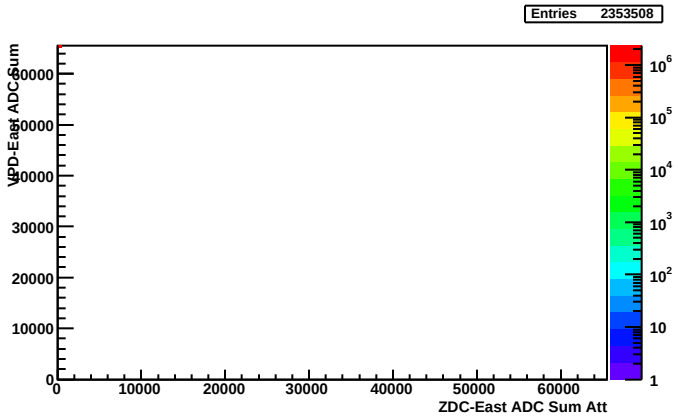

Entries 2353508
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

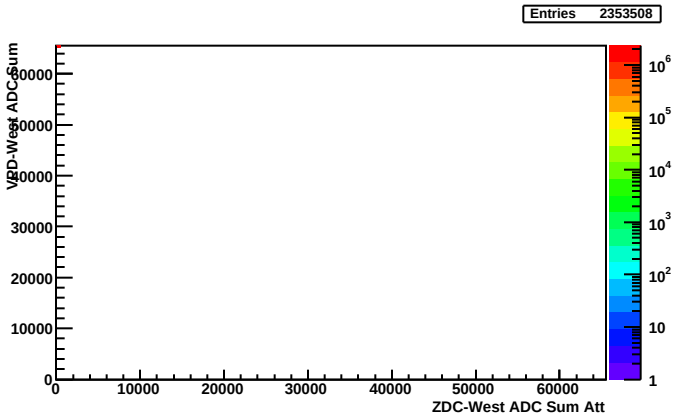

Entries 2353508
BBC-S-East ADC Sum
VPD-East ADC Sum

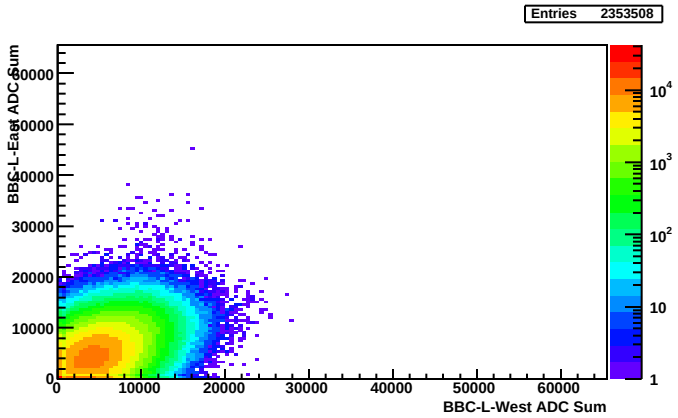

Entries 2353508
BBC-S-West ADC Sum
VPD-West ADC Sum

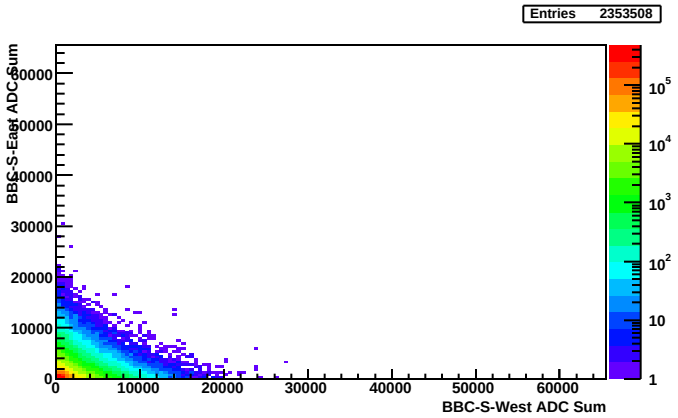

Entries 2353508
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

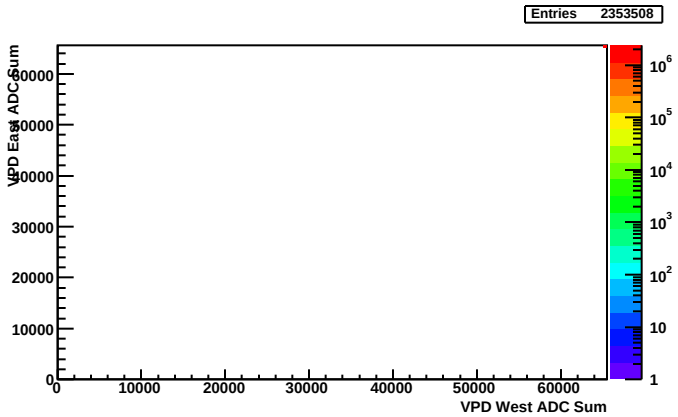

Entries 2353508
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

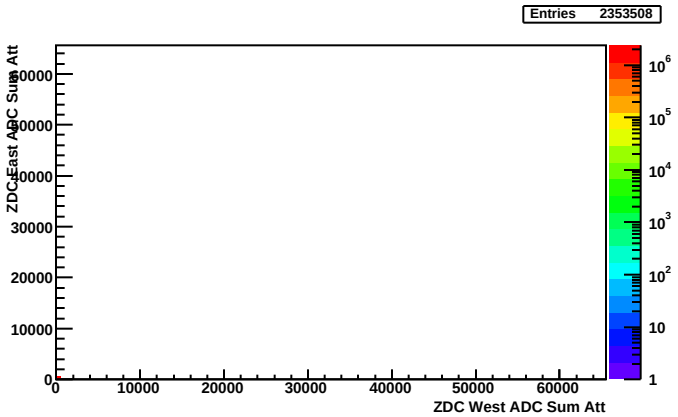

Entries 2353508
VPD-East ADC Sum
ZDC-East ADC Sum Att

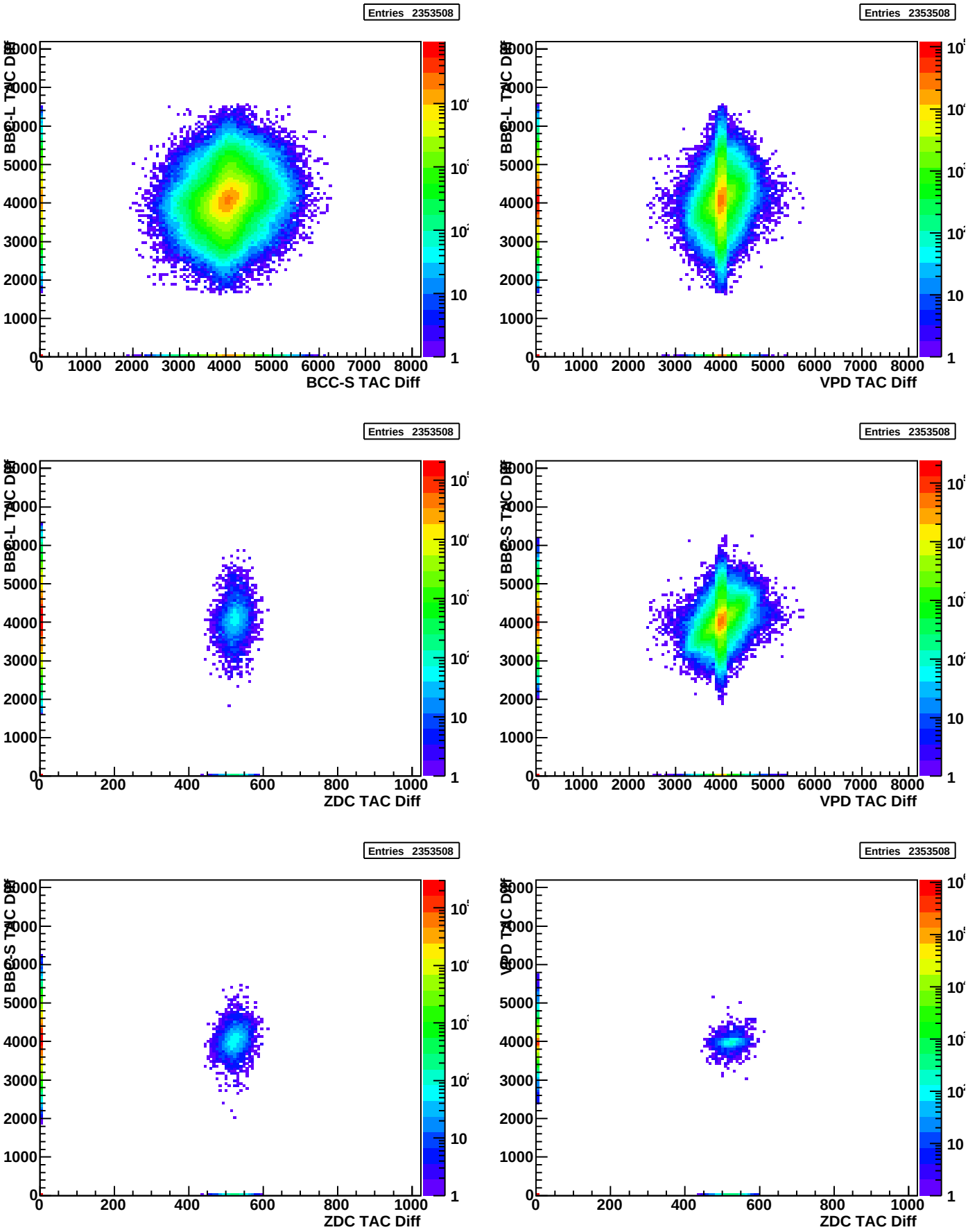

Entries 2353508
VPD-West ADC Sum
ZDC-West ADC Sum Att

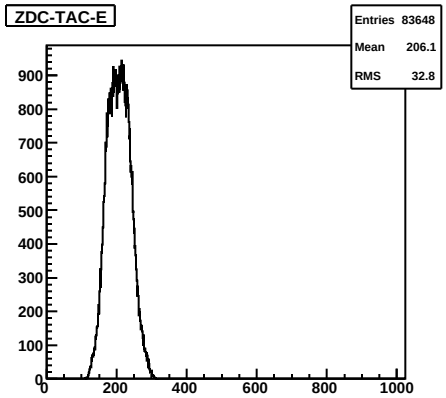

Entries 2353508
BBC-L-East ADC Sum
BBC-L-West ADC Sum

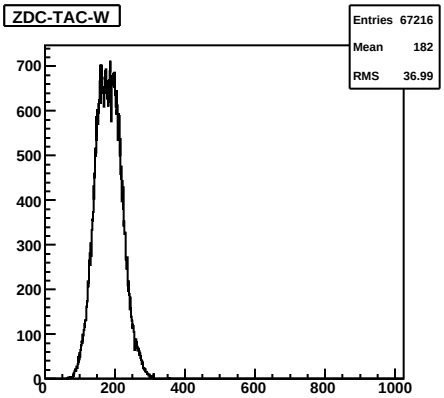

Entries 2353508
BBC-S-East ADC Sum
BBC-S-West ADC Sum

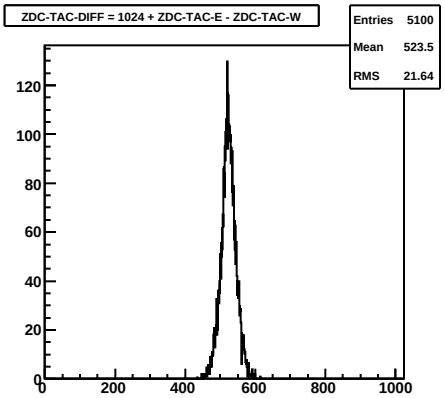

Entries 2353508
VPD East ADC Sum
VPD West ADC Sum

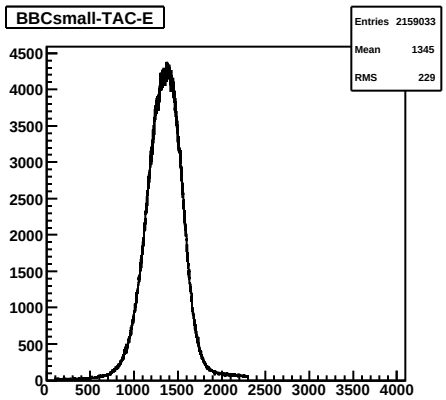

Entries 2353508
ZDC East ADC Sum Att
ZDC West ADC Sum Att

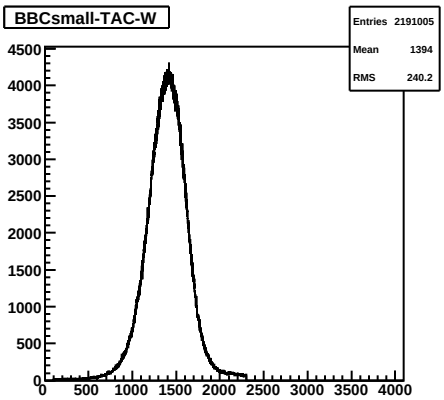

Entries 2353508
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2353508
BBC-L TAC Diff
VPD TAC Diff
Entries 2353508
BBC-L TAC Diff
ZDC TAC Diff
Entries 2353508
BBC-S TAC Diff
VPD TAC Diff
Entries 2353508
BBC-S TAC Diff
ZDC TAC Diff
Entries 2353508
VPD TAC Diff
ZDC TAC Diff

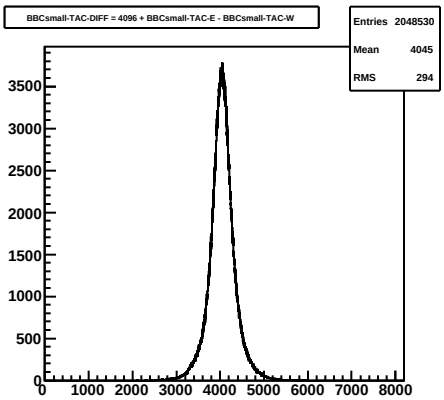

ZDC-TAC-E
Entries 83648
Mean 206.1
RMS 32.8

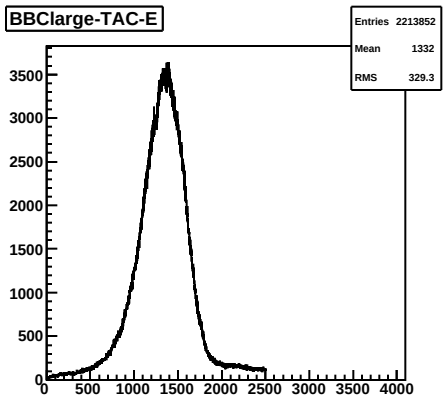

ZDC-TAC-W
Entries 67216
Mean 182
RMS 36.99

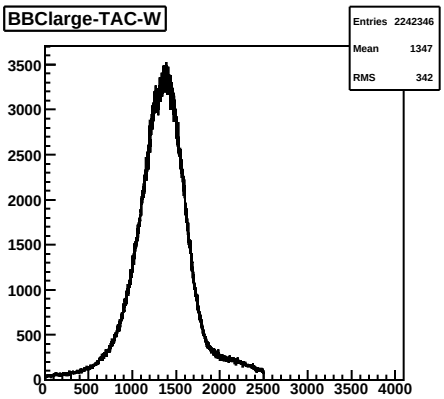

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5100
Mean 523.5
RMS 21.64

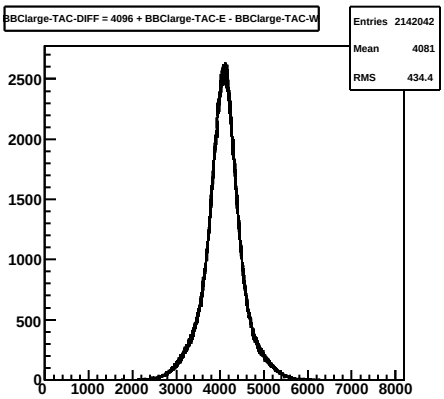

BBCsmall-TAC-E
Entries 2159033
Mean 1345
RMS 229

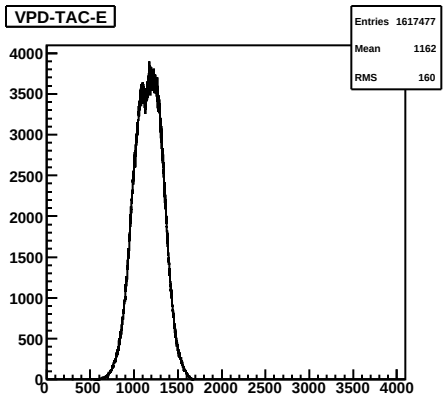

BBCsmall-TAC-W
Entries 2191005
Mean 1394
RMS 240.2

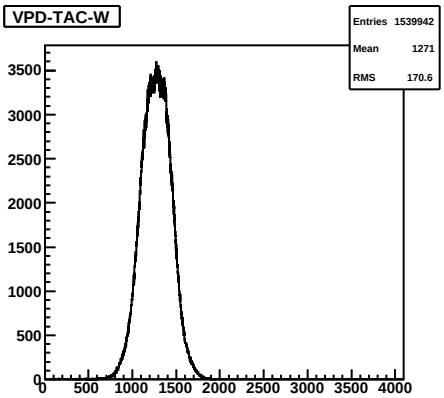

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2048530
Mean 4045
RMS 294

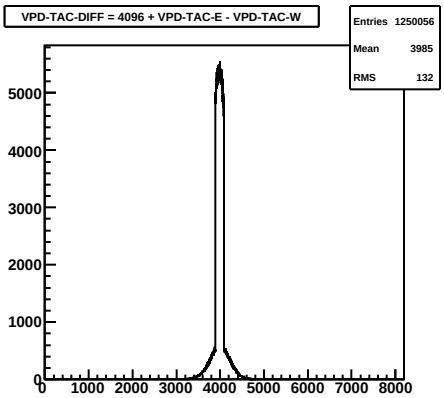

BBClarge-TAC-E
Entries 2213852
Mean 1332
RMS 329.3

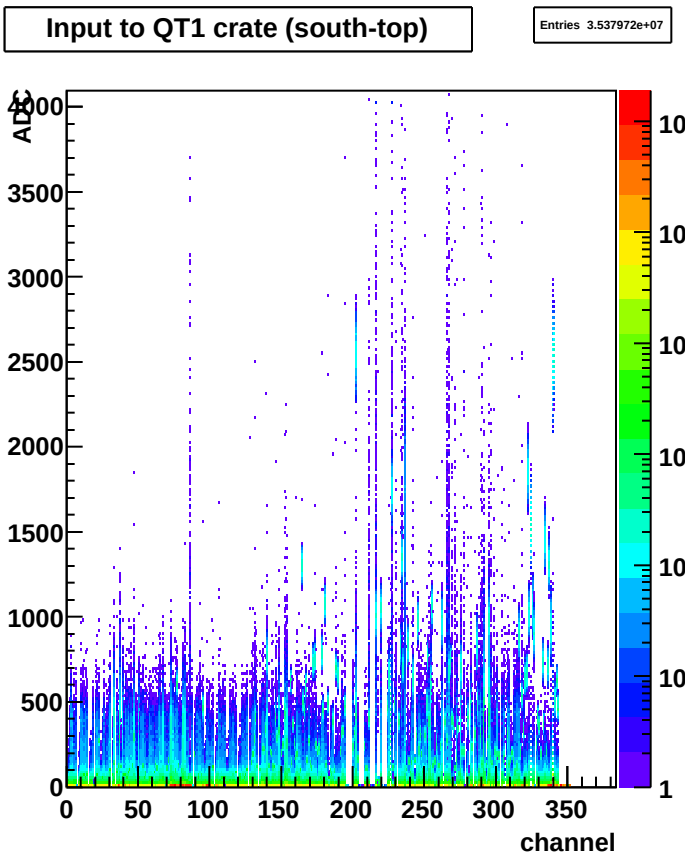

BBClarge-TAC-W
Entries 2242346
Mean 1347
RMS 342

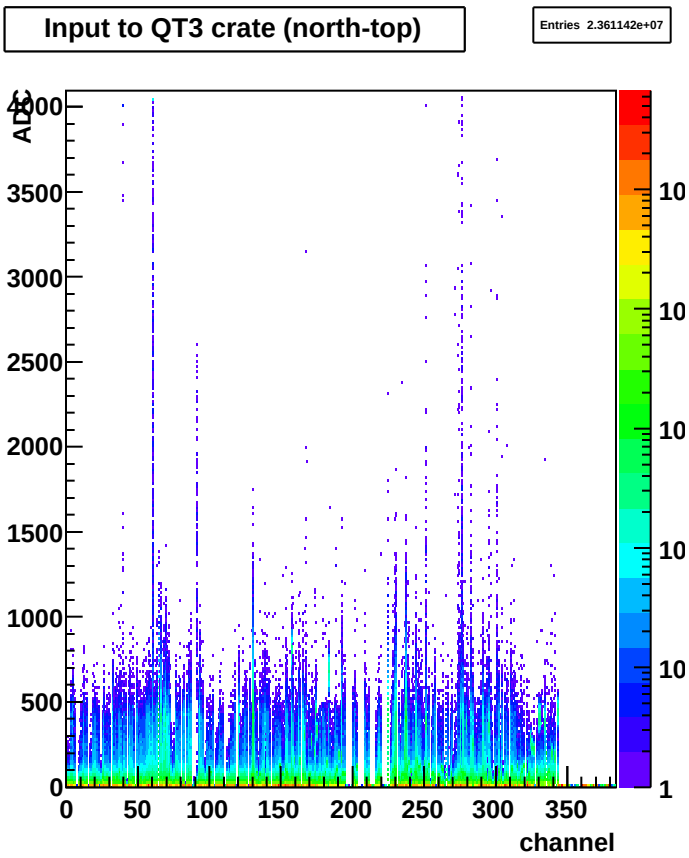

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2142042
Mean 4081
RMS 434.4

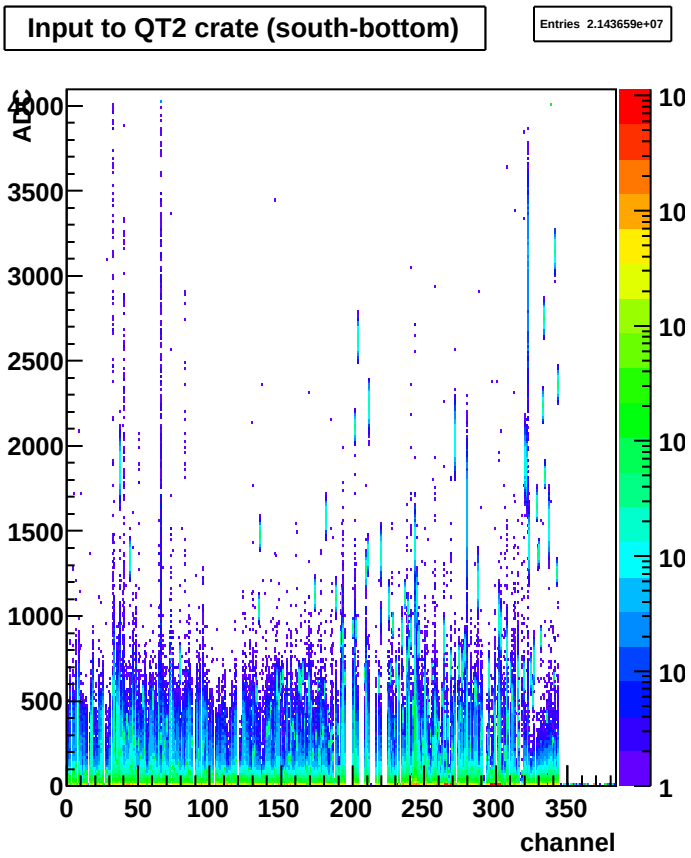

VPD-TAC-E
Entries 1617477
Mean 1162
RMS 160

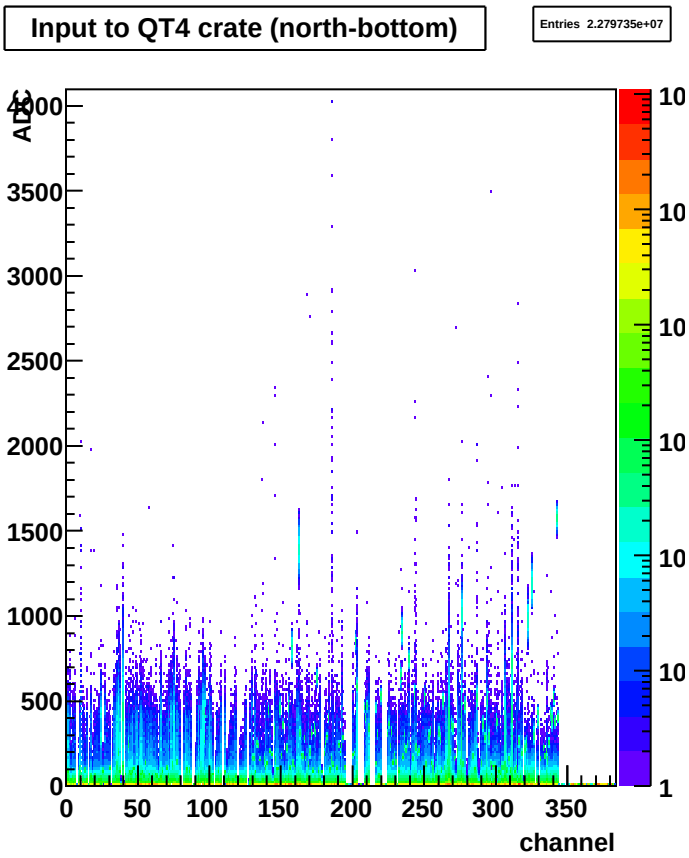

VPD-TAC-W
Entries 1539942
Mean 1271
RMS 170.6

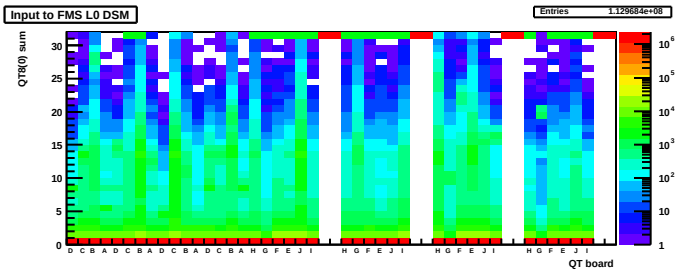

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1250056
Mean 3985
RMS 132

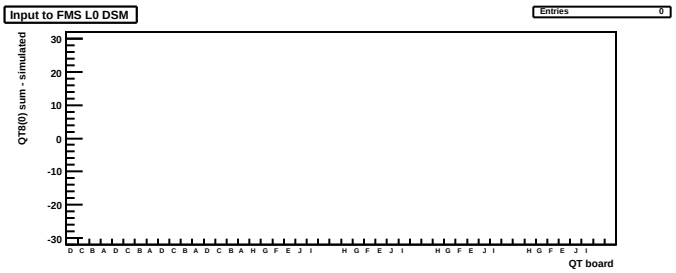

Input to QT1 crate (south-top)
Entries 3.537972e+07
ADC
channel

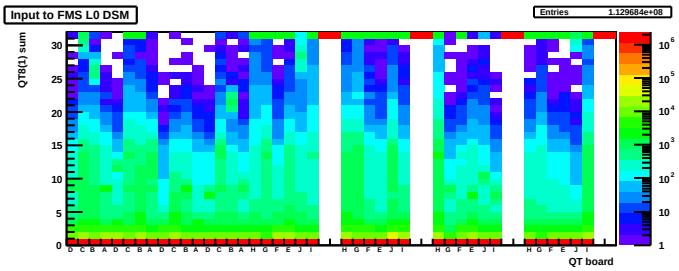

Input to QT3 crate (north-top)
Entries 2.361142e+07
ADC
channel

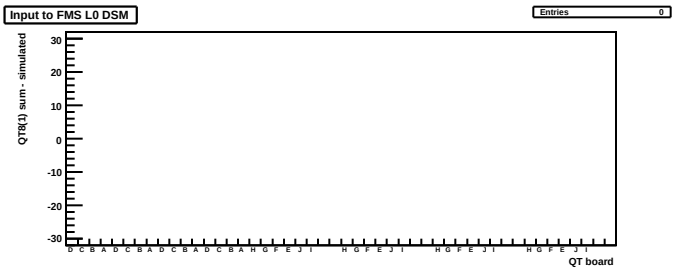

Input to QT2 crate (south-bottom)
Entries 2.143659e+07
ADC
channel

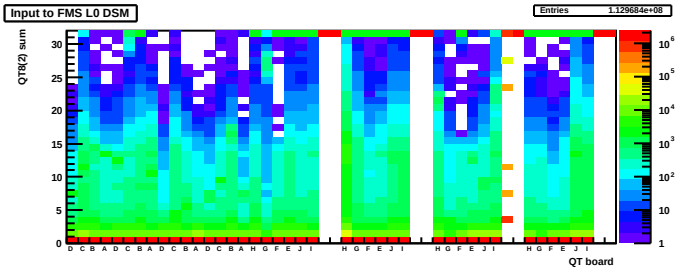

Input to QT4 crate (north-bottom)
Entries 2.279735e+07
ADC
channel

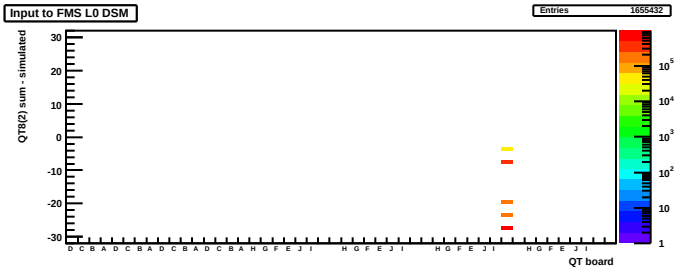

Input to FMS L0 DSM
Entries 1.129684e+08
QT8(0) sum
QT board

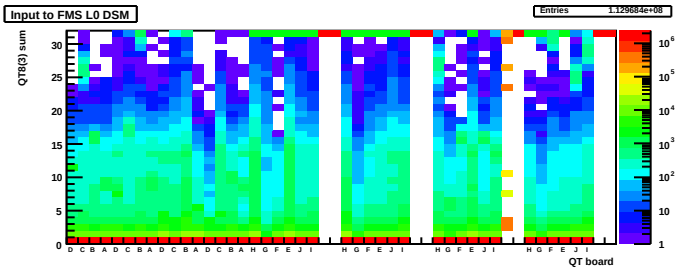

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

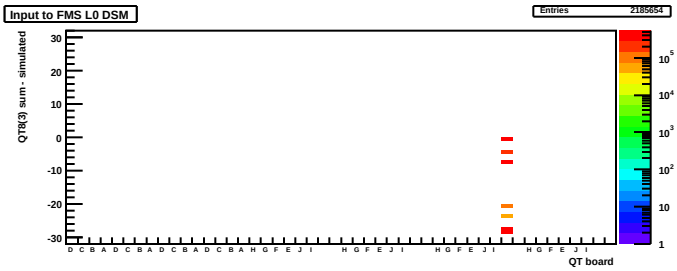

Input to FMS L0 DSM
Entries 1.129684e+08
QT8(1) sum
QT board

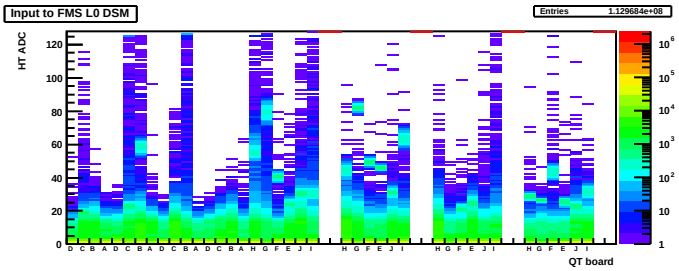

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

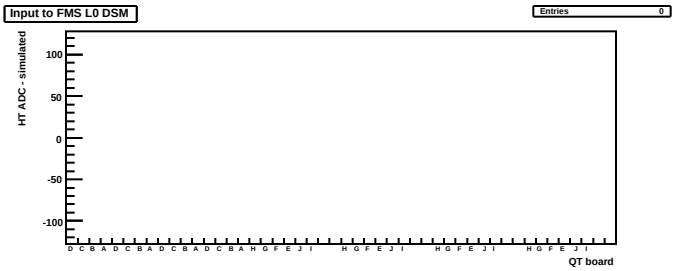

Input to FMS L0 DSM
Entries 1.129684e+08
QT8(2) sum
QT board

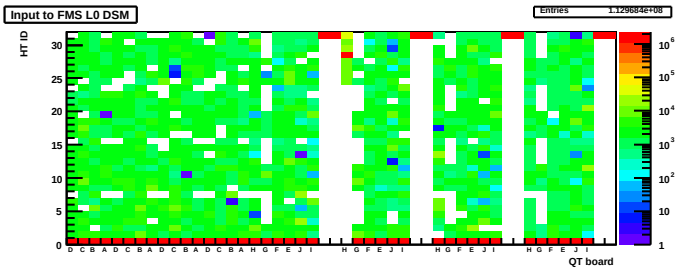

Input to FMS L0 DSM
Entries 1655432
QT8(2) sum - simulated
QT board

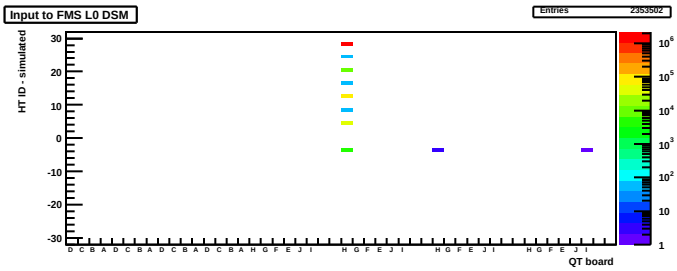

Input to FMS L0 DSM
Entries 1.129684e+08
QT8(3) sum
QT board

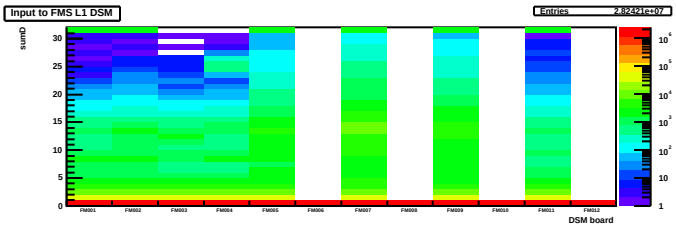

Input to FMS L0 DSM
Entries 2185654
QT8(3) sum - simulated
QT board

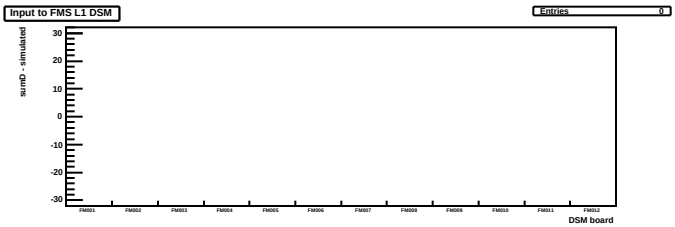

Input to FMS L0 DSM
Entries 1.129684e+08
HT ADC
QT board

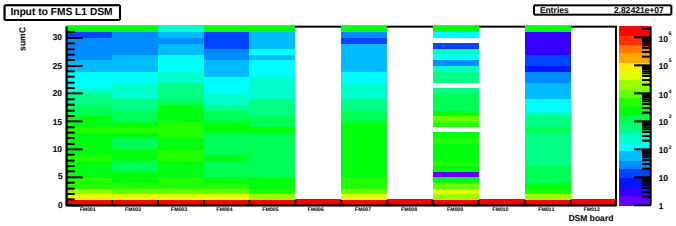

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

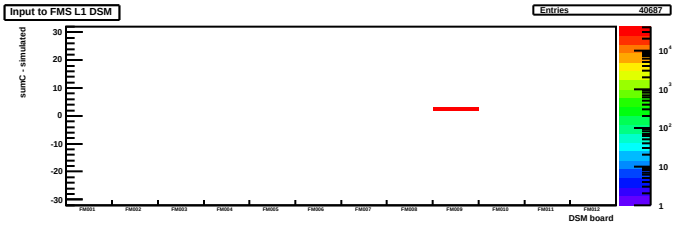

Input to FMS L0 DSM
Entries 1.129684e+08
HT ID
QT board

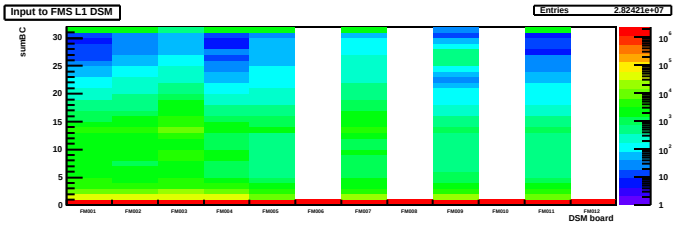

Input to FMS L0 DSM
Entries 2353502
HT ID - simulated
QT board

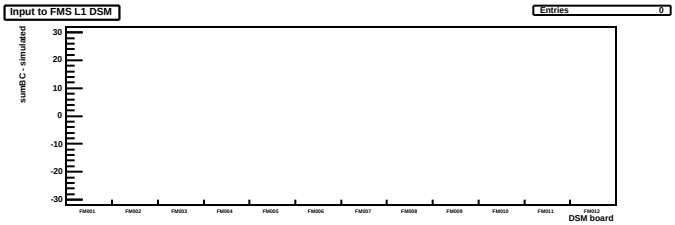

Input to FMS L1 DSM
Entries 2.82421e+07
sumD
DSM board

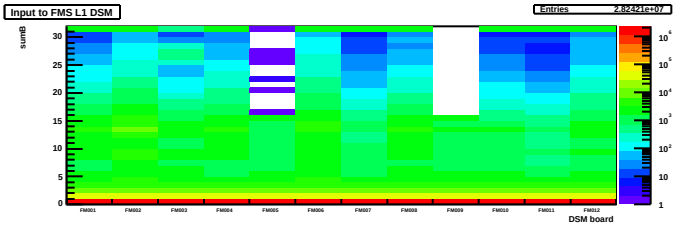

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

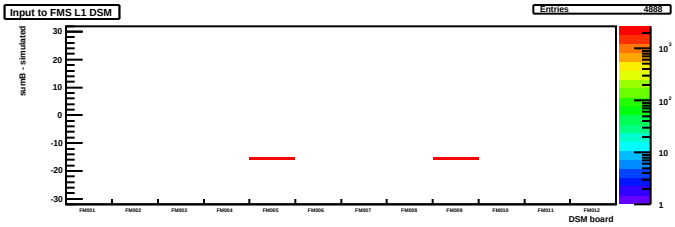

Input to FMS L1 DSM
Entries 2.82421e+07
sumC
DSM board

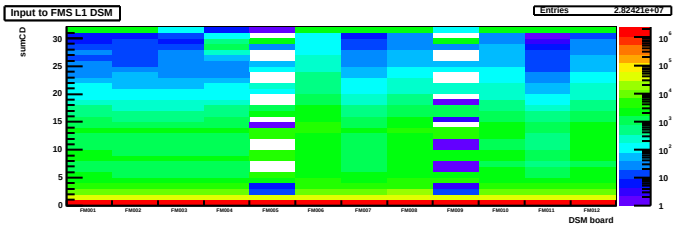

Input to FMS L1 DSM
Entries 40687
sumC - simulated
DSM board

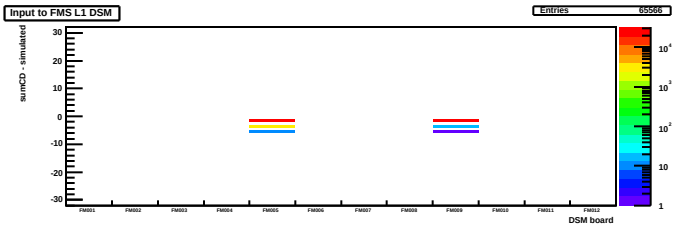

Input to FMS L1 DSM
Entries 2.82421e+07
sumBC
DSM board

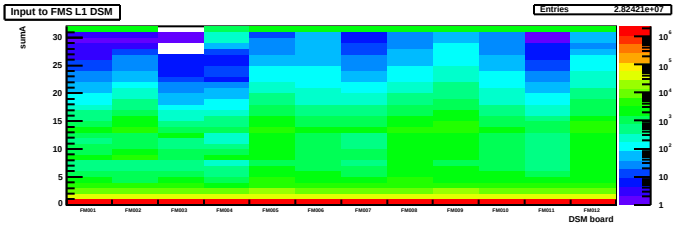

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

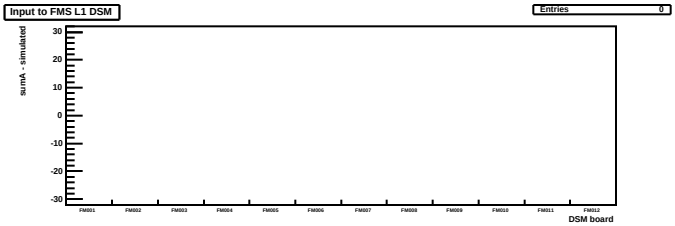

Input to FMS L1 DSM
Entries 2.82421e+07
sumB
DSM board

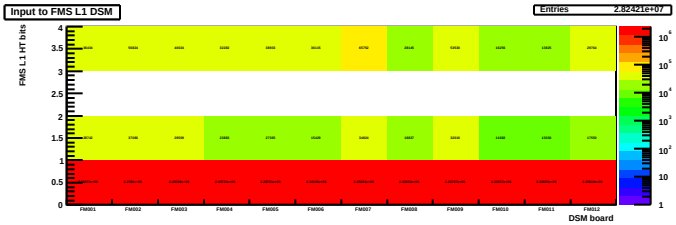

Input to FMS L1 DSM
Entries 4888
sumB - simulated
DSM board

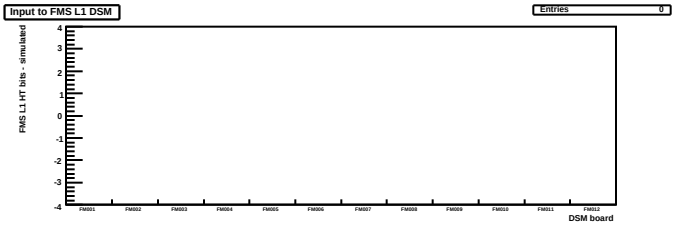

Input to FMS L1 DSM
Entries 2.82421e+07
sumCD
DSM board

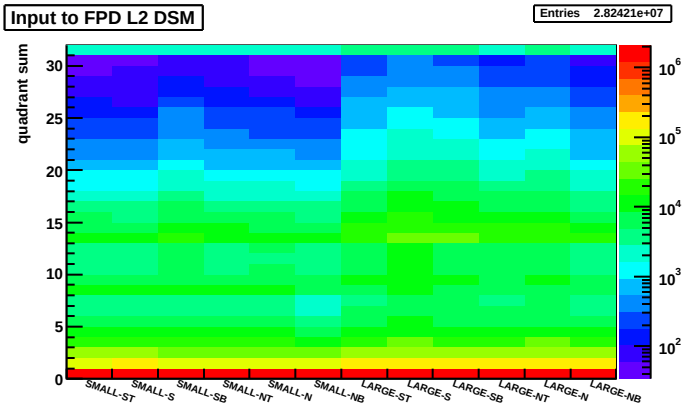

Input to FMS L1 DSM
Entries 65566
sumCD - simulated
DSM board

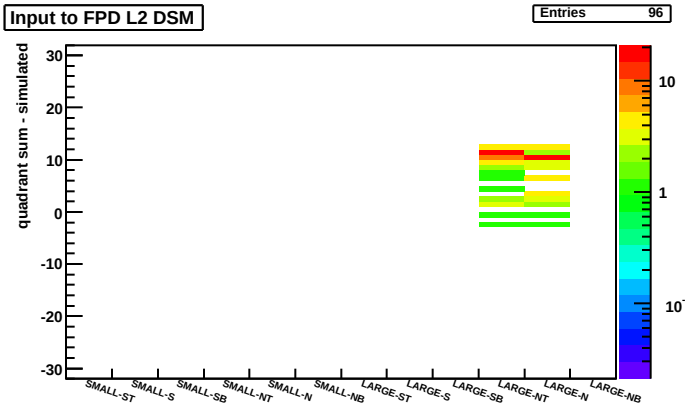

Input to FMS L1 DSM
Entries 2.82421e+07
sumA
DSM board

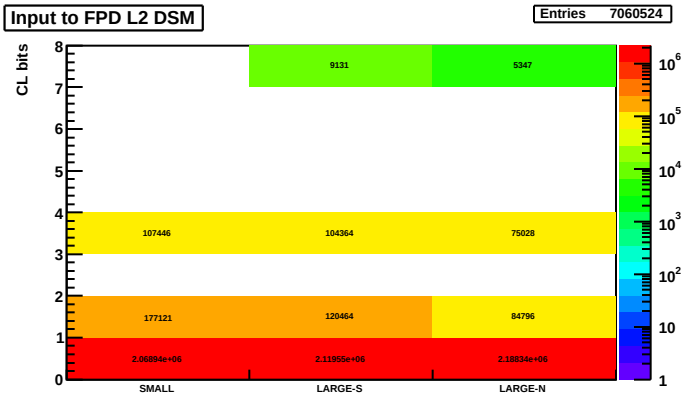

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

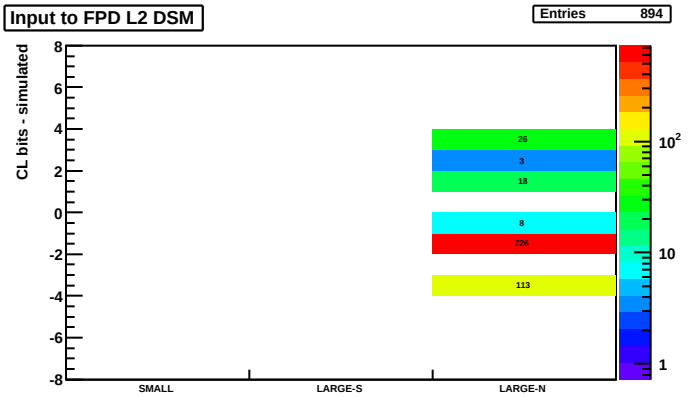

Input to FMS L1 DSM
Entries 2.82421e+07
FMS L1 HT bits
DSM board

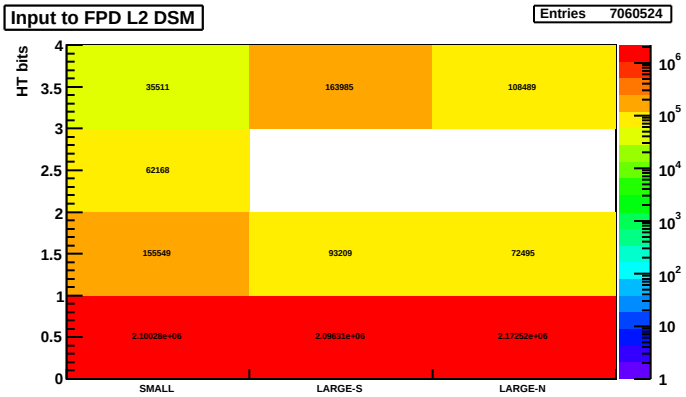

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

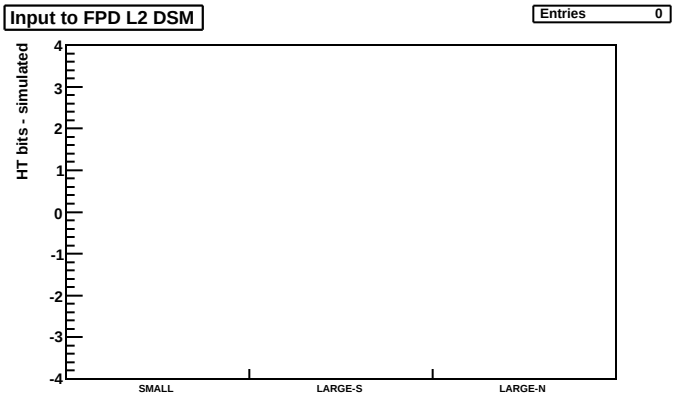

Input to FPD L2 DSM
Entries 2.82421e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

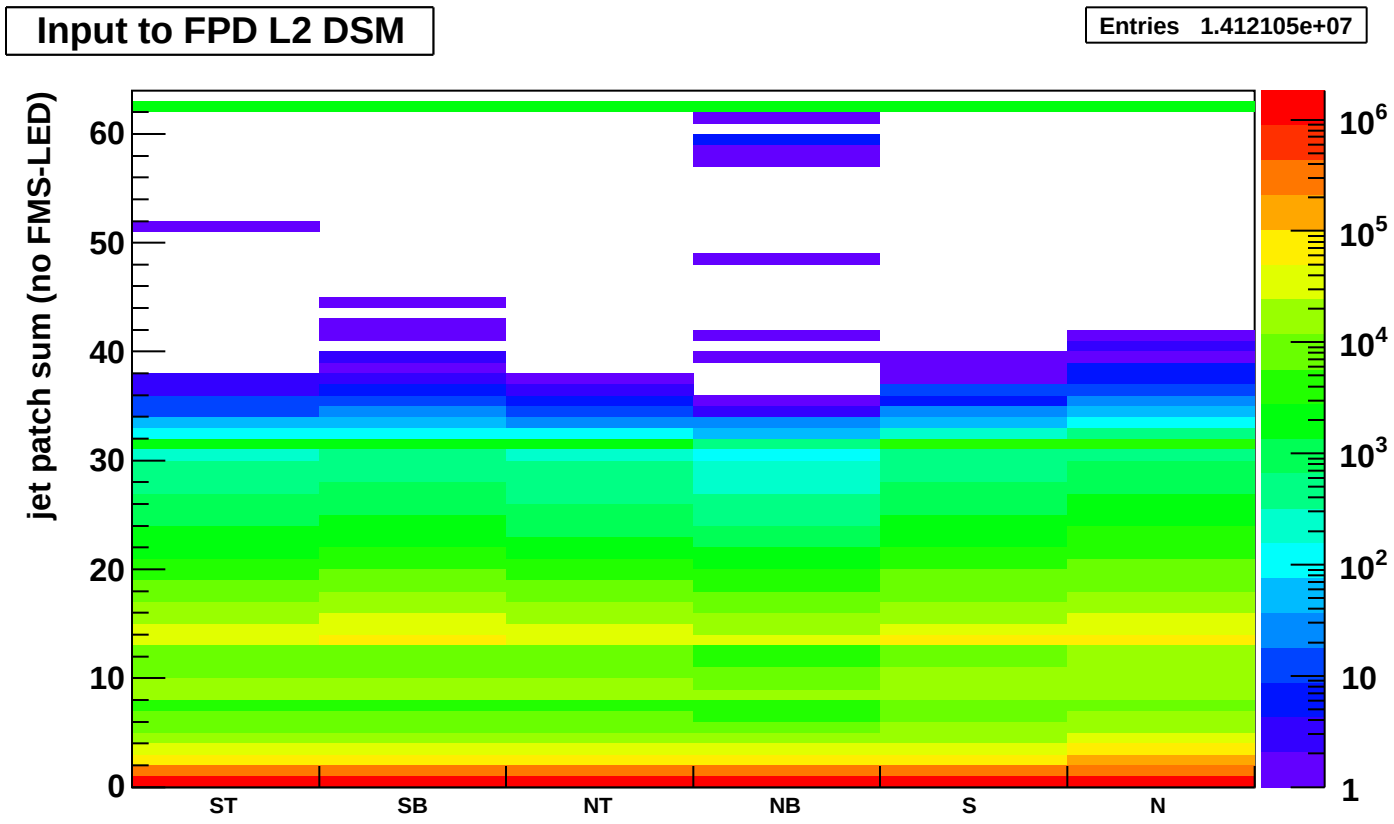

Input to FPD L2 DSM
Entries 96
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

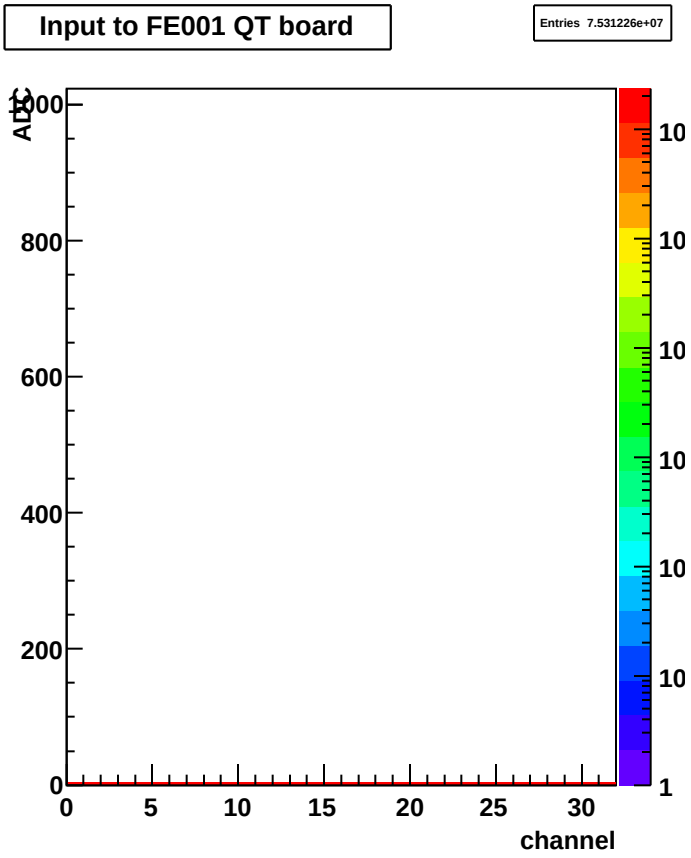

Input to FPD L2 DSM
Entries 7060524
CL bits
9131
5347
107446
104364
75028
177121
120464
84796
2.06894e+06
2.11955e+06
2.18834e+06
SMALL
LARGE-S
LARGE-N

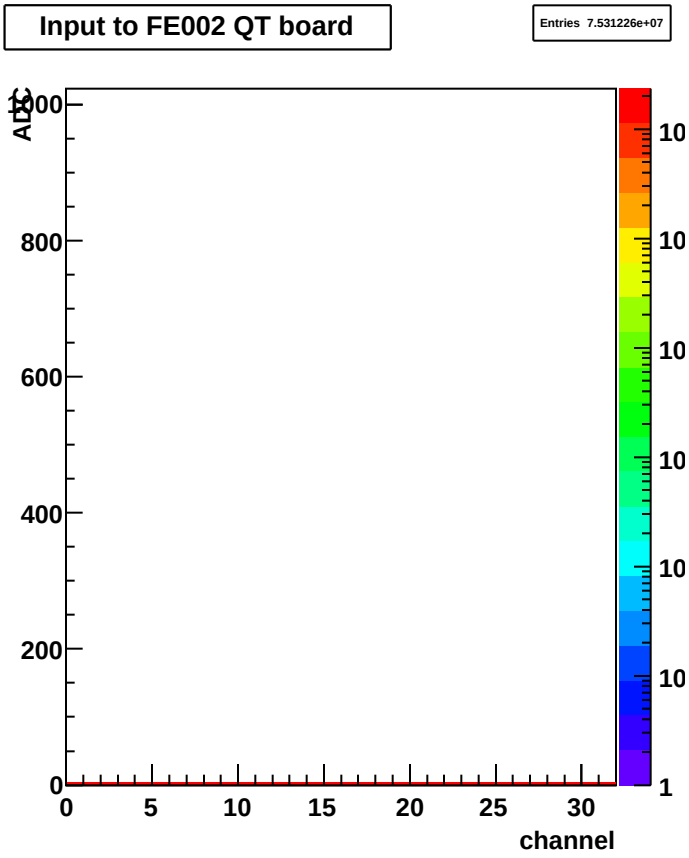

Input to FPD L2 DSM
Entries 894
CL bits - simulated
26
3
18
8
726
113
SMALL
LARGE-S
LARGE-N

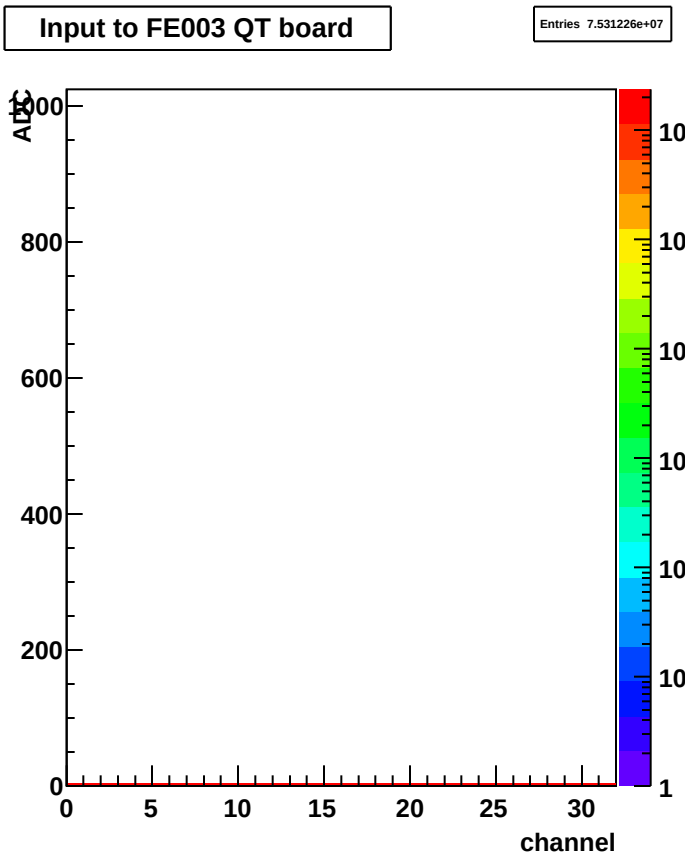

Input to FPD L2 DSM
Entries 7060524
HT bits
35511
163985
108489
62168
155549
93209
72495
2.10028e+06
2.09631e+06
2.17252e+06
SMALL
LARGE-S
LARGE-N

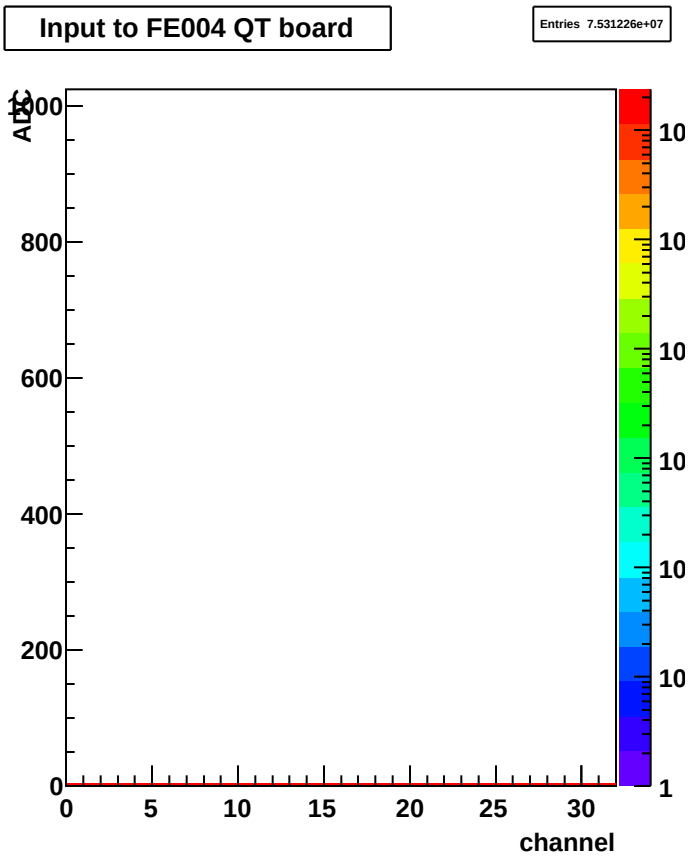

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

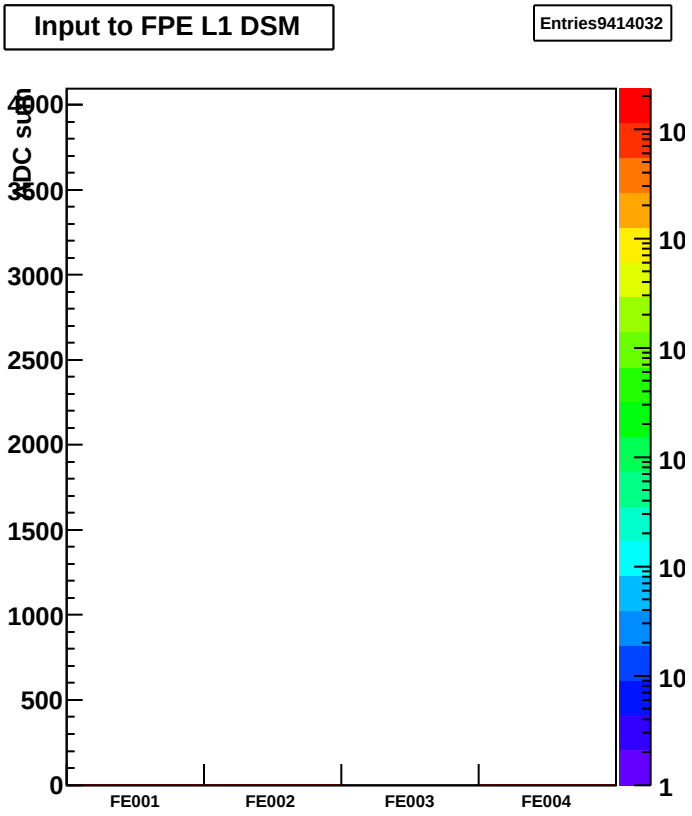

Input to FPD L2 DSM
Entries 1.412105e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 7.531226e+07
ADC
channel

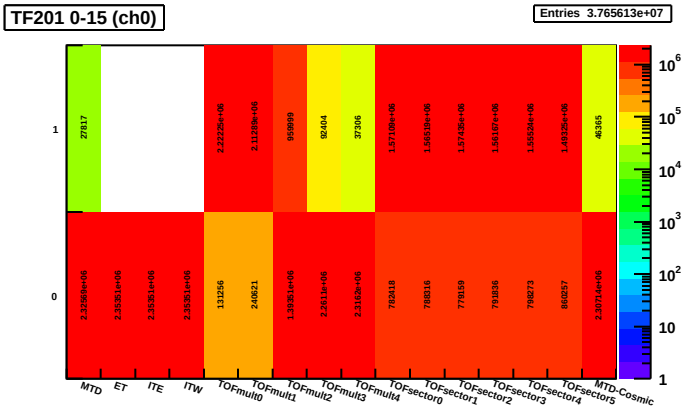

Input to FE002 QT board
Entries 7.531226e+07
ADC
channel


Input to FE003 QT board
Entries 7.531226e+07
ADC
channel

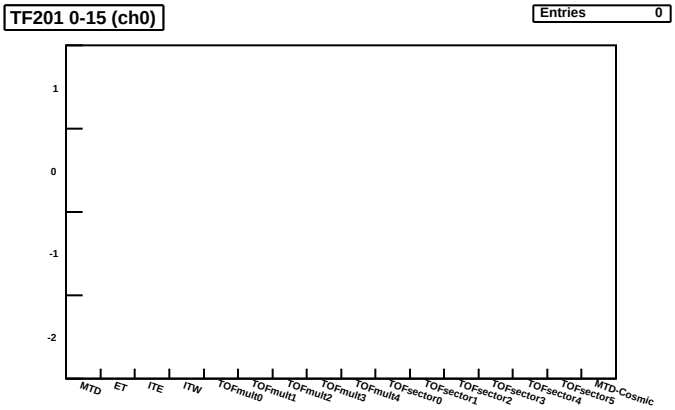

Input to FE004 QT board
Entries 7.531226e+07
ADC
channel


Input to FPE L1 DSM
Entries9414032
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

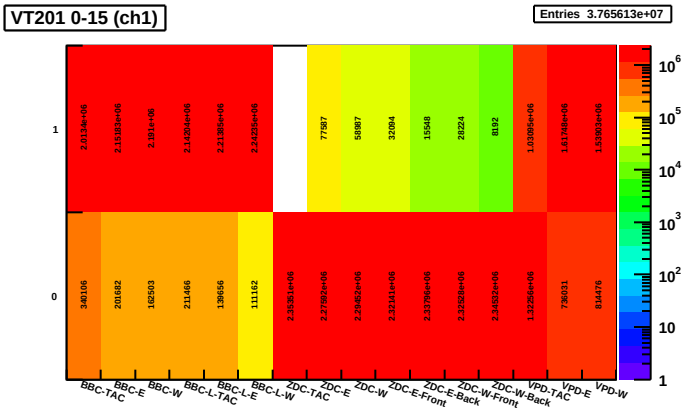
TF201 0-15 (ch0)



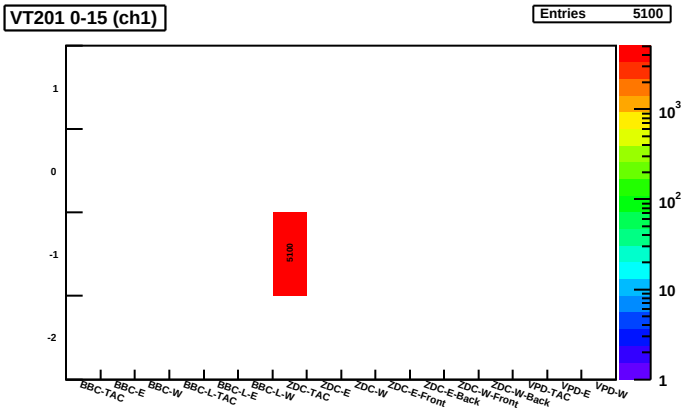
TF201 0-15 (ch0)



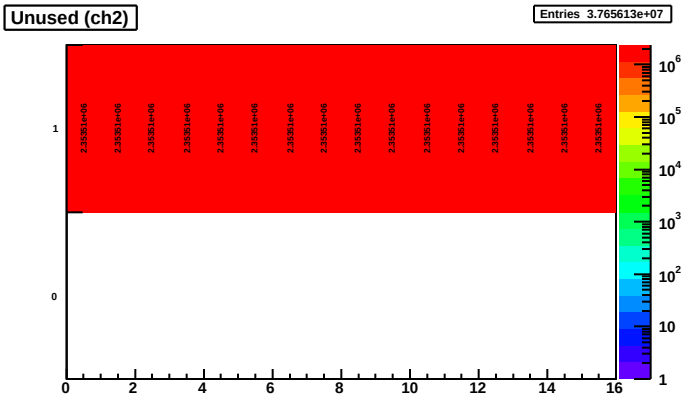
VT201 0-15 (ch1)

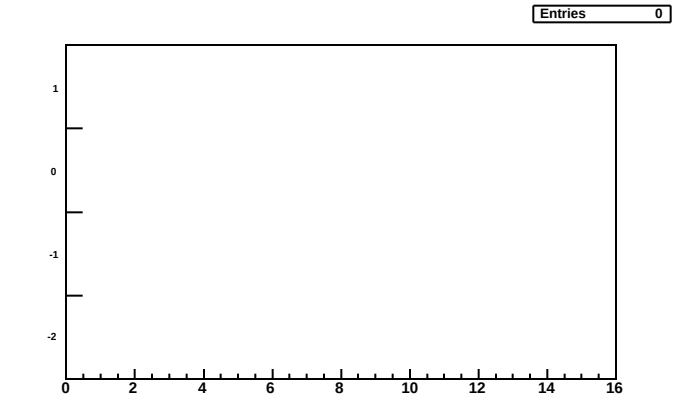


VT201 0-15 (ch1)



Unused (ch2)





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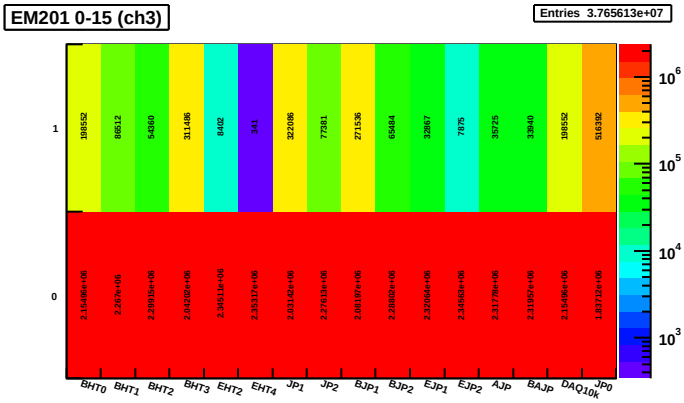

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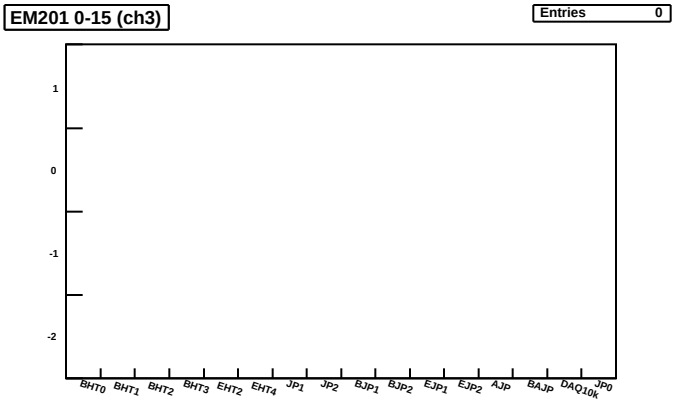
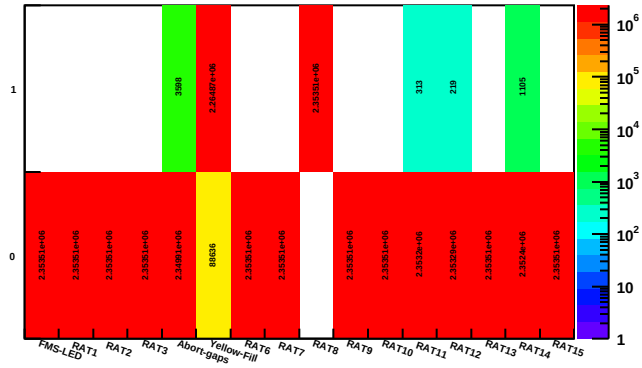
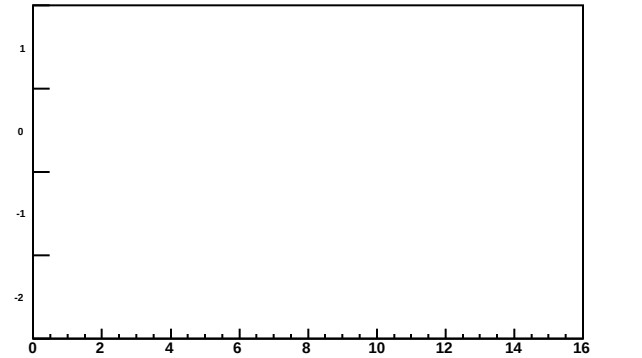
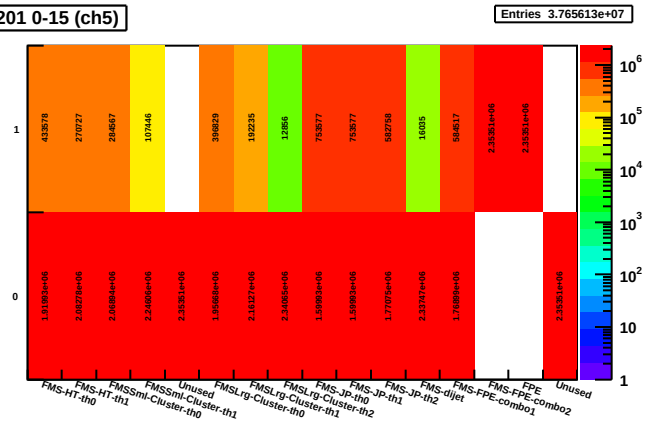


RAT board (ch4)

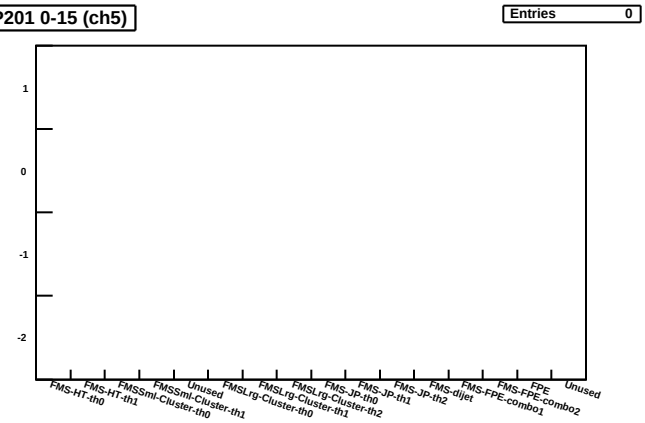




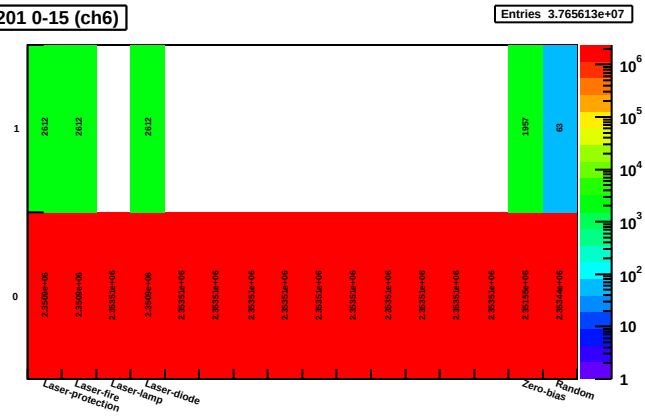
FP201 0-15 (ch5)

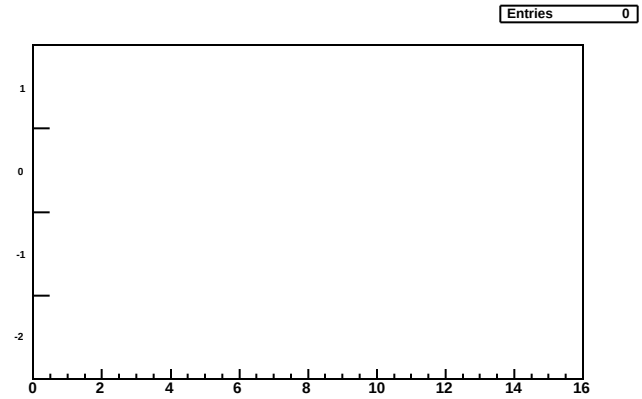
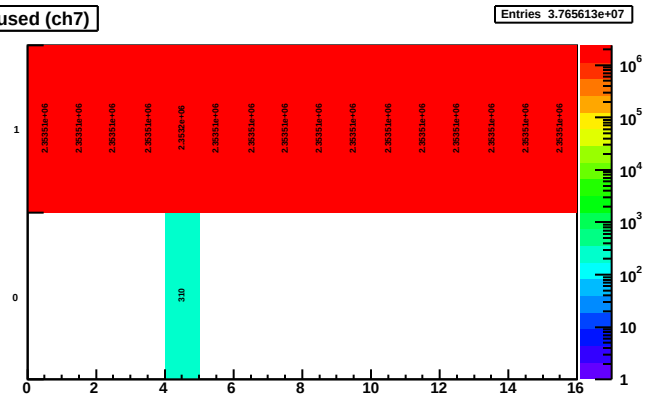
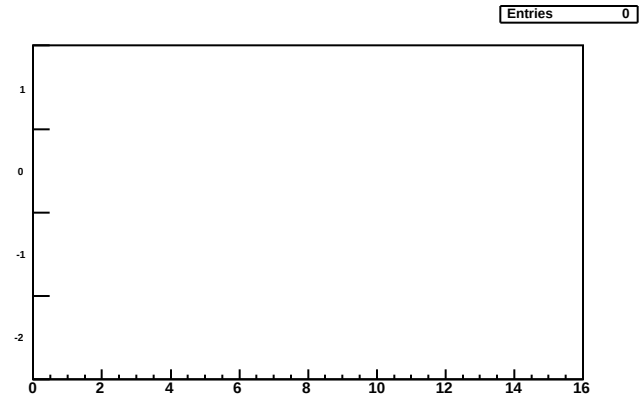


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)