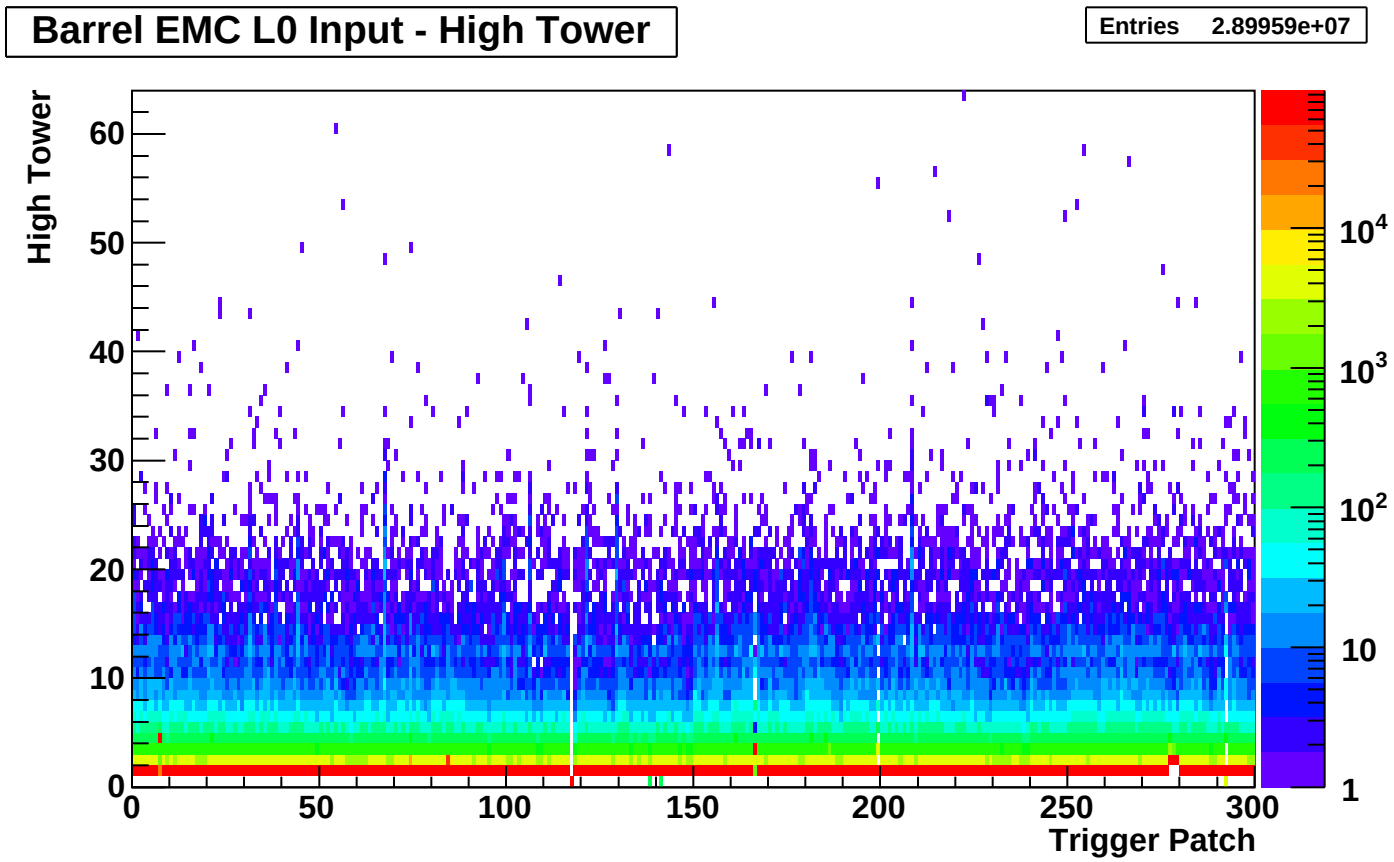
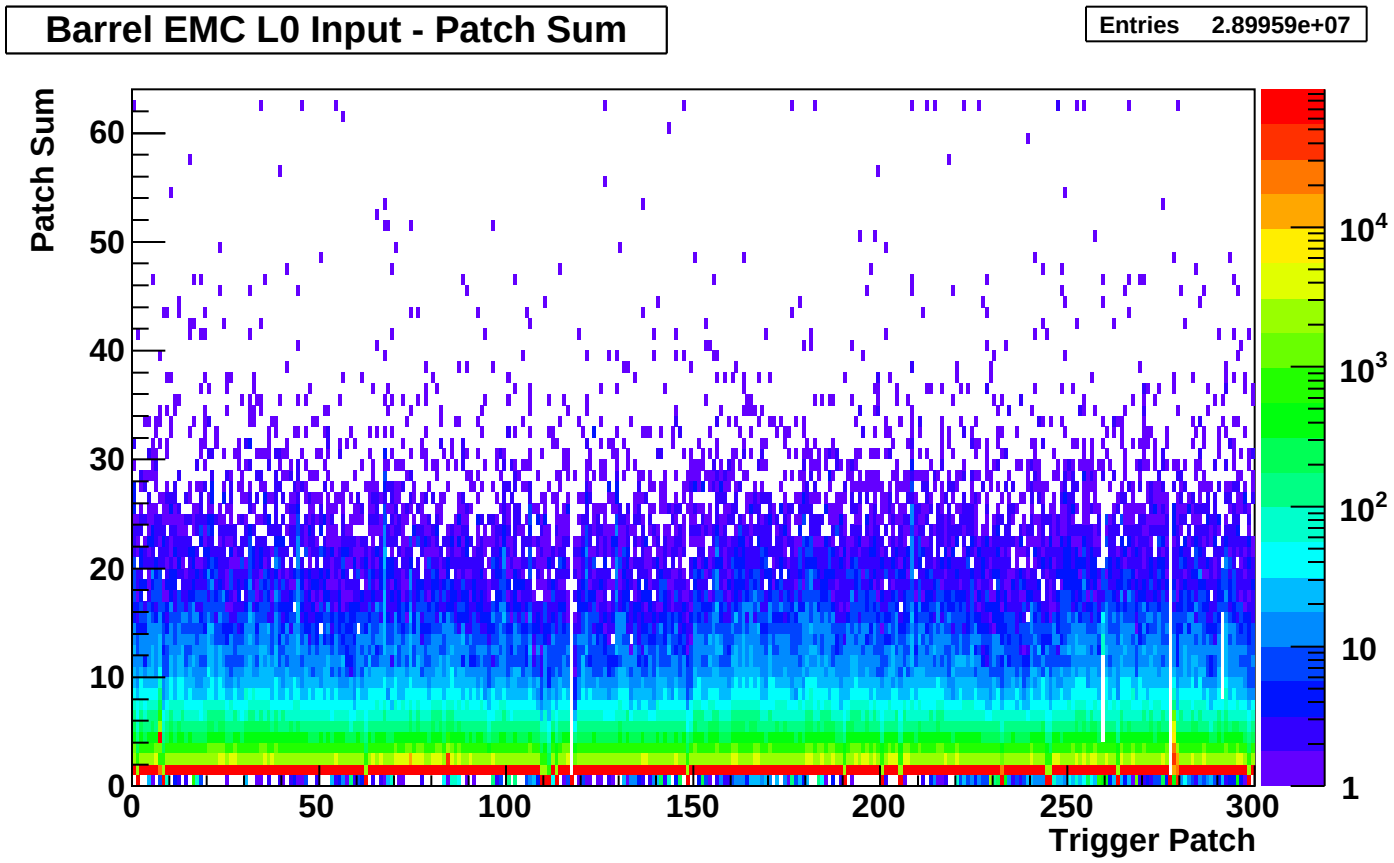
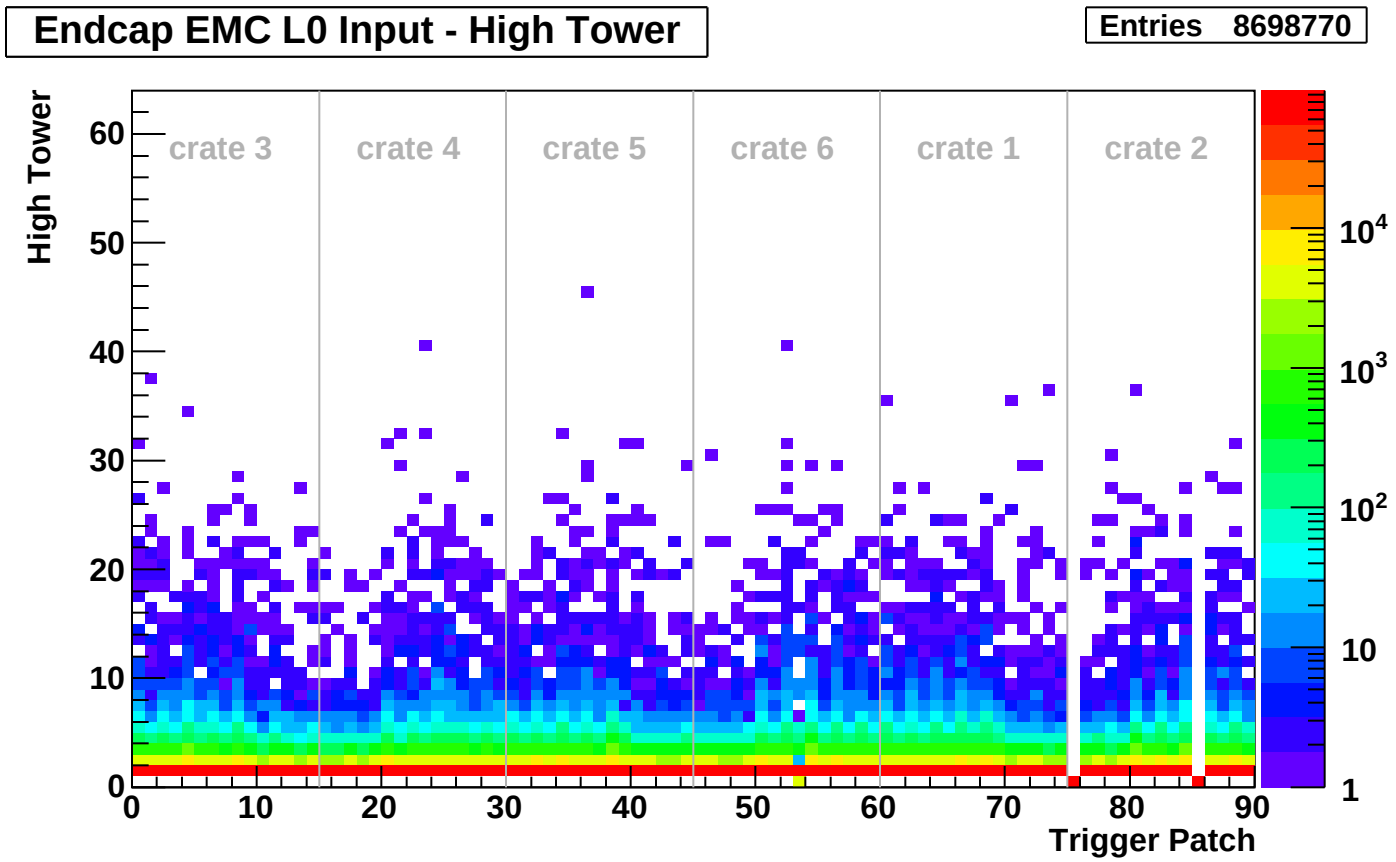
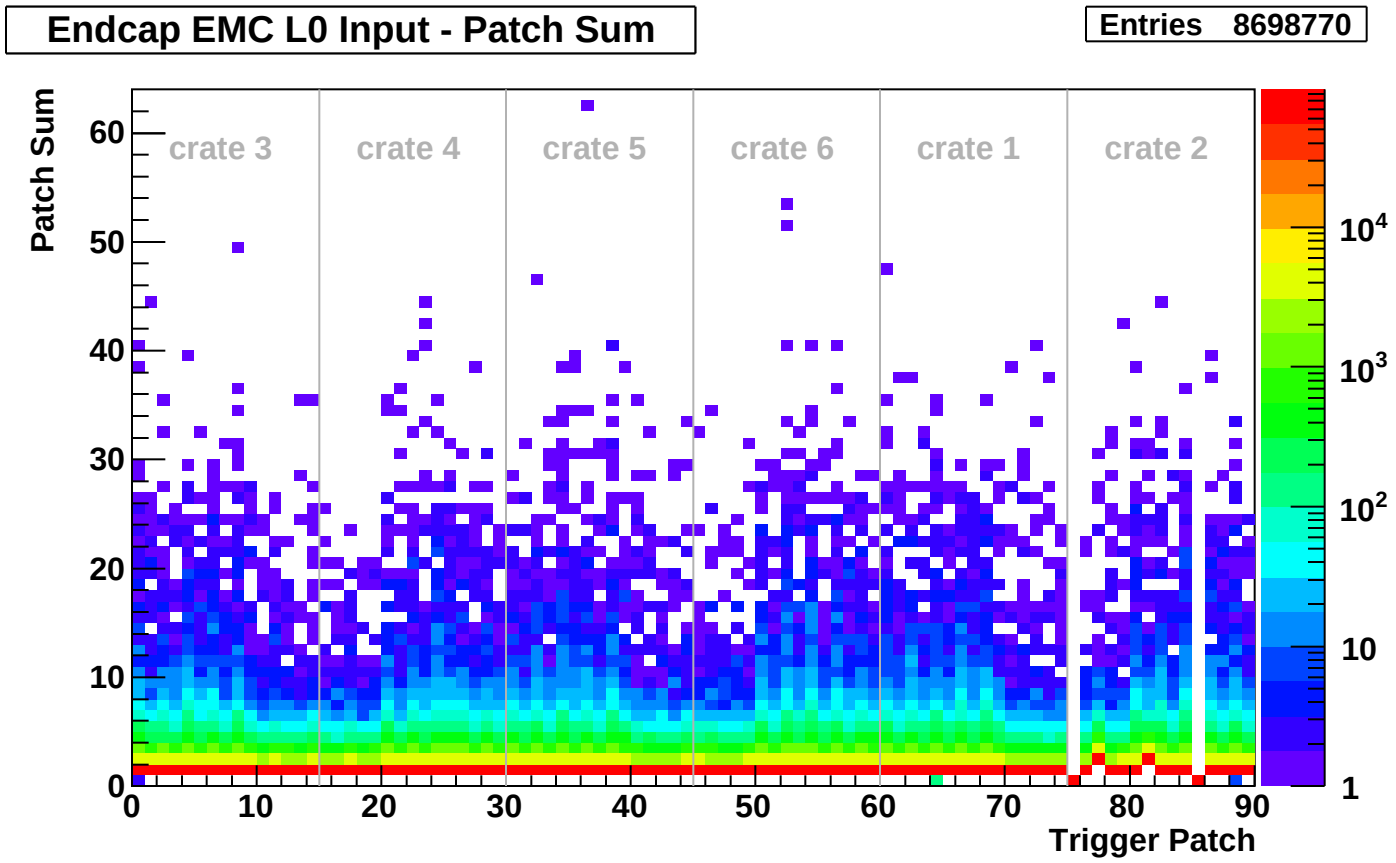
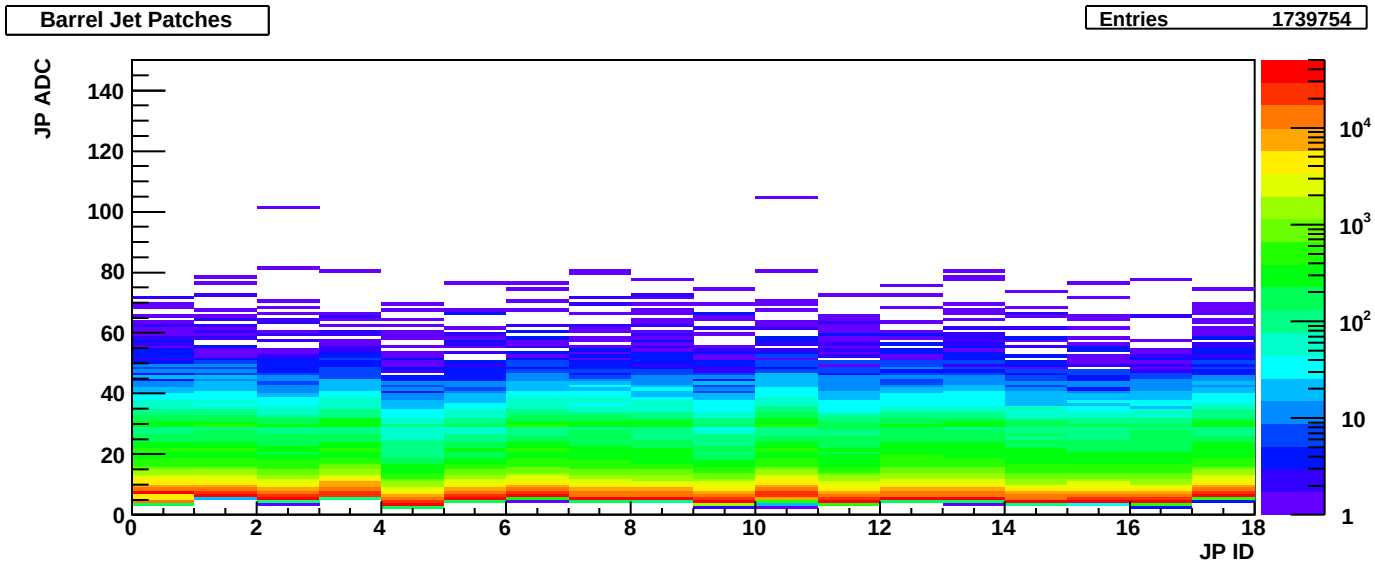
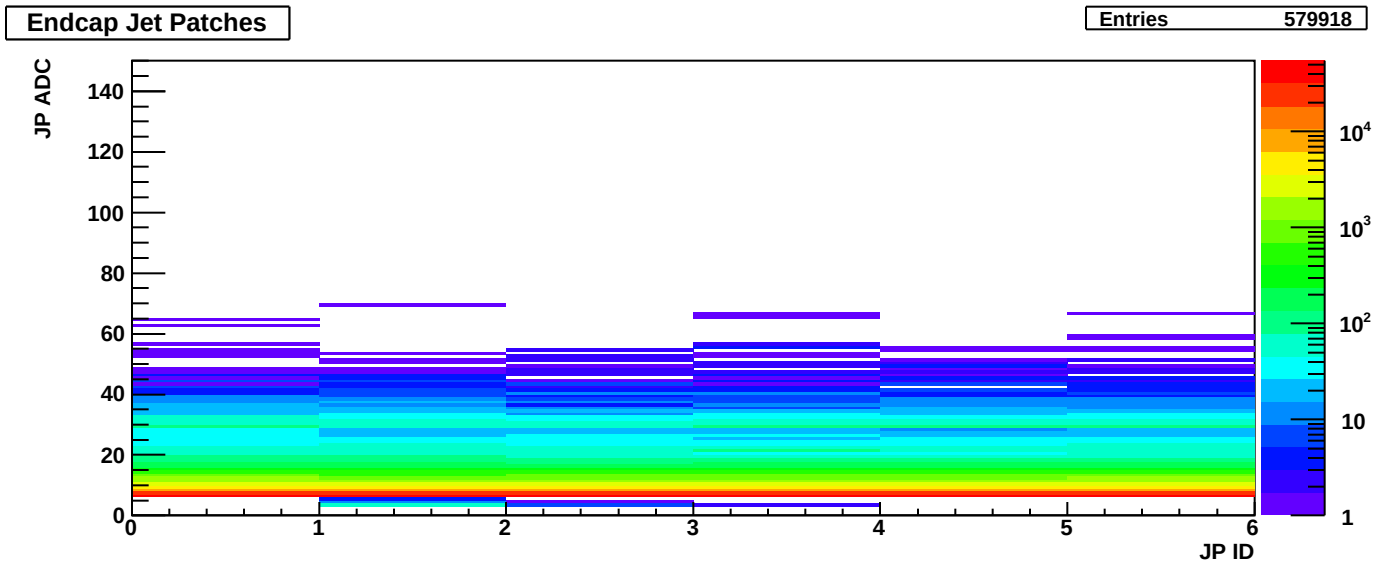

Barrel EMC L0 Input - High Tower
Entries 2.89959e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

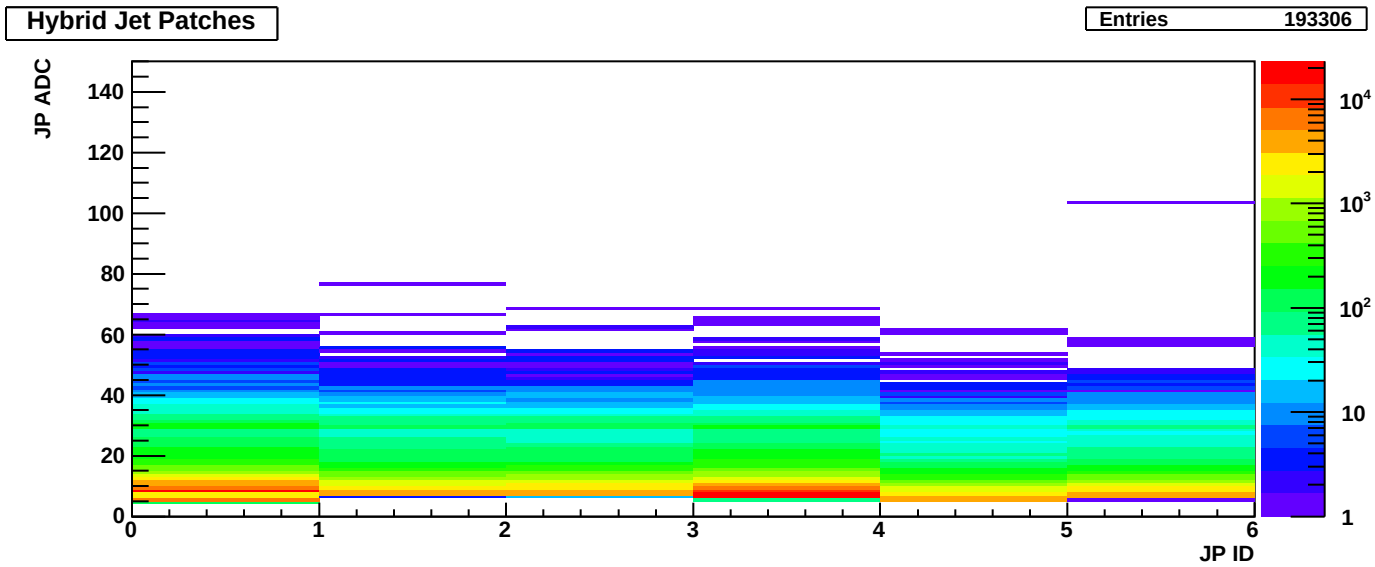

Barrel EMC L0 Input - Patch Sum
Entries 2.89959e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

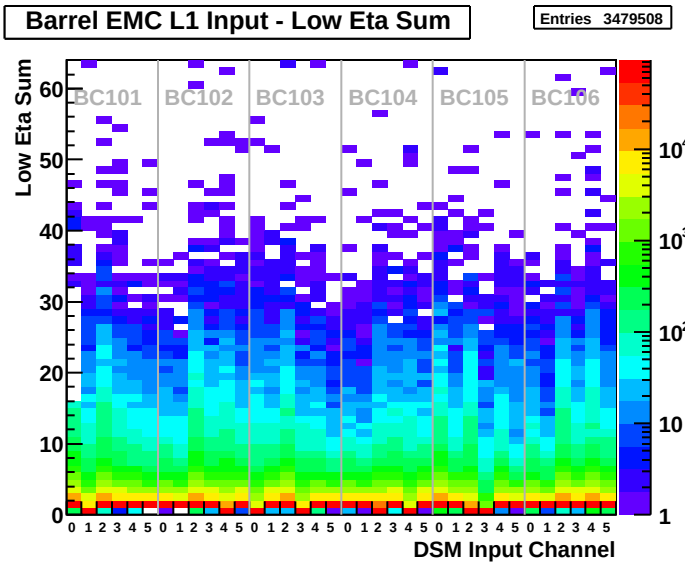

Endcap EMC L0 Input - High Tower
Entries 8698770
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

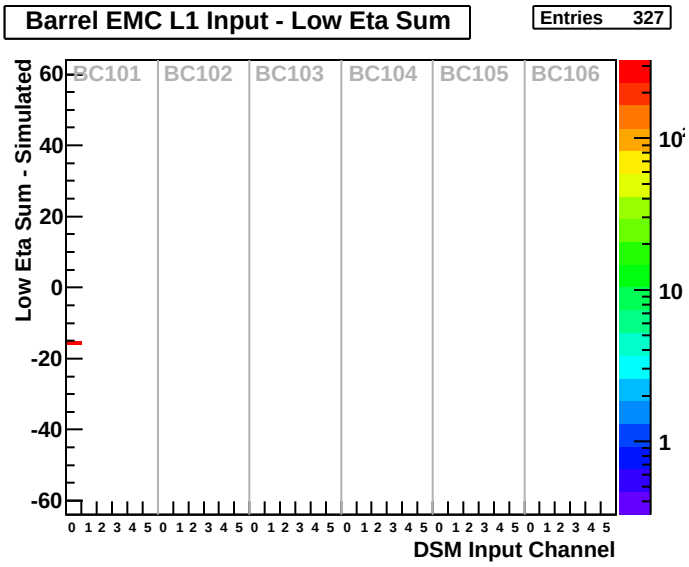

Endcap EMC L0 Input - Patch Sum
Entries 8698770
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

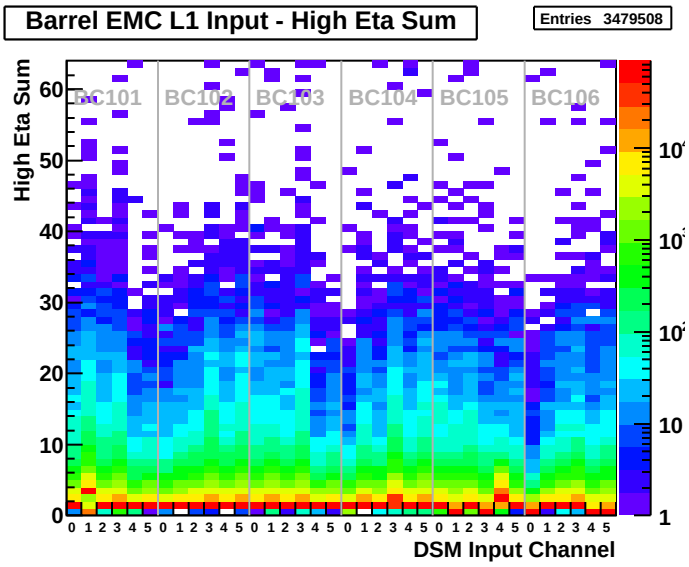

Barrel Jet Patches
Entries 1739754
JP ADC
JP ID

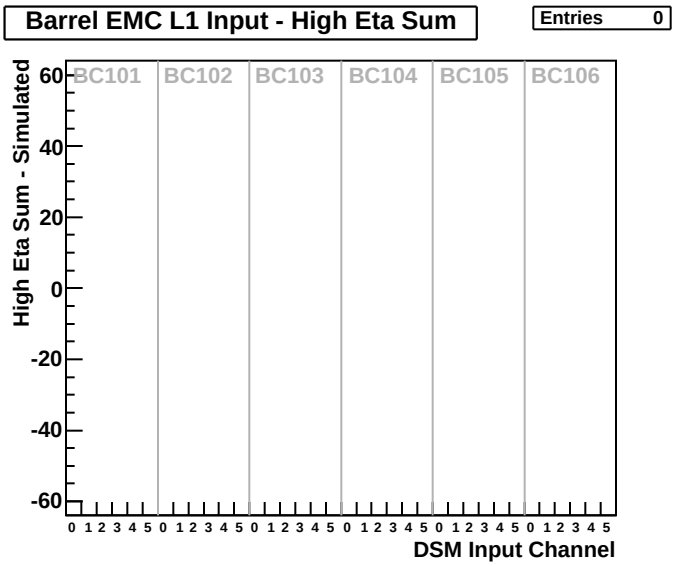

Endcap Jet Patches
Entries 579918
JP ADC
JP ID

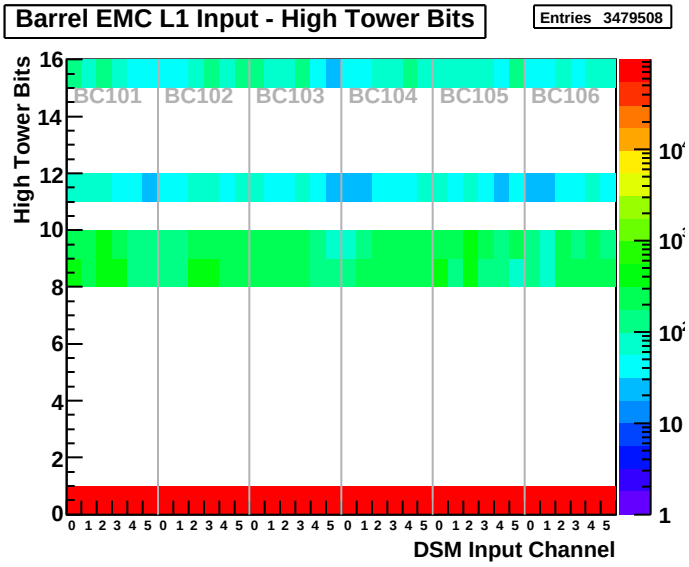

Hybrid Jet Patches
Entries 193306
JP ADC
JP ID

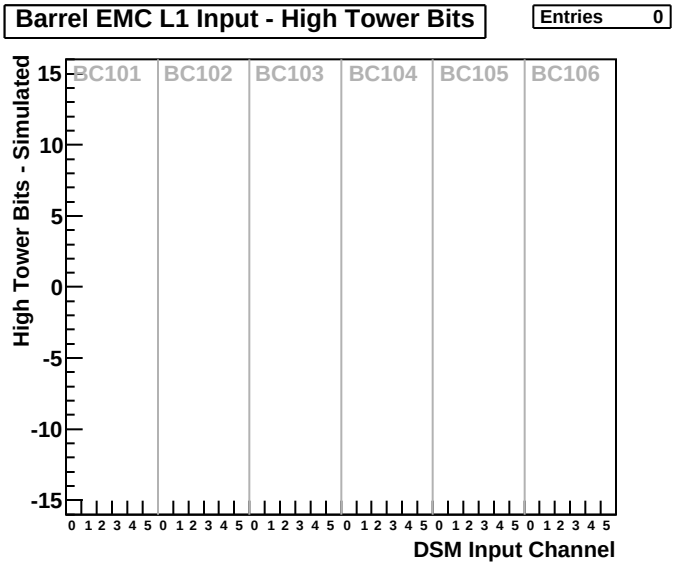

Barrel EMC L1 Input - Low Eta Sum
Entries 3479508
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

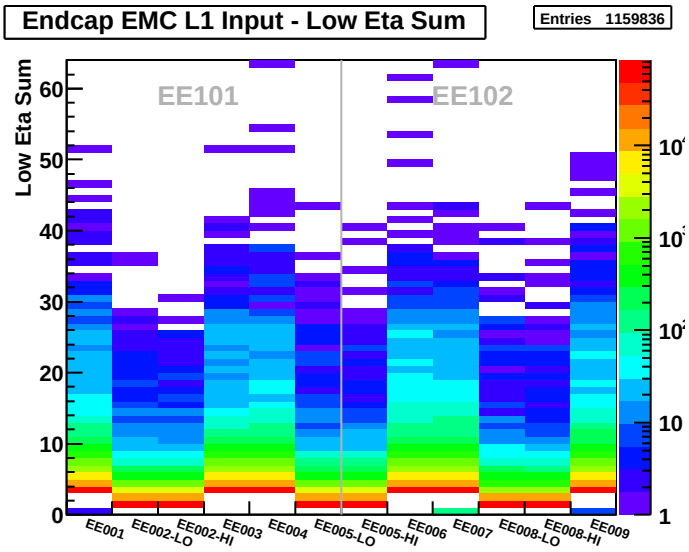

Barrel EMC L1 Input - Low Eta Sum
Entries 327
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

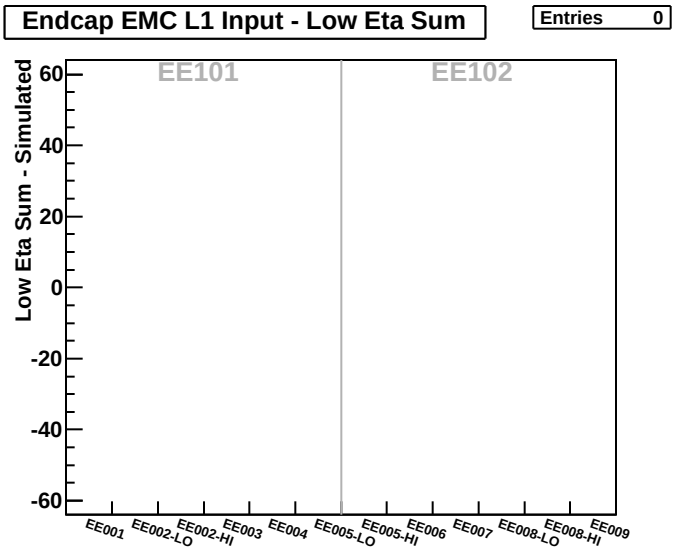

Barrel EMC L1 Input - High Eta Sum
Entries 3479508
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

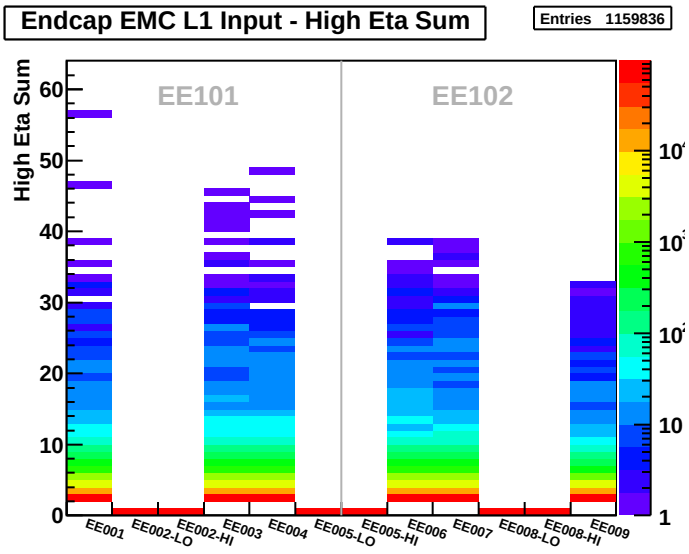

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

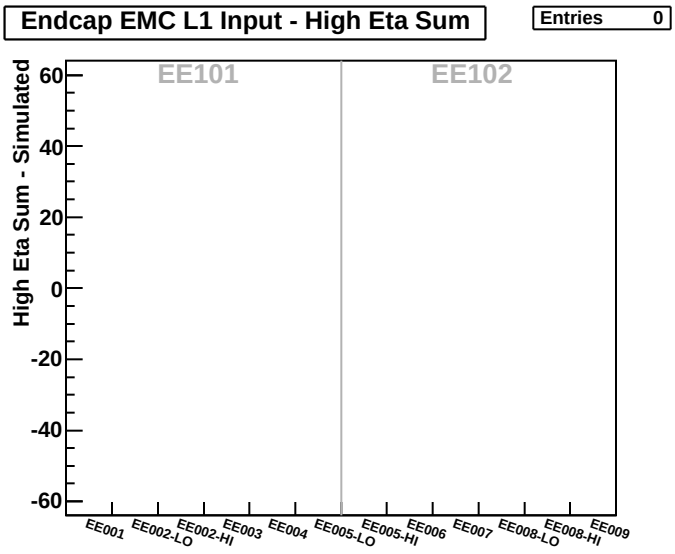

Barrel EMC L1 Input - High Tower Bits
Entries 3479508
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

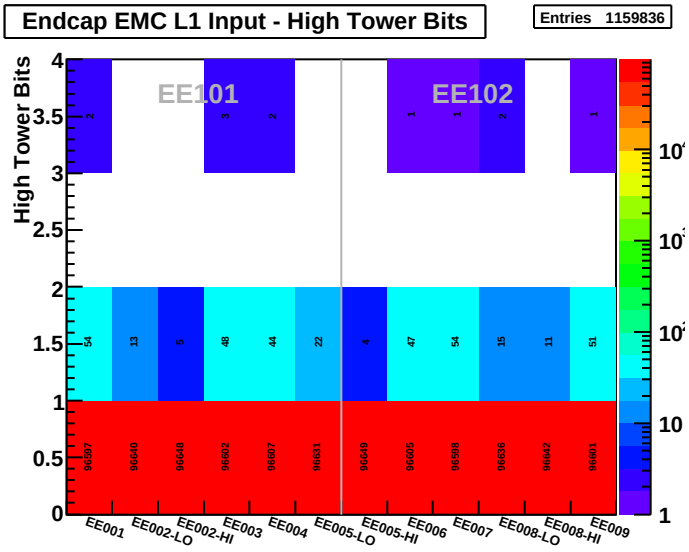

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

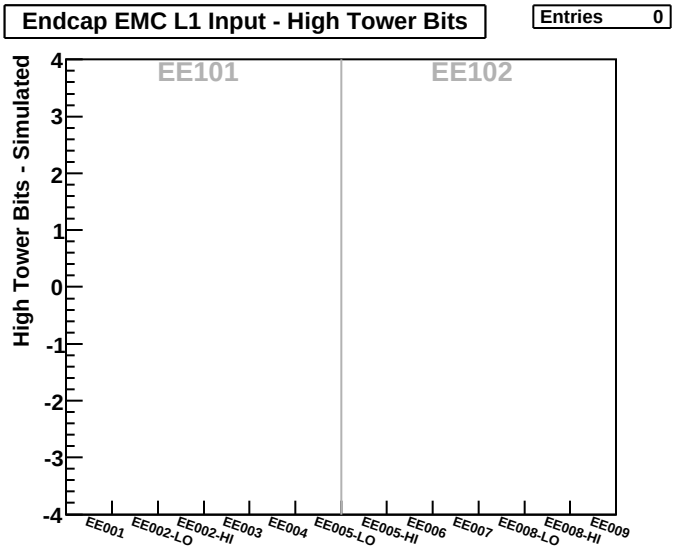

Endcap EMC L1 Input - Low Eta Sum
Entries 1159836
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

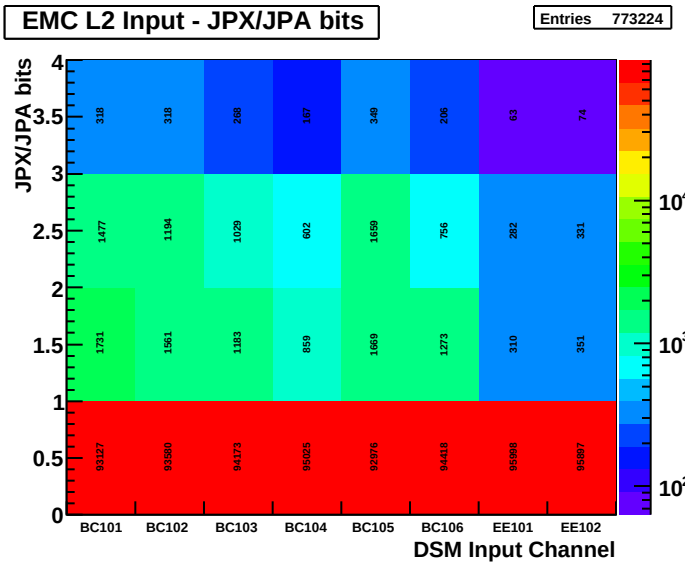

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

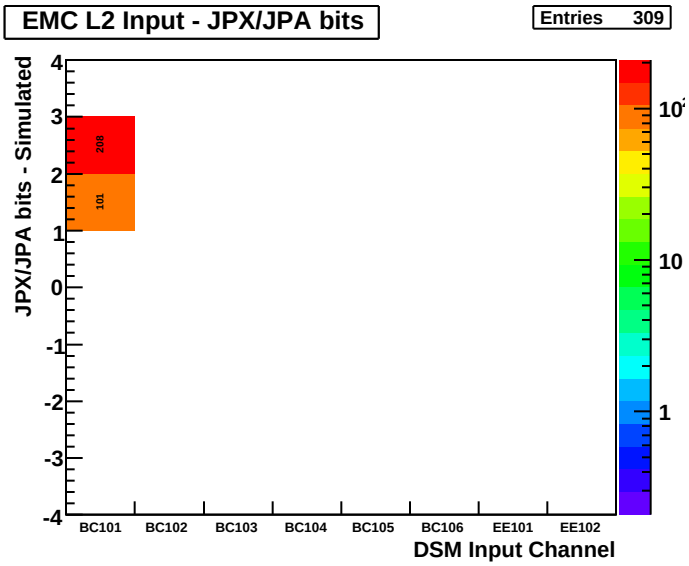

Endcap EMC L1 Input - High Eta Sum
Entries 1159836
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

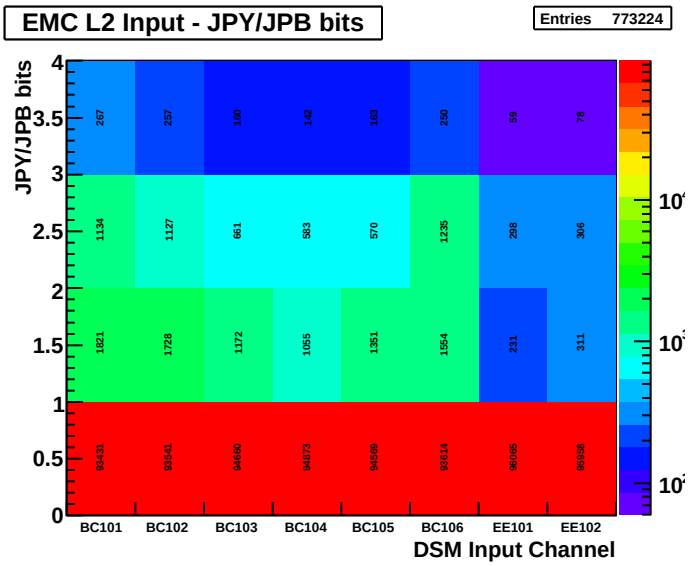

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

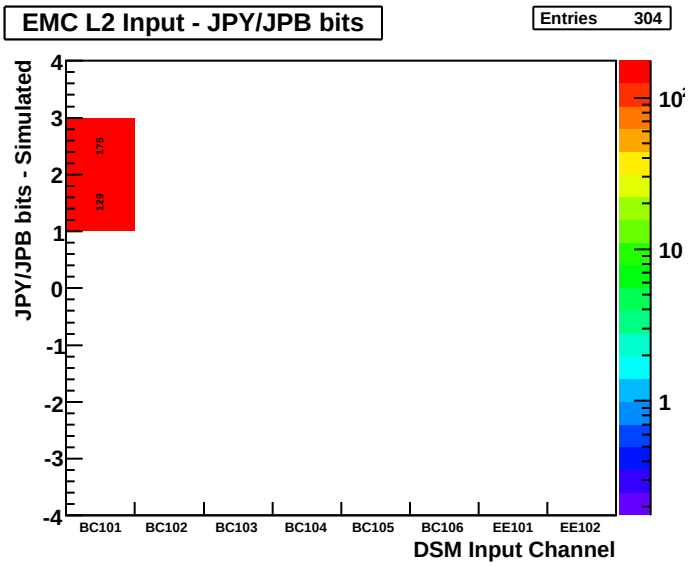

Endcap EMC L1 Input - High Tower Bits
Entries 1159836
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

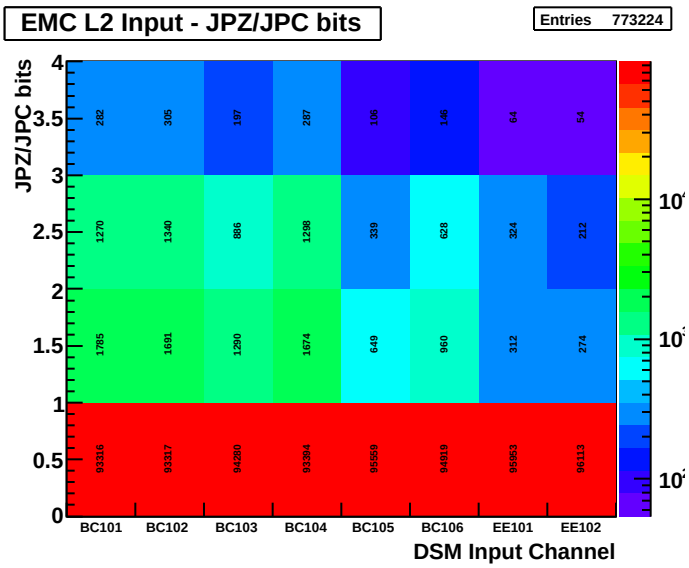

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

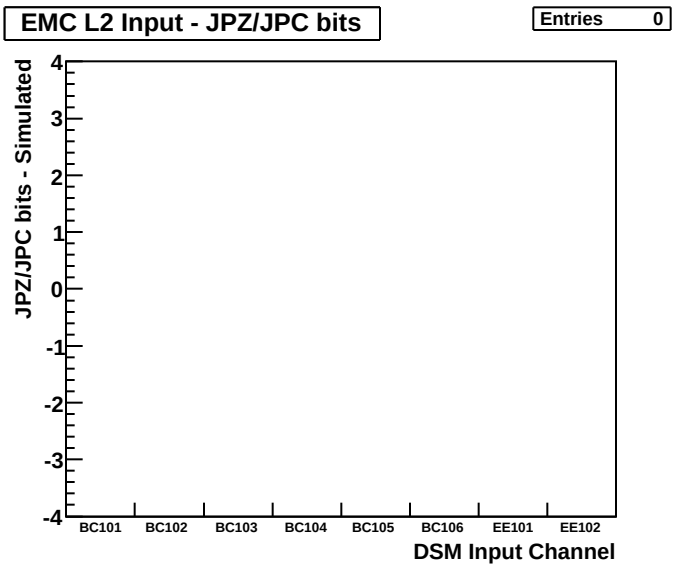

EMC L2 Input - JPX/JPA bits
Entries 773224
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
318 318 268 167 349 206 63 74
1477 1194 1029 602 1659 756 282 331
1731 1561 1183 859 1669 1273 310 351
93127 93580 94173 95025 92976 94418 95998 95897

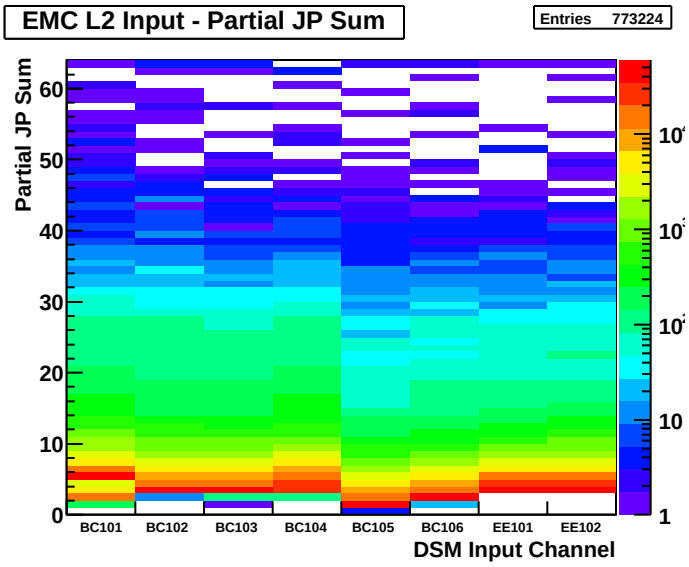

EMC L2 Input - JPX/JPA bits
Entries 309
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
208
101

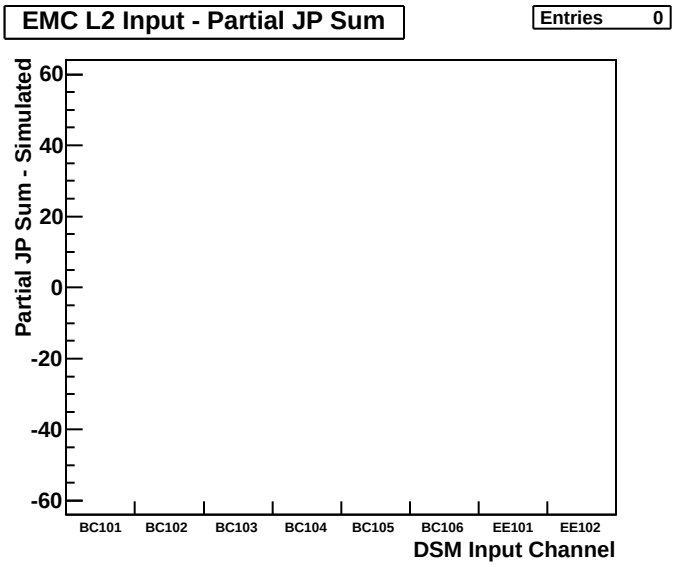

EMC L2 Input - JPY/JPB bits
Entries 773224
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
267 257 160 142 163 250 59 78
1134 1127 661 583 570 1235 298 306
1821 1728 1172 1055 1351 1554 231 311
93431 93541 94660 94873 94569 93614 96065 95958

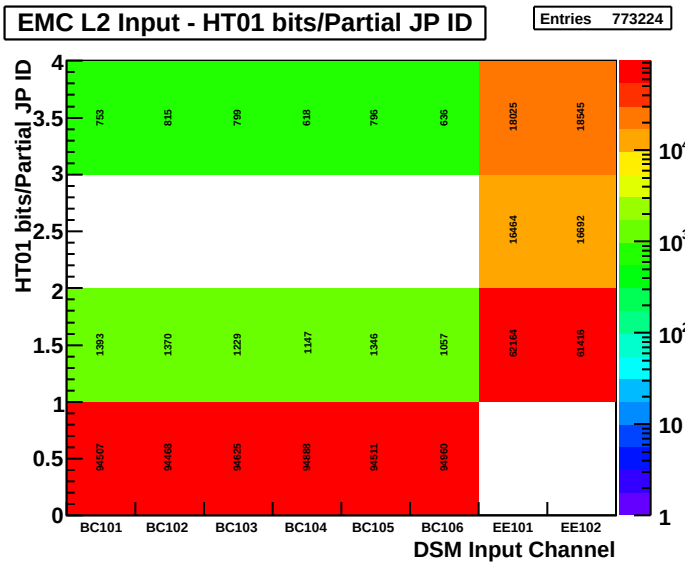

EMC L2 Input - JPY/JPB bits
Entries 304
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
175
129

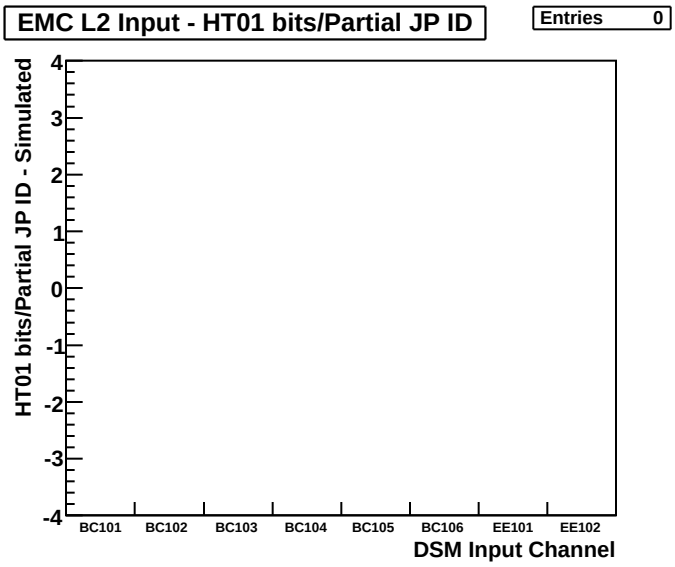

EMC L2 Input - JPZ/JPC bits
Entries 773224
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
282 305 197 267 106 146 64 54
1270 1340 886 1298 339 628 324 212
1785 1691 1290 1674 649 960 312 274
93316 93317 94280 93394 95559 94919 95963 96113

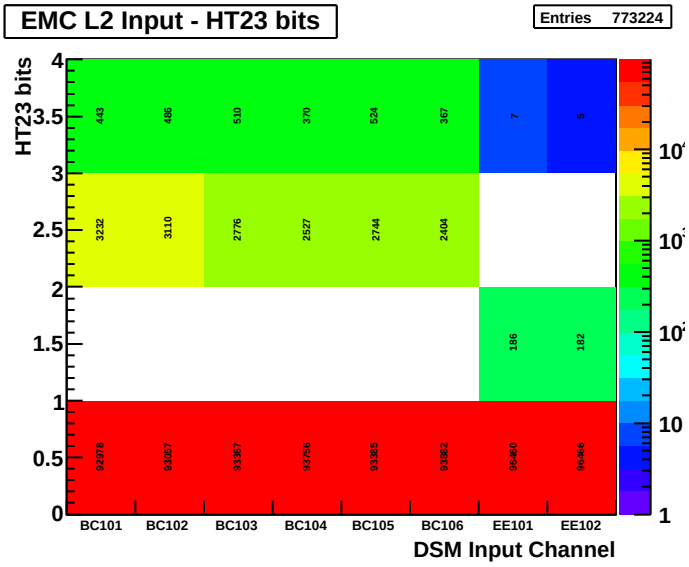

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

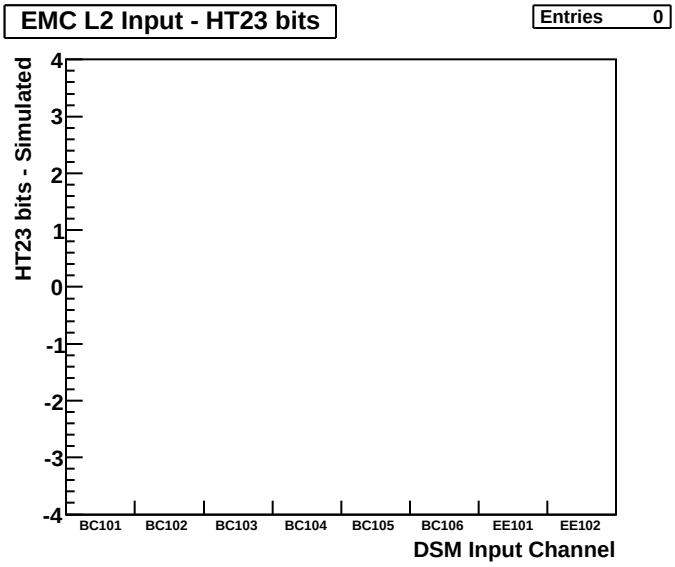

EMC L2 Input - Partial JP Sum
Entries 773224
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

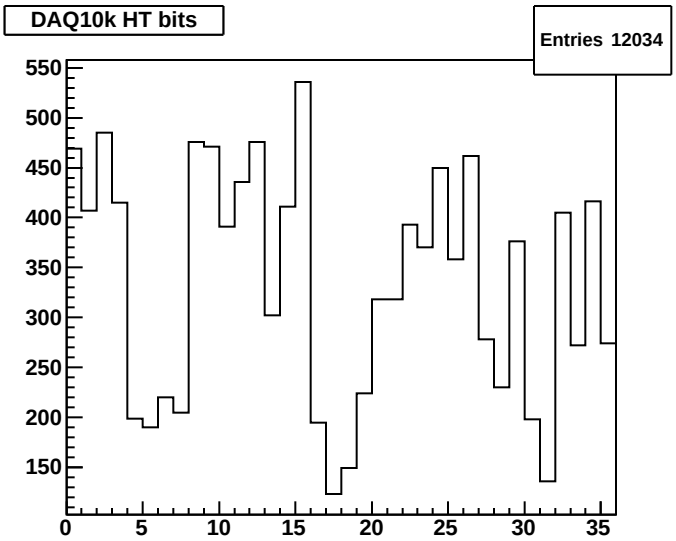

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

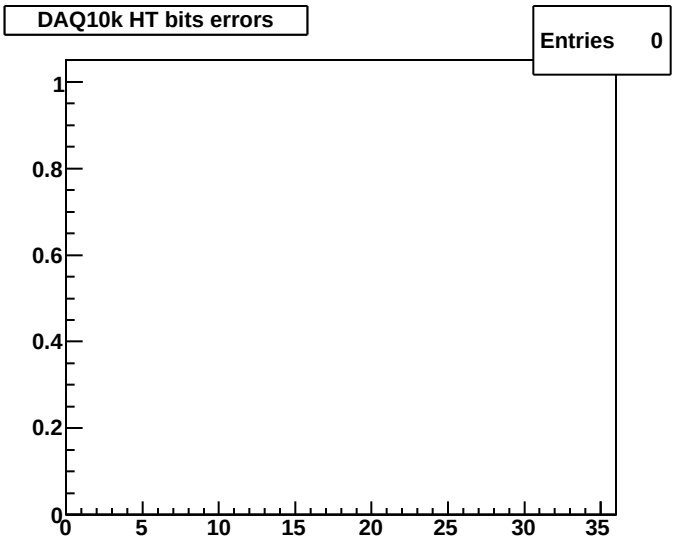

EMC L2 Input - HT01 bits/Partial JP ID
Entries 773224
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

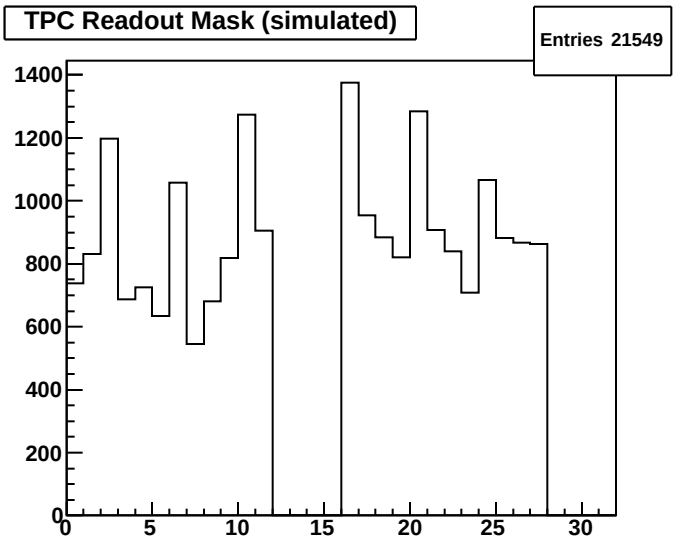

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

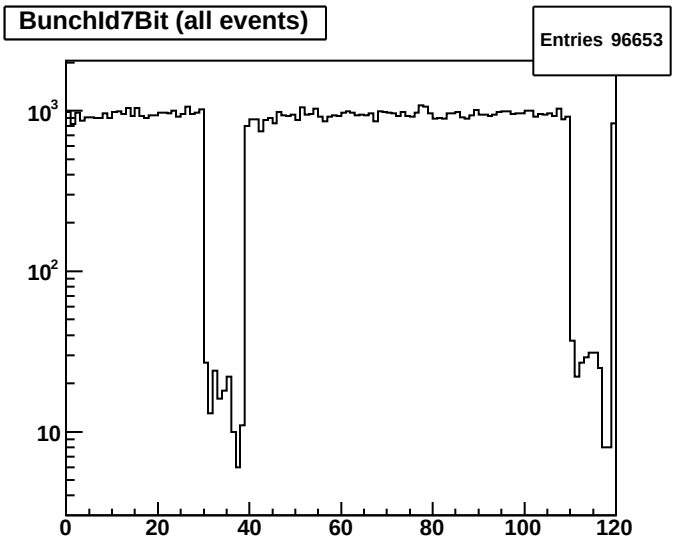

EMC L2 Input - HT23 bits
Entries 773224
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

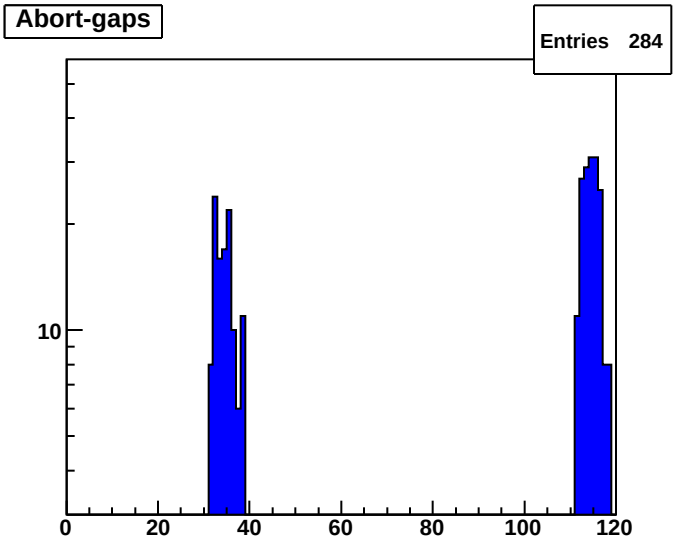

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

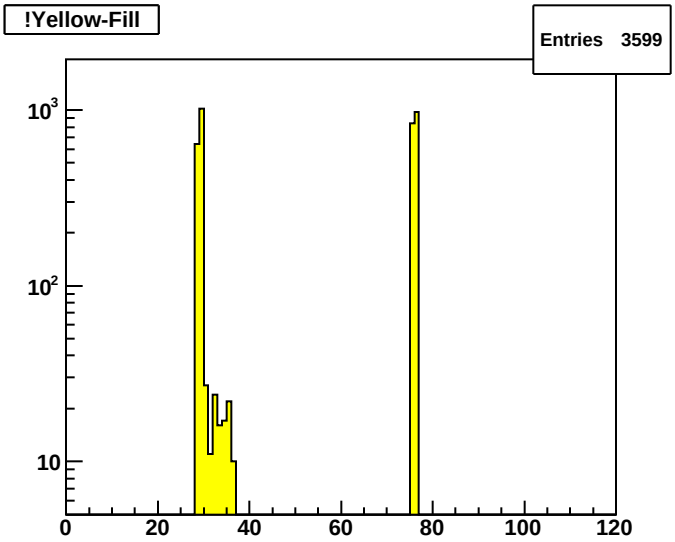

DAQ10k HT bits
Entries 12034

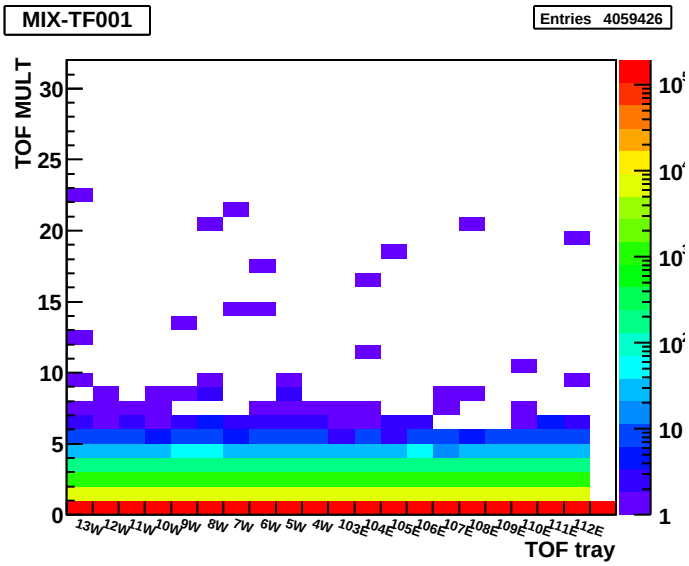

DAQ10k HT bits errors
Entries 0

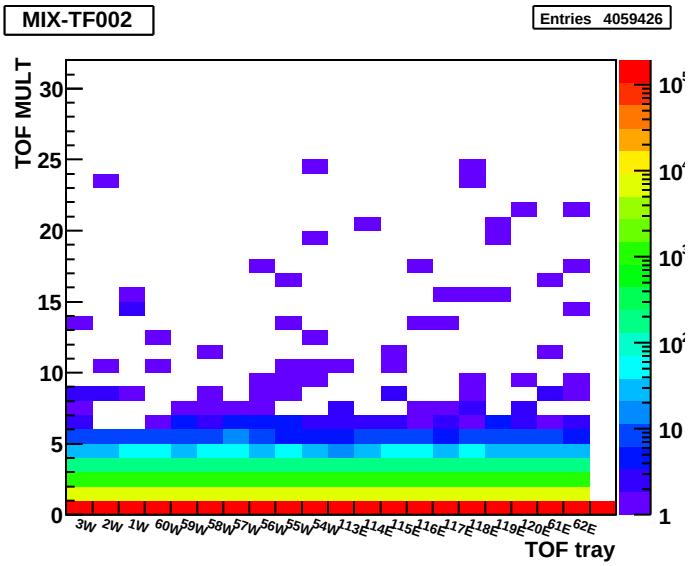

TPC Readout Mask (simulated)
Entries 21549

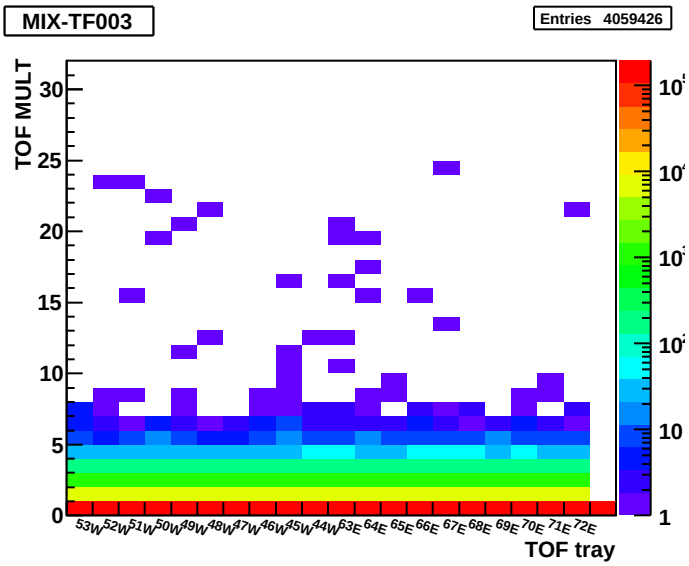

BunchId7Bit (all events)
Entries 96653

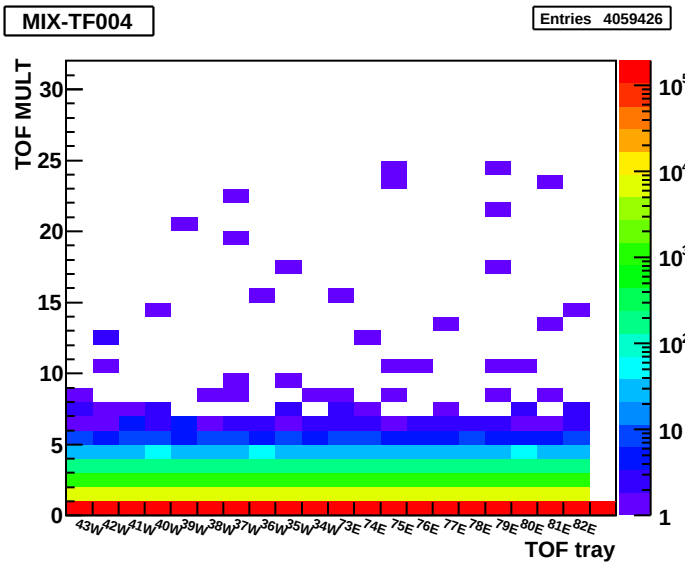

Abort-gaps
Entries 284

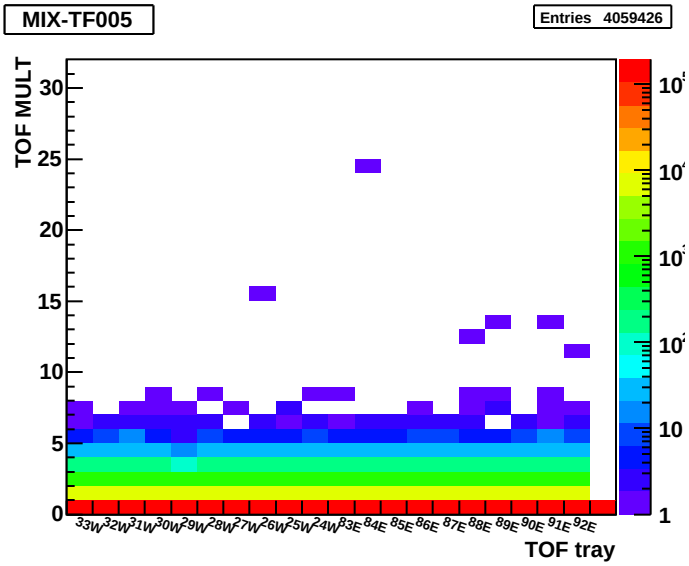

!Yellow-Fill
Entries 3599

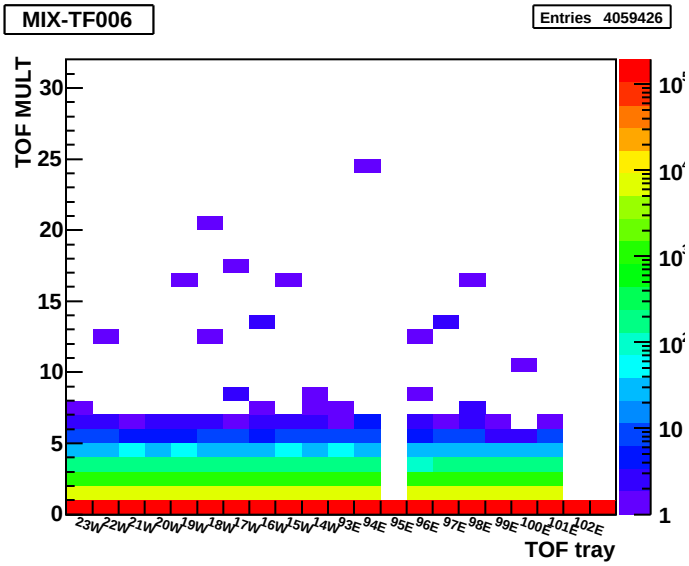

MIX-TF001
Entries 4059426
TOF MULT
TOF tray

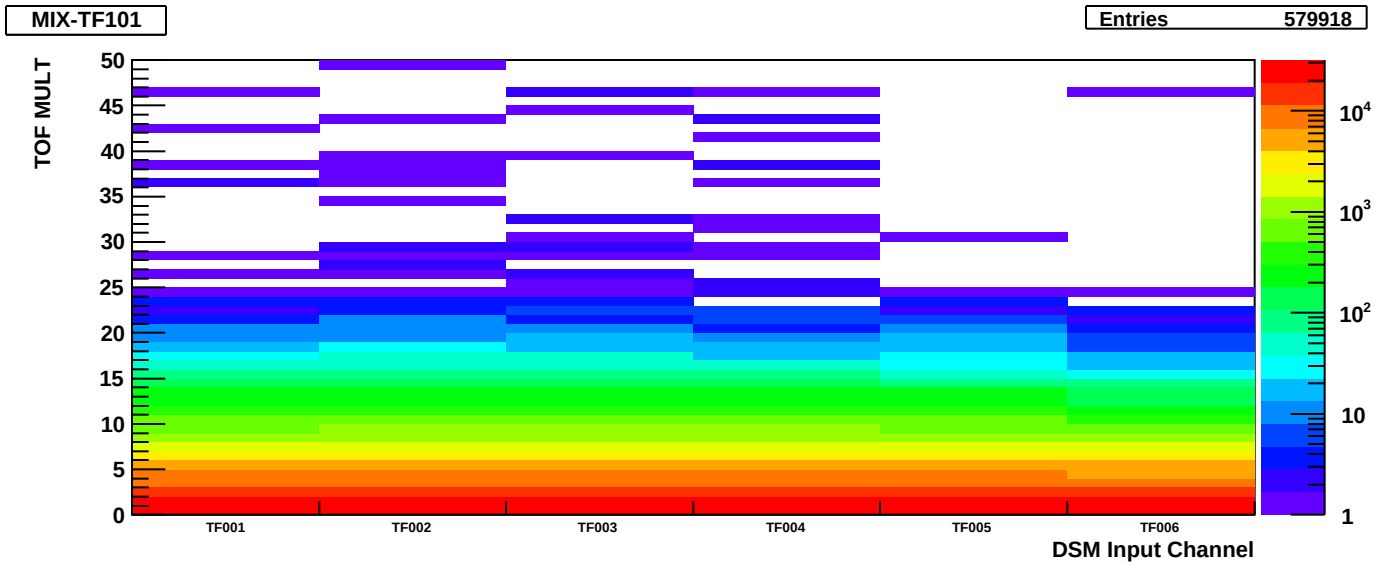

MIX-TF002
Entries 4059426
TOF MULT
TOF tray

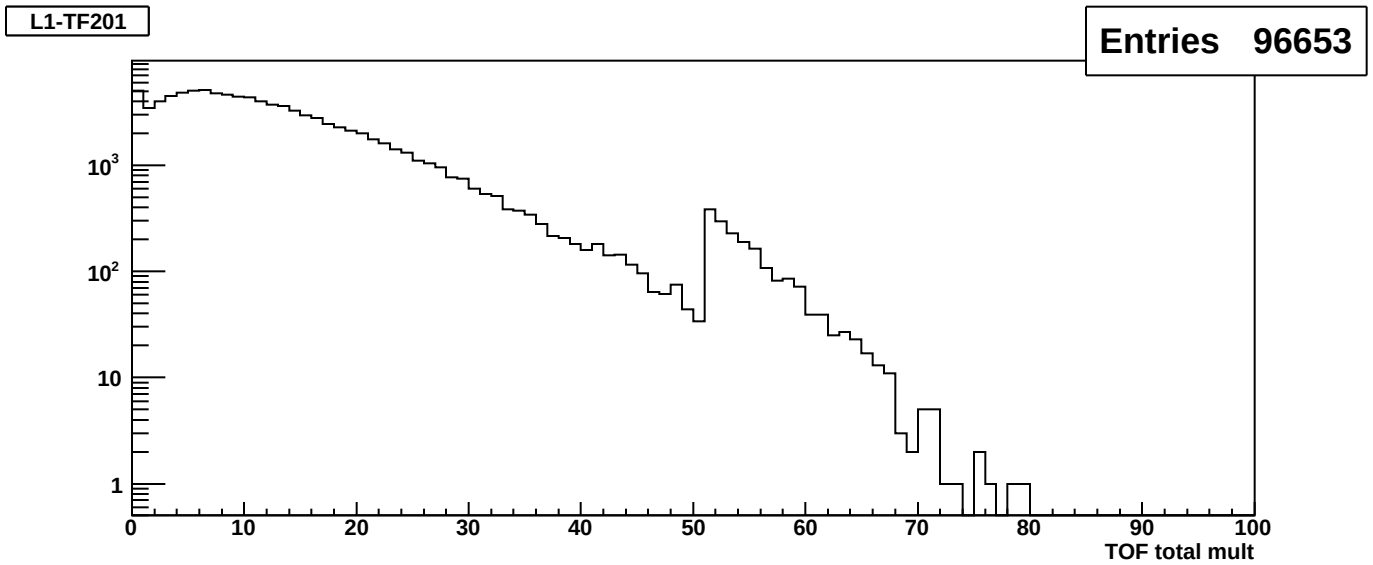

MIX-TF003
Entries 4059426
TOF MULT
TOF tray

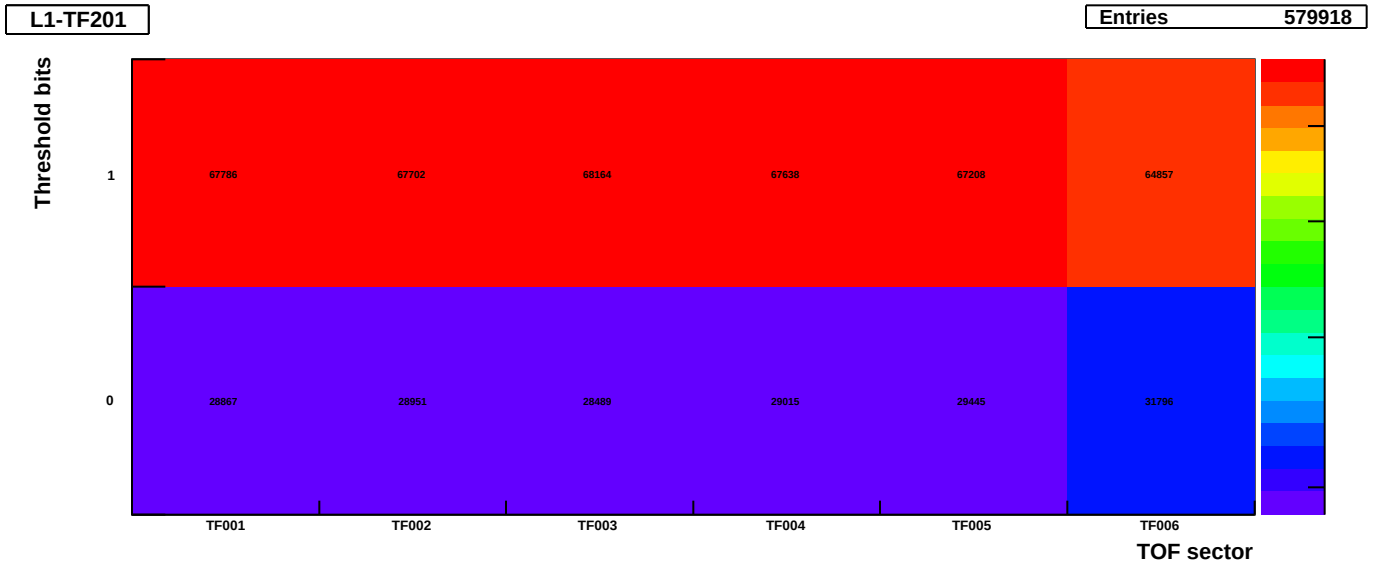

MIX-TF004
Entries 4059426
TOF MULT
TOF tray

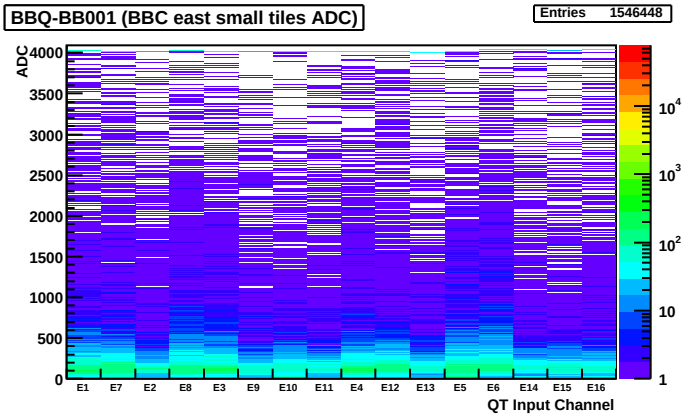

MIX-TF005
Entries 4059426
TOF MULT
TOF tray

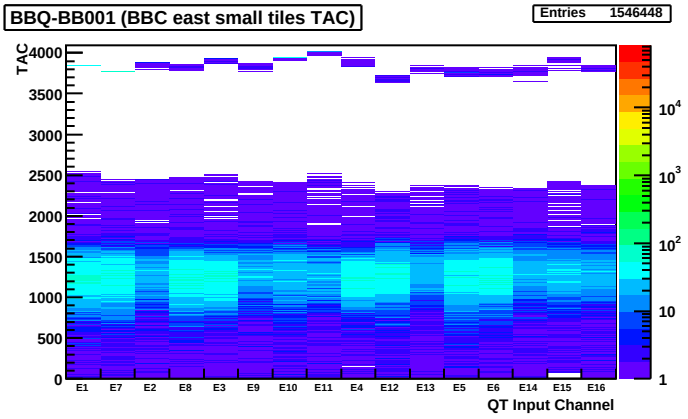

MIX-TF006
Entries 4059426
TOF MULT
TOF tray

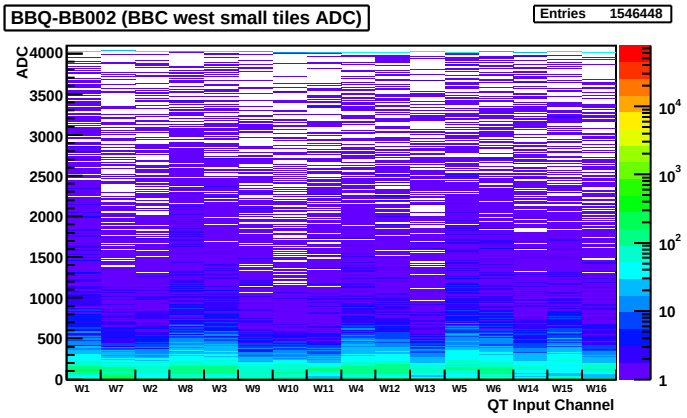

MIX-TF101
Entries 579918
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

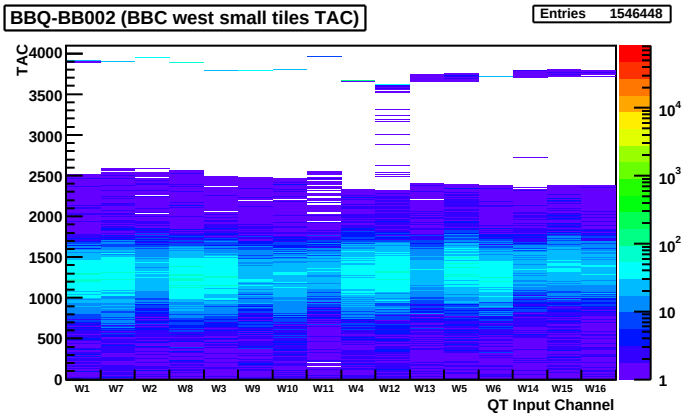

L1-TF201
Entries 96653
TOF total mult

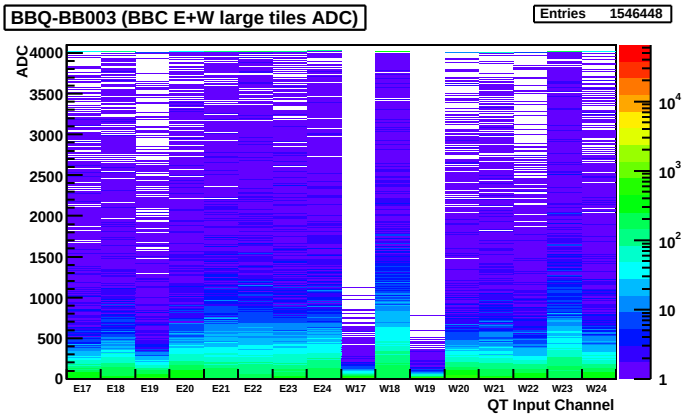

L1-TF201
Entries 579918
Threshold bits
67786
67702
68164
67638
67208
64857
28867
28951
28489
29015
29445
31796
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

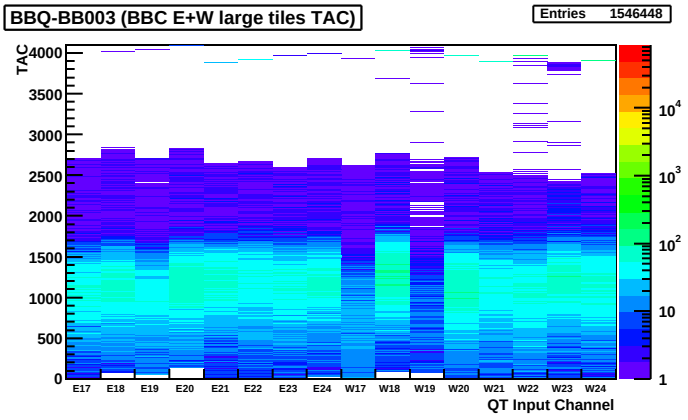

BBQ-BB001 (BBC east small tiles ADC)
Entries 1546448
ADC
QT Input Channel

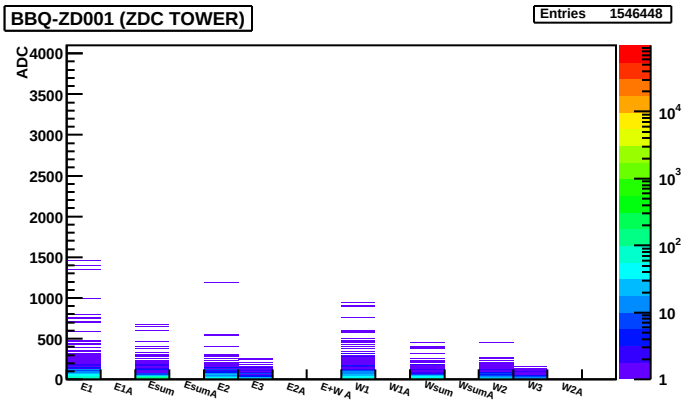

BBQ-BB001 (BBC east small tiles TAC)
Entries 1546448
TAC
QT Input Channel

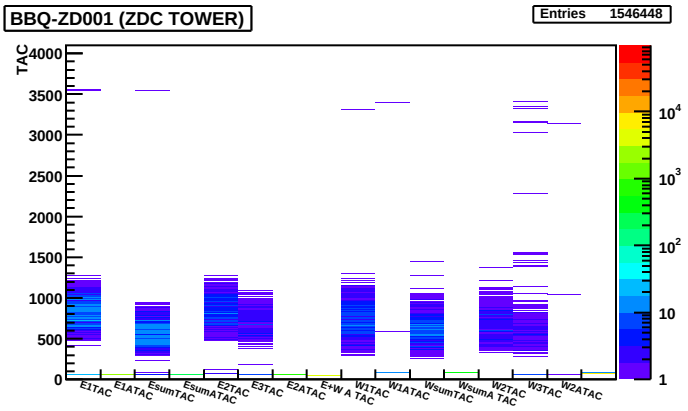

BBQ-BB002 (BBC west small tiles ADC)
Entries 1546448
ADC
QT Input Channel

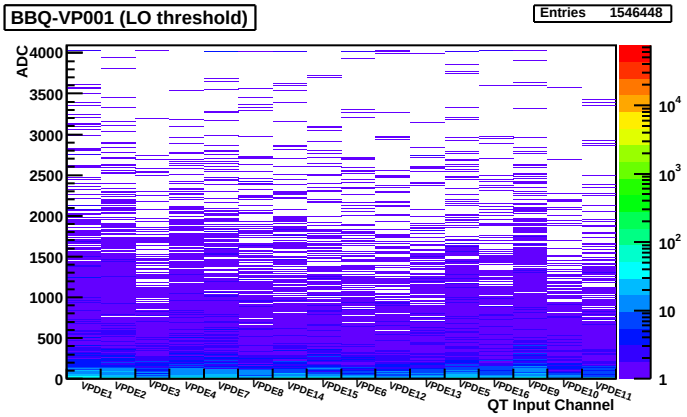

BBQ-BB002 (BBC west small tiles TAC)
Entries 1546448
TAC
QT Input Channel

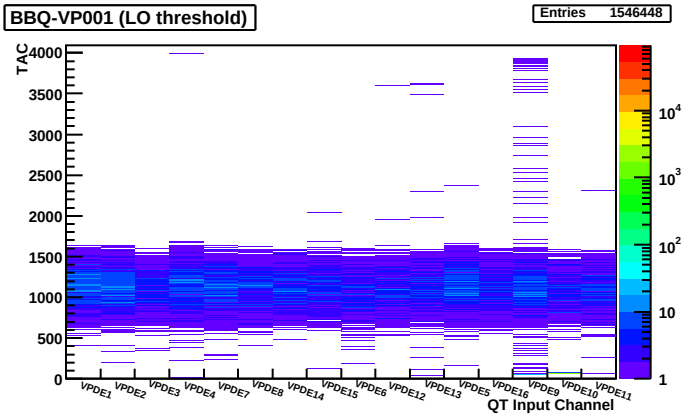

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1546448
ADC
QT Input Channel

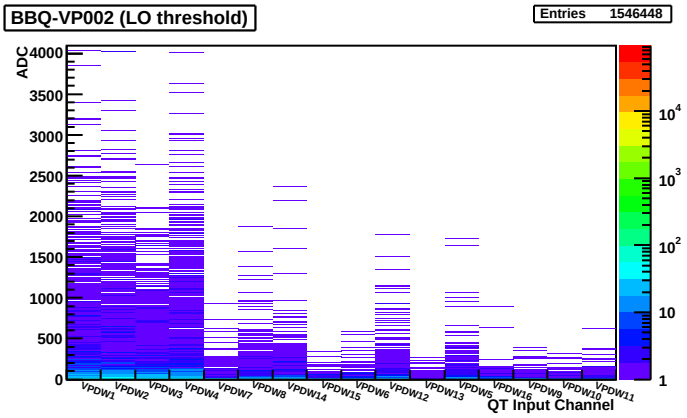

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1546448
TAC
QT Input Channel

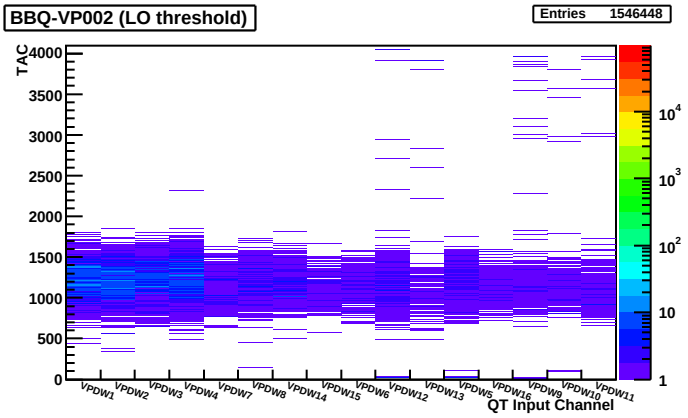

BBQ-ZD001 (ZDC TOWER)
Entries 1546448
ADC

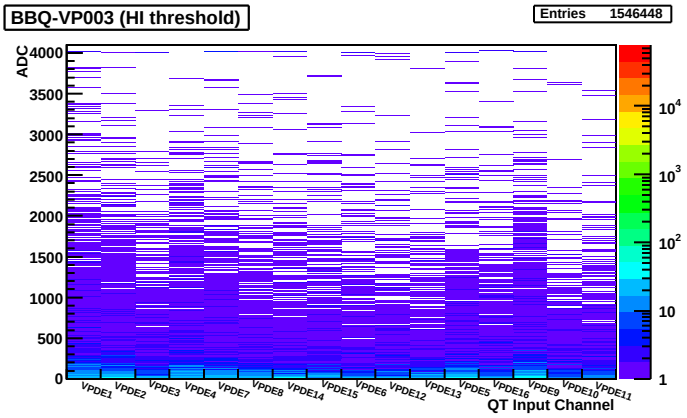

BBQ-ZD001 (ZDC TOWER)
Entries 1546448
TAC

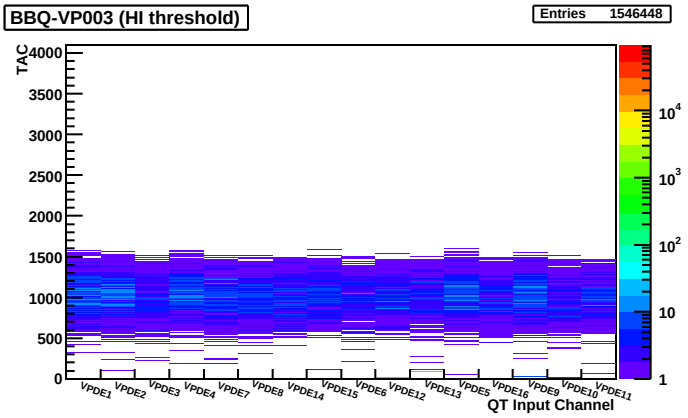

BBQ-VP001 (LO threshold)
Entries 1546448
ADC
QT Input Channel

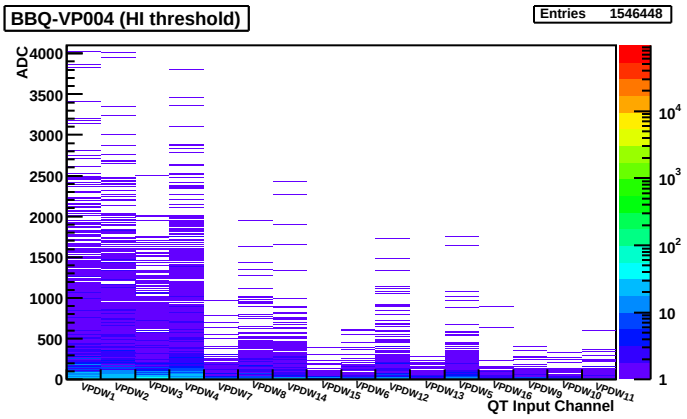

BBQ-VP001 (LO threshold)
Entries 1546448
TAC
QT Input Channel

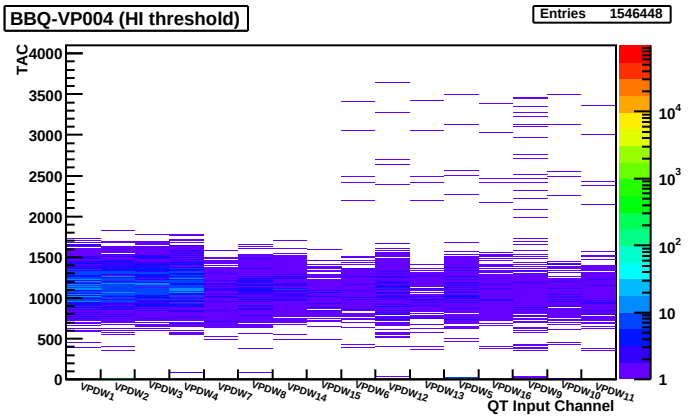

BBQ-VP002 (LO threshold)
Entries 1546448
ADC
QT Input Channel

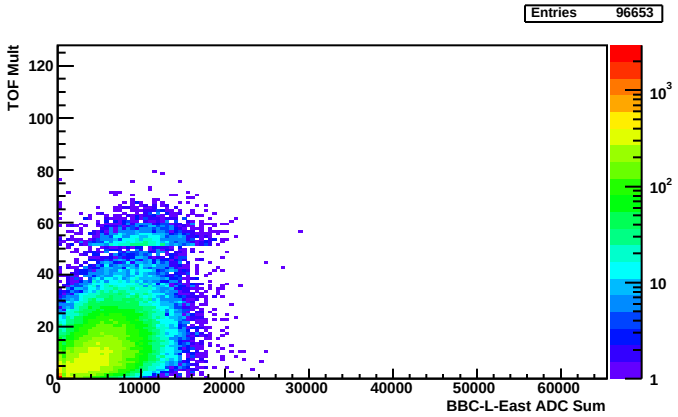

BBQ-VP002 (LO threshold)
Entries 1546448
TAC
QT Input Channel

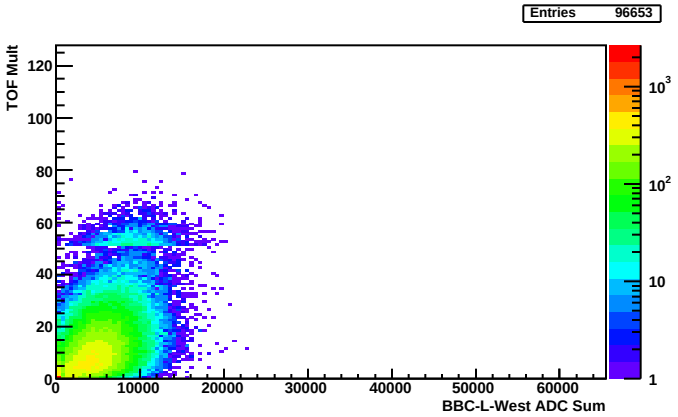

BBQ-VP003 (HI threshold)
Entries 1546448
ADC
QT Input Channel

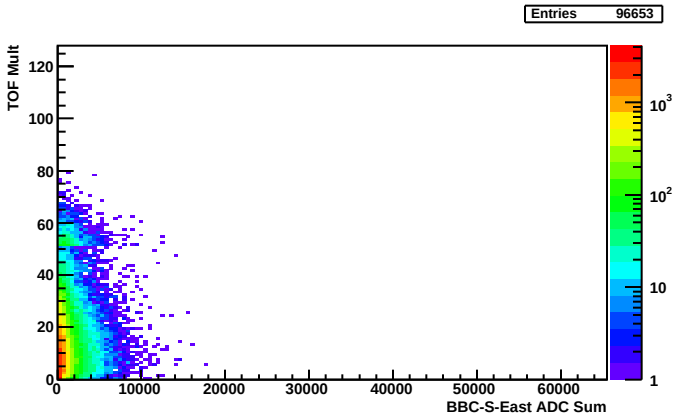

BBQ-VP003 (HI threshold)
Entries 1546448
TAC
QT Input Channel

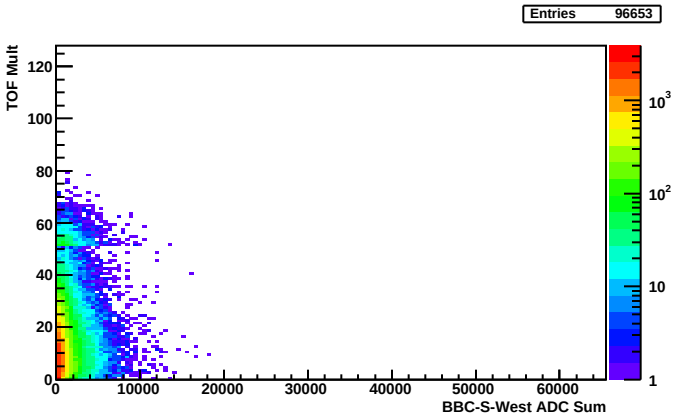

BBQ-VP004 (HI threshold)
Entries 1546448
ADC
QT Input Channel

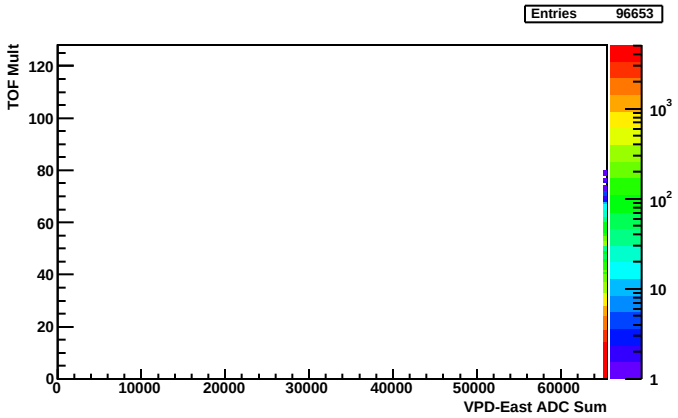

BBQ-VP004 (HI threshold)
Entries 1546448
TAC
QT Input Channel

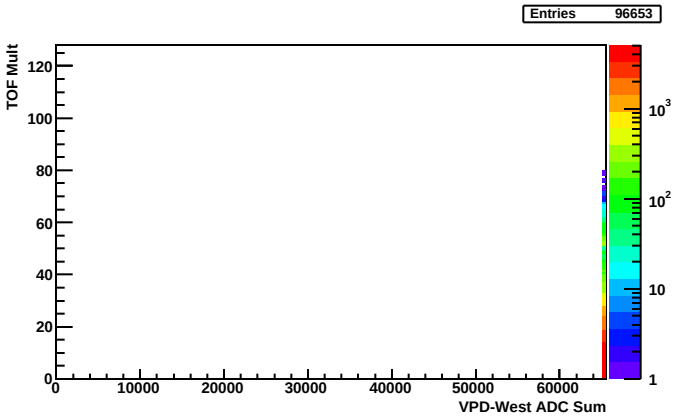

Entries 96653
TOF Mult
BBC-L-East ADC Sum

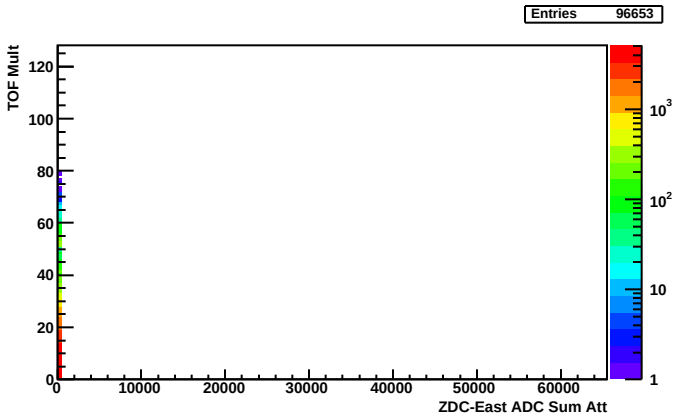

Entries 96653
TOF Mult
BBC-L-West ADC Sum

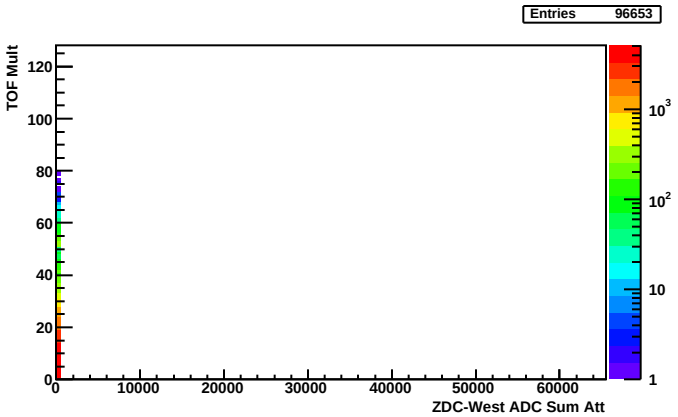

Entries 96653
TOF Mult
BBC-S-East ADC Sum

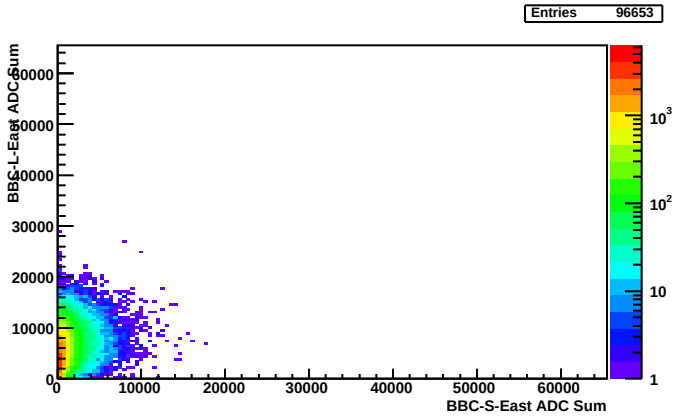

Entries 96653
TOF Mult
BBC-S-West ADC Sum

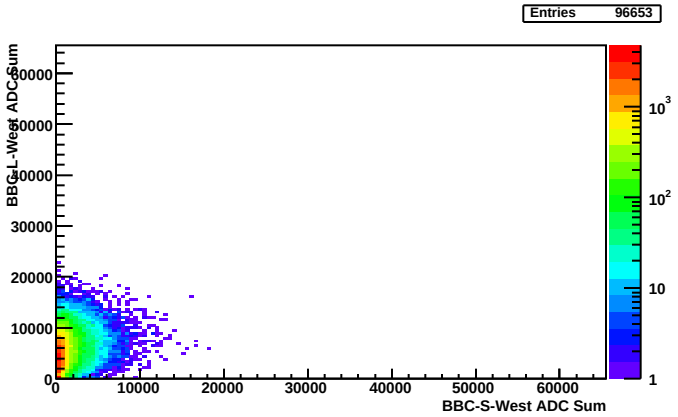

Entries 96653
TOF Mult
VPD-East ADC Sum

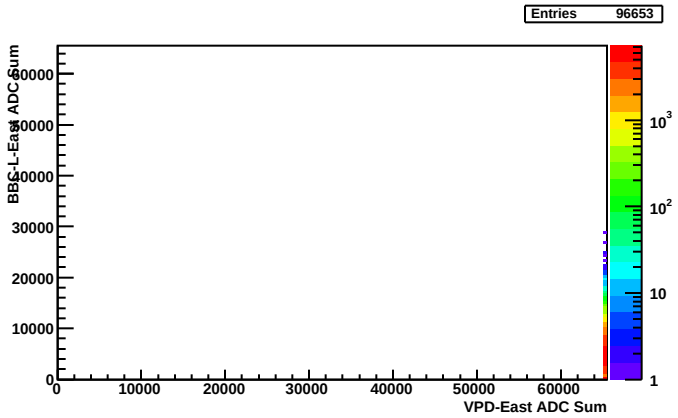

Entries 96653
TOF Mult
VPD-West ADC Sum

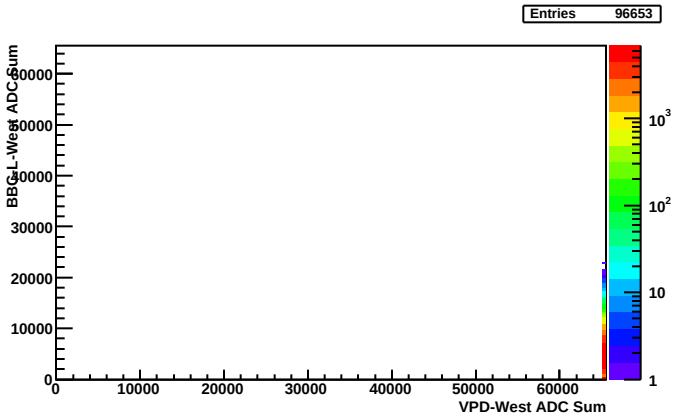

Entries 96653
TOF Mult
ZDC-East ADC Sum Att


Entries 96653
TOF Mult
ZDC-West ADC Sum Att


Entries 96653
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 96653
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 96653
BBC-L-East ADC Sum
VPD-East ADC Sum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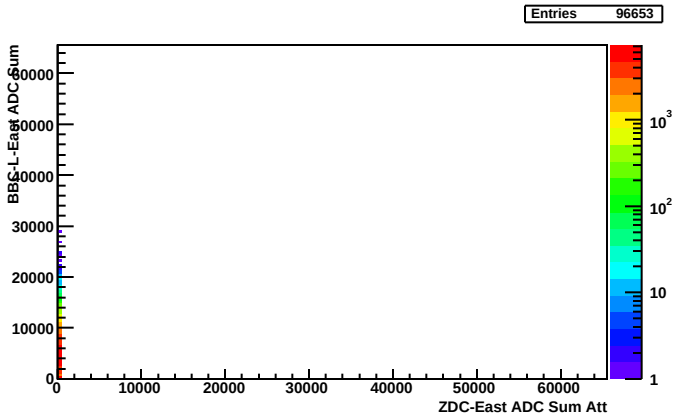
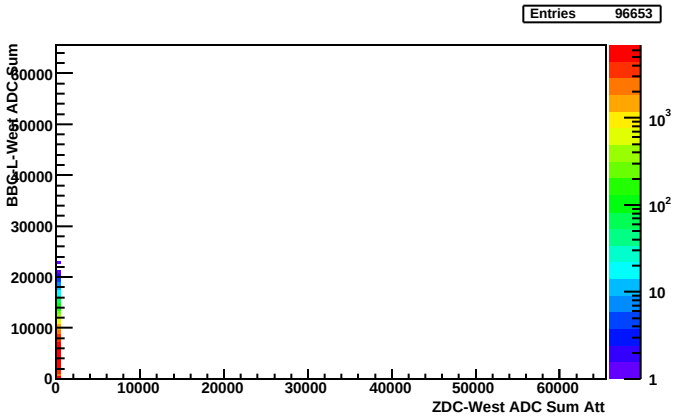
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 96653
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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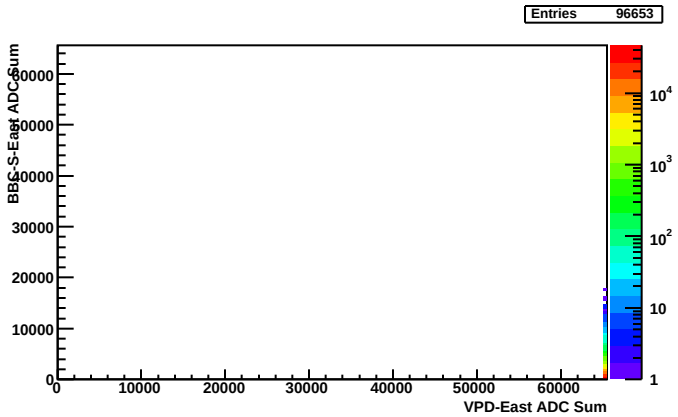
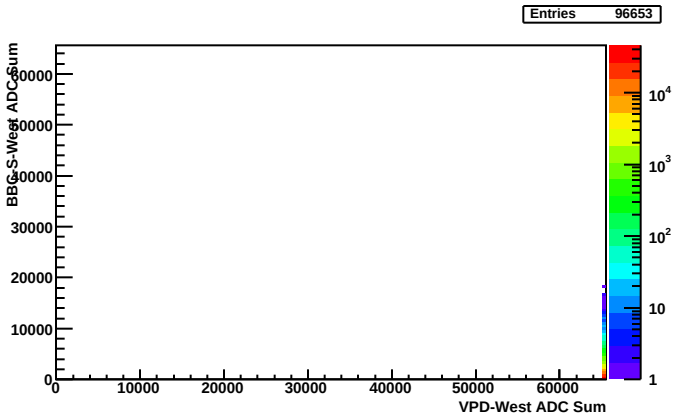
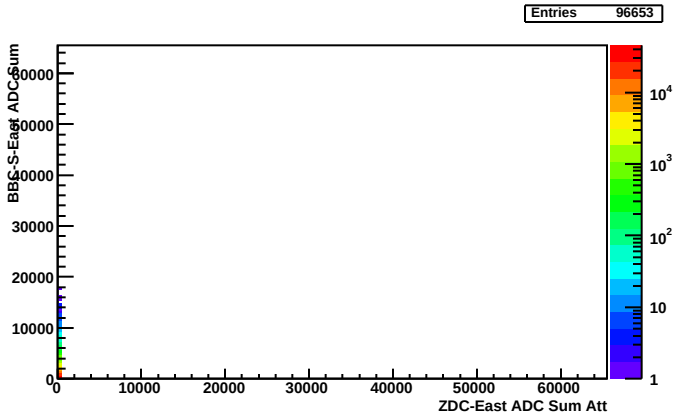
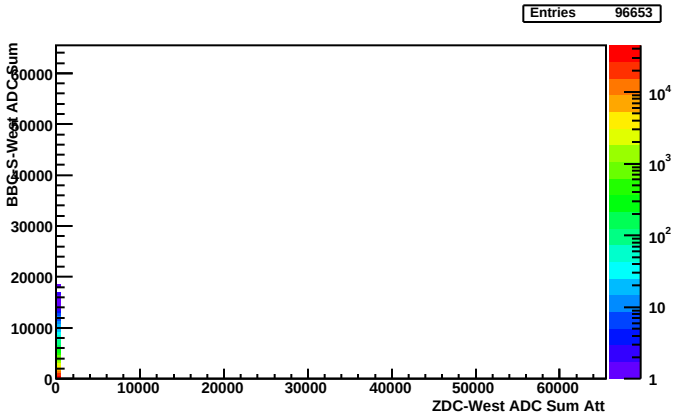
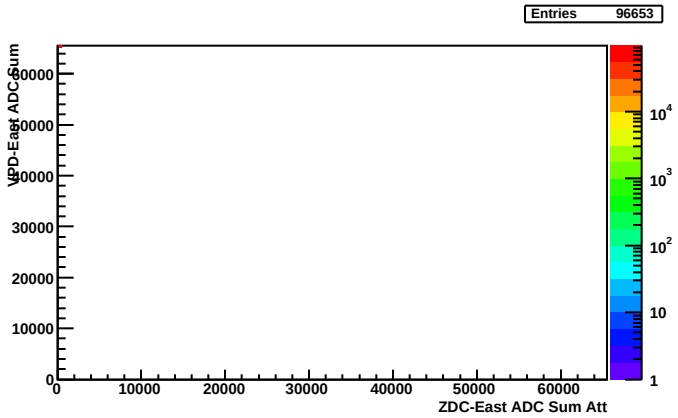
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

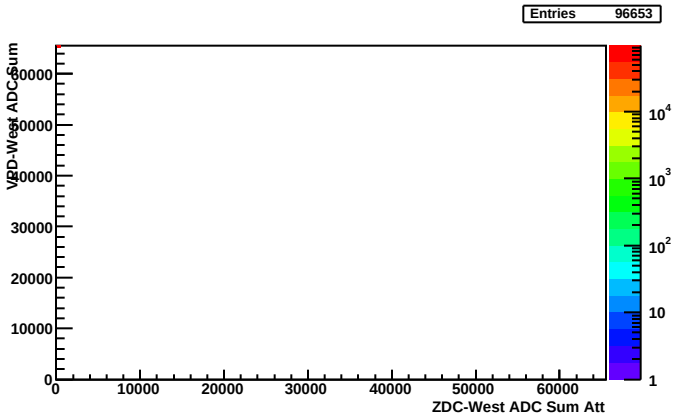

Entries 96653
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 96653
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 96653
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 96653
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 96653
VPD-East ADC Sum
ZDC-East ADC Sum Att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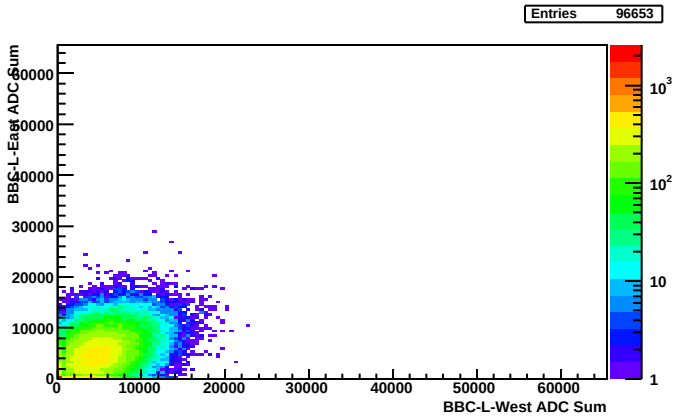
VPD-West ADC Sum
ZDC-West ADC Sum Att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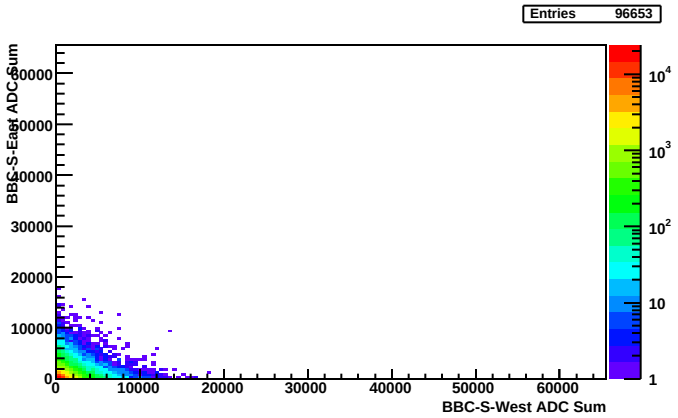
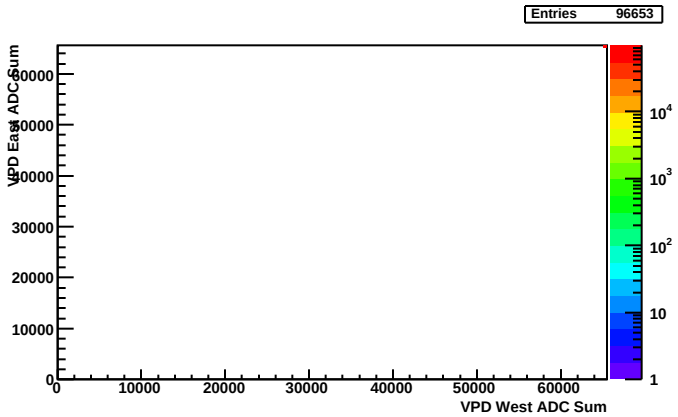
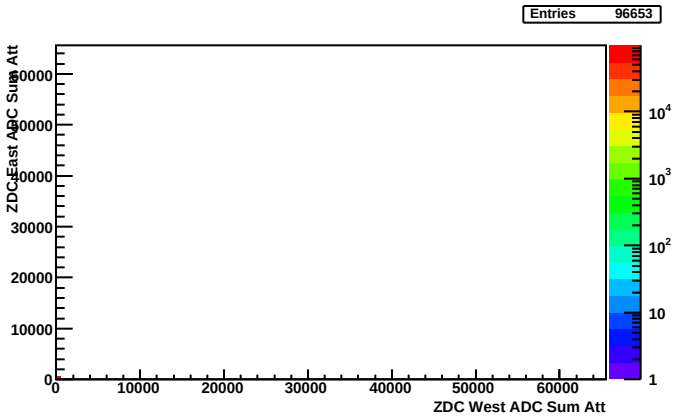
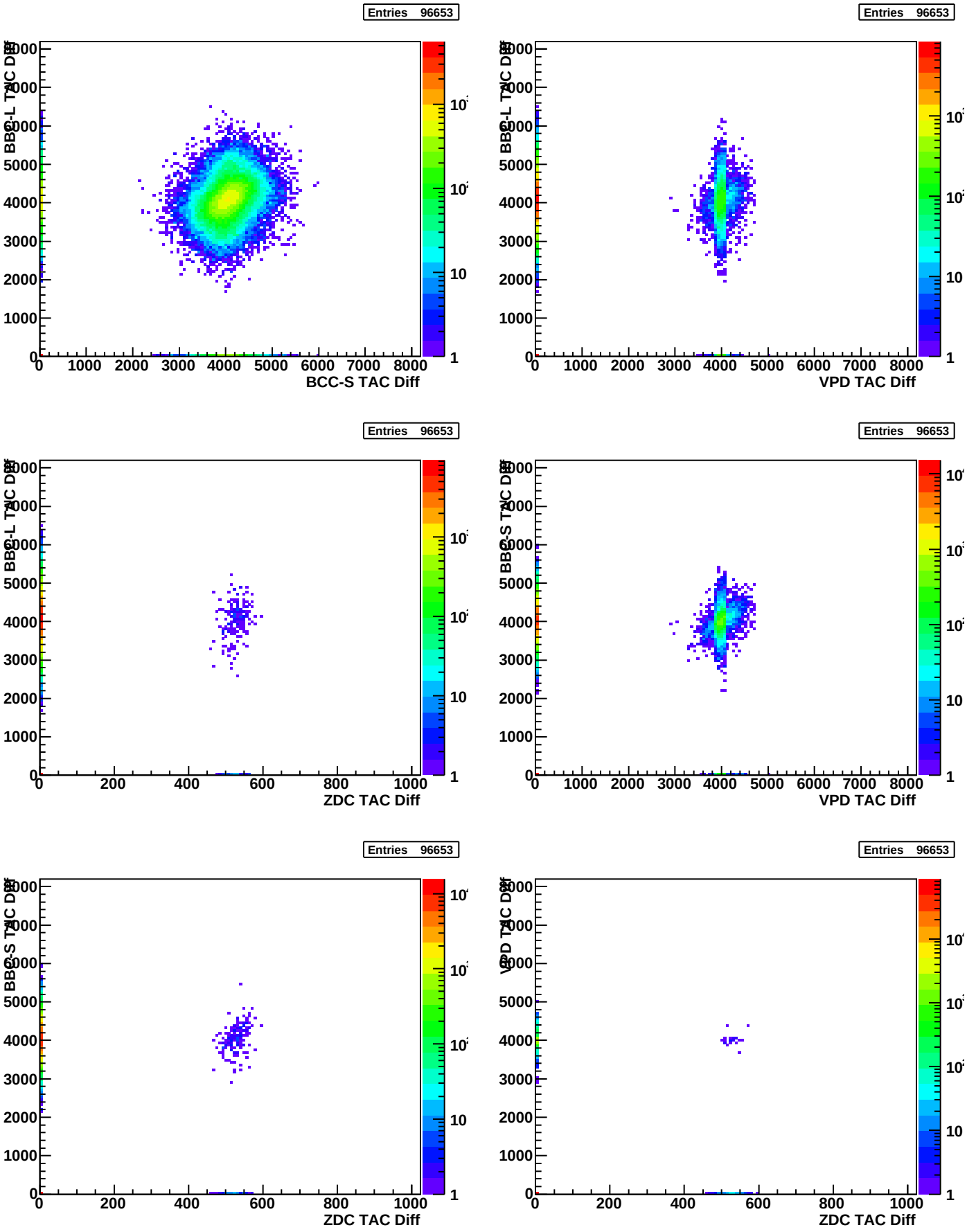
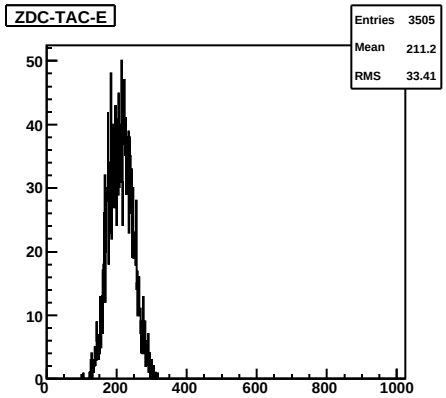
BBC-L-East ADC Sum
BBC-L-West ADC Sum

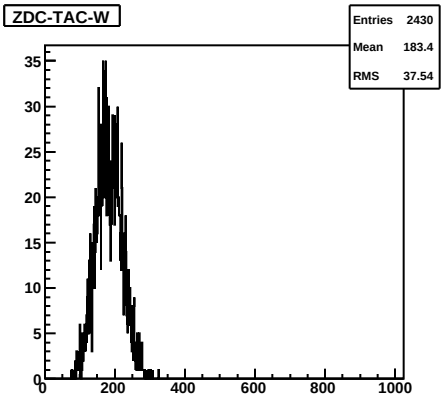

Entries 96653
BBC-S-East ADC Sum
BBC-S-West ADC Sum

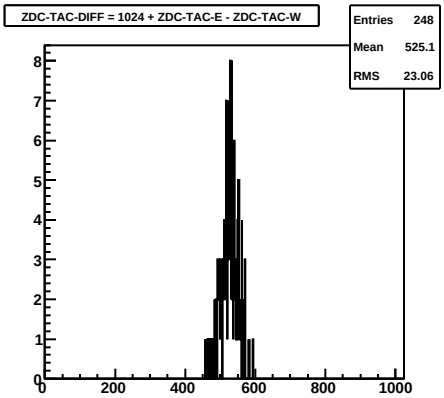

Entries 96653
VPD East ADC Sum
VPD West ADC Sum

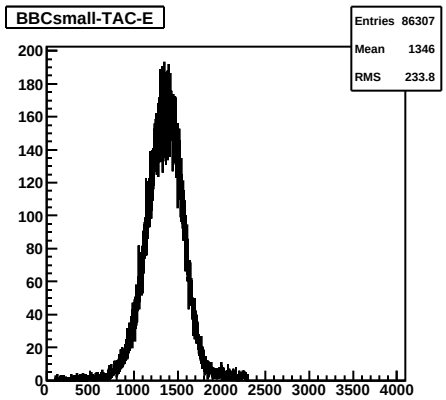

Entries 96653
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

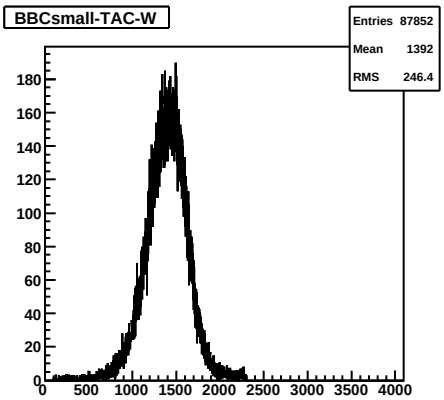

Entries 96653
BBC-L TAC Diff
BCC-S TAC Diff
Entries 96653
BBC-L TAC Diff
VPD TAC Diff
Entries 96653
BBC-L TAC Diff
ZDC TAC Diff
Entries 96653
BBC-S TAC Diff
VPD TAC Diff
Entries 96653
BBC-S TAC Diff
ZDC TAC Diff
Entries 96653
VPD TAC Diff
ZDC TAC Diff

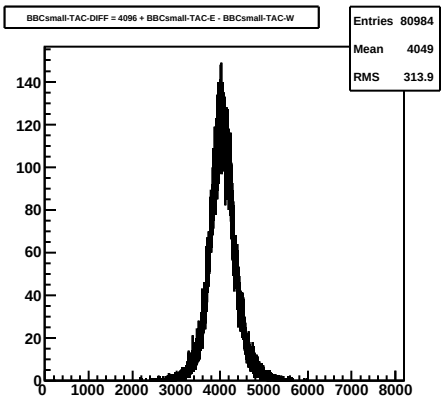

ZDC-TAC-E
Entries 3505
Mean 211.2
RMS 33.41

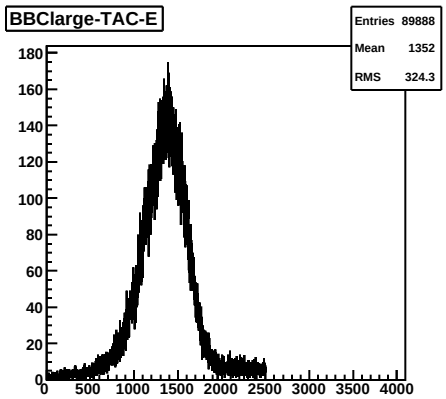

ZDC-TAC-W
Entries 2430
Mean 183.4
RMS 37.54

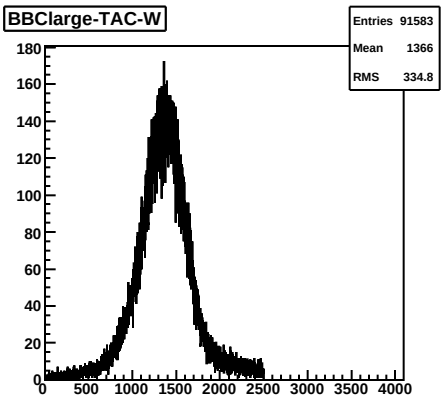

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 248
Mean 525.1
RMS 23.06

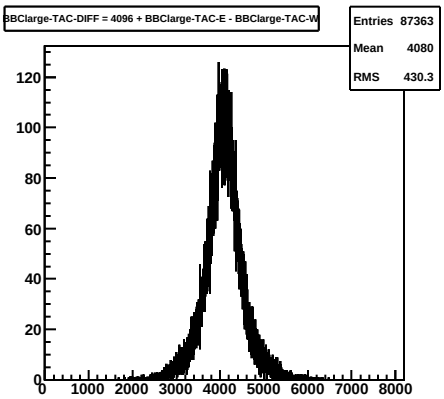

BBCsmall-TAC-E
Entries 86307
Mean 1346
RMS 233.8

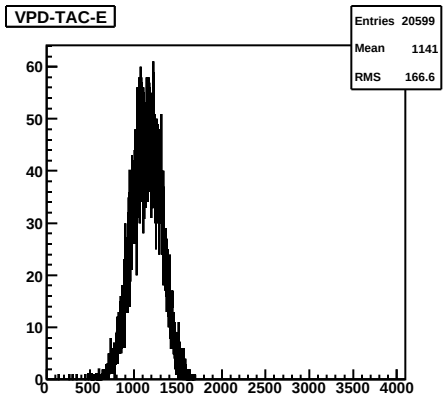

BBCsmall-TAC-W
Entries 87852
Mean 1392
RMS 246.4

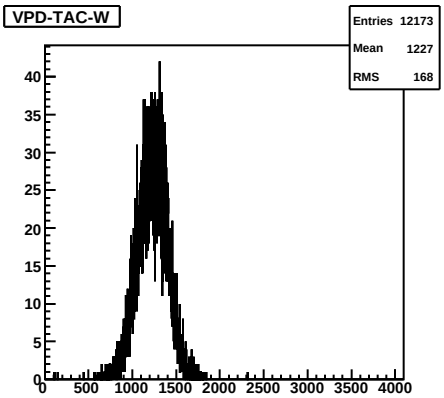

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 80984
Mean 4049
RMS 313.9

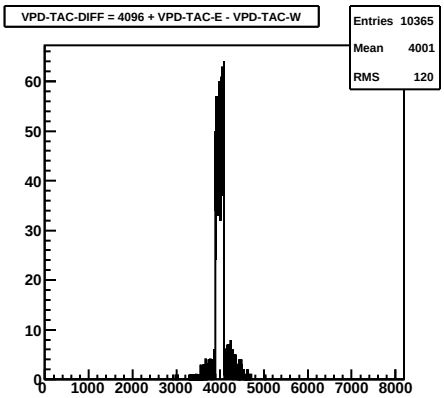

BBClarge-TAC-E
Entries 89888
Mean 1352
RMS 324.3

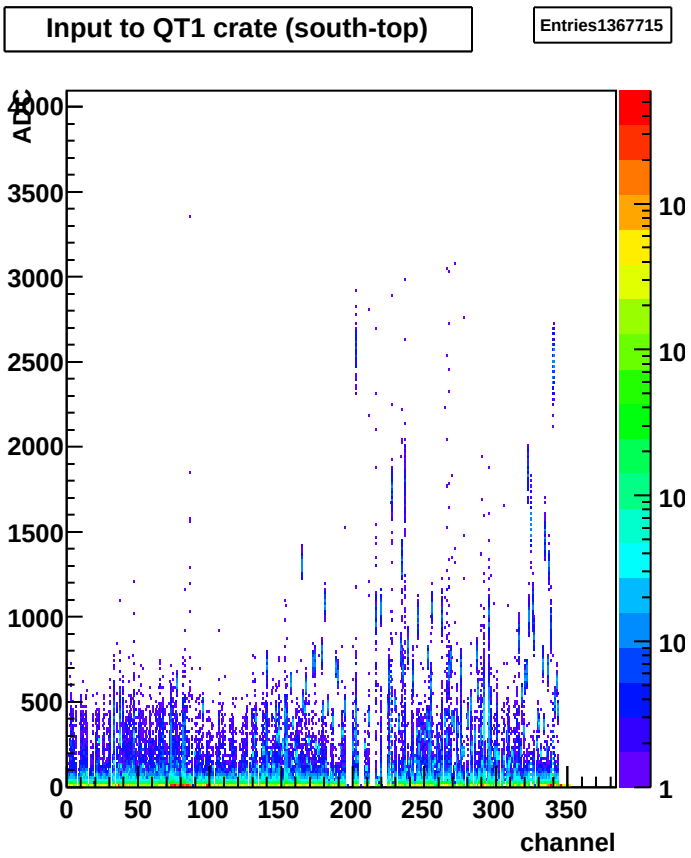

BBClarge-TAC-W
Entries 91583
Mean 1366
RMS 334.8

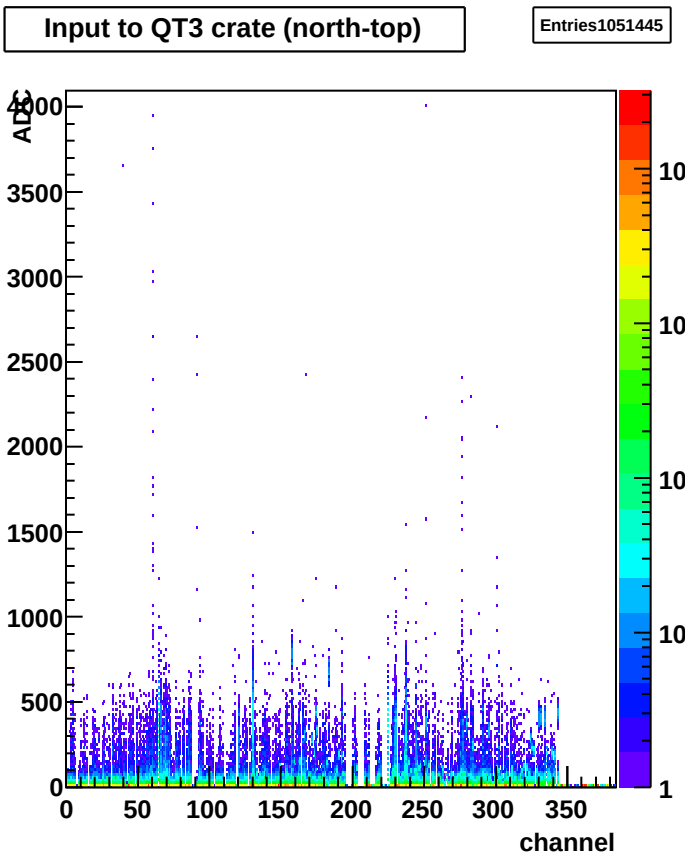

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 87363
Mean 4080
RMS 430.3

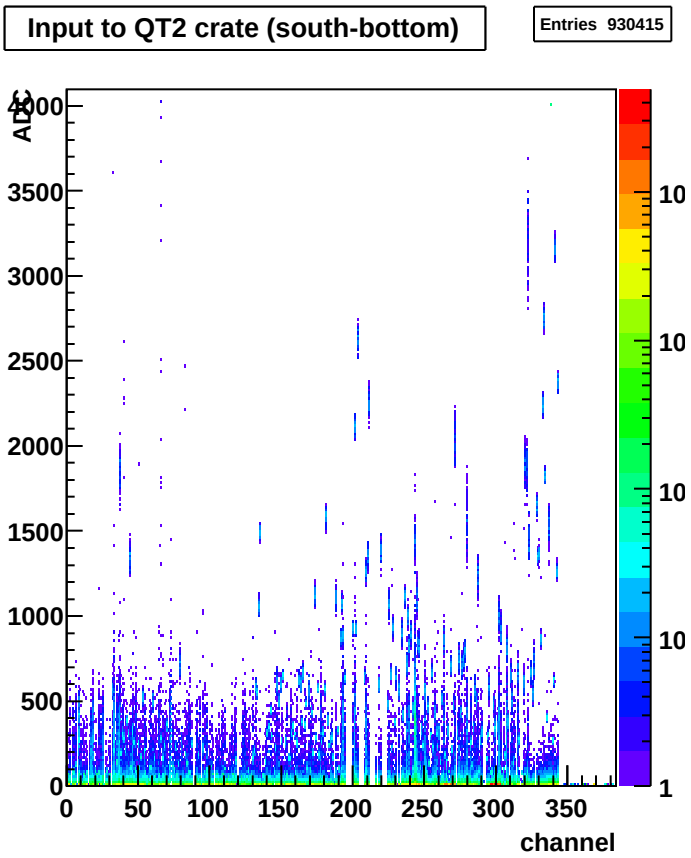

VPD-TAC-E
Entries 20599
Mean 1141
RMS 166.6

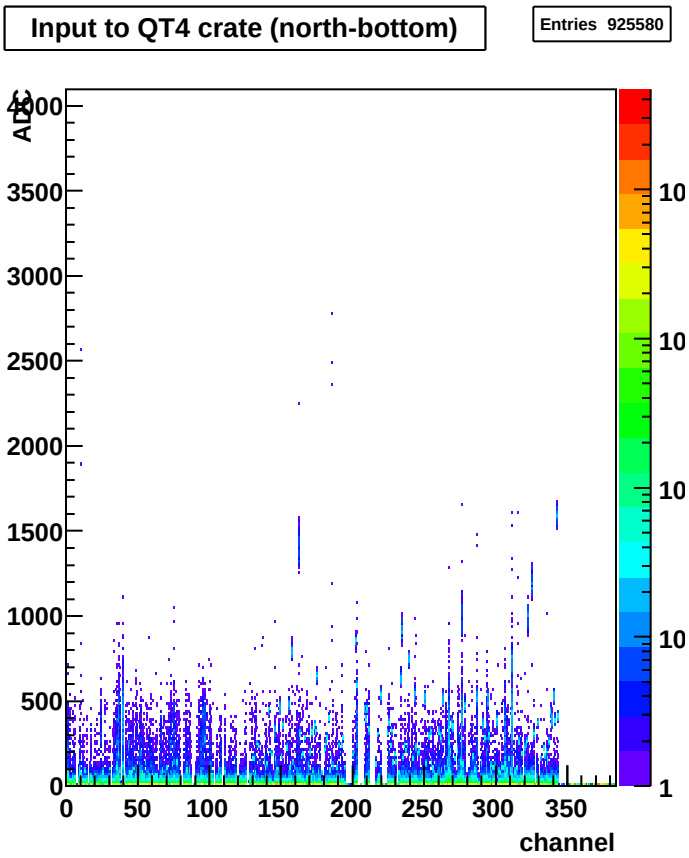

VPD-TAC-W
Entries 12173
Mean 1227
RMS 168

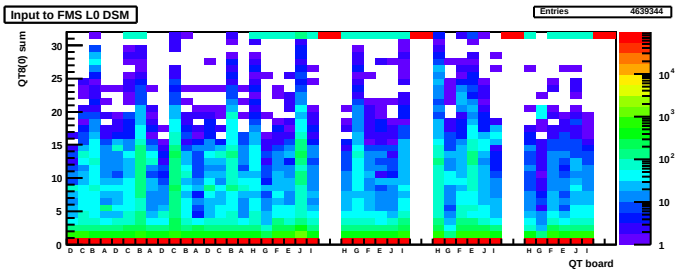

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 10365
Mean 4001
RMS 120

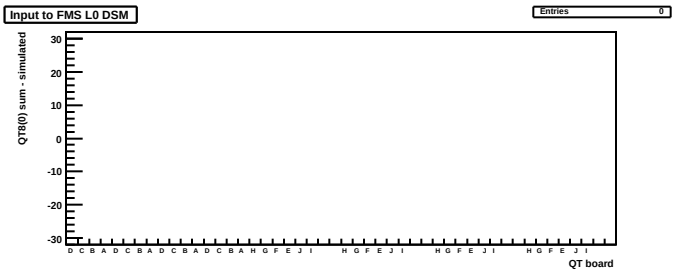

Input to QT1 crate (south-top)
Entries1367715
ADC
channel

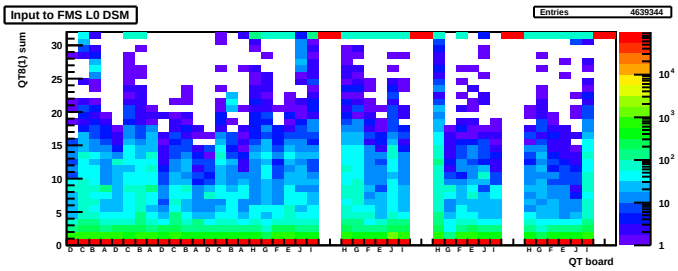

Input to QT3 crate (north-top)
Entries1051445
ADC
channel

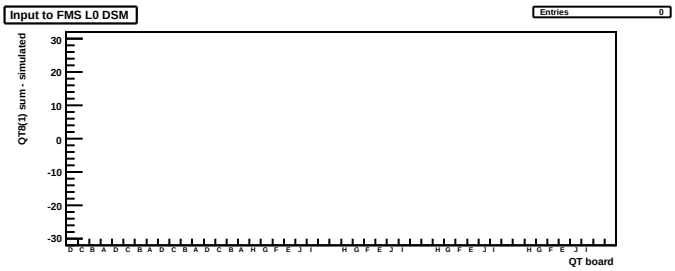

Input to QT2 crate (south-bottom)
Entries 930415
ADC
channel

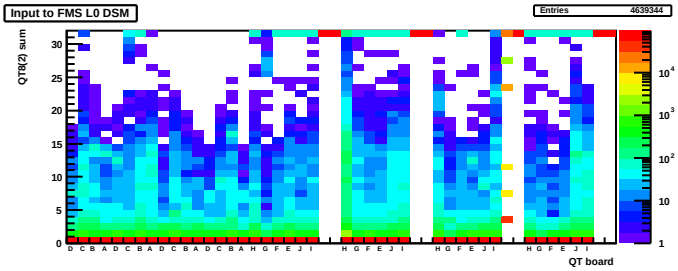

Input to QT4 crate (north-bottom)
Entries 925580
ADC
channel

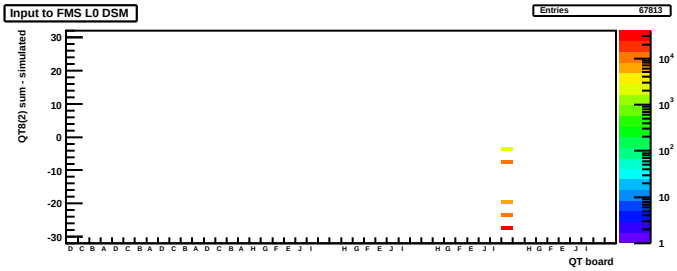

Input to FMS L0 DSM
Entries 4639344
QT8(0) sum
QT board

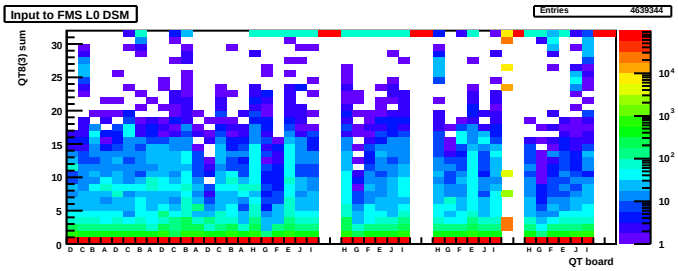

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

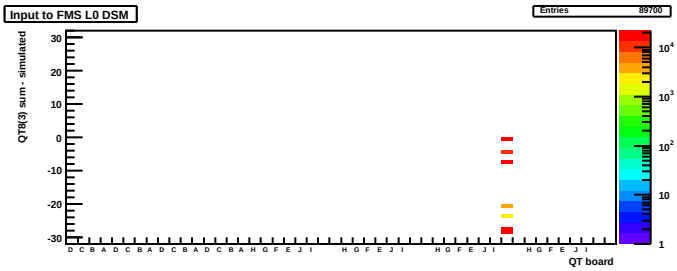

Input to FMS L0 DSM
Entries 4639344
QT8(1) sum
QT board

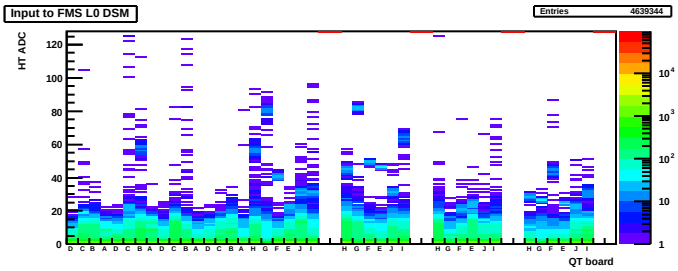

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

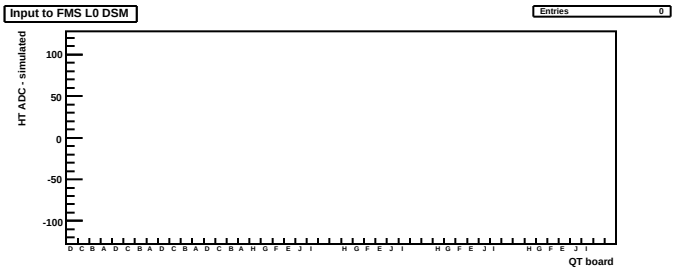

Input to FMS L0 DSM
Entries 4639344
QT8(2) sum
QT board

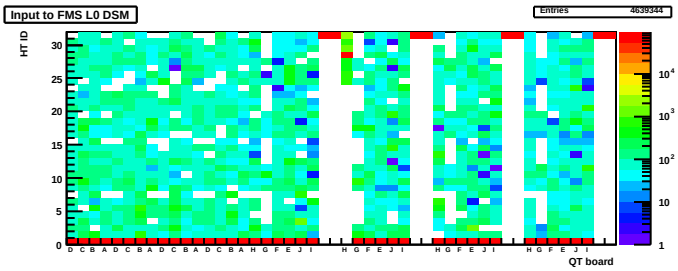

Input to FMS L0 DSM
Entries 67813
QT8(2) sum - simulated
QT board

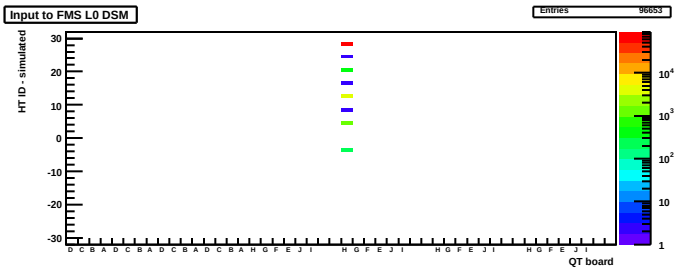

Input to FMS L0 DSM
Entries 4639344
QT8(3) sum
QT board

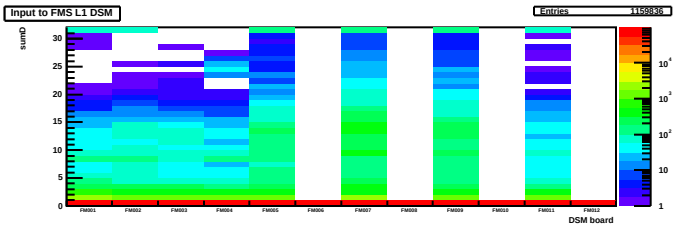

Input to FMS L0 DSM
Entries 89700
QT8(3) sum - simulated
QT board

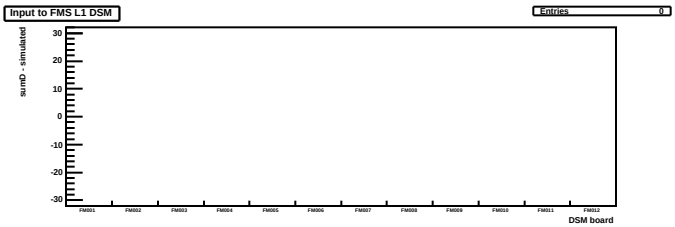

Input to FMS L0 DSM
Entries 4639344
HT ADC
QT board

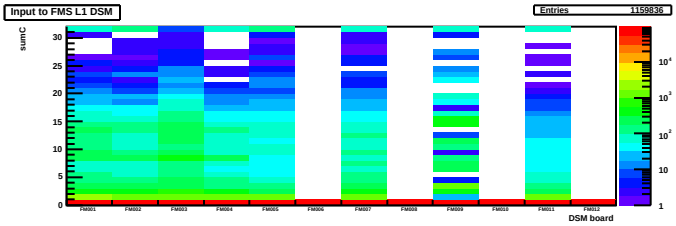

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

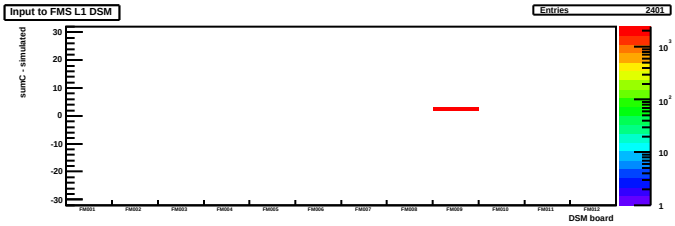

Input to FMS L0 DSM
Entries 4639344
HT ID
QT board

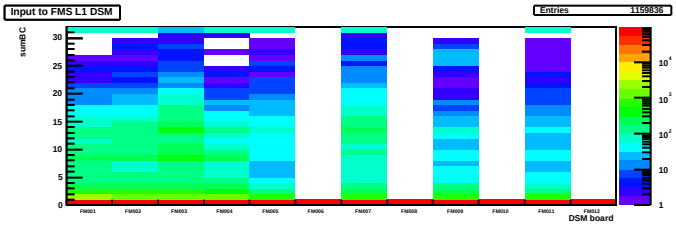

Input to FMS L0 DSM
Entries 96653
HT ID - simulated
QT board

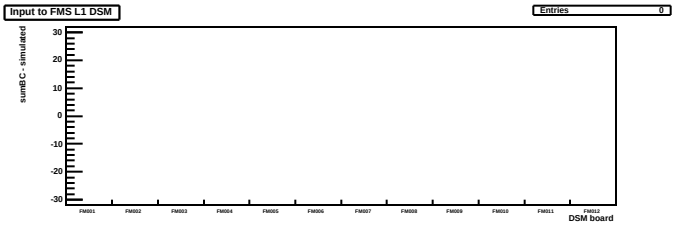

Input to FMS L1 DSM
Entries 1159836
sumD
DSM board

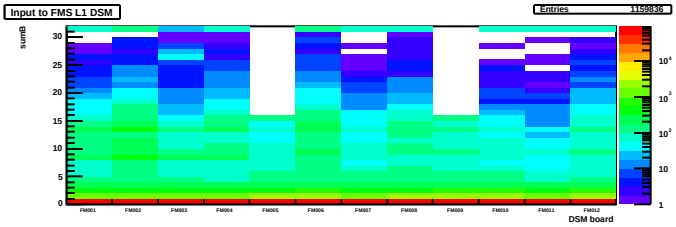

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

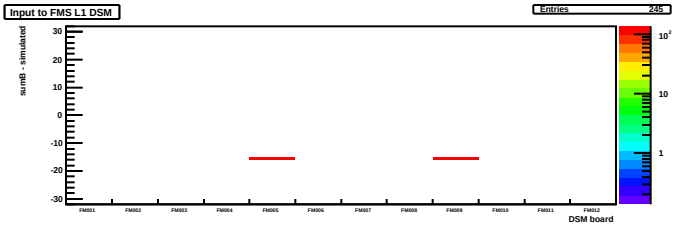

Input to FMS L1 DSM
Entries 1159836
sumC
DSM board

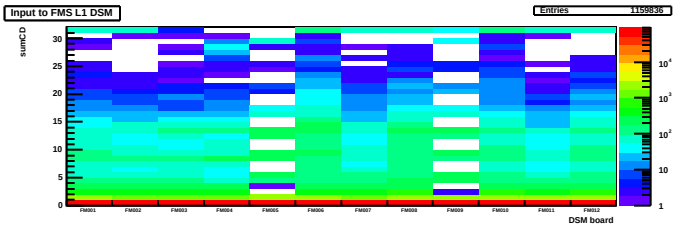

Input to FMS L1 DSM
Entries 2401
sumC - simulated
DSM board

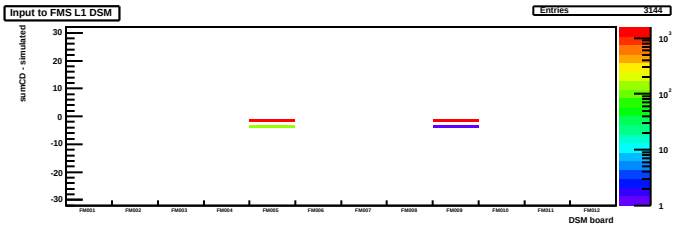

Input to FMS L1 DSM
Entries 1159836
sumBC
DSM board

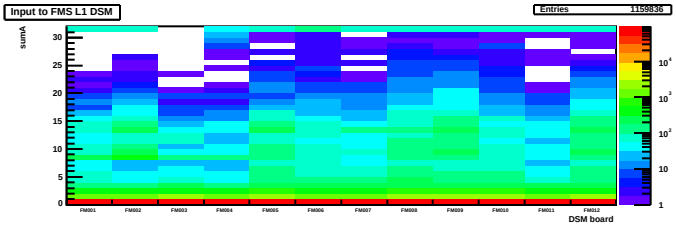

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

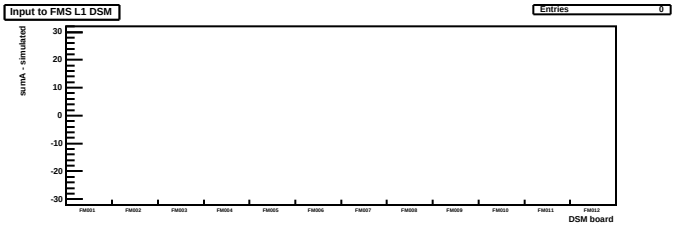

Input to FMS L1 DSM
Entries 1159836
sumB
DSM board

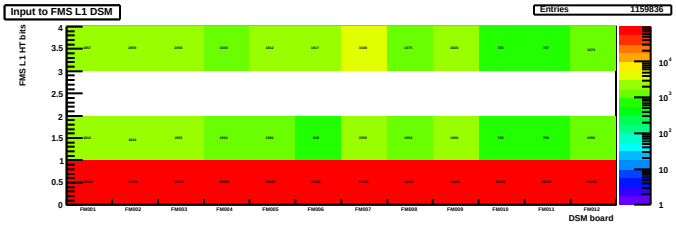

Input to FMS L1 DSM
Entries 245
sumB - simulated
DSM board

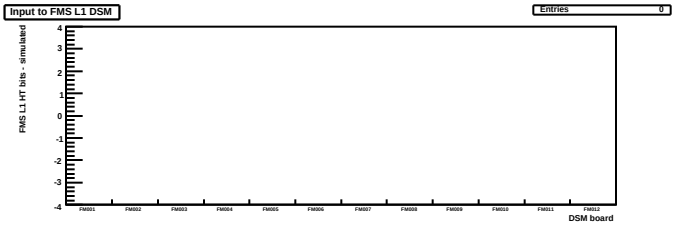

Input to FMS L1 DSM
Entries 1159836
sumCD
DSM board

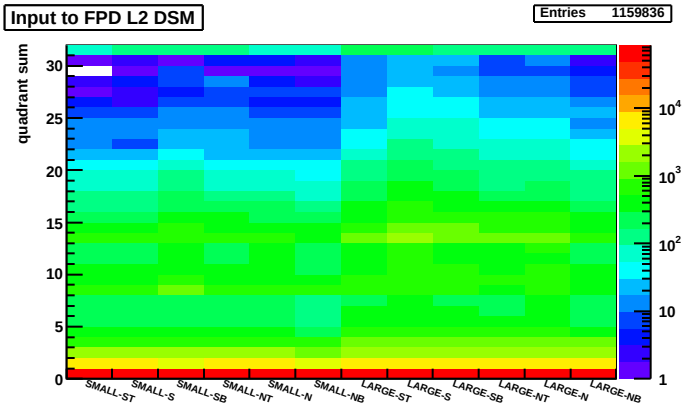

Input to FMS L1 DSM
Entries 3144
sumCD - simulated
DSM board

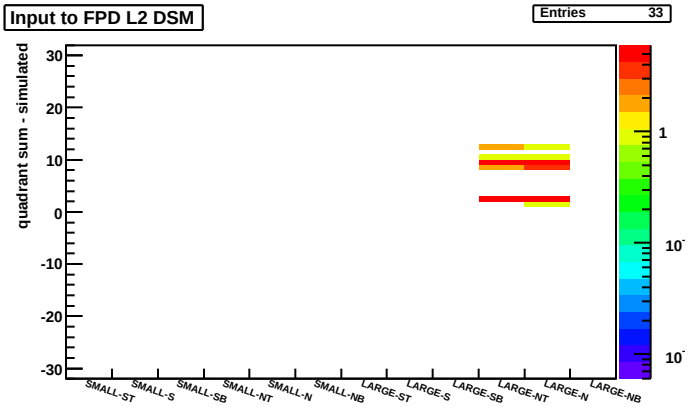

Input to FMS L1 DSM
Entries 1159836
sumA
DSM board

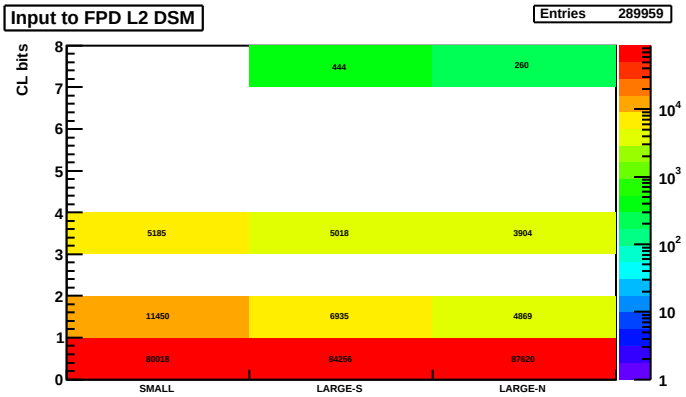

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

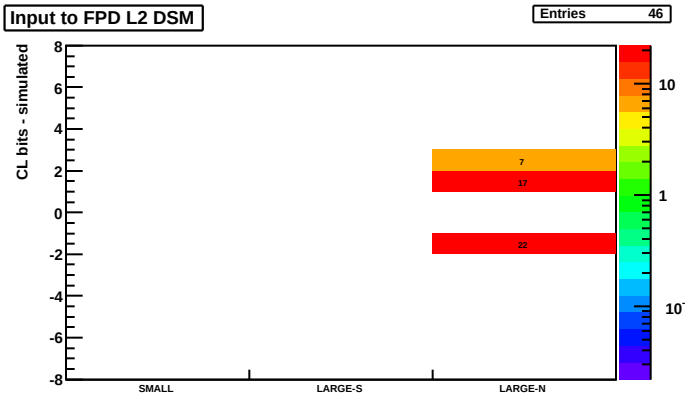

Input to FMS L1 DSM
Entries 1159836
FMS L1 HT bits
DSM board

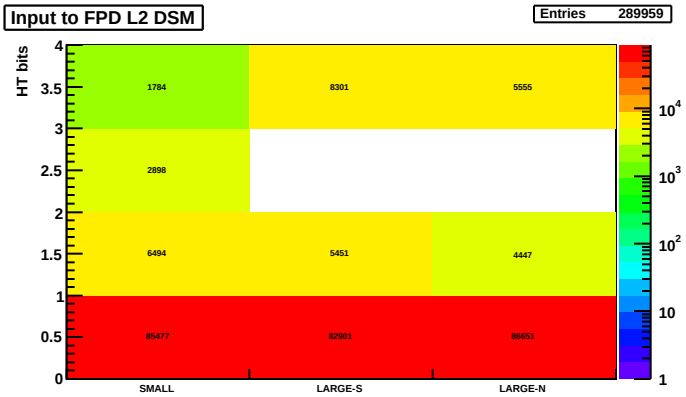

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

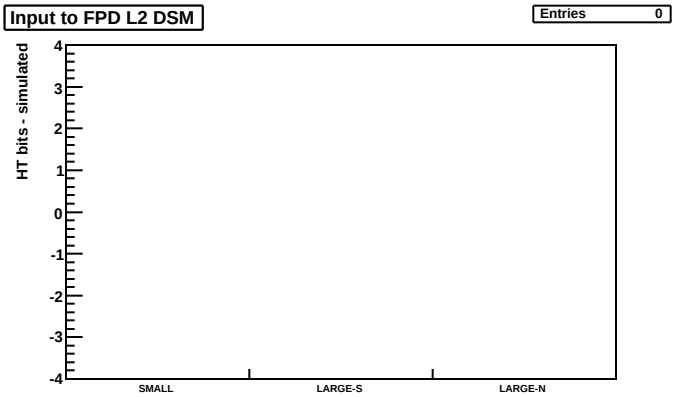

Input to FPD L2 DSM
Entries 1159836
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

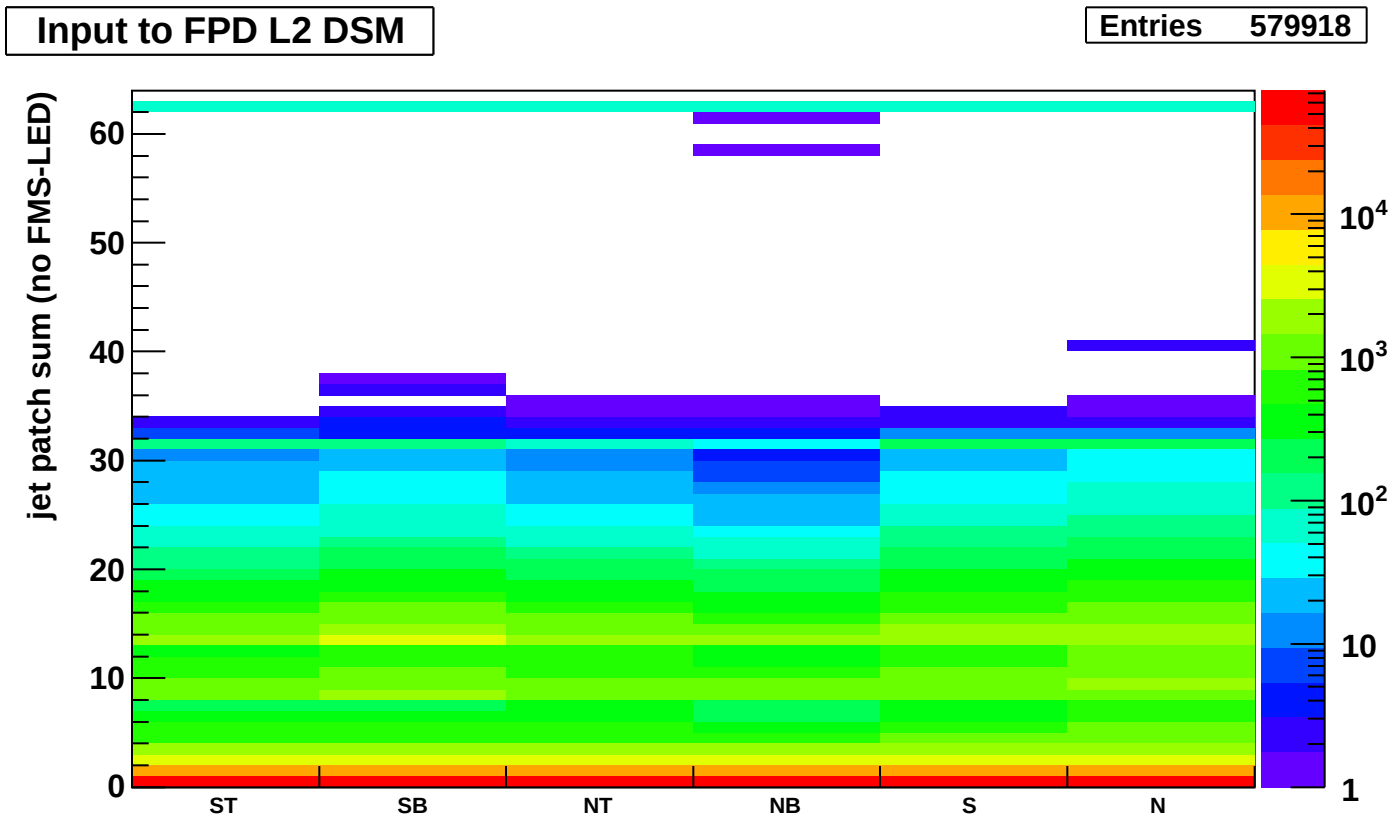

Input to FPD L2 DSM
Entries 33
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

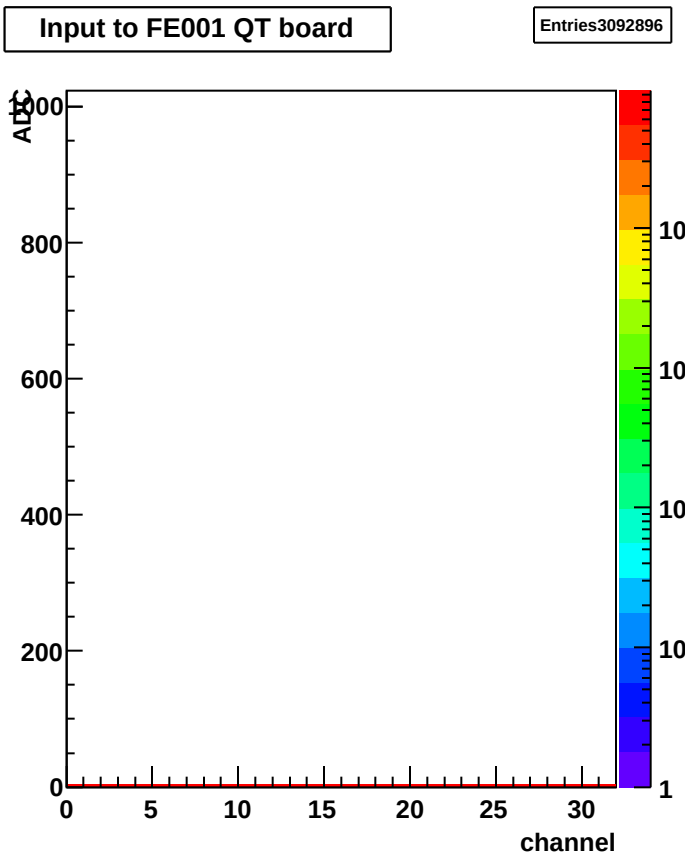

Input to FPD L2 DSM
Entries 289959
CL bits
444
260
5185
5018
3904
11450
6935
4869
80018
84256
87620
SMALL
LARGE-S
LARGE-N

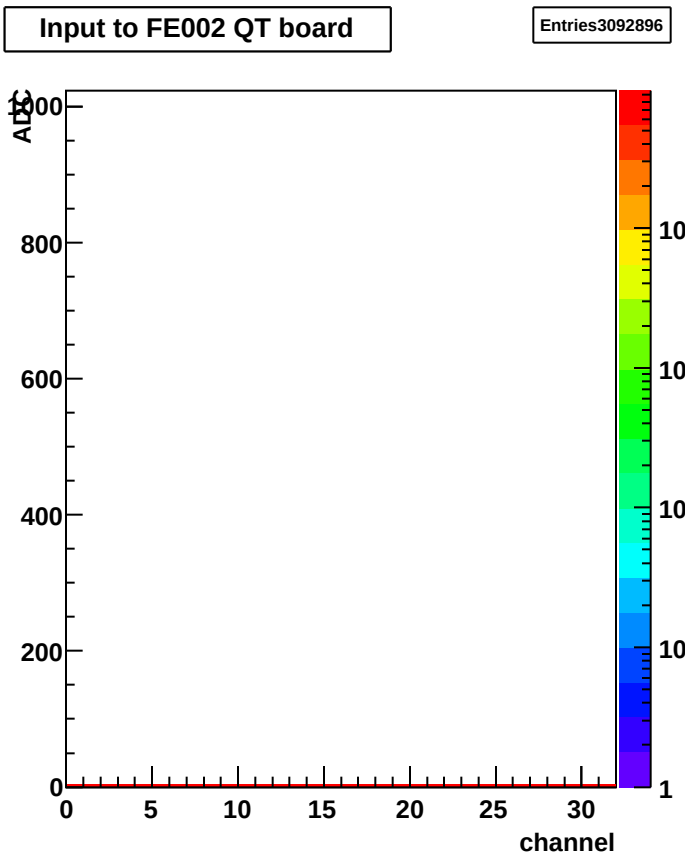

Input to FPD L2 DSM
Entries 46
CL bits - simulated
7
17
22
SMALL
LARGE-S
LARGE-N

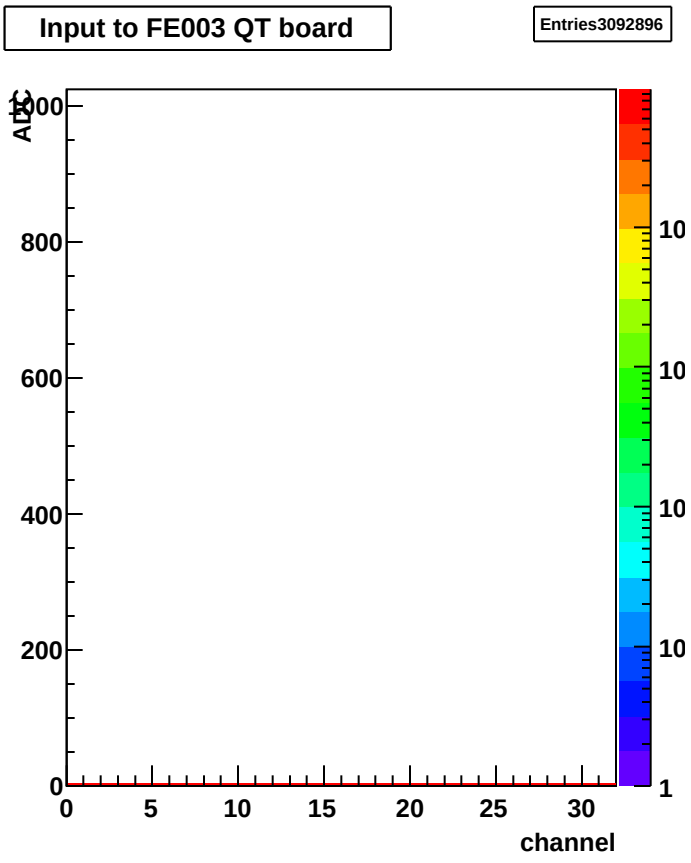

Input to FPD L2 DSM
Entries 289959
HT bits
1784
8301
5555
2898
6494
5451
4447
85477
82901
86651
SMALL
LARGE-S
LARGE-N

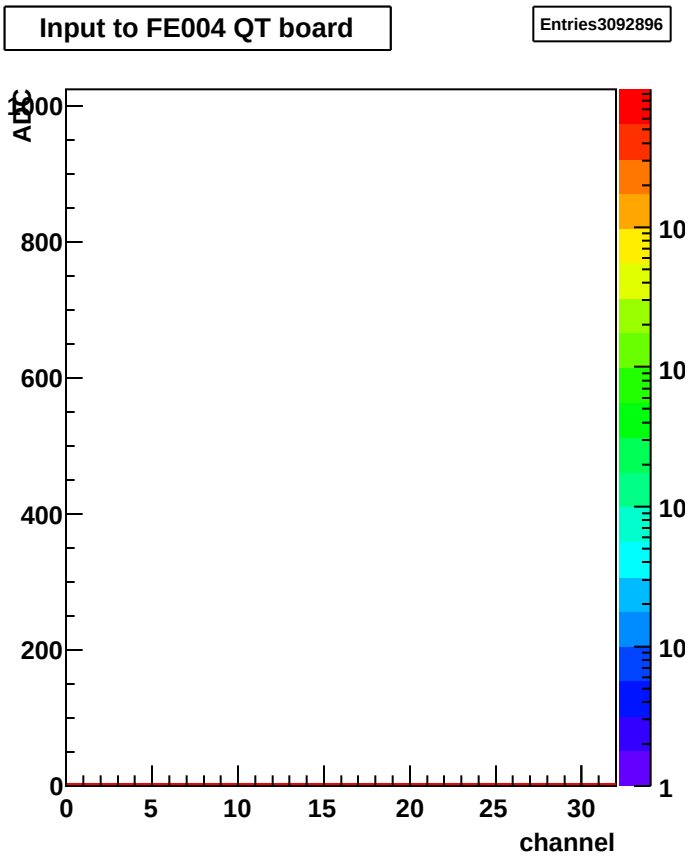

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

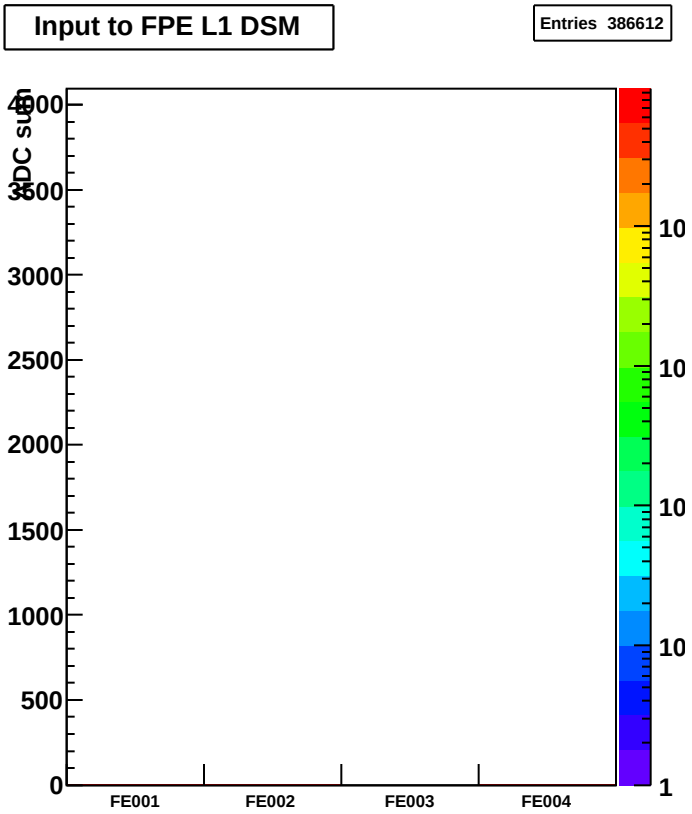

Input to FPD L2 DSM
Entries 579918
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

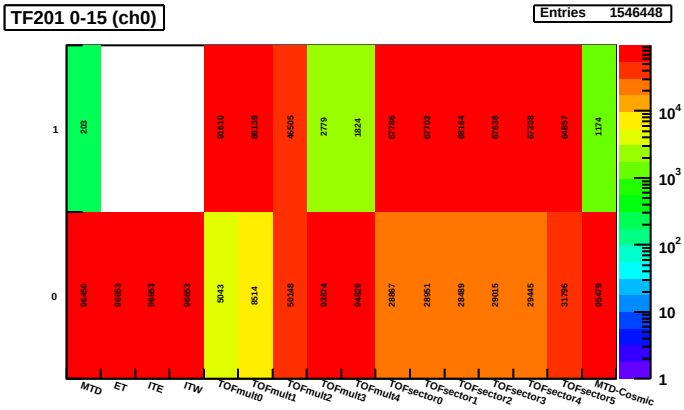

Input to FE001 QT board
Entries3092896
ADC
channel

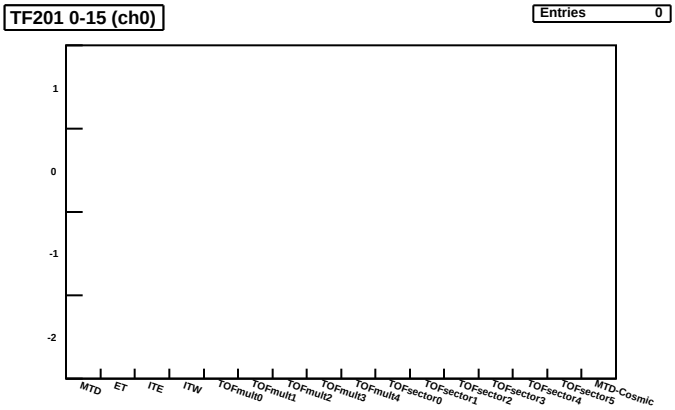

Input to FE002 QT board
Entries3092896
ADC
channel

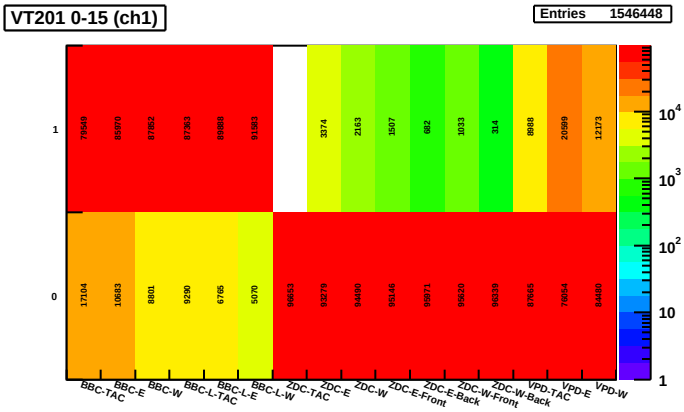

Input to FE003 QT board
Entries3092896
ADC
channel

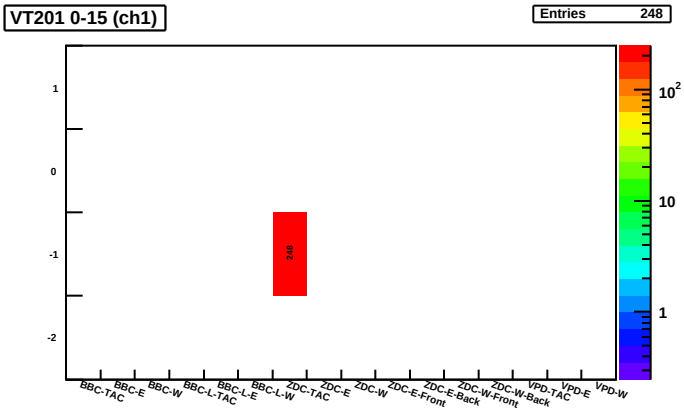

Input to FE004 QT board
Entries3092896
ADC
channel

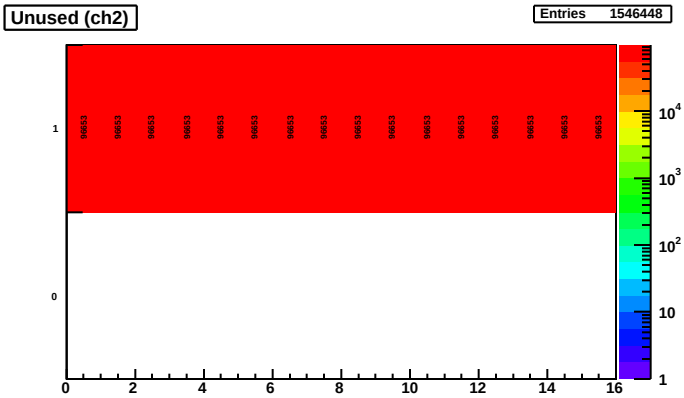

Input to FPE L1 DSM
Entries 386612
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

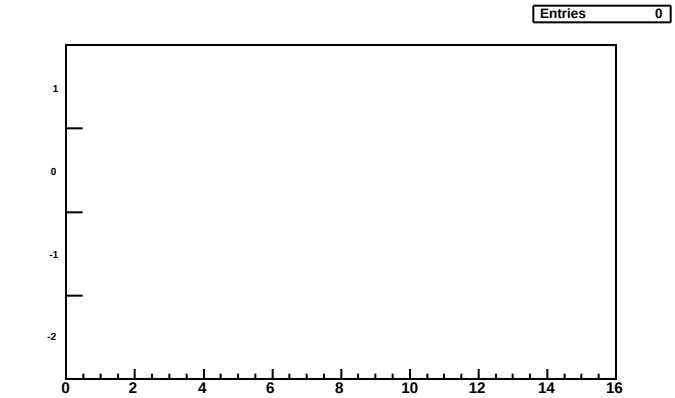

TF201 0-15 (ch0)
Entries 1546448
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

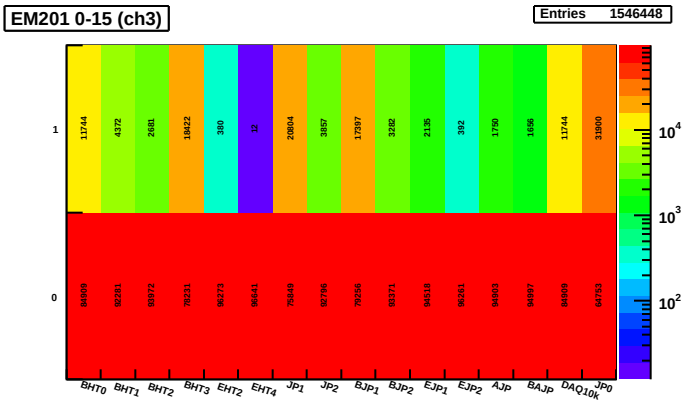

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 1546448
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 248
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 1546448


Entries 0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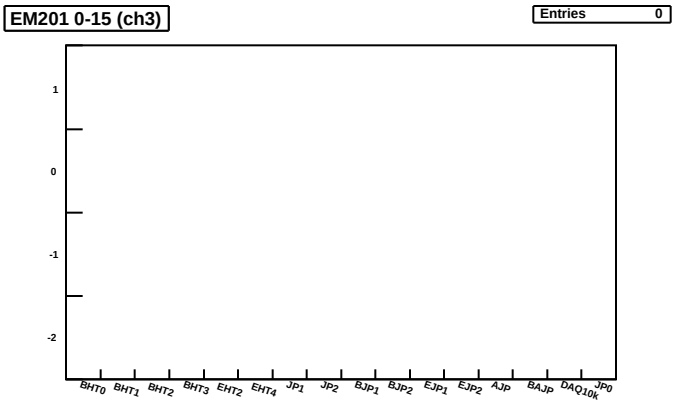
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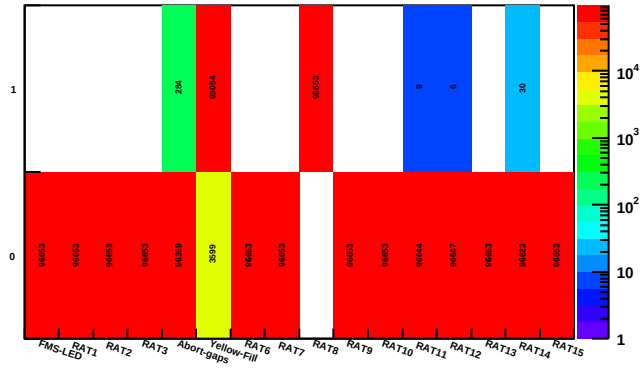
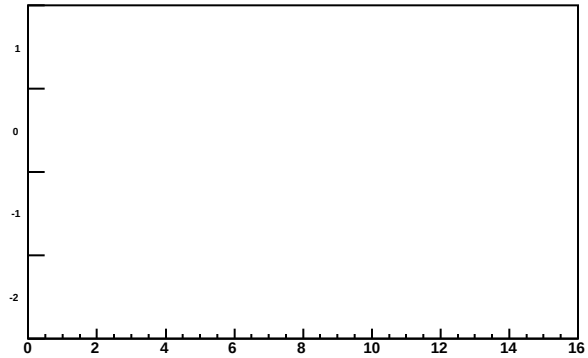
RAT board (ch4)





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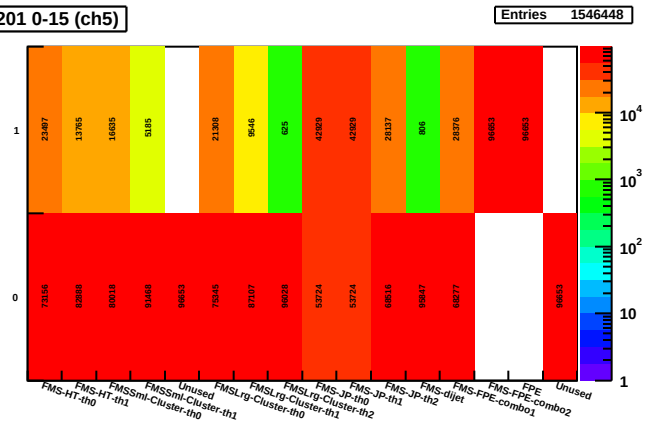

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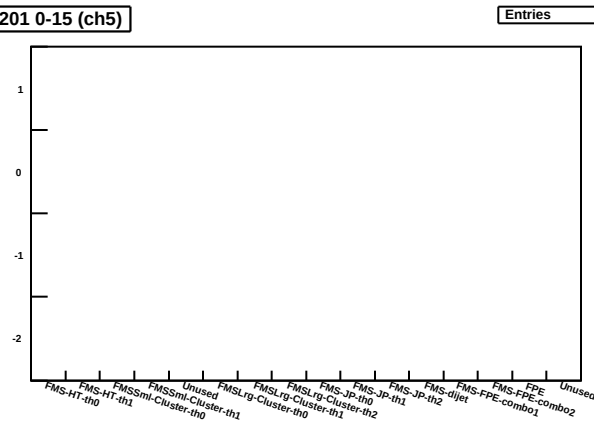

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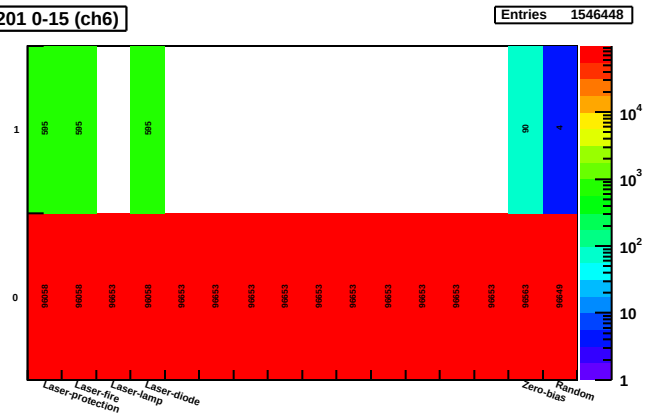

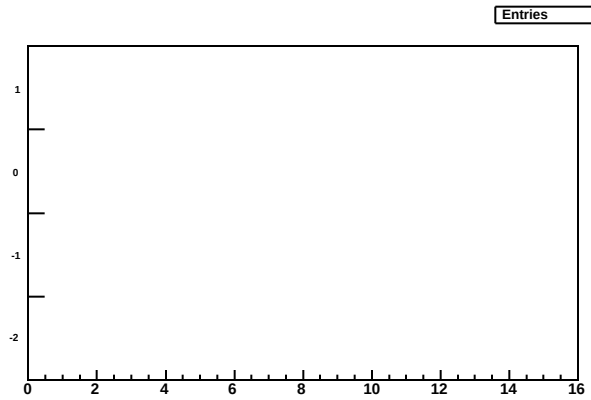
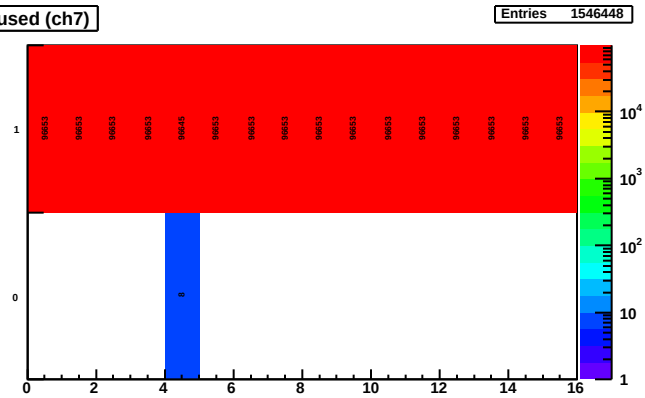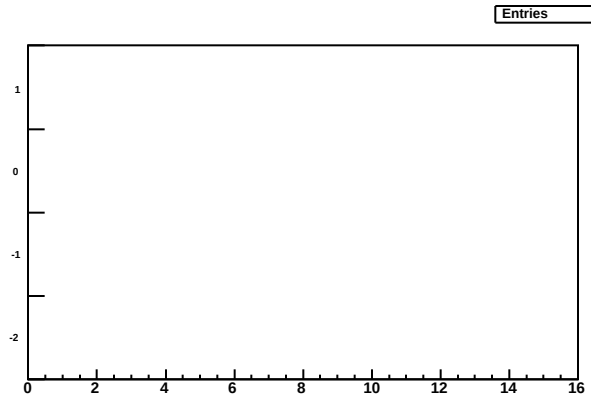
Unused (ch7)