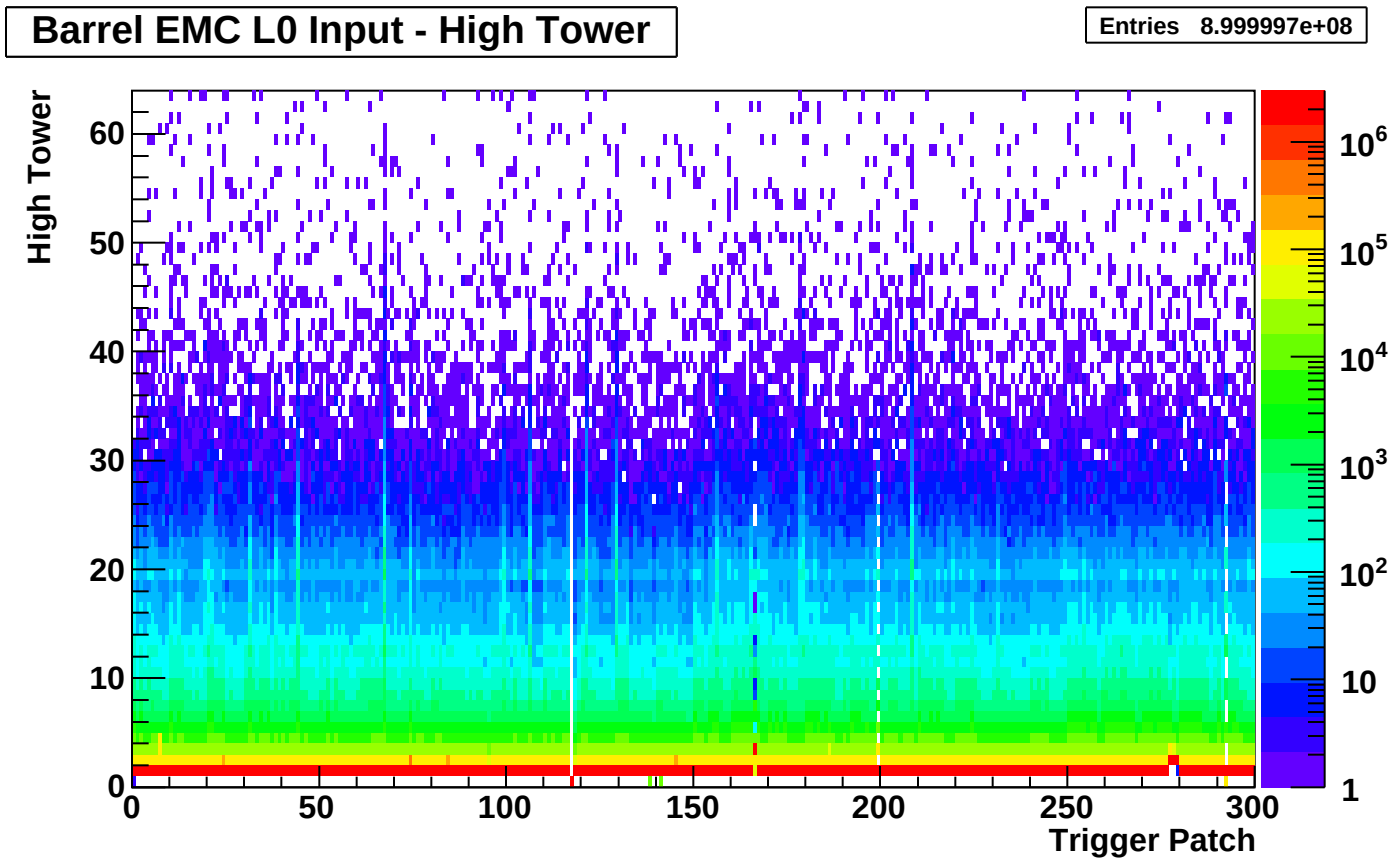
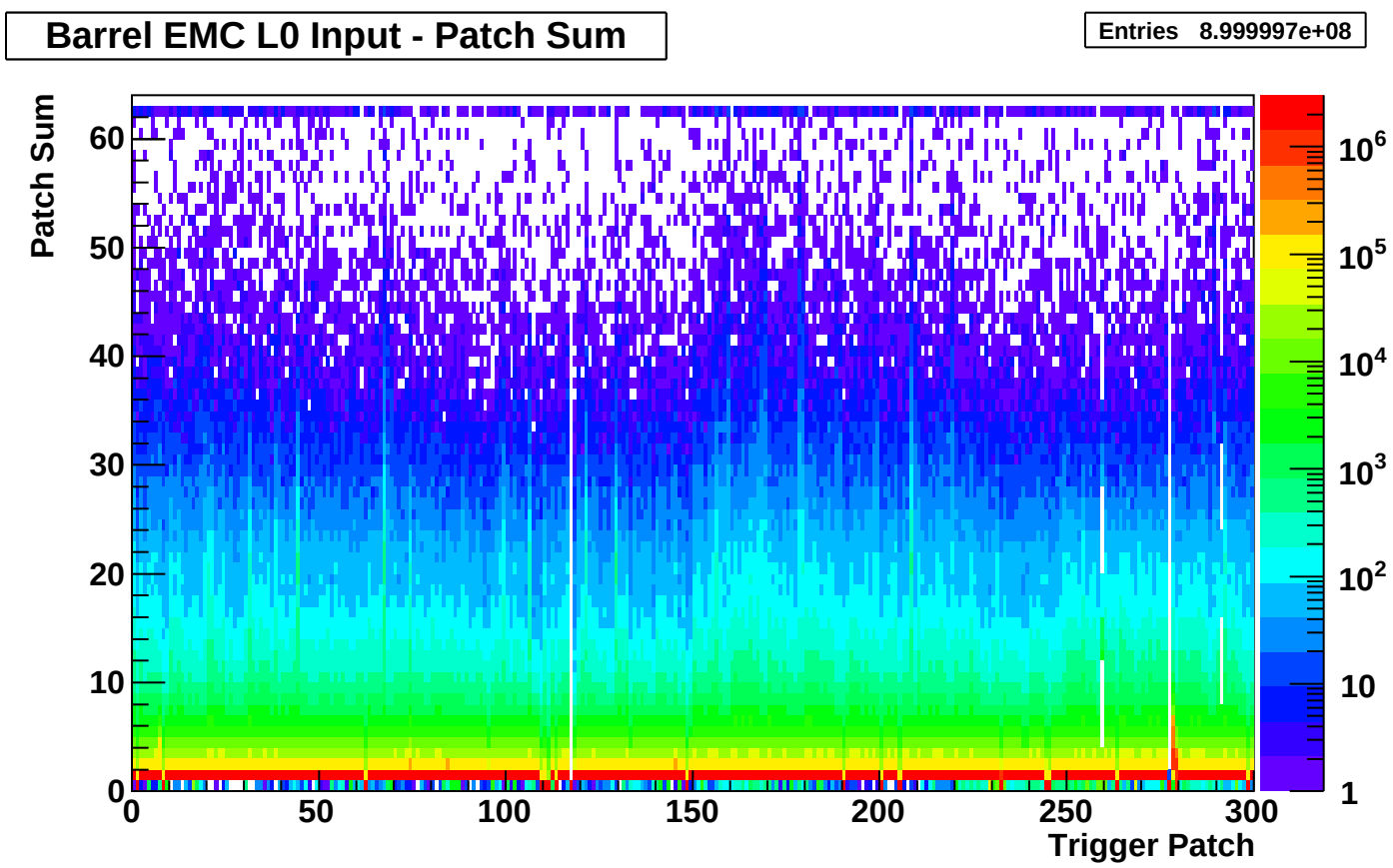
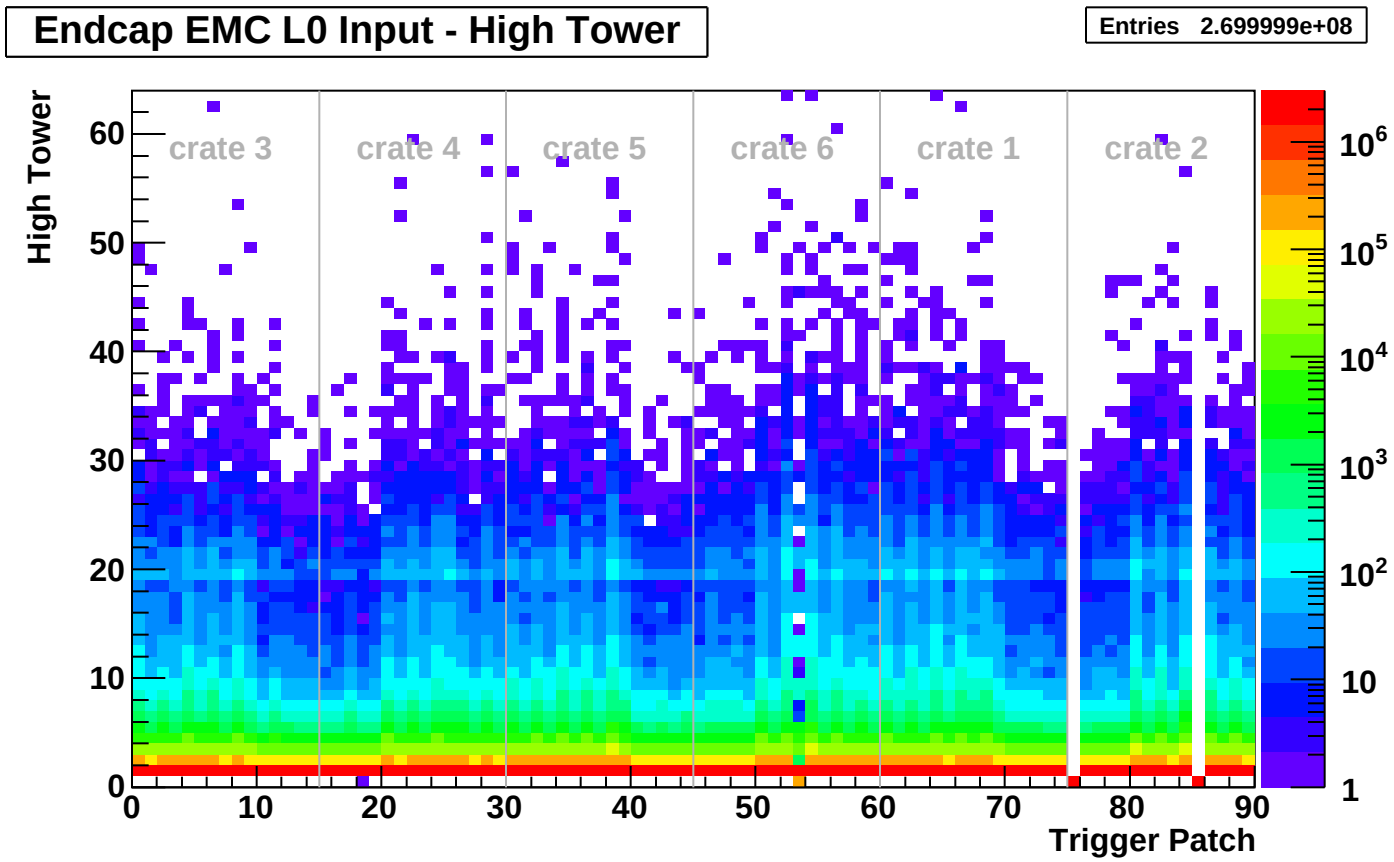
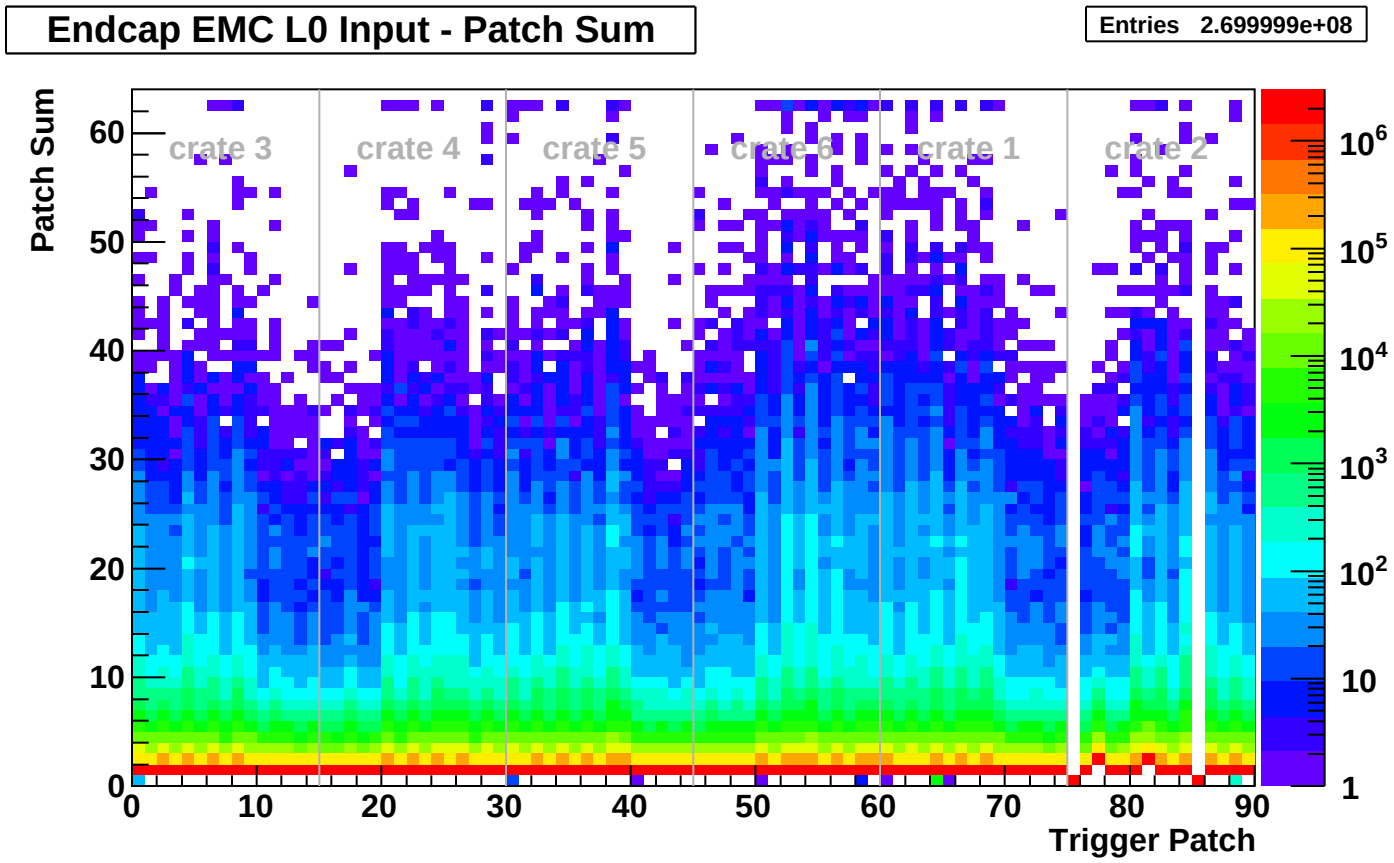
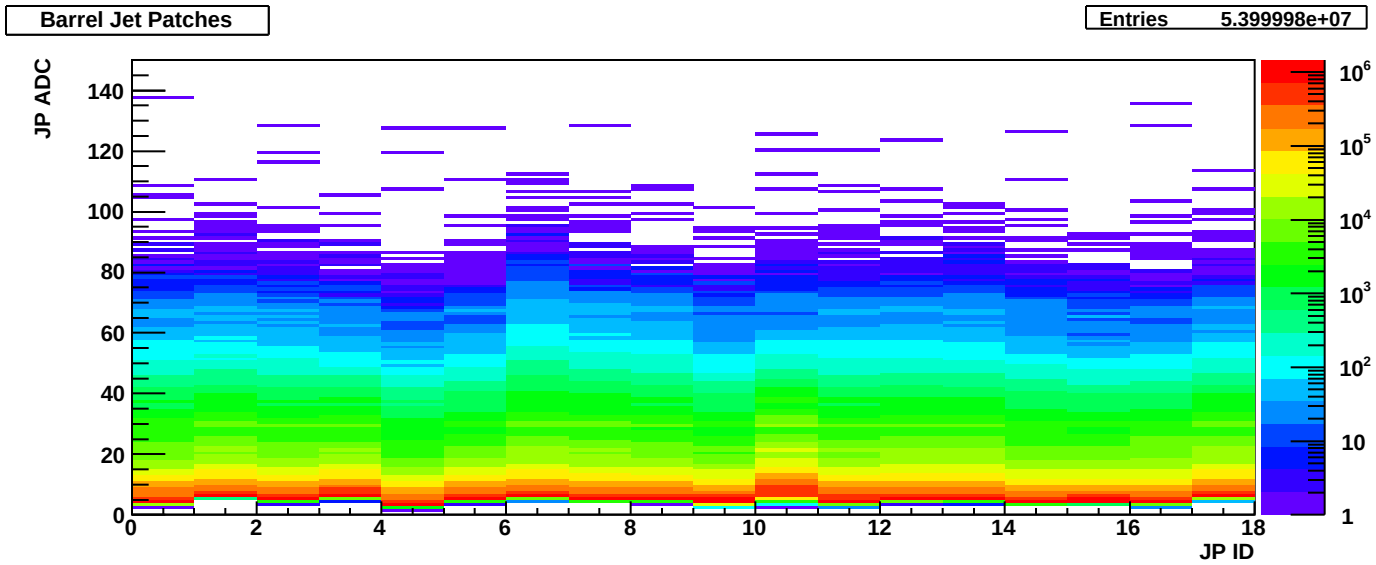
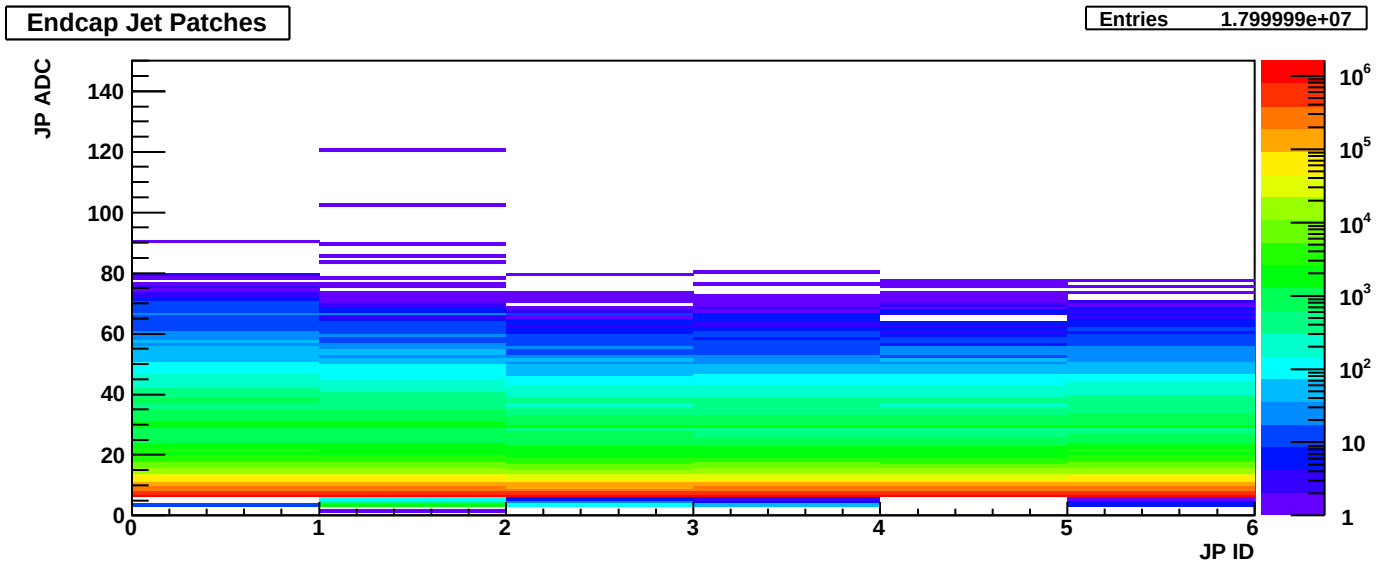

Barrel EMC L0 Input - High Tower
Entries 8.999997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

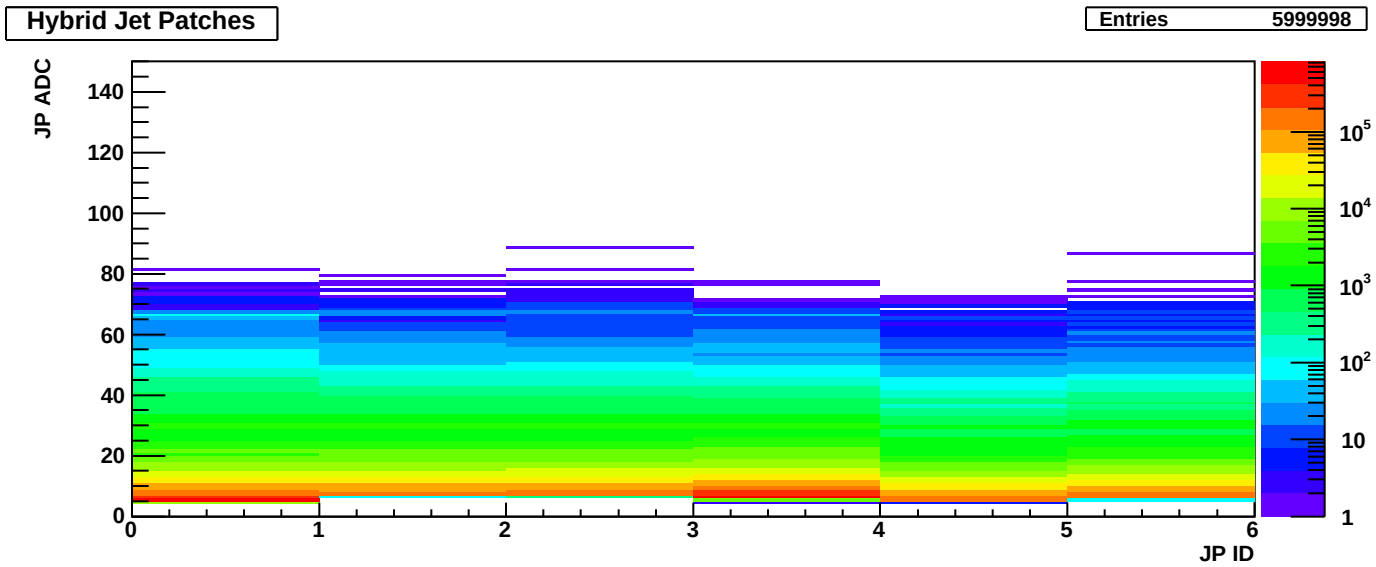

Barrel EMC L0 Input - Patch Sum
Entries 8.999997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

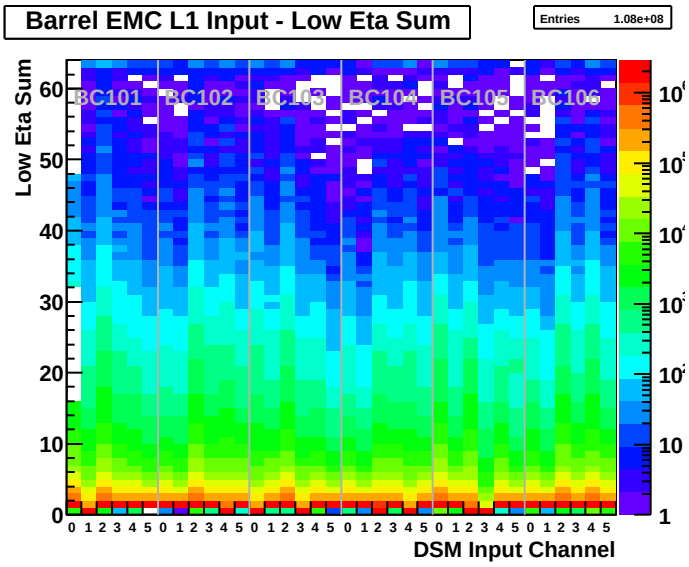

Endcap EMC L0 Input - High Tower
Entries 2.699999e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

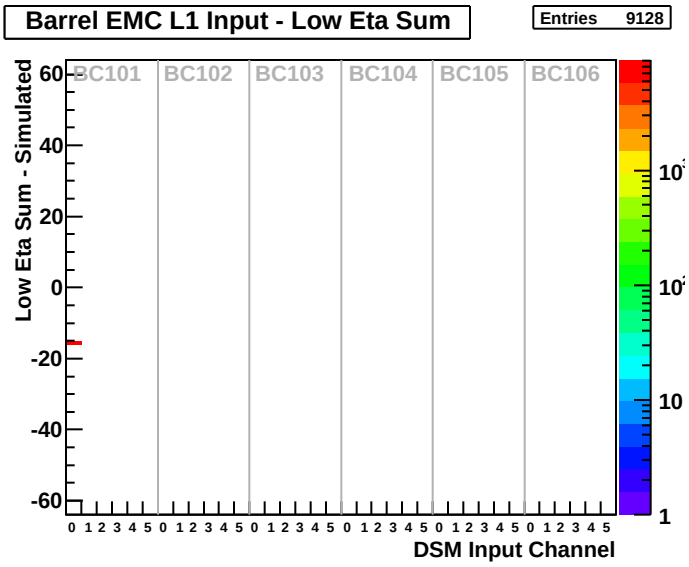

Endcap EMC L0 Input - Patch Sum
Entries 2.699999e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

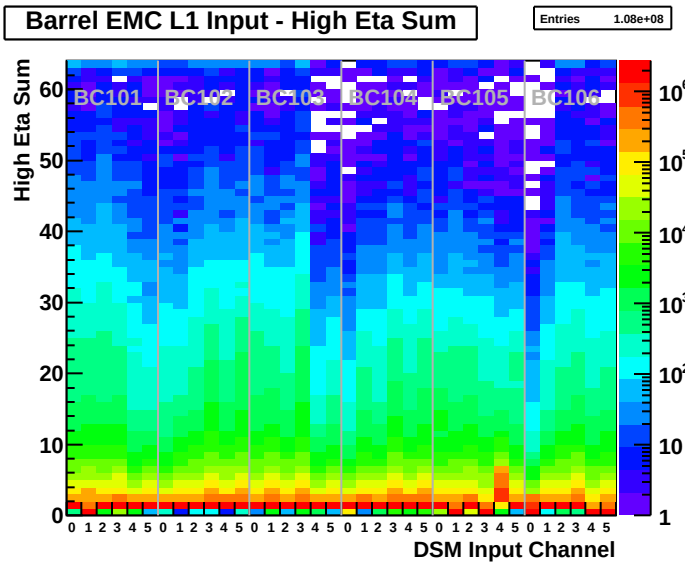

Barrel Jet Patches
Entries 5.399998e+07
JP ADC
JP ID

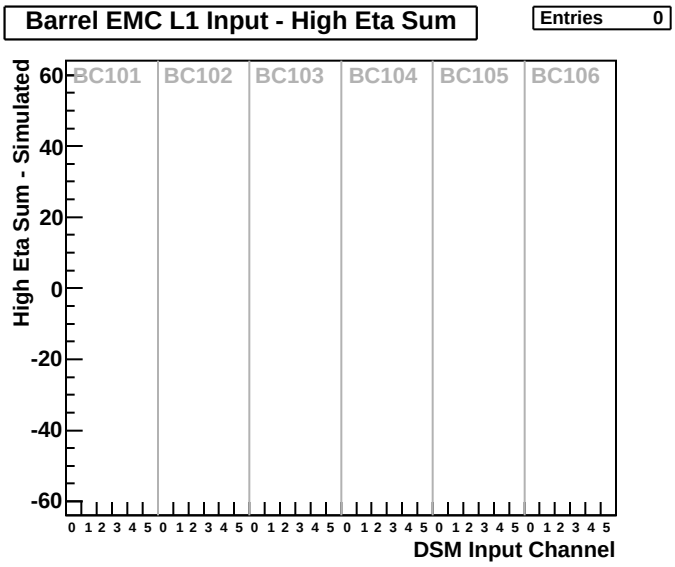

Endcap Jet Patches
Entries 1.799999e+07
JP ADC
JP ID

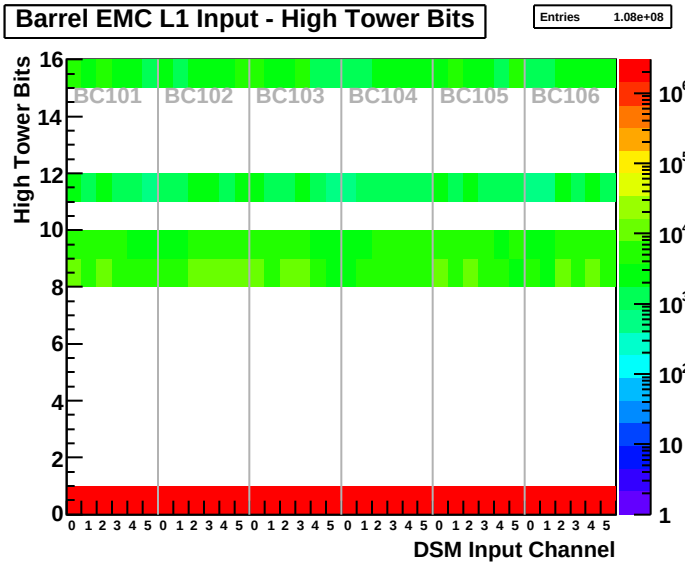

Hybrid Jet Patches
Entries 5999998
JP ADC
JP ID

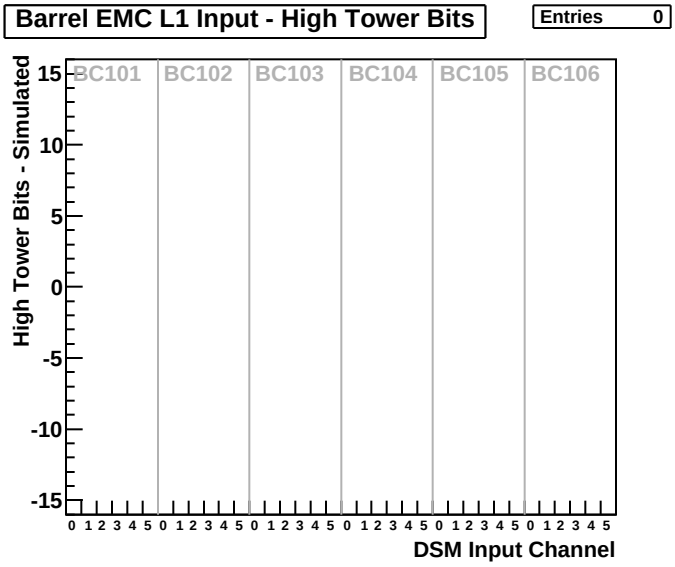

Barrel EMC L1 Input - Low Eta Sum
Entries 1.08e+08
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

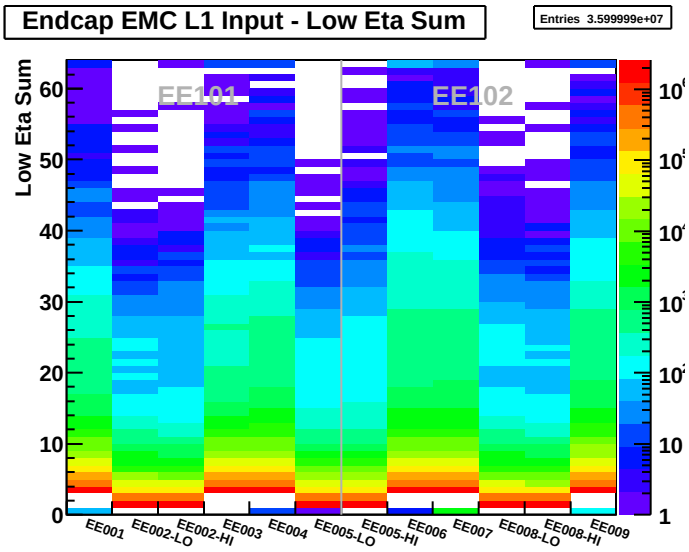

Barrel EMC L1 Input - Low Eta Sum
Entries 9128
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

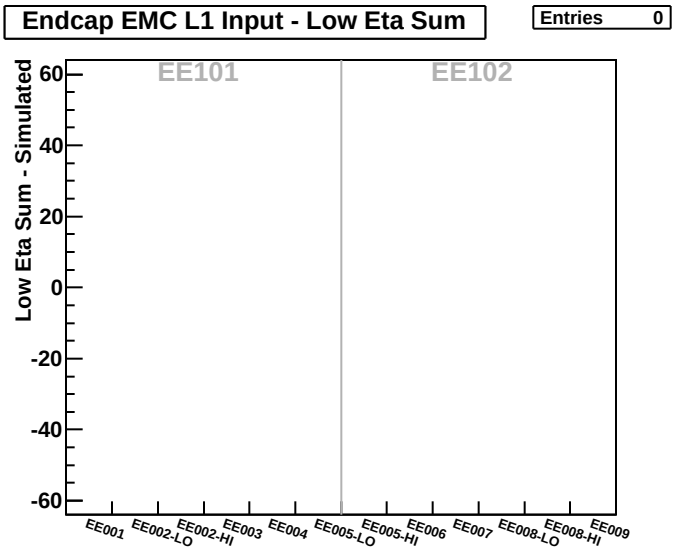

Barrel EMC L1 Input - High Eta Sum
Entries 1.08e+08
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

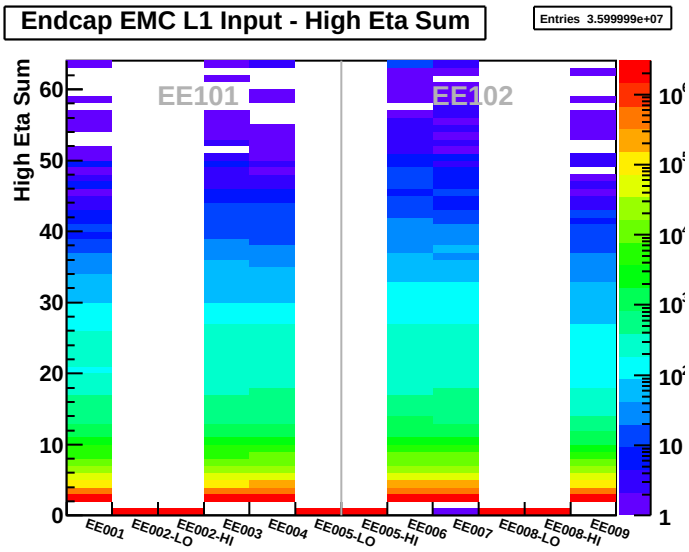

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

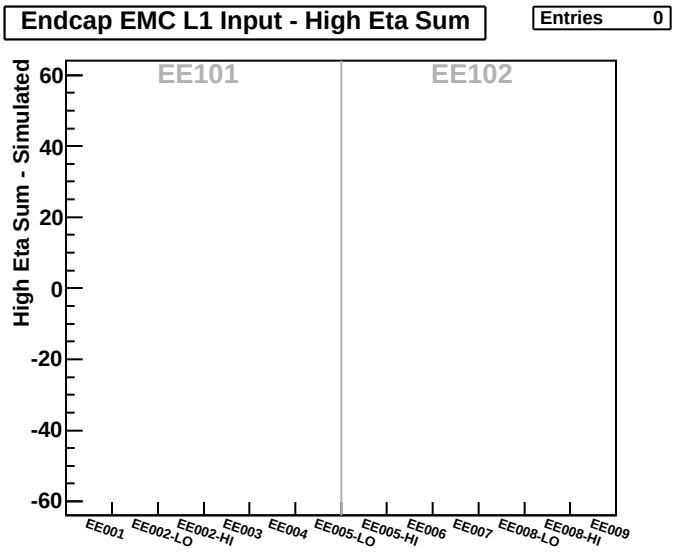

Barrel EMC L1 Input - High Tower Bits
Entries 1.08e+08
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

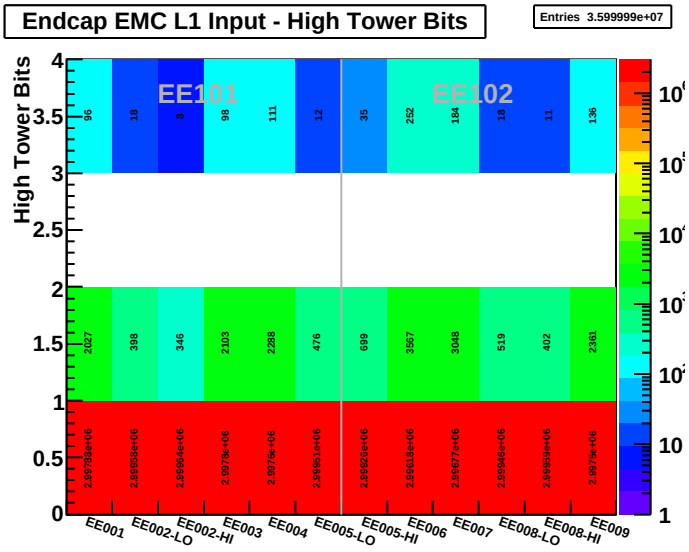

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

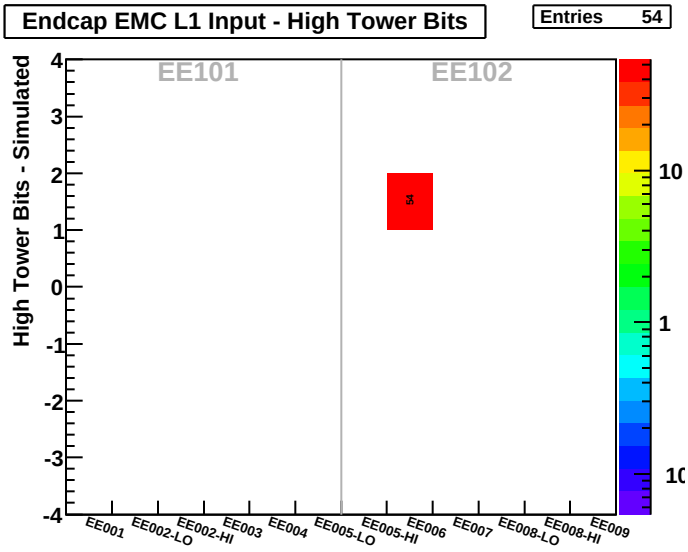

Endcap EMC L1 Input - Low Eta Sum
Entries 3.599999e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

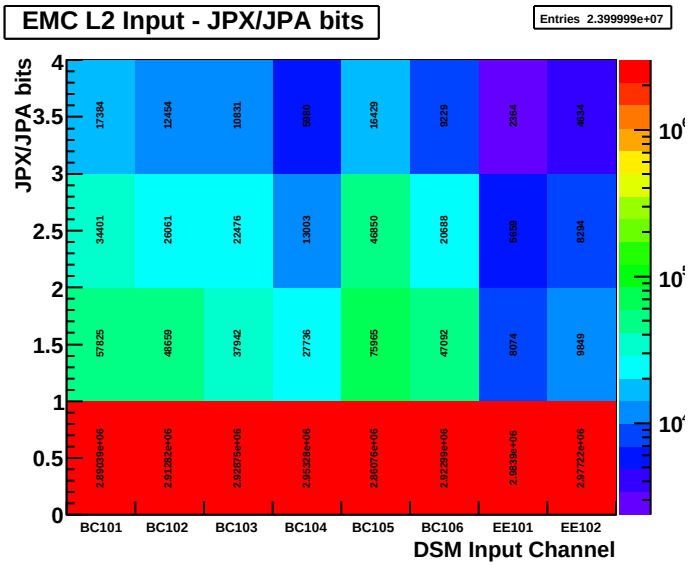

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

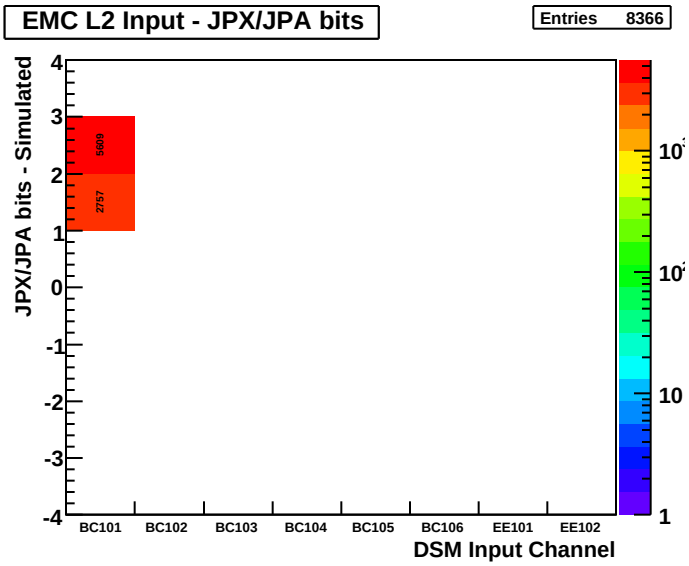

Endcap EMC L1 Input - High Eta Sum
Entries 3.599999e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

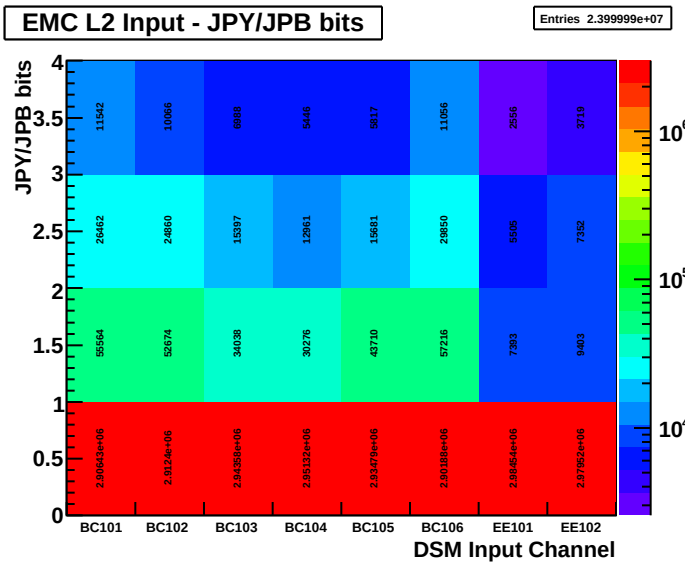

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

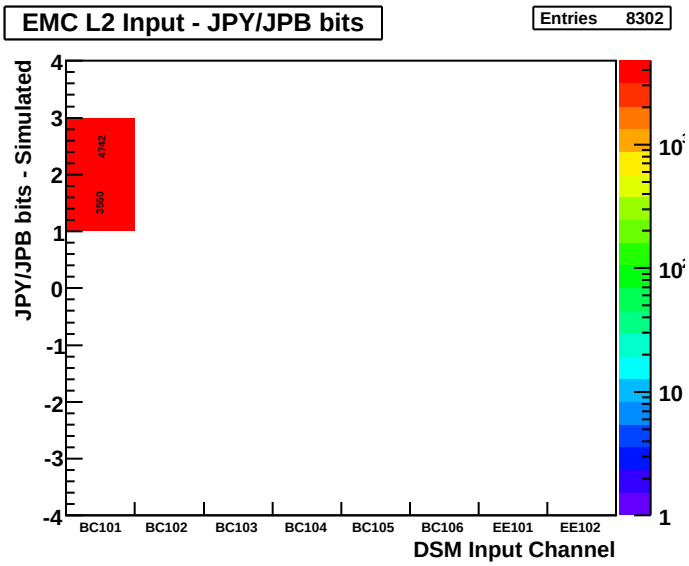

Endcap EMC L1 Input - High Tower Bits
Entries 3.599999e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

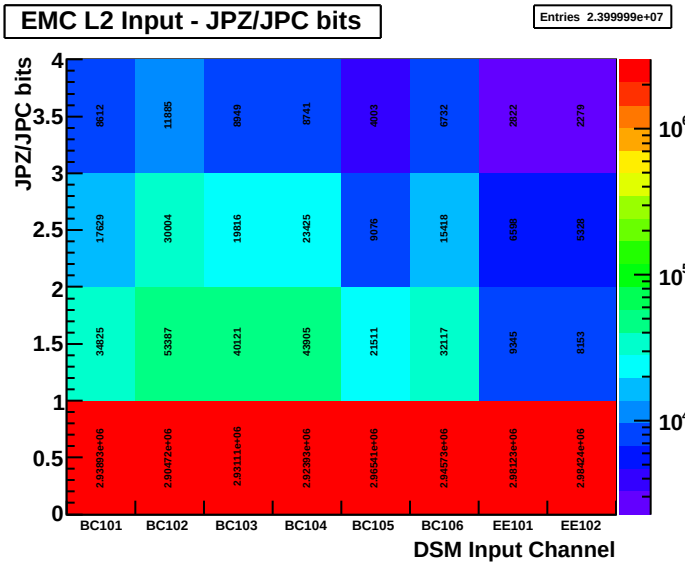

Endcap EMC L1 Input - High Tower Bits
Entries 54
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

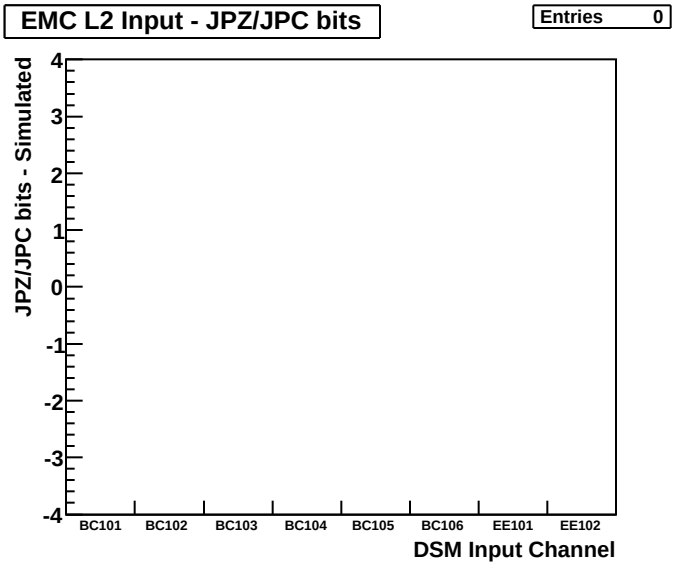

EMC L2 Input - JPX/JPA bits
Entries 2.399999e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
17384 12454 10831 5980 16429 9229 2364 4634
34401 26061 22476 13003 46850 20688 5689 8294
57825 48659 37942 27736 75965 47092 8074 9849
2.89039e+06 2.91282e+06 2.92875e+06 2.95328e+06 2.86076e+06 2.92299e+06 2.8839e+06 2.97722e+06

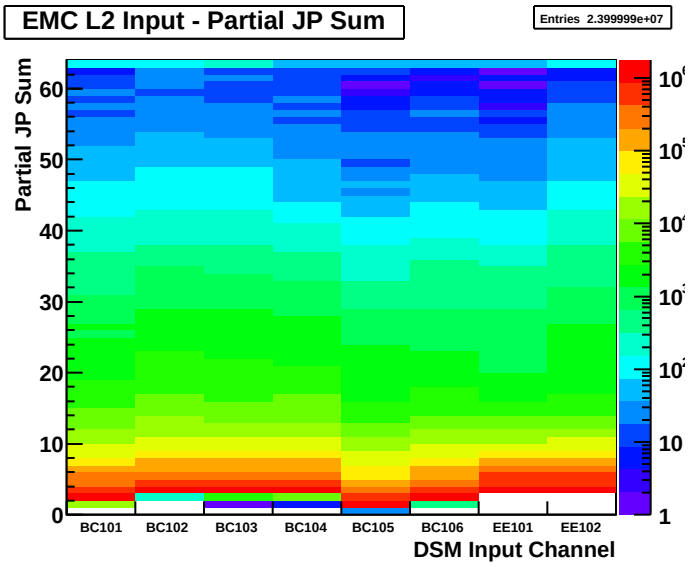

EMC L2 Input - JPX/JPA bits
Entries 8366
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5609
2757

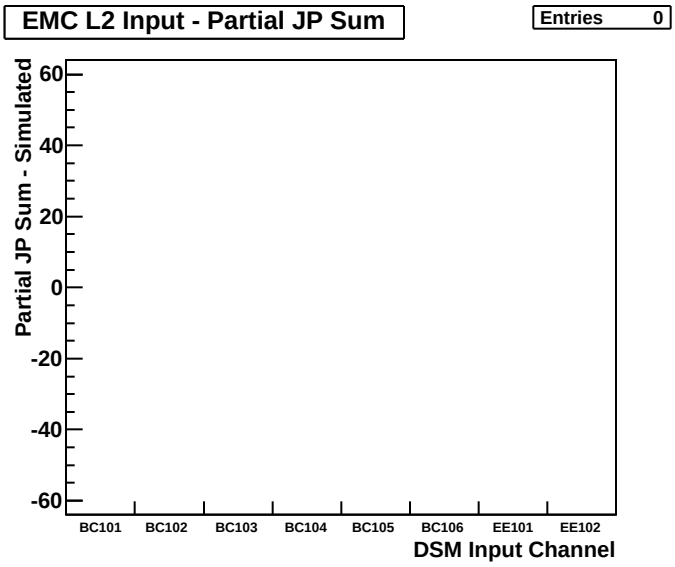

EMC L2 Input - JPY/JPB bits
Entries 2.399999e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
11542 10066 6988 5446 5317 11056 2556 3719
26462 24860 15397 12961 15681 29850 5505 7352
55564 52674 34038 30276 43710 57216 7393 9403
2.90643e+06 2.8124e+06 2.94358e+06 2.95132e+06 2.93479e+06 2.90189e+06 2.98454e+06 2.97952e+06

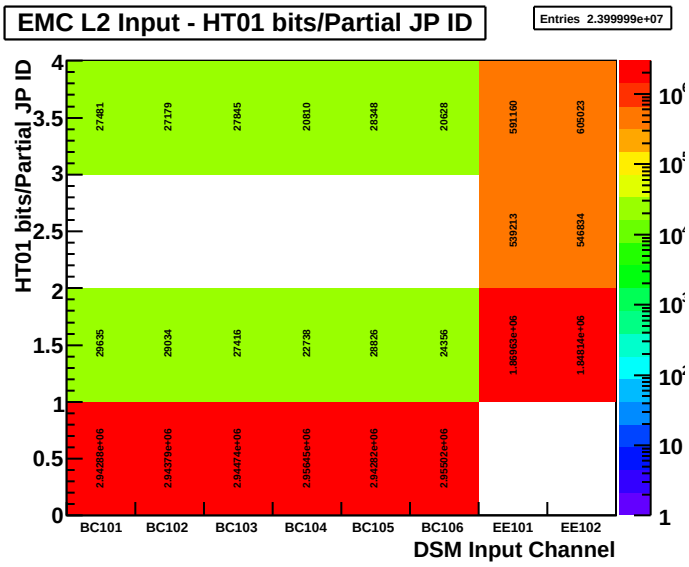

EMC L2 Input - JPY/JPB bits
Entries 8302
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4742
3560

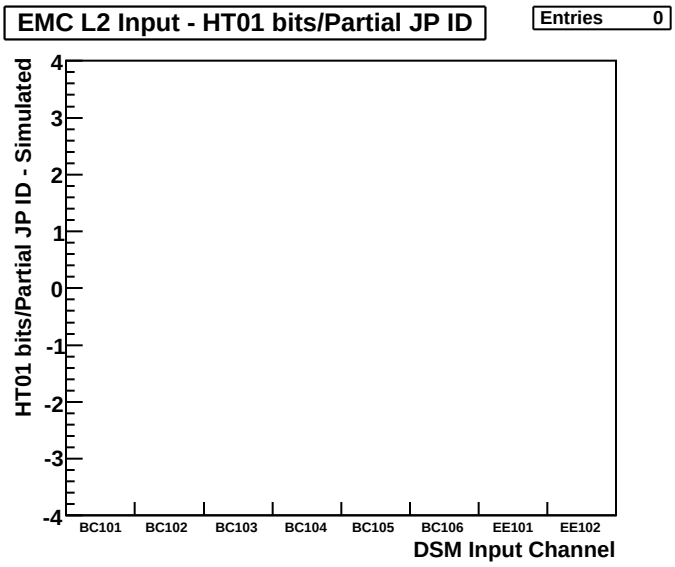

EMC L2 Input - JPZ/JPC bits
Entries 2.399999e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
8612 11885 8849 8741 4903 6732 2822 2279
17629 30004 19816 23425 9076 15418 6588 5328
34825 53387 40121 43905 21511 32117 9345 8153
2.93893e+06 2.90472e+06 2.93111e+06 2.92393e+06 2.96541e+06 2.94573e+06 2.98123e+06 2.98424e+06

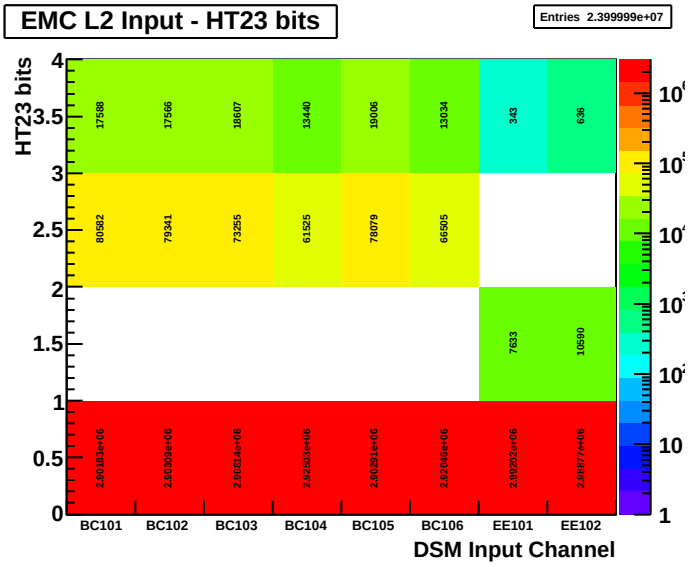

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

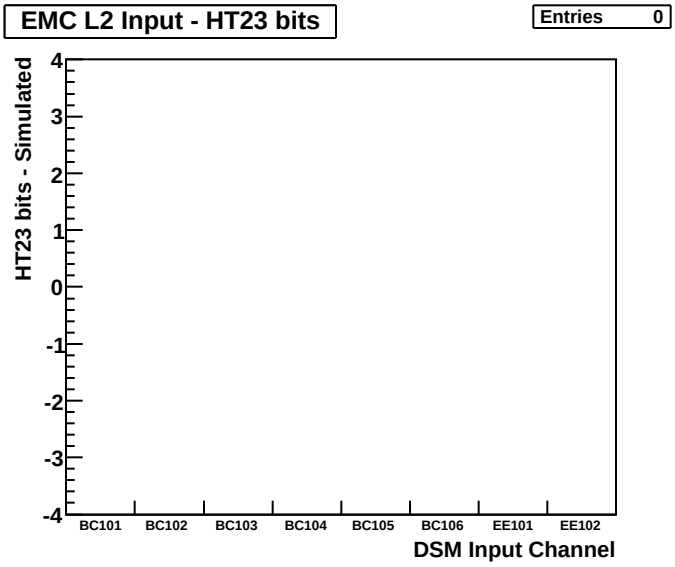

EMC L2 Input - Partial JP Sum
Entries 2.399999e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

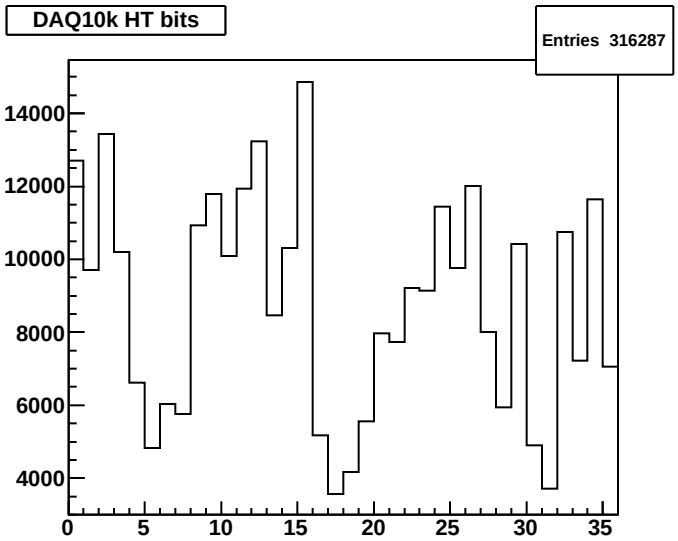

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

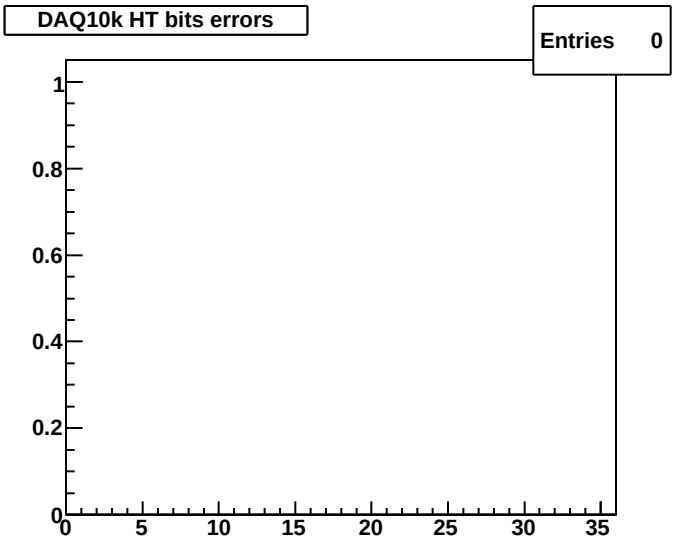

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2.399999e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

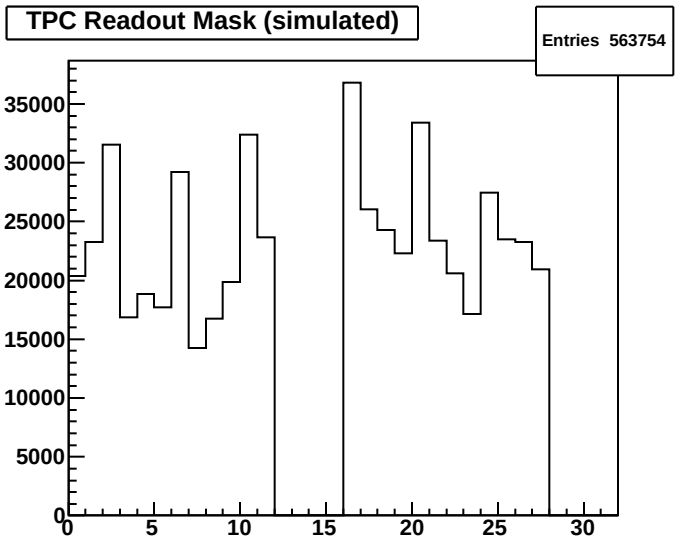

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

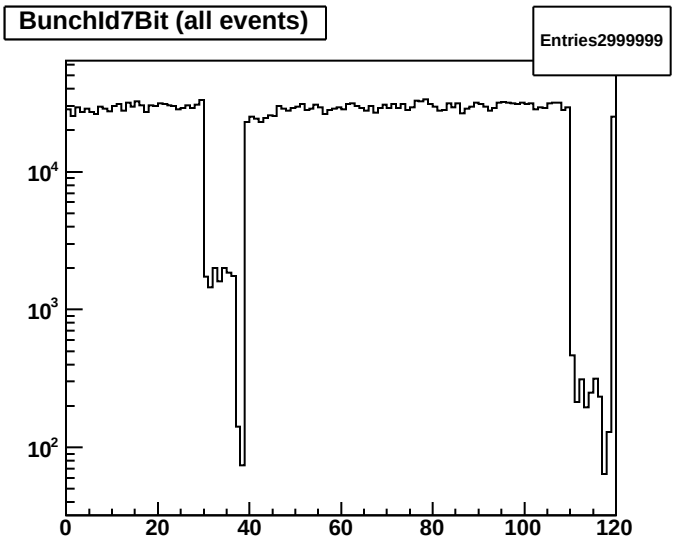

EMC L2 Input - HT23 bits
Entries 2.399999e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

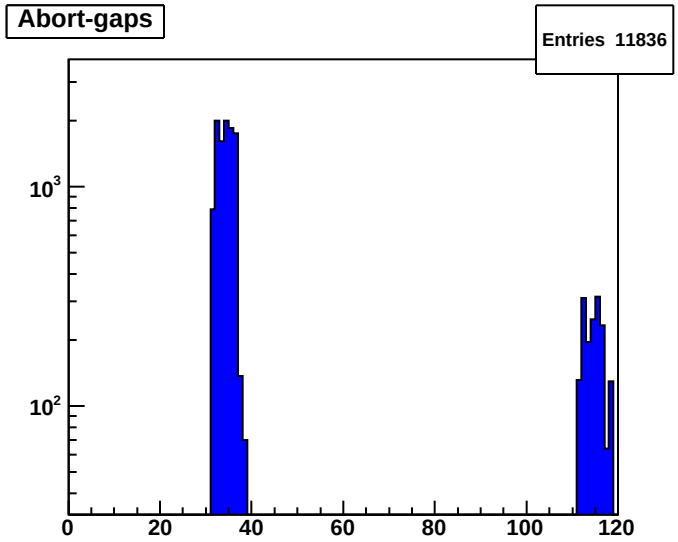

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

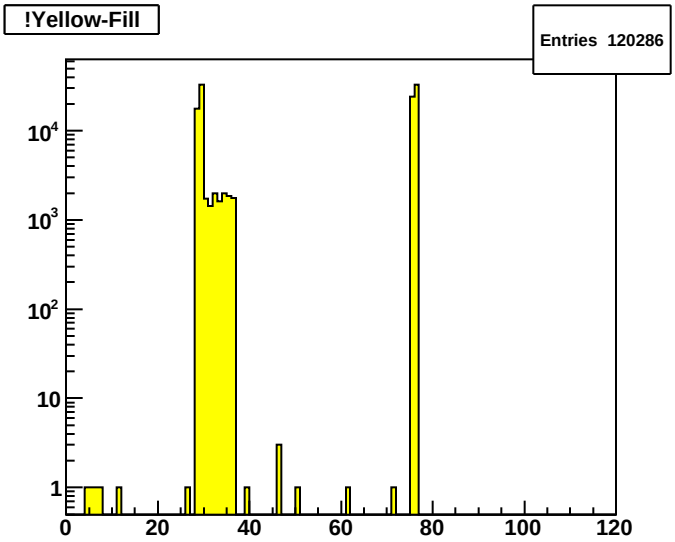

DAQ10k HT bits
Entries 316287

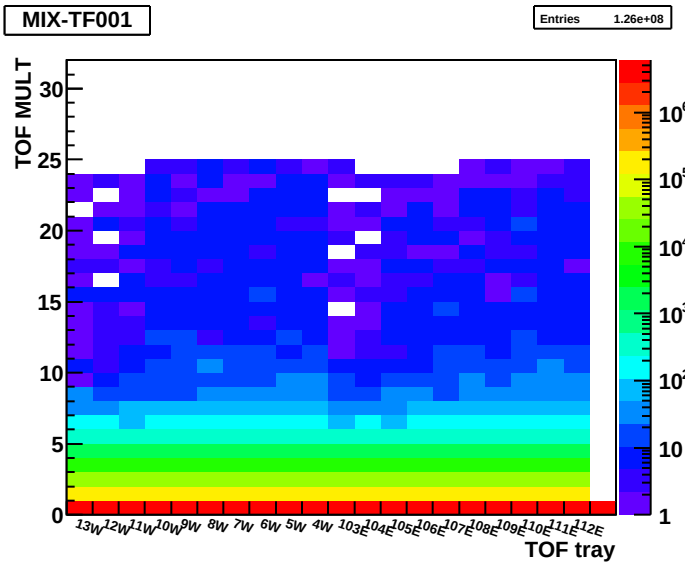

DAQ10k HT bits errors
Entries 0

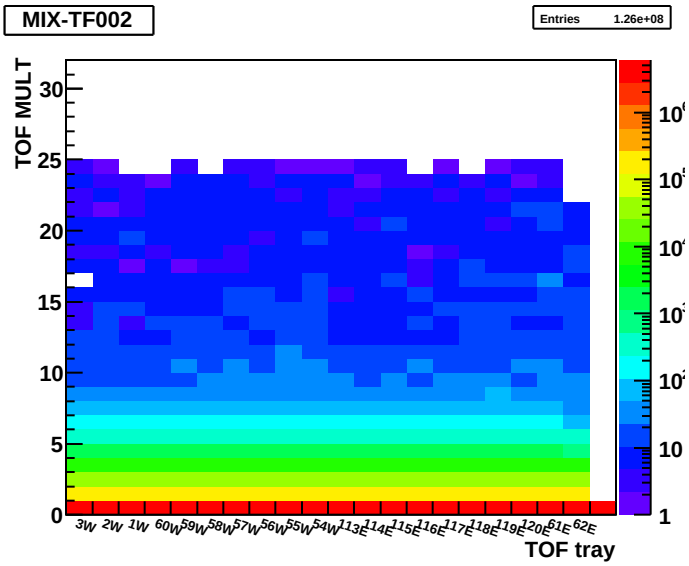

TPC Readout Mask (simulated)
Entries 563754

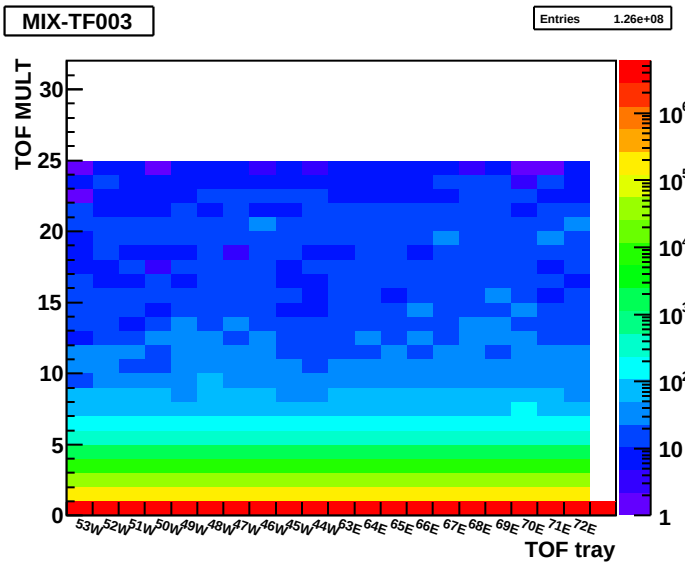

BunchId7Bit (all events)
Entries2999999

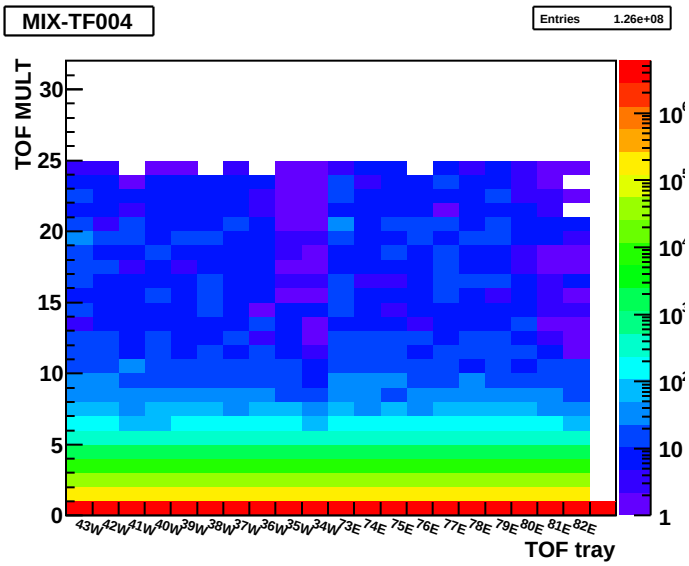

Abort-gaps
Entries 11836

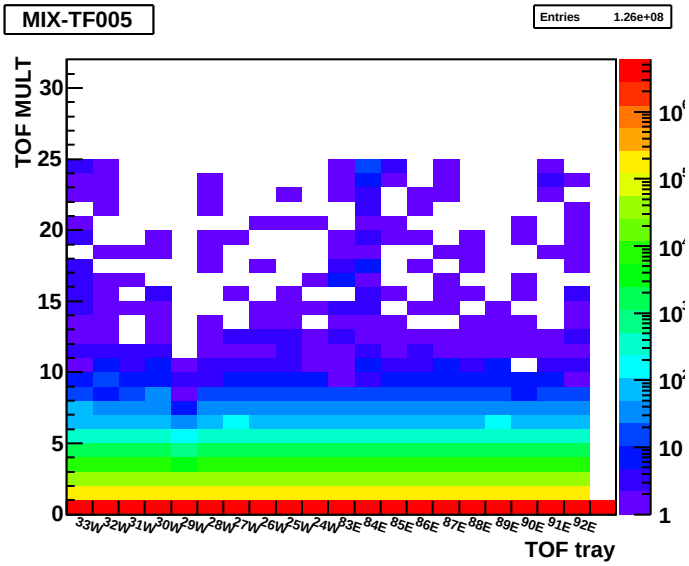

!Yellow-Fill
Entries 120286

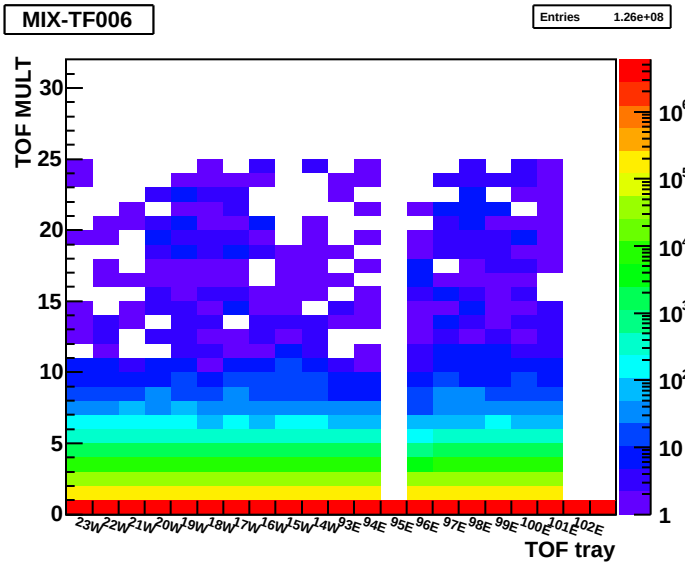

MIX-TF001
Entries 1.26e+08
TOF MULT
TOF tray

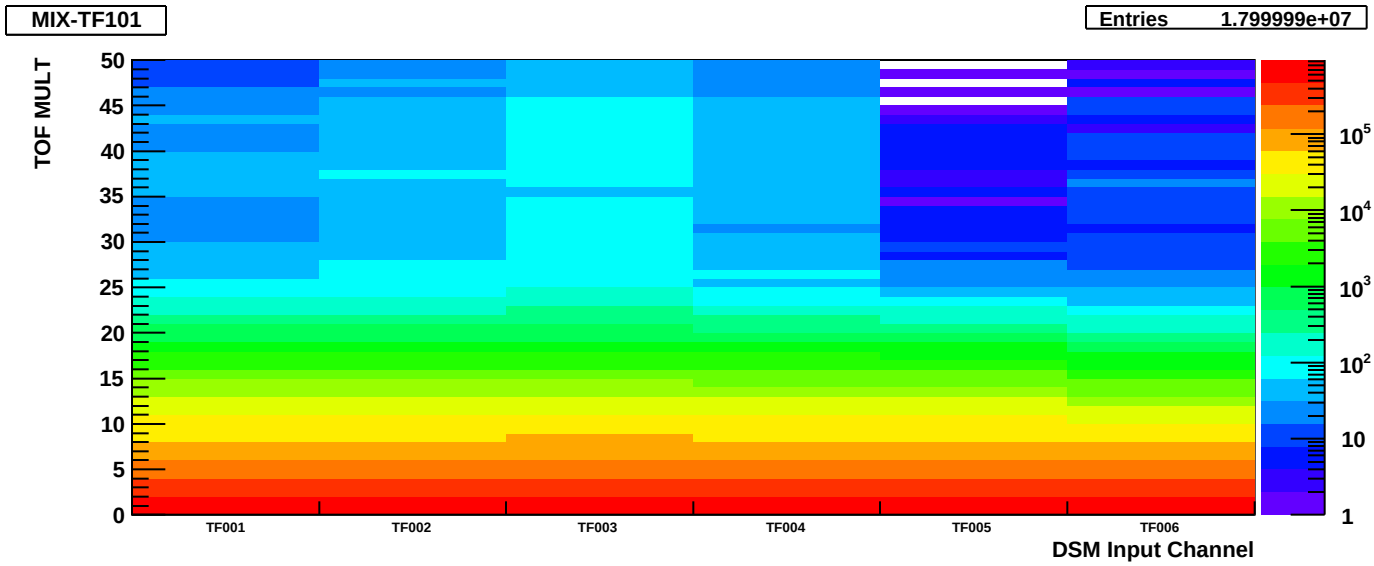

MIX-TF002
Entries 1.26e+08
TOF MULT
TOF tray

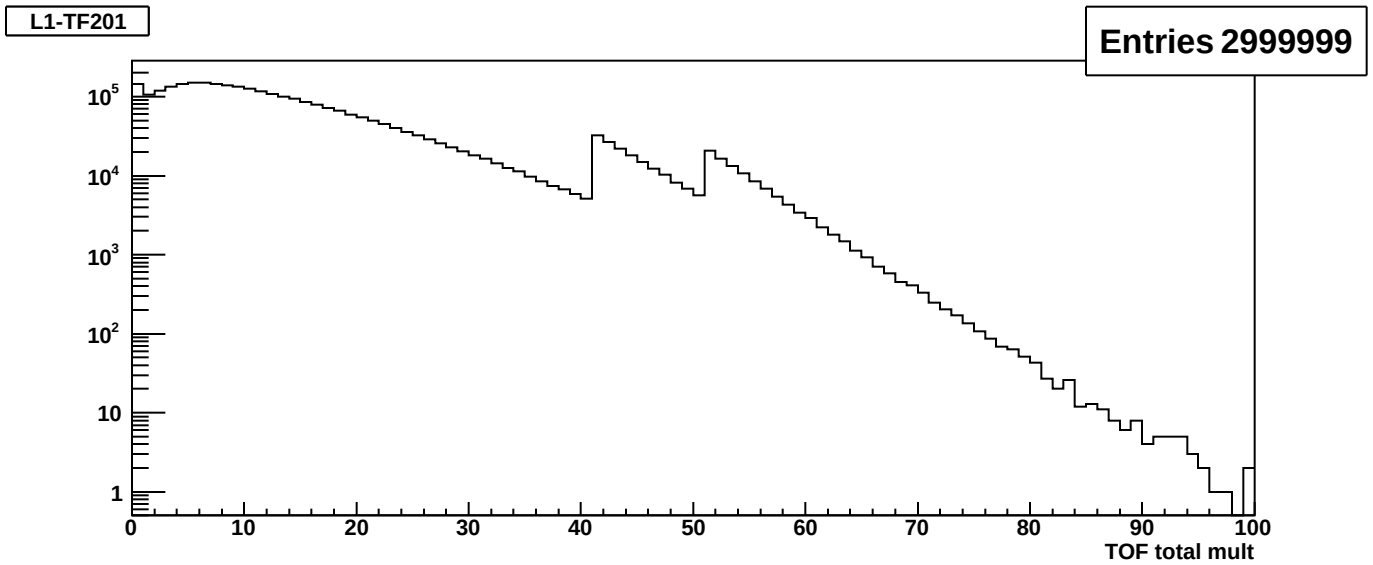

MIX-TF003
Entries 1.26e+08
TOF MULT
TOF tray

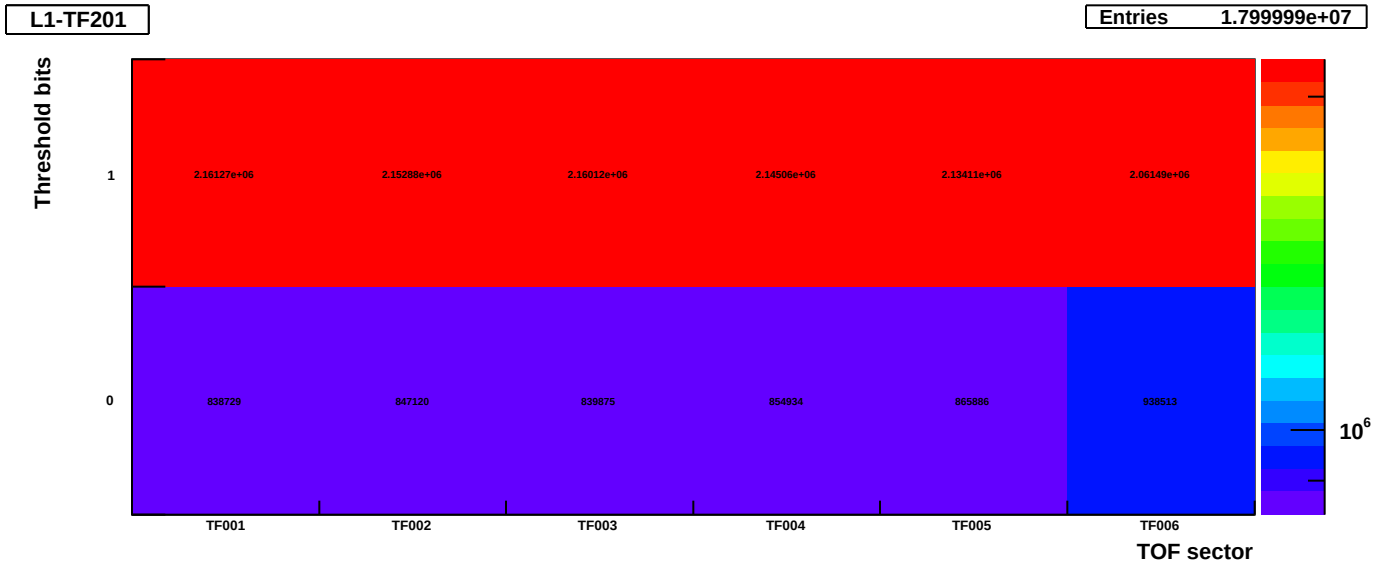

MIX-TF004
Entries 1.26e+08
TOF MULT
TOF tray

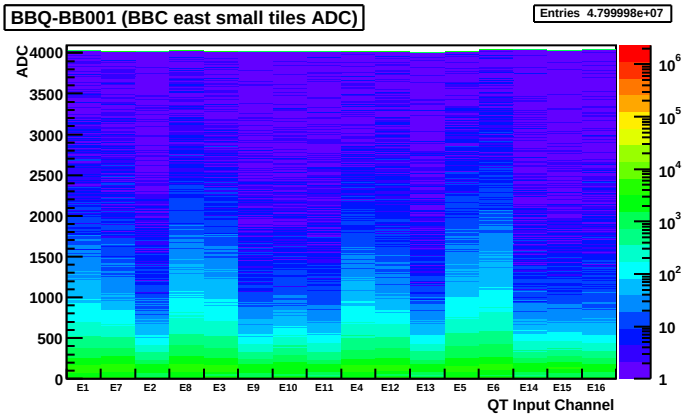

MIX-TF005
Entries 1.26e+08
TOF MULT
TOF tray

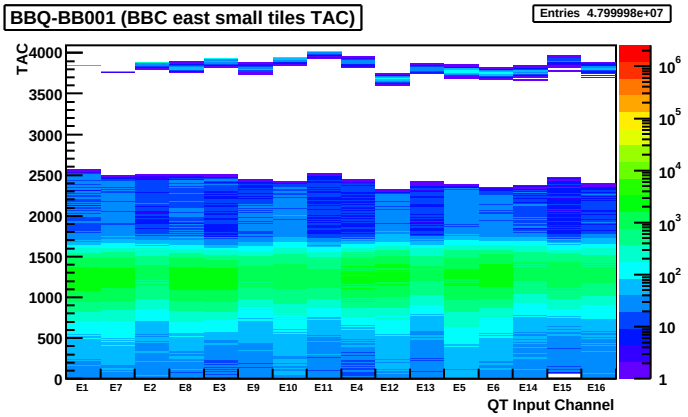

MIX-TF006
Entries 1.26e+08
TOF MULT
TOF tray

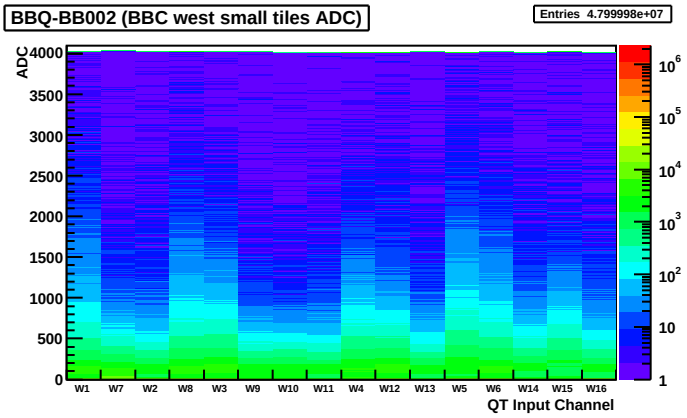

MIX-TF101
Entries 1.799999e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

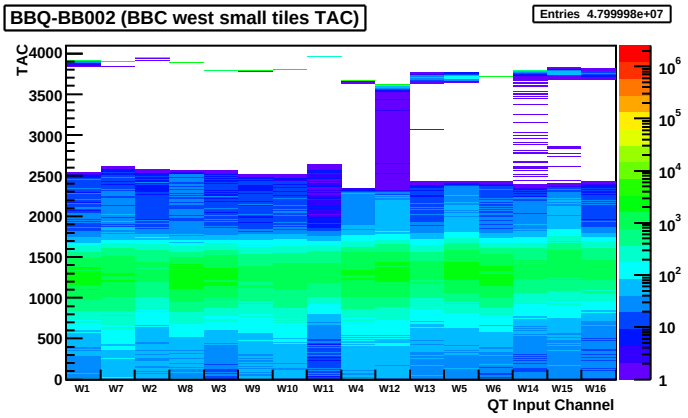

L1-TF201
Entries 2999999
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

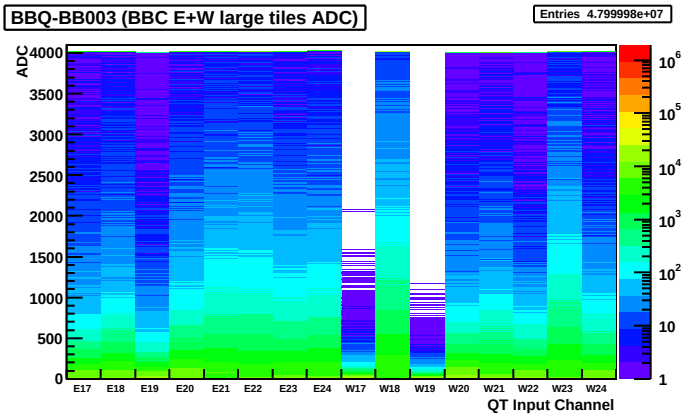

L1-TF201
Entries 1.799999e+07
Threshold bits
1
0
2.16127e+06
2.15288e+06
2.16012e+06
2.14506e+06
2.13411e+06
2.06149e+06
838729
847120
839875
854934
865886
938513
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

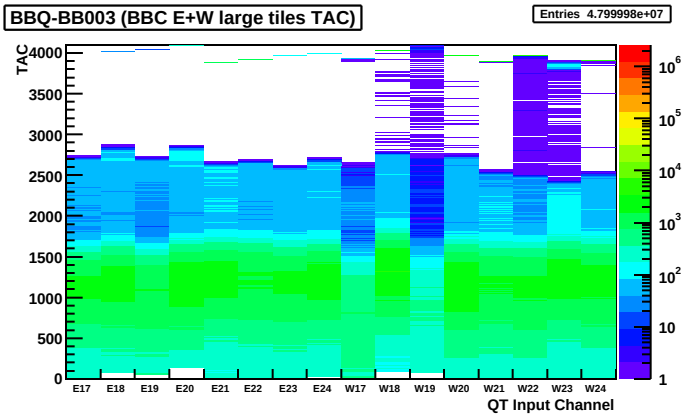

BBQ-BB001 (BBC east small tiles ADC)
Entries 4.799998e+07
ADC
QT Input Channel

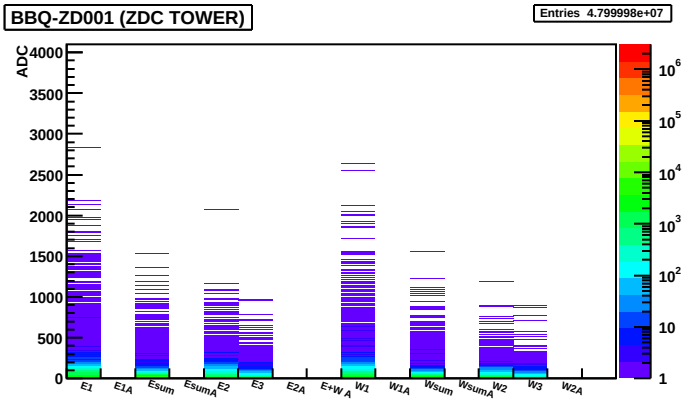

BBQ-BB001 (BBC east small tiles TAC)
Entries 4.799998e+07
TAC
QT Input Channel

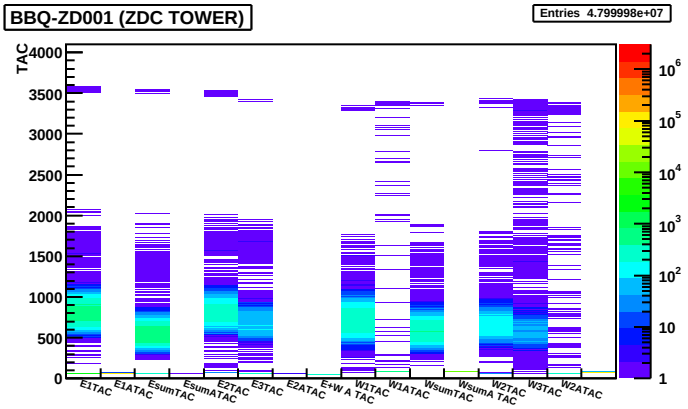

BBQ-BB002 (BBC west small tiles ADC)
Entries 4.799998e+07
ADC
QT Input Channel

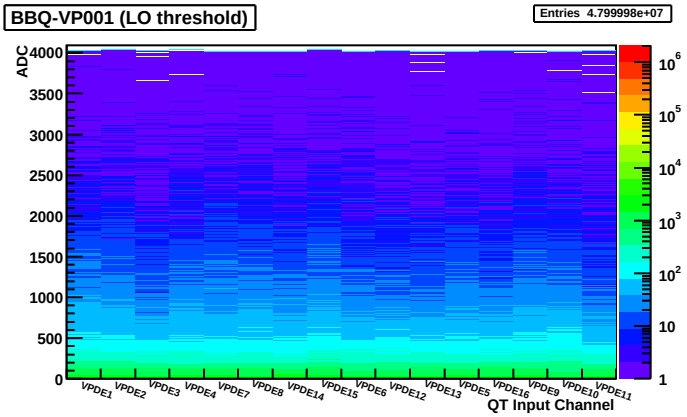

BBQ-BB002 (BBC west small tiles TAC)
Entries 4.799998e+07
TAC
QT Input Channel

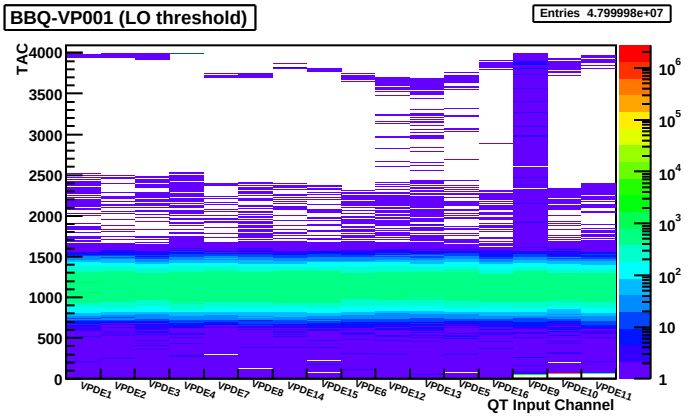

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4.799998e+07
ADC
QT Input Channel

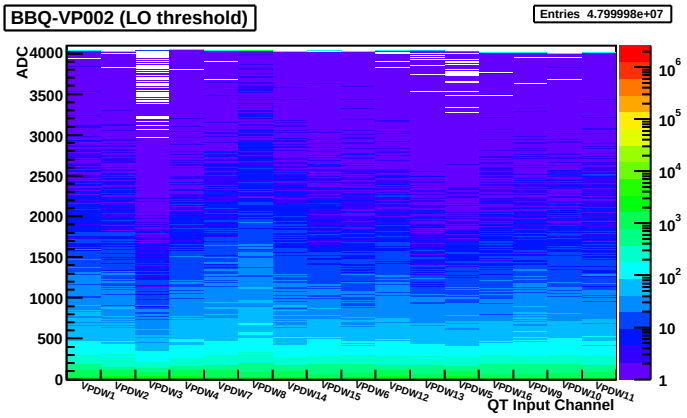

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4.799998e+07
TAC
QT Input Channel

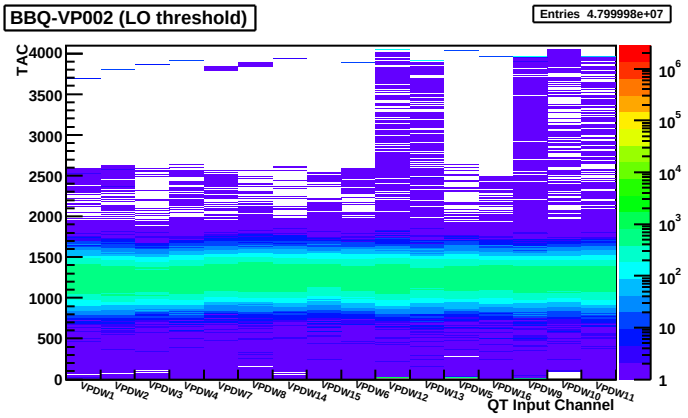

BBQ-ZD001 (ZDC TOWER)
Entries 4.799998e+07
ADC

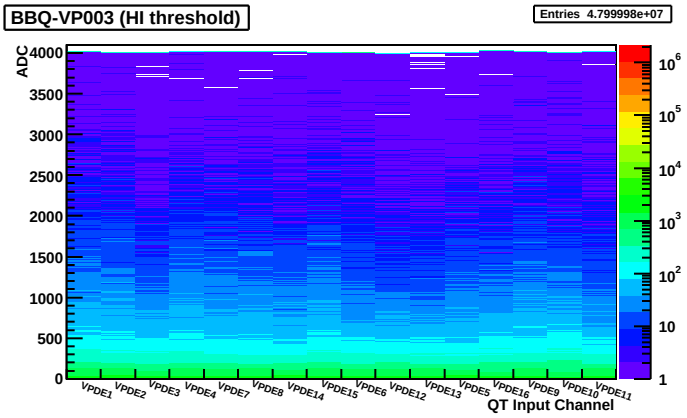

BBQ-ZD001 (ZDC TOWER)
Entries 4.799998e+07
TAC

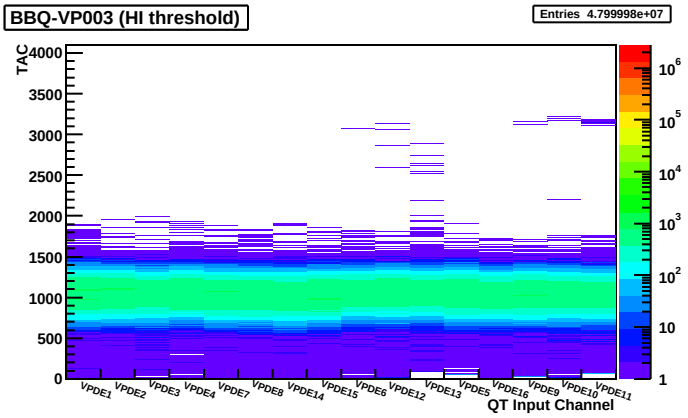

BBQ-VP001 (LO threshold)
Entries 4.799998e+07
ADC
QT Input Channel

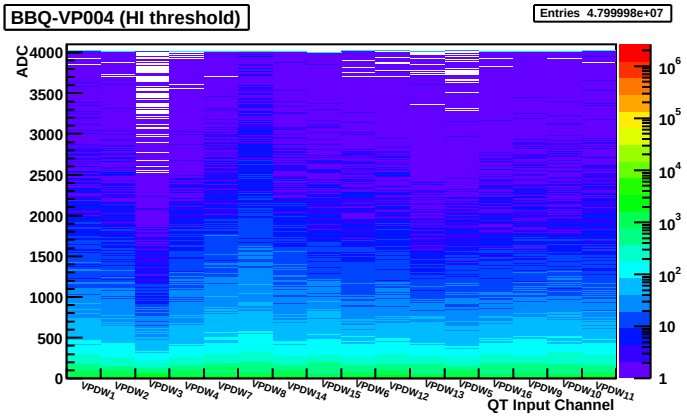

BBQ-VP001 (LO threshold)
Entries 4.799998e+07
TAC
QT Input Channel

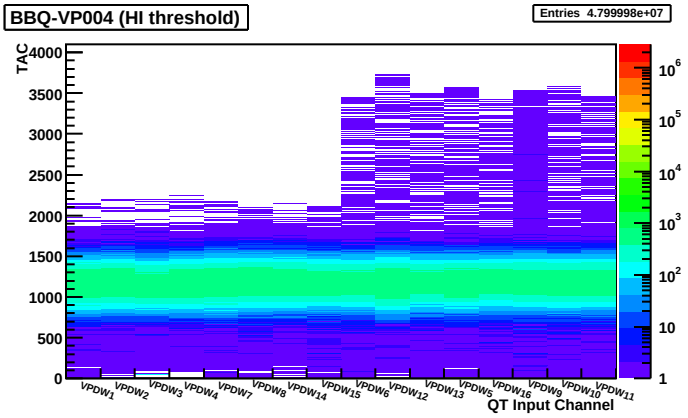

BBQ-VP002 (LO threshold)
Entries 4.799998e+07
ADC
QT Input Channel

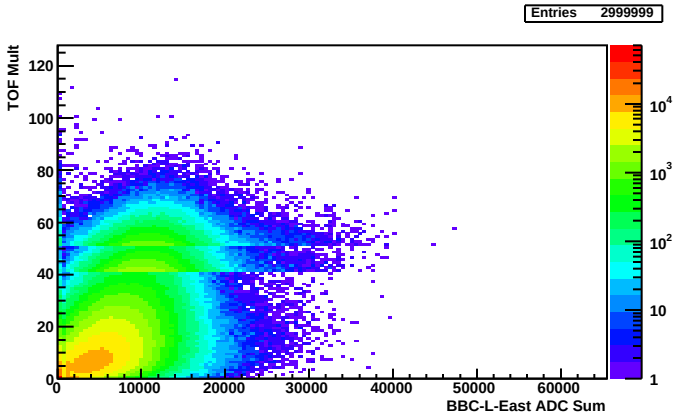

BBQ-VP002 (LO threshold)
Entries 4.799998e+07
TAC
QT Input Channel

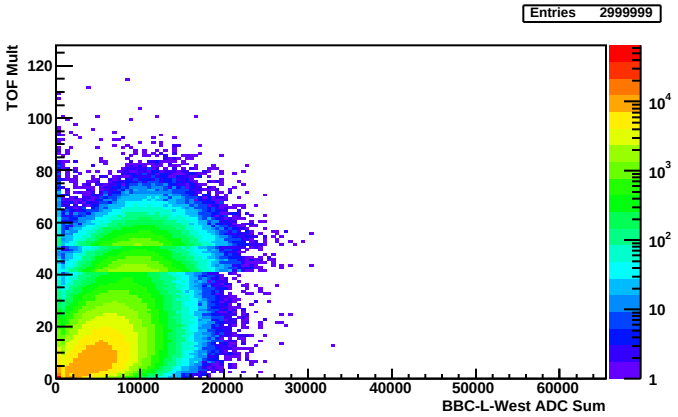

BBQ-VP003 (HI threshold)
Entries 4.799998e+07
ADC
QT Input Channel

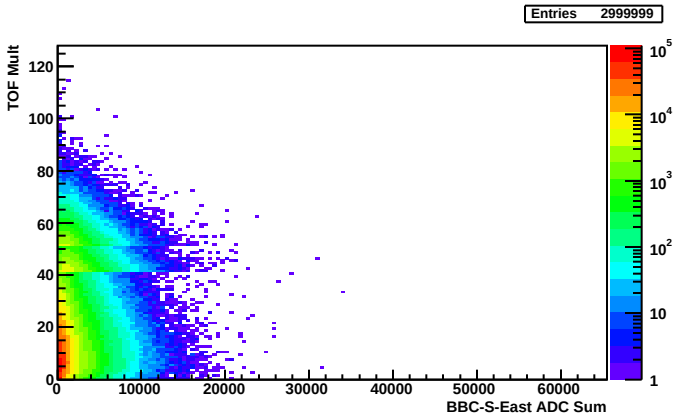

BBQ-VP003 (HI threshold)
Entries 4.799998e+07
TAC
QT Input Channel

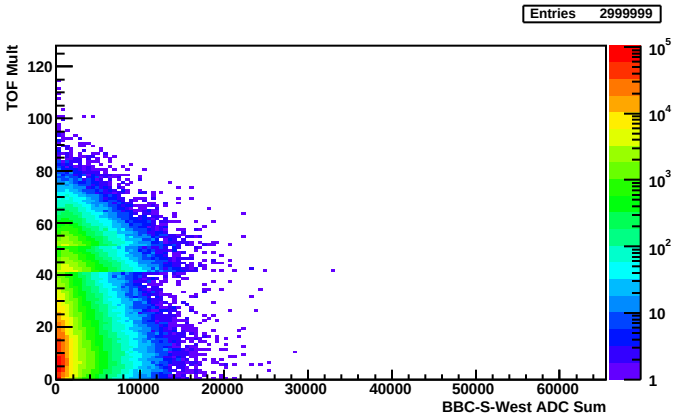

BBQ-VP004 (HI threshold)
Entries 4.799998e+07
ADC
QT Input Channel

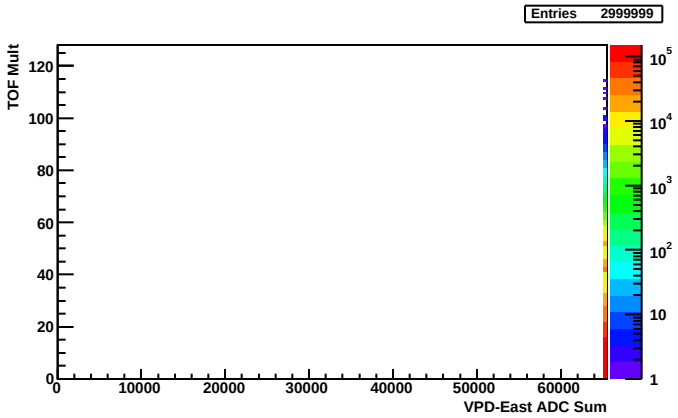

BBQ-VP004 (HI threshold)
Entries 4.799998e+07
TAC
QT Input Channel

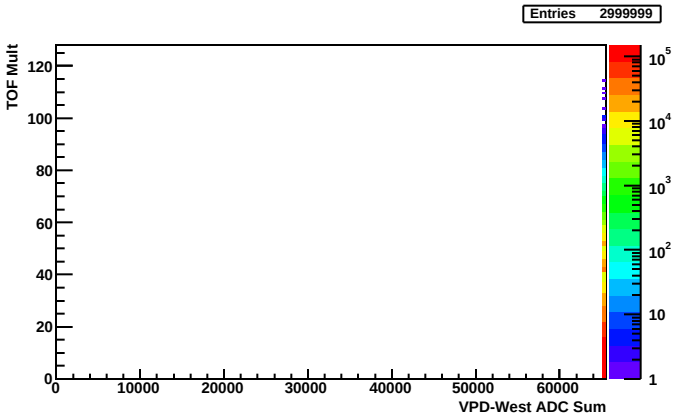

Entries 2999999
TOF Mult
BBC-L-East ADC Sum

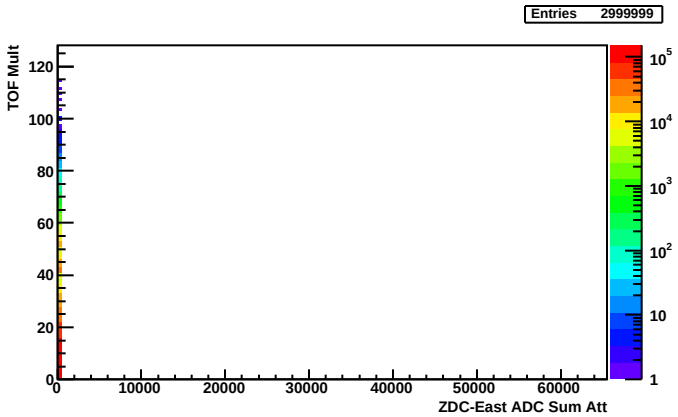

Entries 2999999
TOF Mult
BBC-L-West ADC Sum

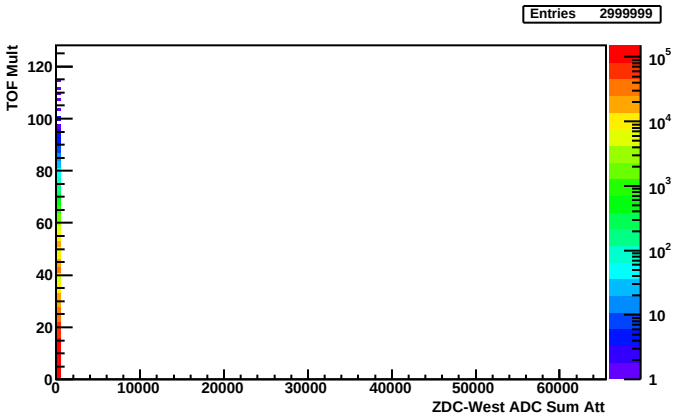

Entries 2999999
TOF Mult
BBC-S-East ADC Sum

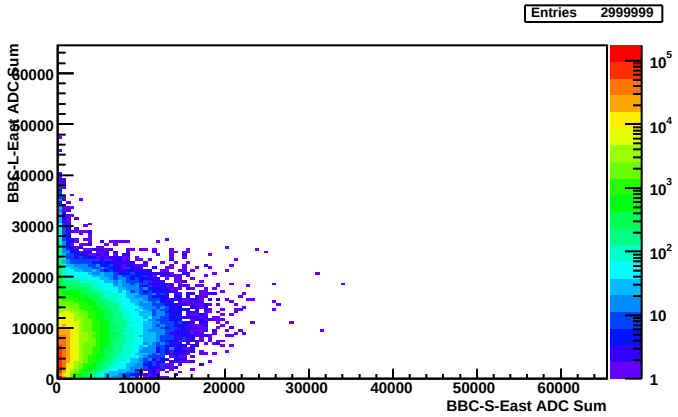

Entries 2999999
TOF Mult
BBC-S-West ADC Sum

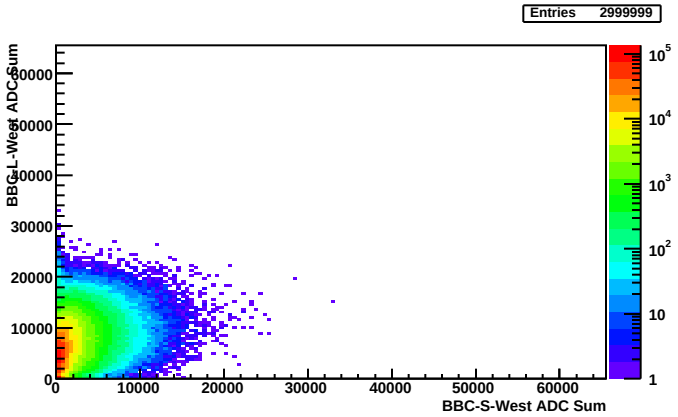

Entries 2999999
TOF Mult
VPD-East ADC Sum

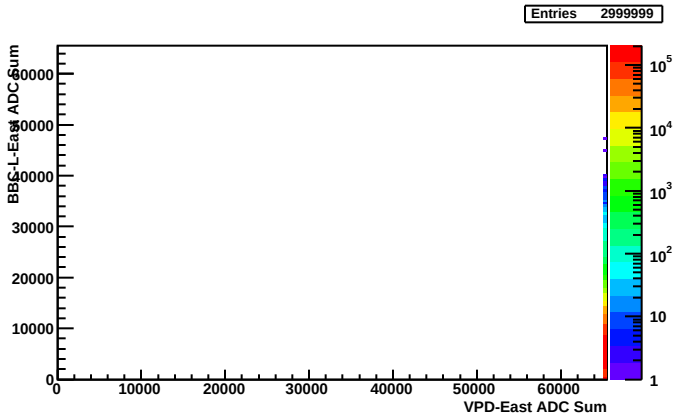

Entries 2999999
TOF Mult
VPD-West ADC Sum

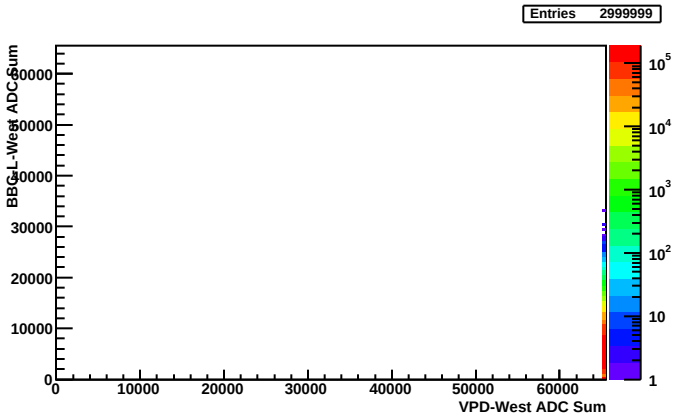

Entries 2999999
TOF Mult
ZDC-East ADC Sum Att

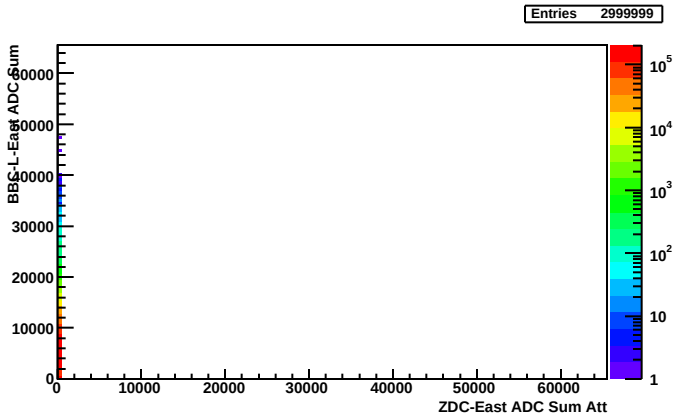

Entries 2999999
TOF Mult
ZDC-West ADC Sum Att

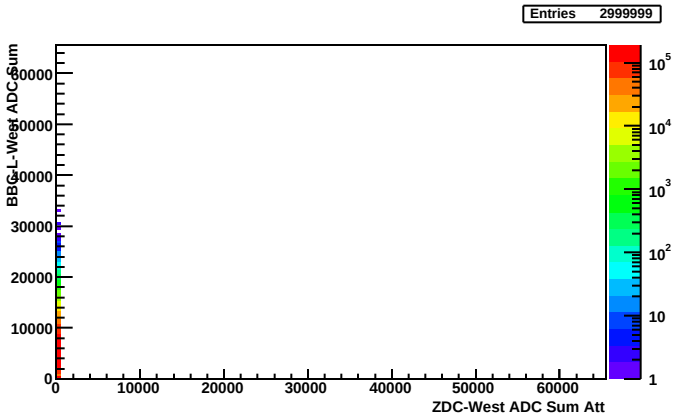

Entries 2999999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

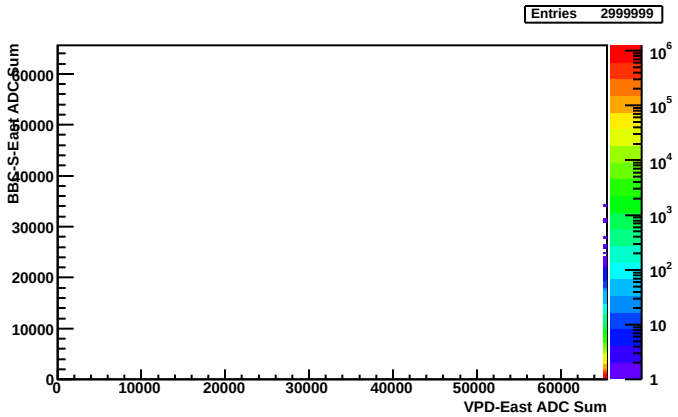

Entries 2999999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

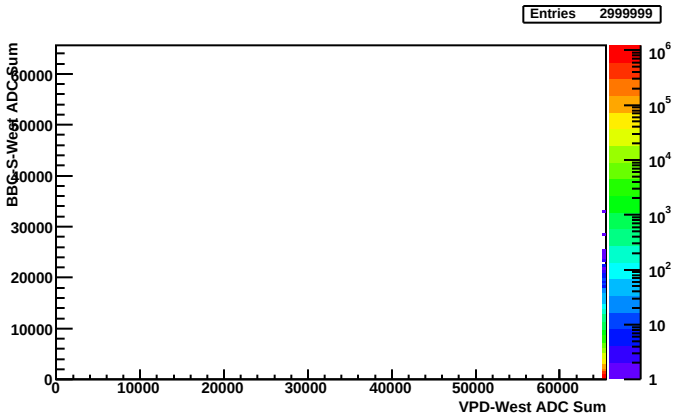

Entries 2999999
BBC-L-East ADC Sum
VPD-East ADC Sum

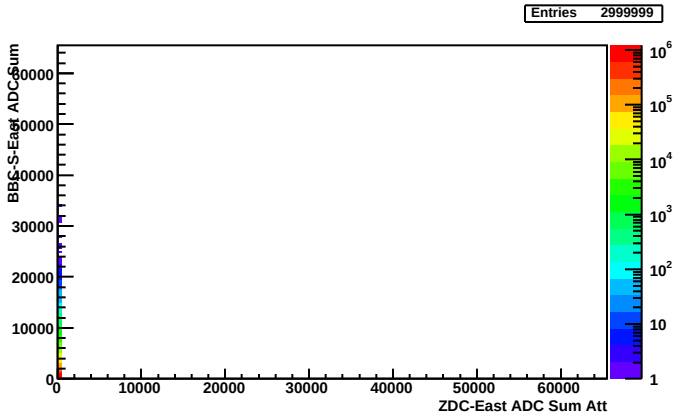

Entries 2999999
BBC-L-West ADC Sum
VPD-West ADC Sum

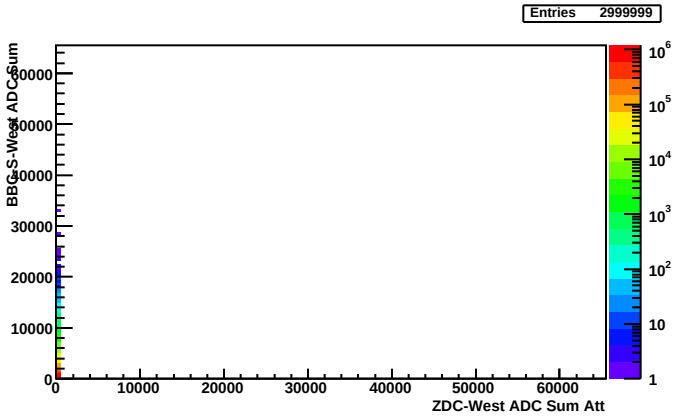

Entries 2999999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

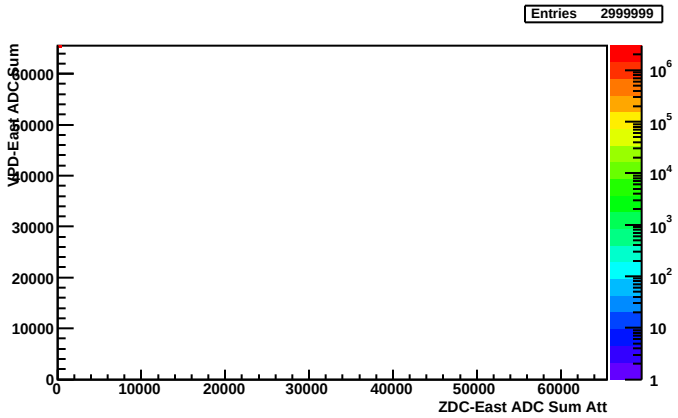

Entries 2999999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

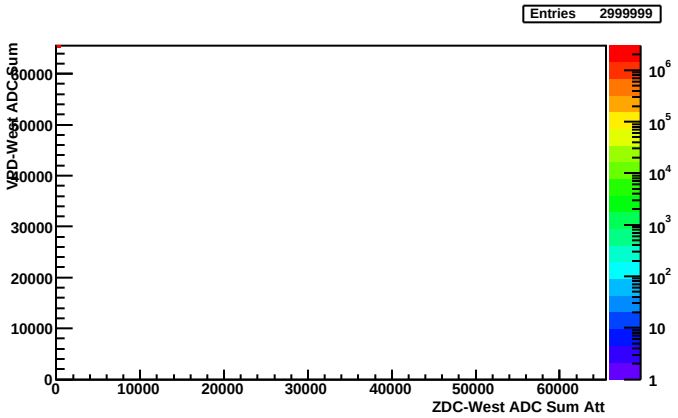

Entries 2999999
BBC-S-East ADC Sum
VPD-East ADC Sum

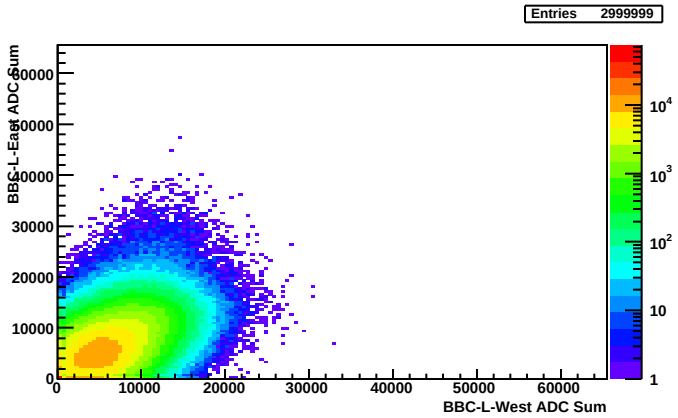

Entries 2999999
BBC-S-West ADC Sum
VPD-West ADC Sum

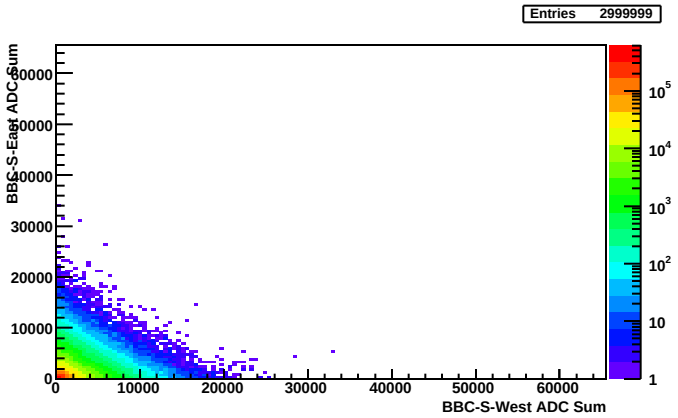

Entries 2999999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

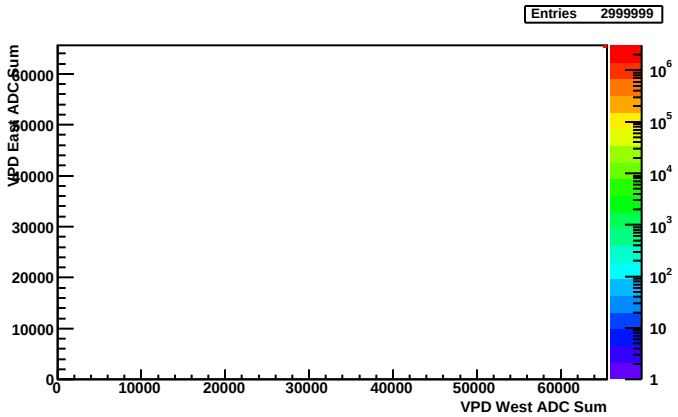

Entries 2999999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

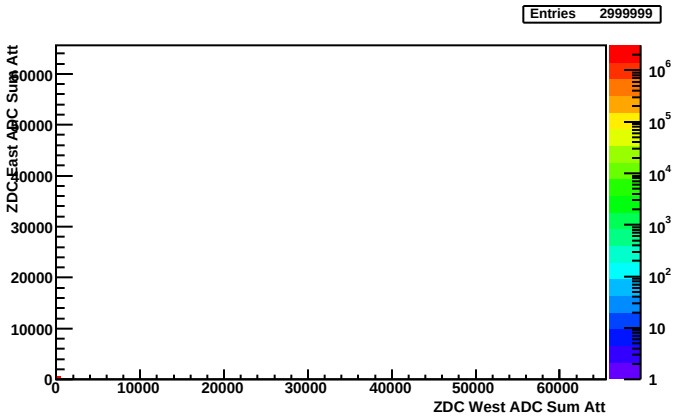

Entries 2999999
VPD-East ADC Sum
ZDC-East ADC Sum Att

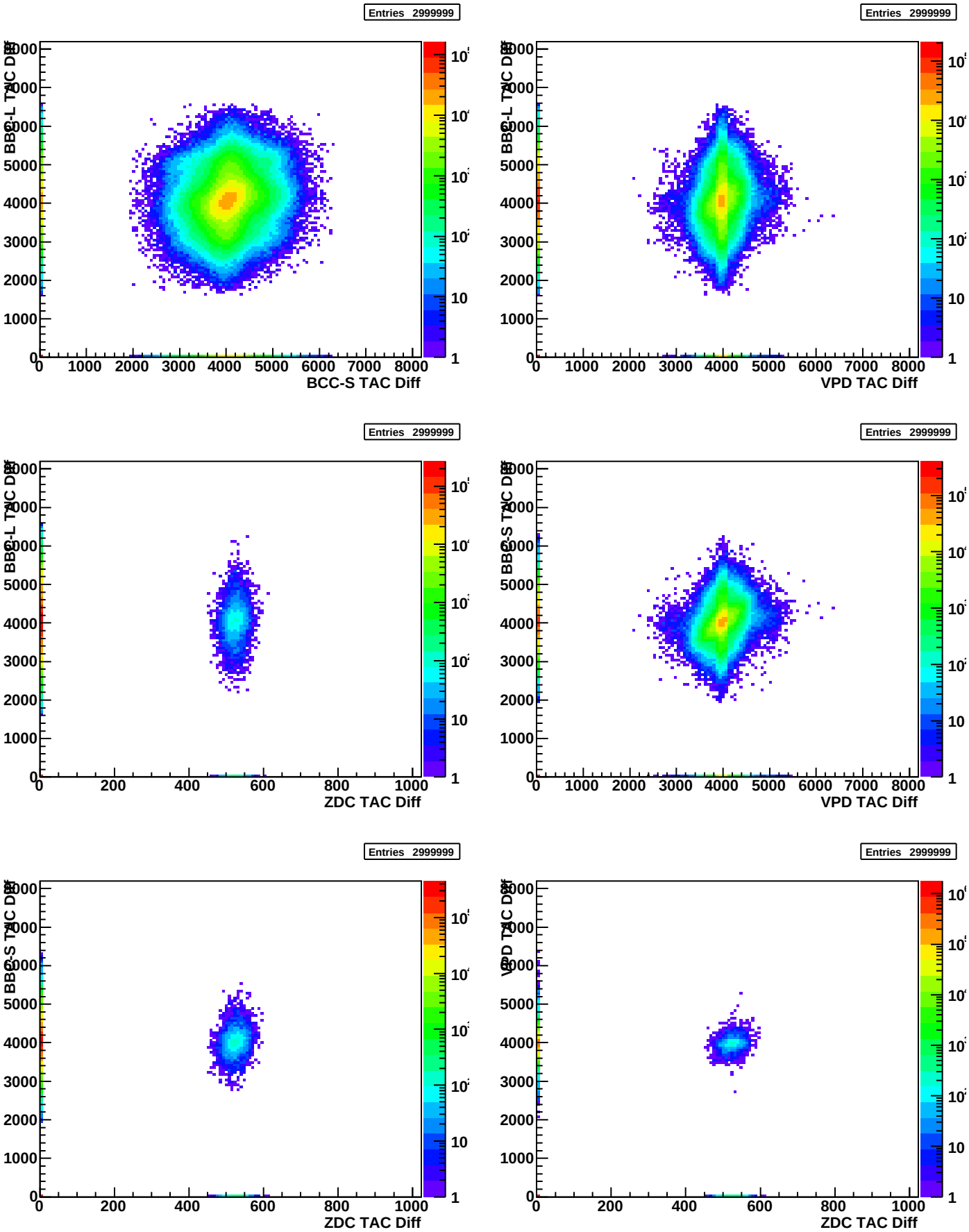

Entries 2999999
VPD-West ADC Sum
ZDC-West ADC Sum Att

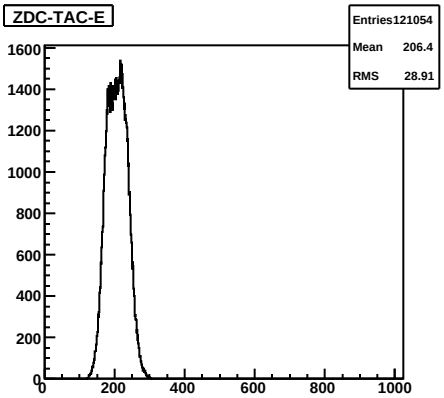

Entries 2999999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

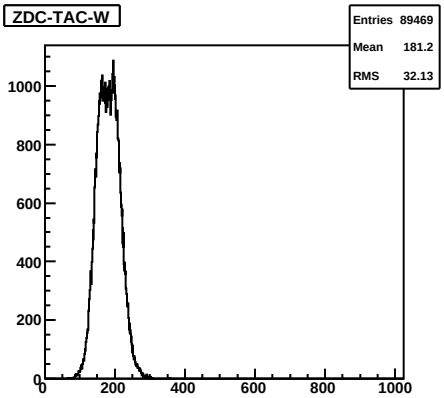

Entries 2999999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

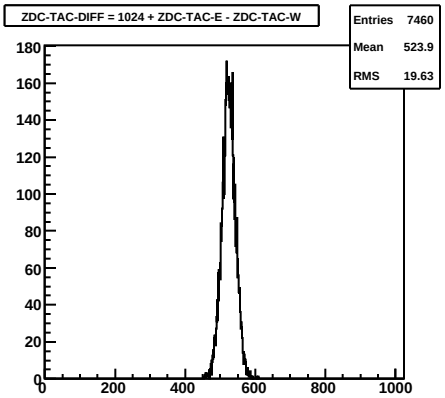

Entries 2999999
VPD East ADC Sum
VPD West ADC Sum

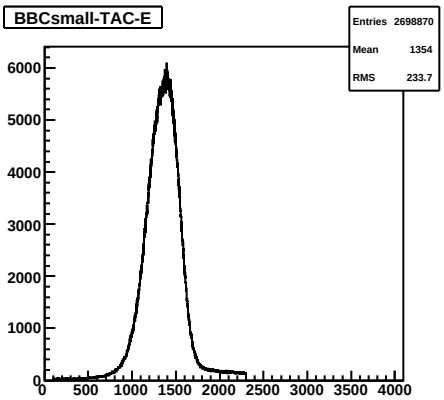

Entries 2999999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

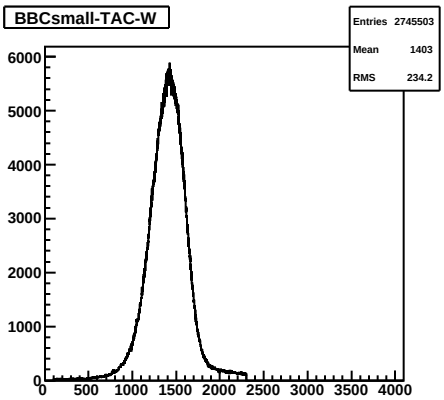

Entries 2999999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2999999
BBC-L TAC Diff
VPD TAC Diff
Entries 2999999
BBC-L TAC Diff
ZDC TAC Diff
Entries 2999999
BBC-S TAC Diff
VPD TAC Diff
Entries 2999999
BBC-S TAC Diff
ZDC TAC Diff
Entries 2999999
VPD TAC Diff
ZDC TAC Diff

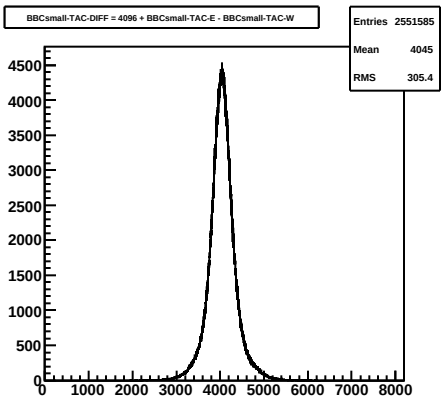

ZDC-TAC-E
Entries121054
Mean 206.4
RMS 28.91

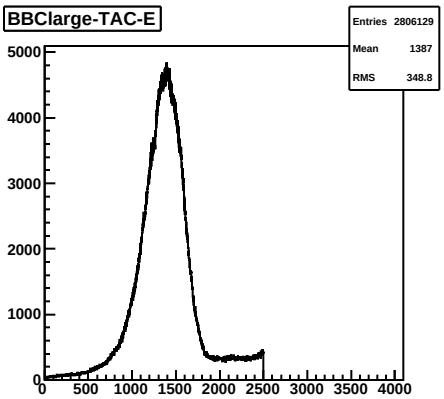

ZDC-TAC-W
Entries 89469
Mean 181.2
RMS 32.13

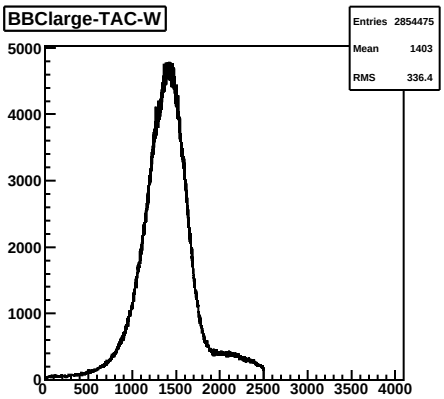

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 7460
Mean 523.9
RMS 19.63

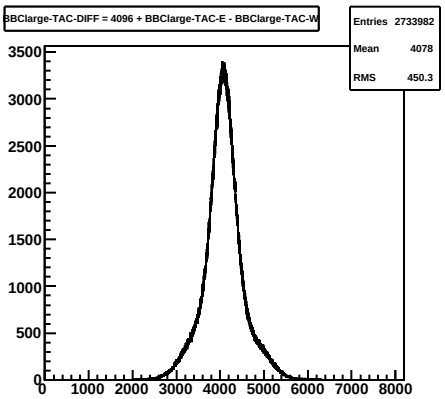

BBCsmall-TAC-E
Entries 2698870
Mean 1354
RMS 233.7

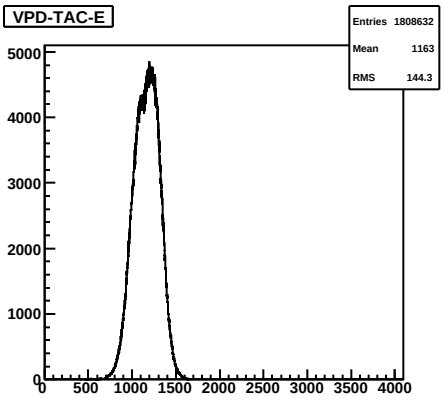

BBCsmall-TAC-W
Entries 2745503
Mean 1403
RMS 234.2

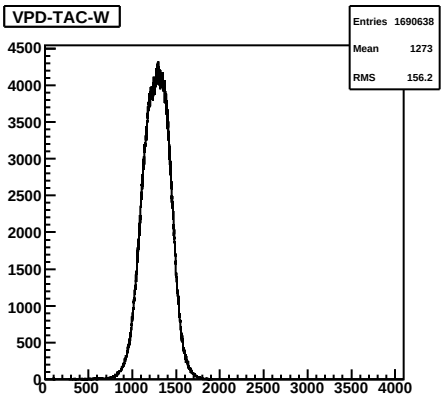

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2551585
Mean 4045
RMS 305.4

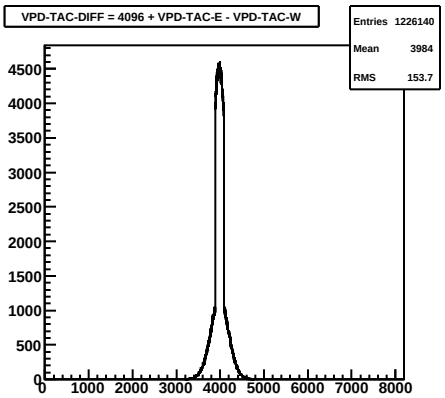

BBClarge-TAC-E
Entries 2806129
Mean 1387
RMS 348.8

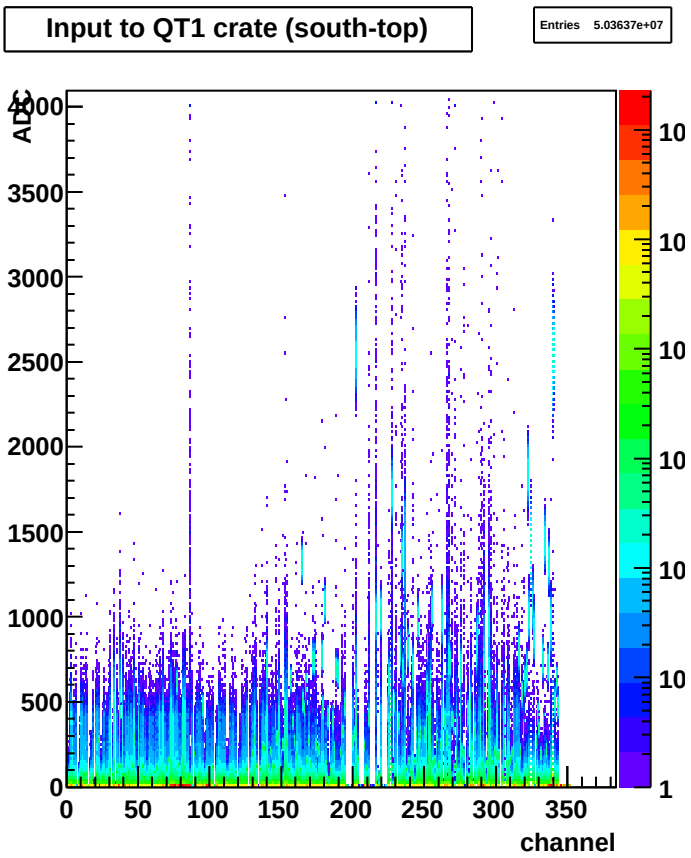

BBClarge-TAC-W
Entries 2854475
Mean 1403
RMS 336.4

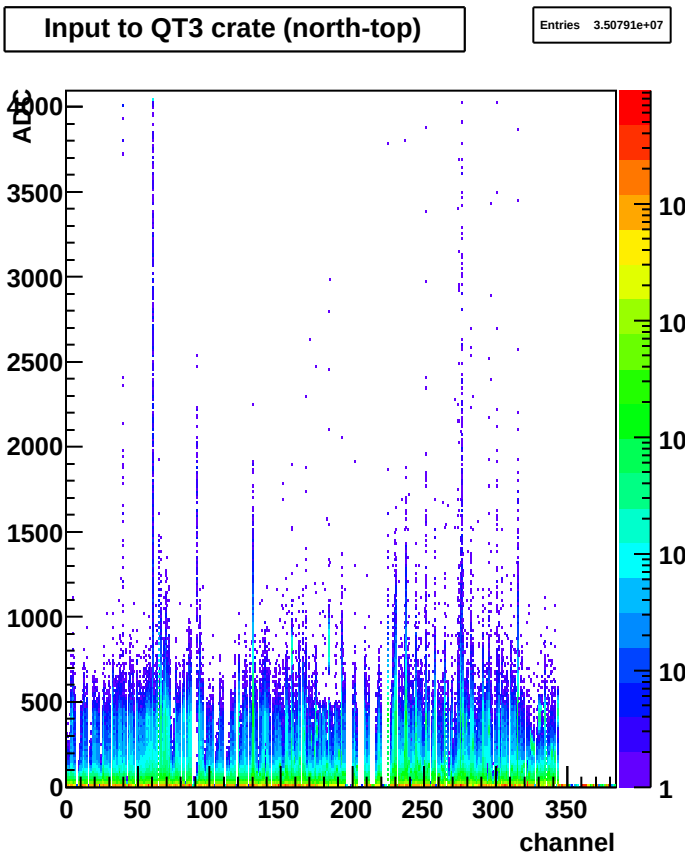

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2733982
Mean 4078
RMS 450.3

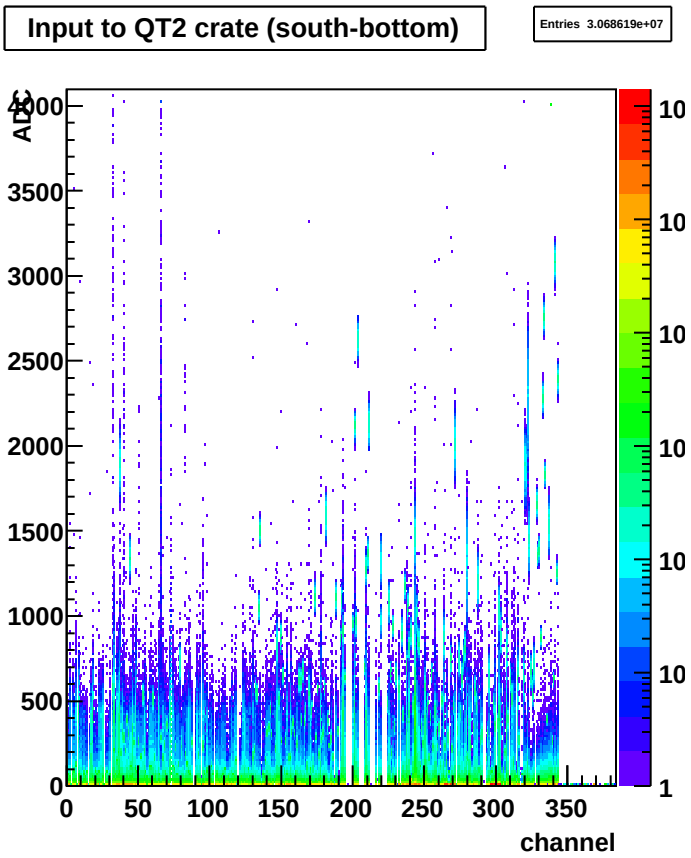

VPD-TAC-E
Entries 1808632
Mean 1163
RMS 144.3

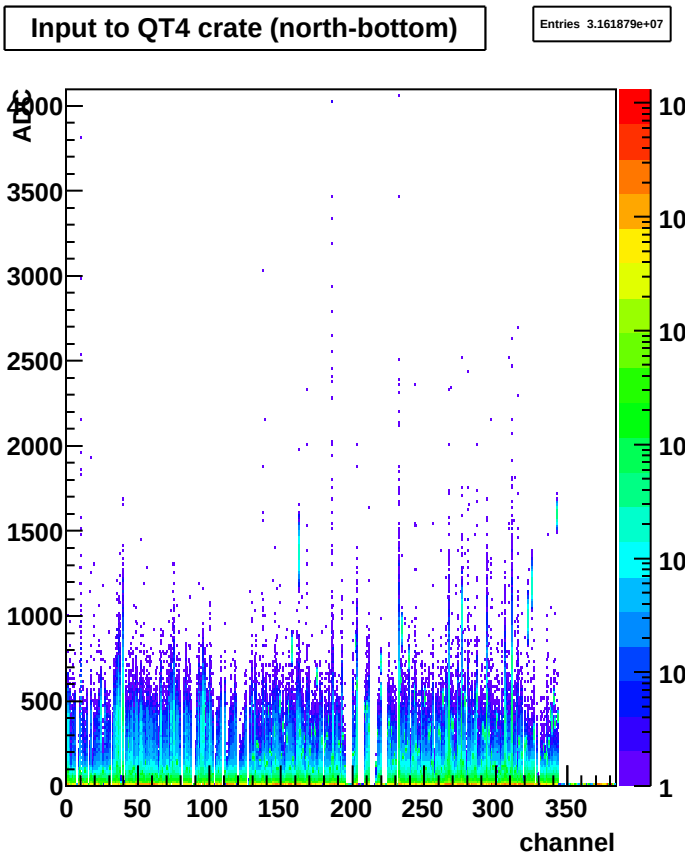

VPD-TAC-W
Entries 1690638
Mean 1273
RMS 156.2

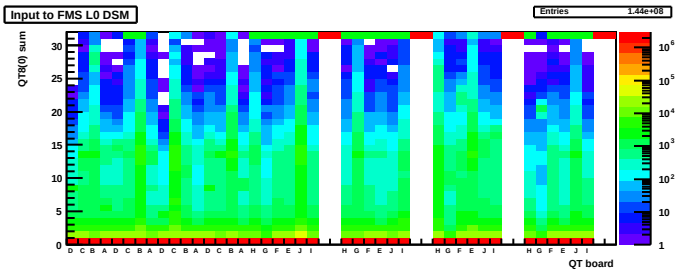

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1226140
Mean 3984
RMS 153.7

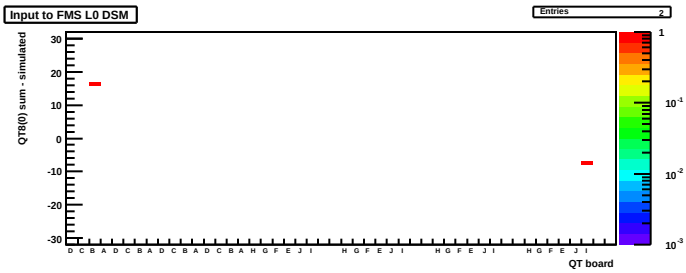

Input to QT1 crate (south-top)
Entries 5.03637e+07
ADC
channel

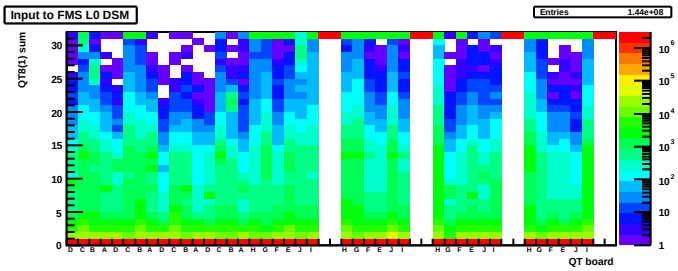

Input to QT3 crate (north-top)
Entries 3.50791e+07
ADC
channel

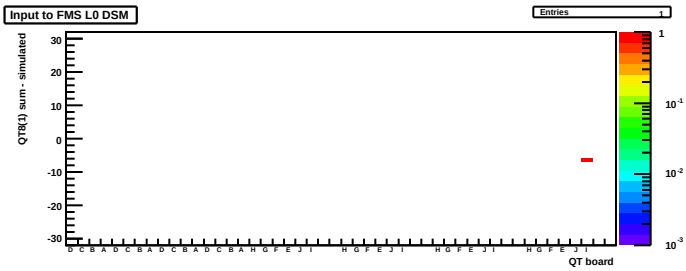

Input to QT2 crate (south-bottom)
Entries 3.068619e+07
ADC
channel

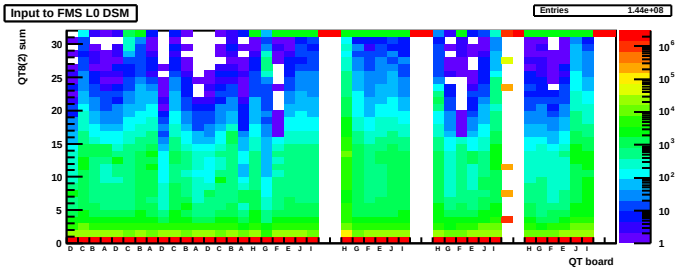

Input to QT4 crate (north-bottom)
Entries 3.161879e+07
ADC
channel

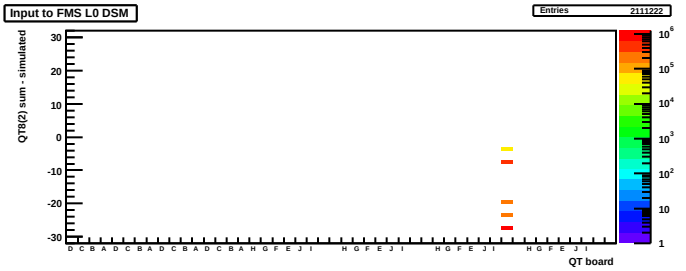

Input to FMS L0 DSM
Entries 1.44e+08
QT8(0) sum
QT board

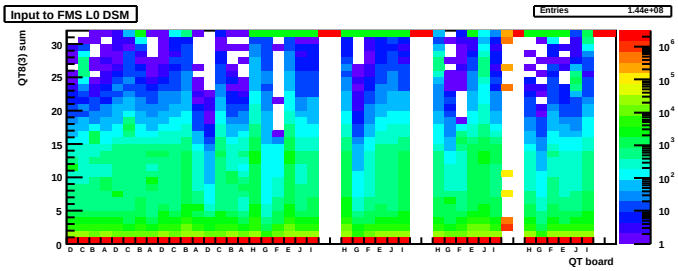

Input to FMS L0 DSM
Entries 2
QT8(0) sum - simulated
QT board

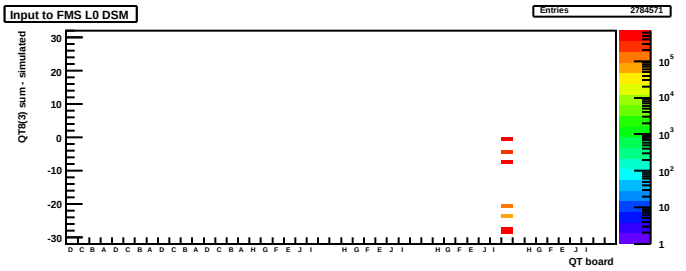

Input to FMS L0 DSM
Entries 1.44e+08
QT8(1) sum
QT board

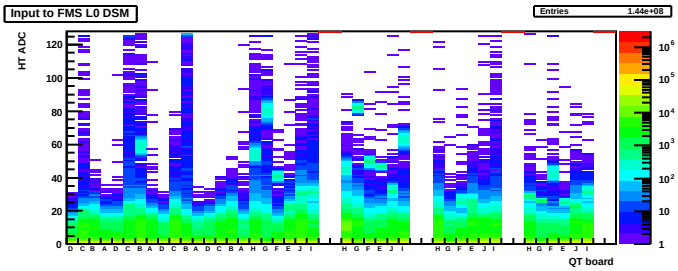

Input to FMS L0 DSM
Entries 1
QT8(1) sum - simulated
QT board

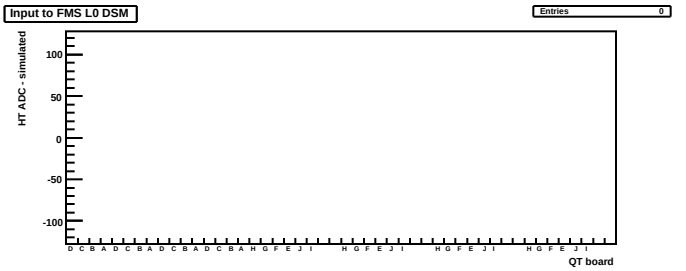

Input to FMS L0 DSM
Entries 1.44e+08
QT8(2) sum
QT board

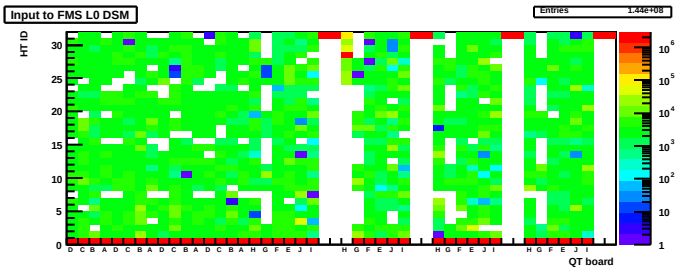

Input to FMS L0 DSM
Entries 2111222
QT8(2) sum - simulated
QT board

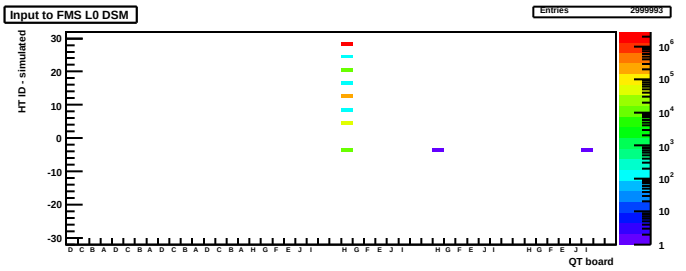

Input to FMS L0 DSM
Entries 1.44e+08
QT8(3) sum
QT board

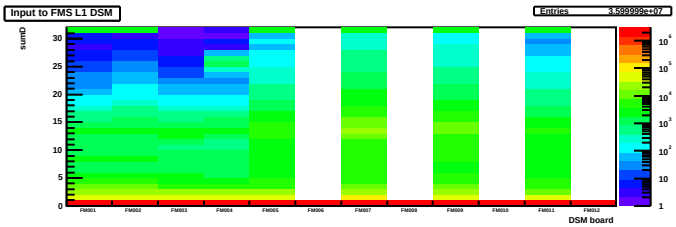

Input to FMS L0 DSM
Entries 2784571
QT8(3) sum - simulated
QT board

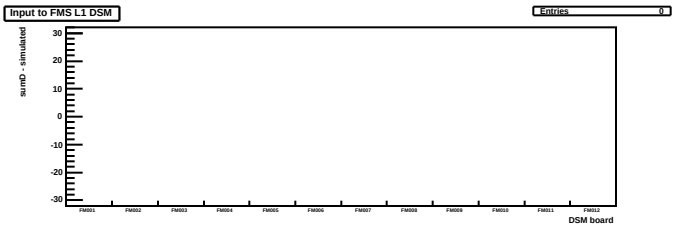

Input to FMS L0 DSM
Entries 1.44e+08
HT ADC
QT board

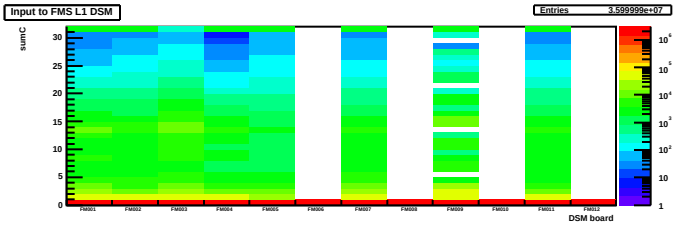

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

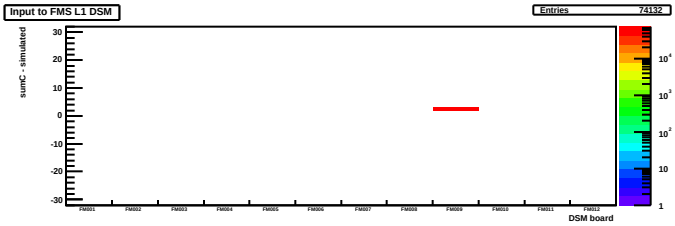

Input to FMS L0 DSM
Entries 1.44e+08
HT ID
QT board

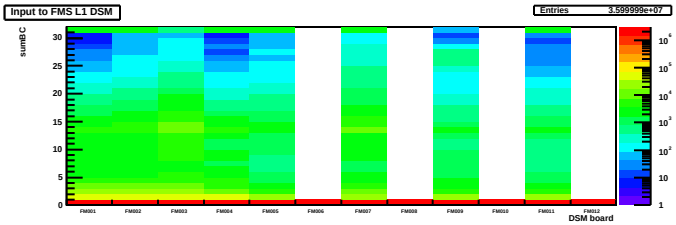

Input to FMS L0 DSM
Entries 2999993
HT ID - simulated
QT board

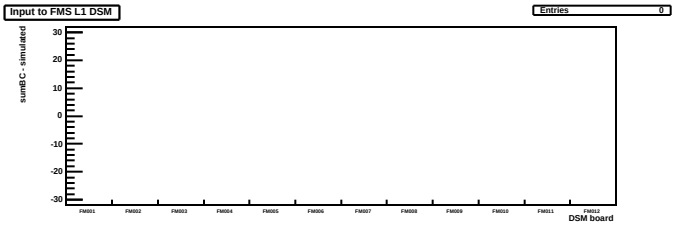

Input to FMS L1 DSM
Entries 3.599999e+07
sumD
DSM board

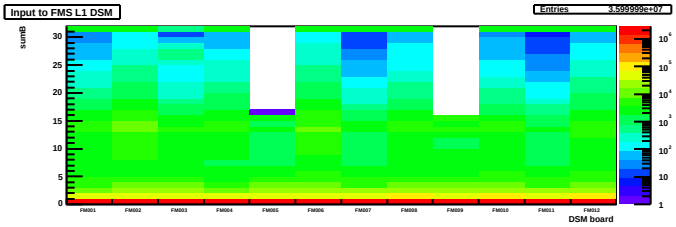

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

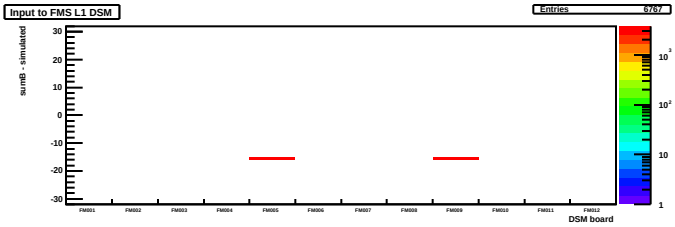

Input to FMS L1 DSM
Entries 3.599999e+07
sumC
DSM board

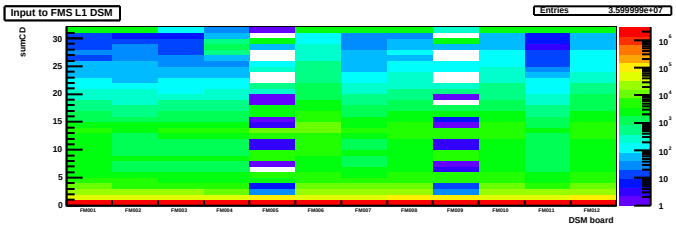

Input to FMS L1 DSM
Entries 74132
sumC - simulated
DSM board

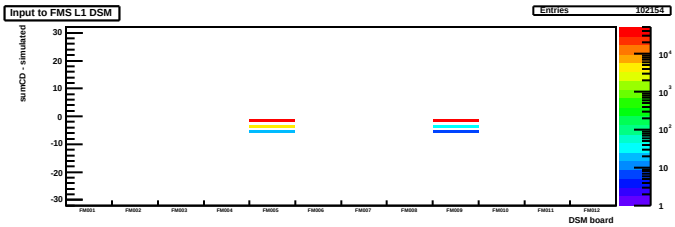

Input to FMS L1 DSM
Entries 3.599999e+07
sumBC
DSM board

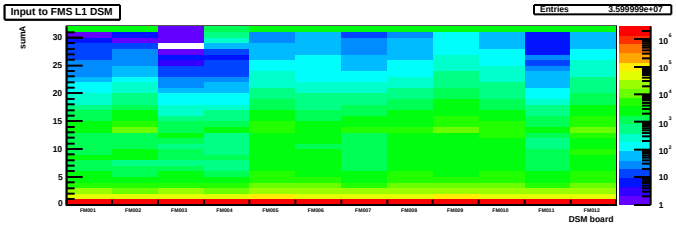

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

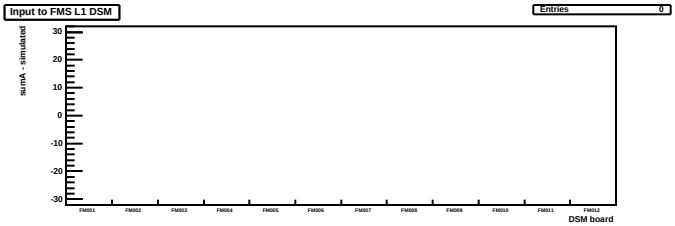

Input to FMS L1 DSM
Entries 3.599999e+07
sumB
DSM board

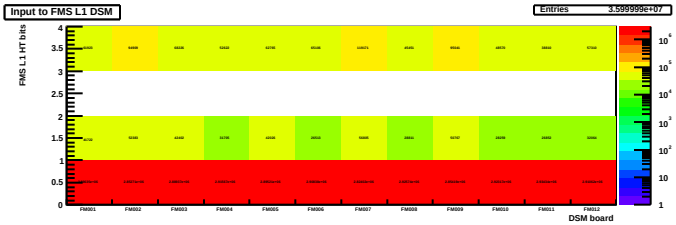

Input to FMS L1 DSM
Entries 6767
sumB - simulated
DSM board

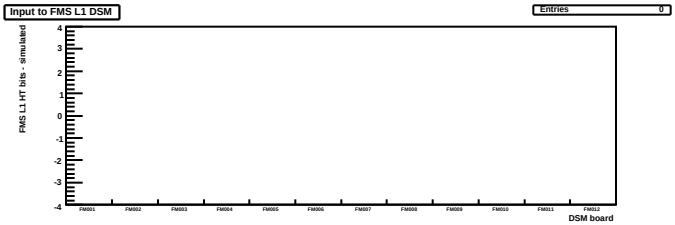

Input to FMS L1 DSM
Entries 3.599999e+07
sumCD
DSM board

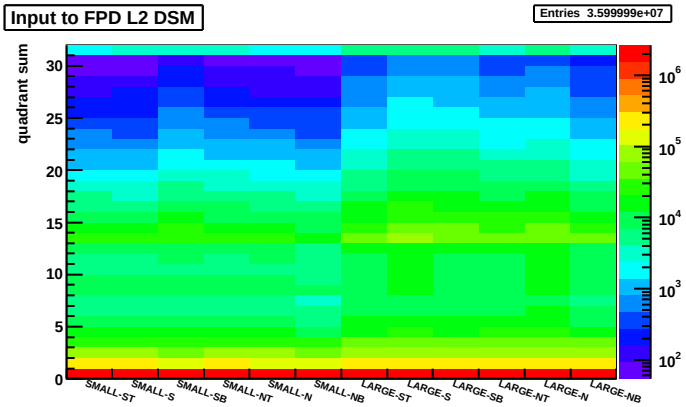

Input to FMS L1 DSM
Entries 102154
sumCD - simulated
DSM board

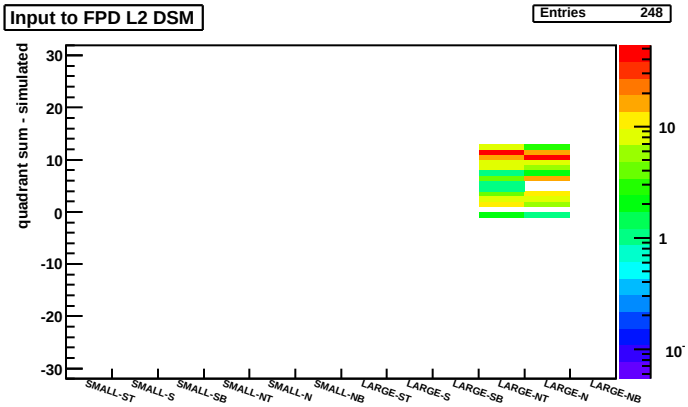

Input to FMS L1 DSM
Entries 3.599999e+07
sumA
DSM board

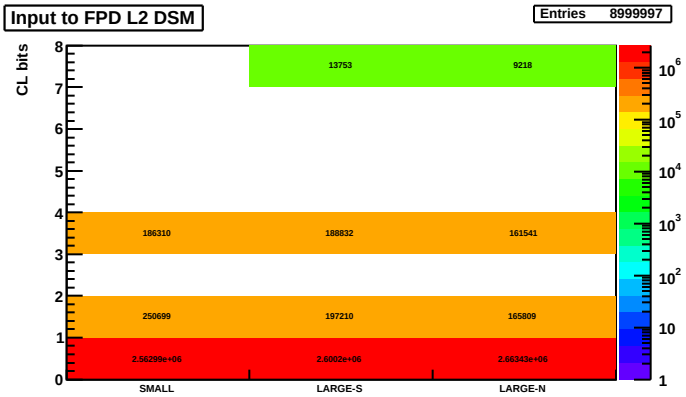

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

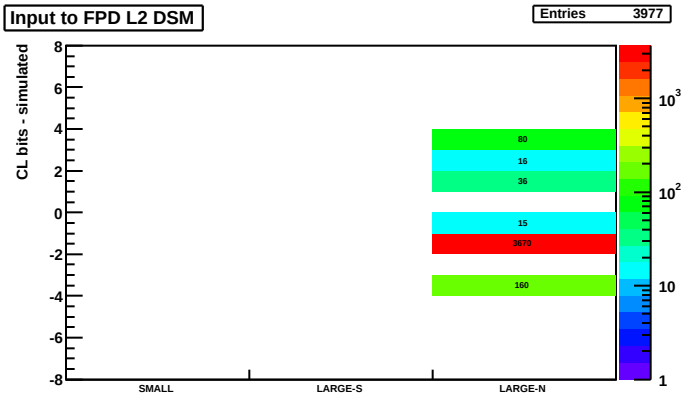

Input to FMS L1 DSM
Entries 3.599999e+07
FMS L1 HT bits
DSM board

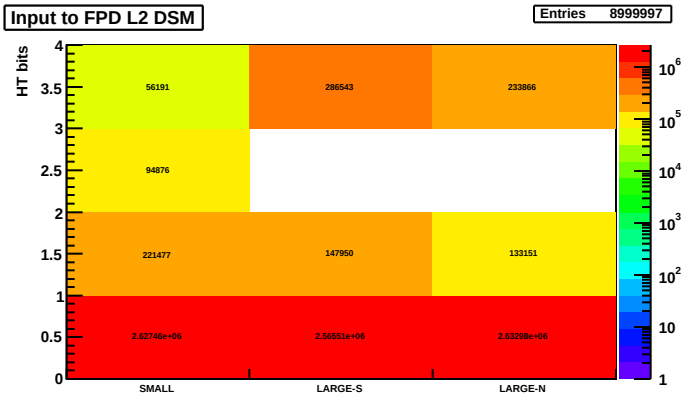

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

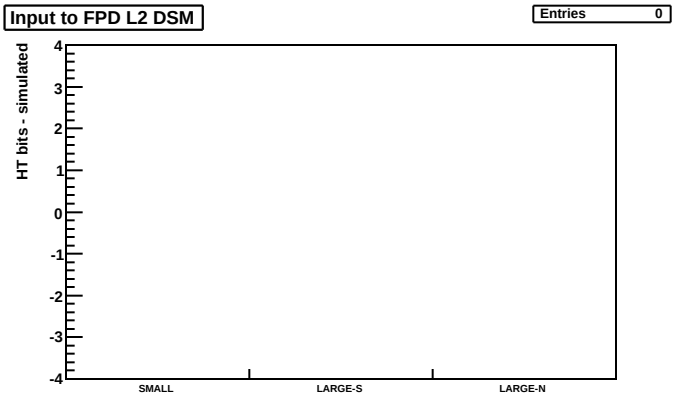

Input to FPD L2 DSM
Entries 3.599999e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

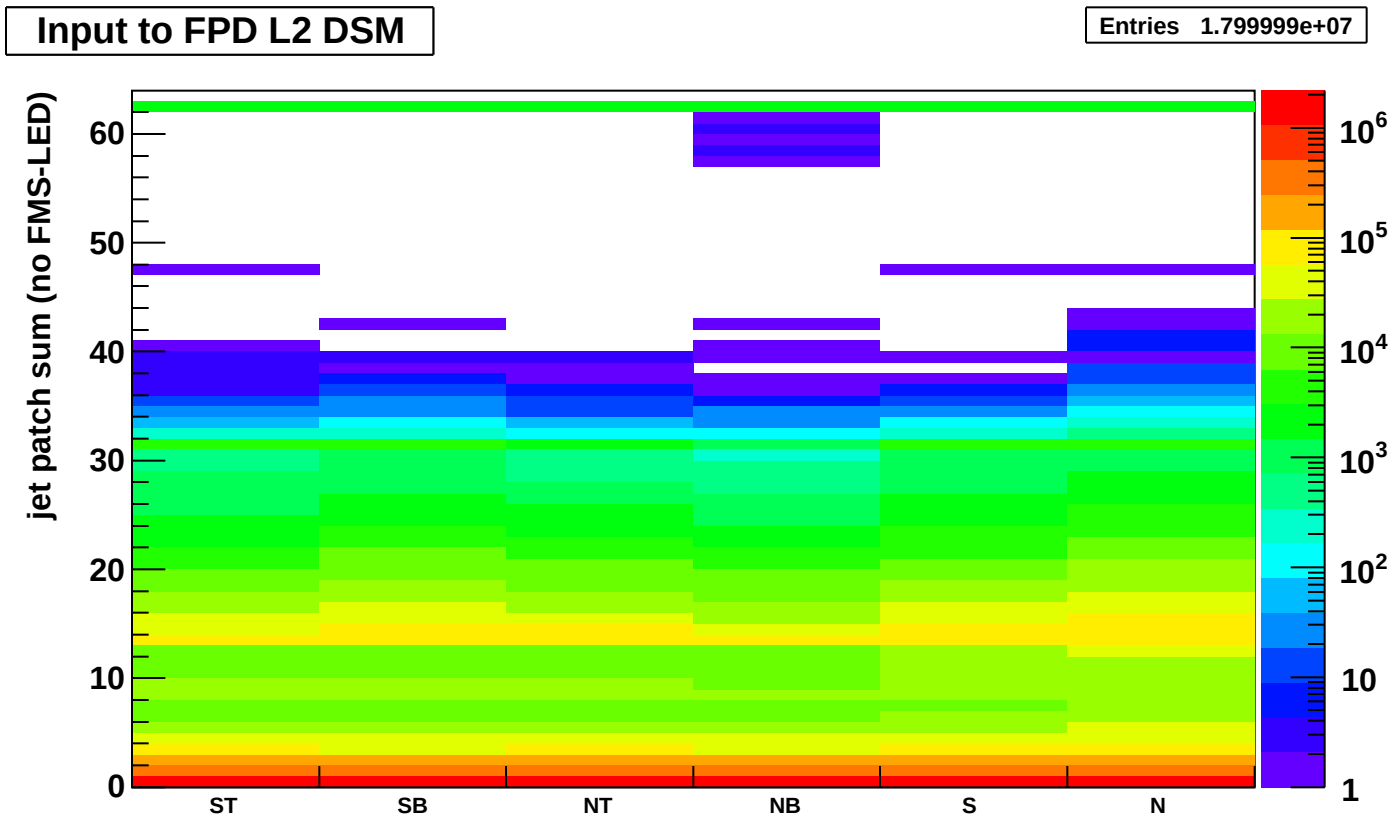

Input to FPD L2 DSM
Entries 248
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

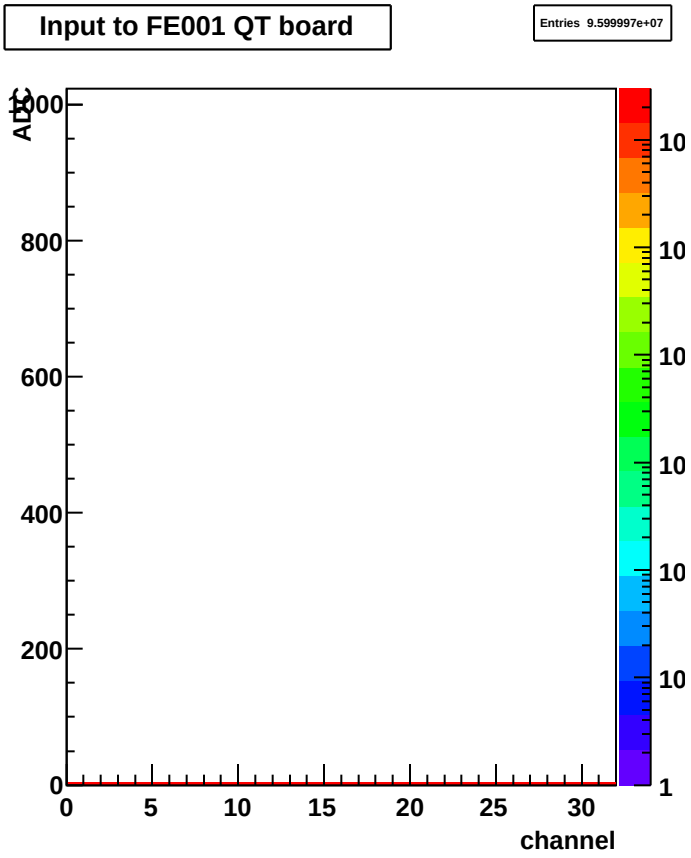

Input to FPD L2 DSM
Entries 8999997
CL bits
13753
9218
186310
188832
161541
250699
197210
165809
2.56299e+06
2.6002e+06
2.66343e+06
SMALL
LARGE-S
LARGE-N

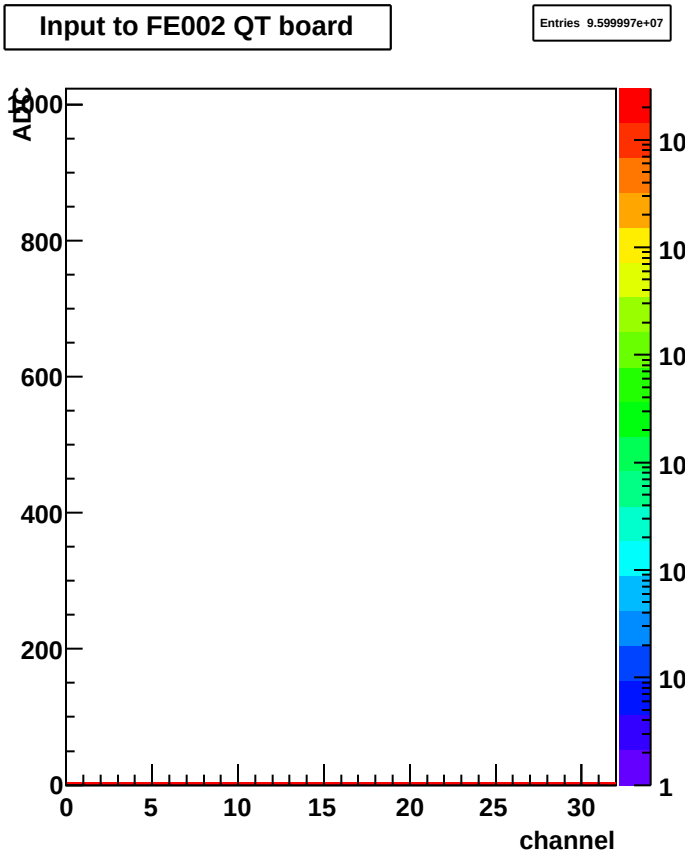

Input to FPD L2 DSM
Entries 3977
CL bits - simulated
80
16
36
15
3670
160
SMALL
LARGE-S
LARGE-N

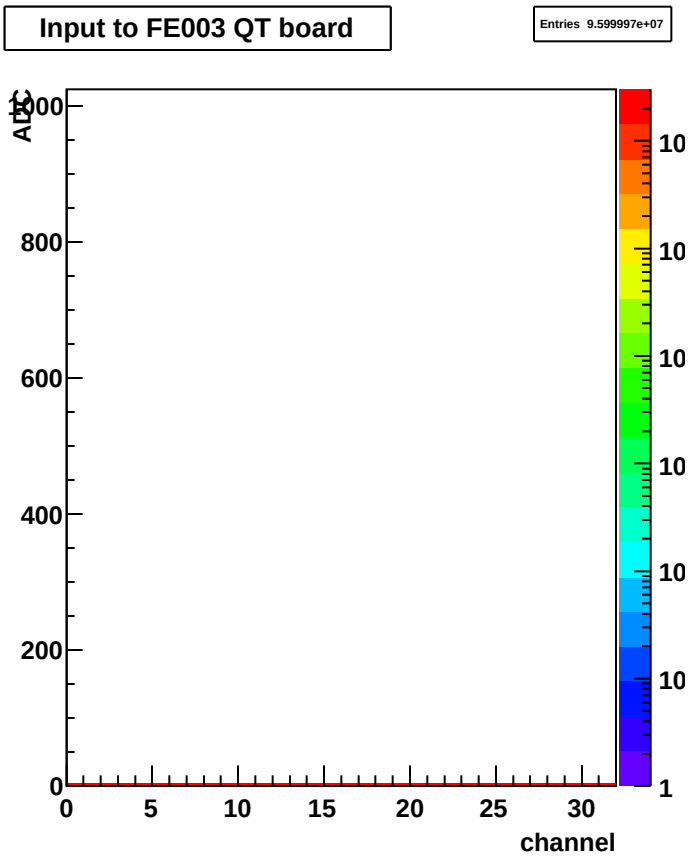

Input to FPD L2 DSM
Entries 8999997
HT bits
56191
286543
233866
94876
221477
147950
133151
2.62746e+06
2.56551e+06
2.63298e+06
SMALL
LARGE-S
LARGE-N

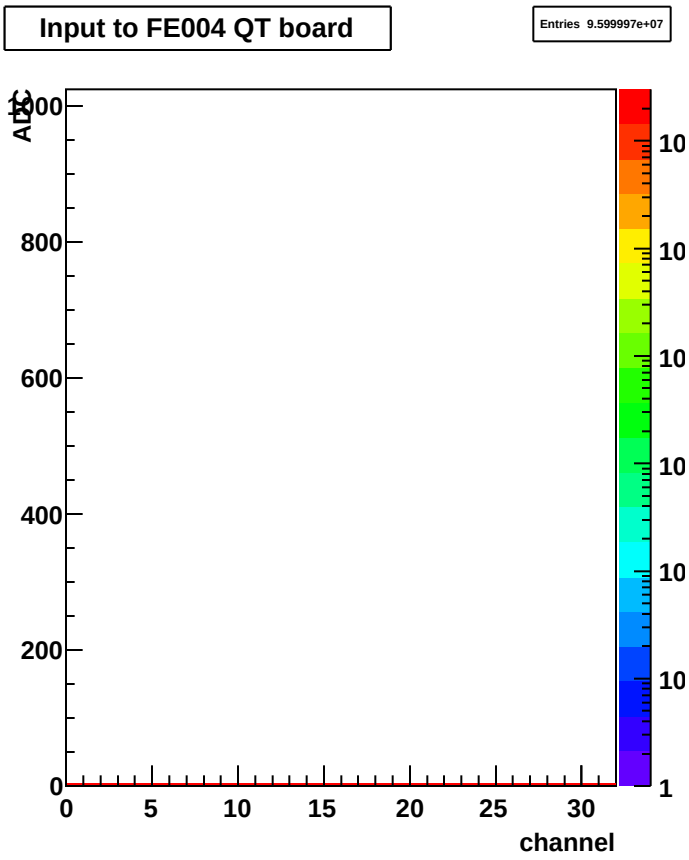

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

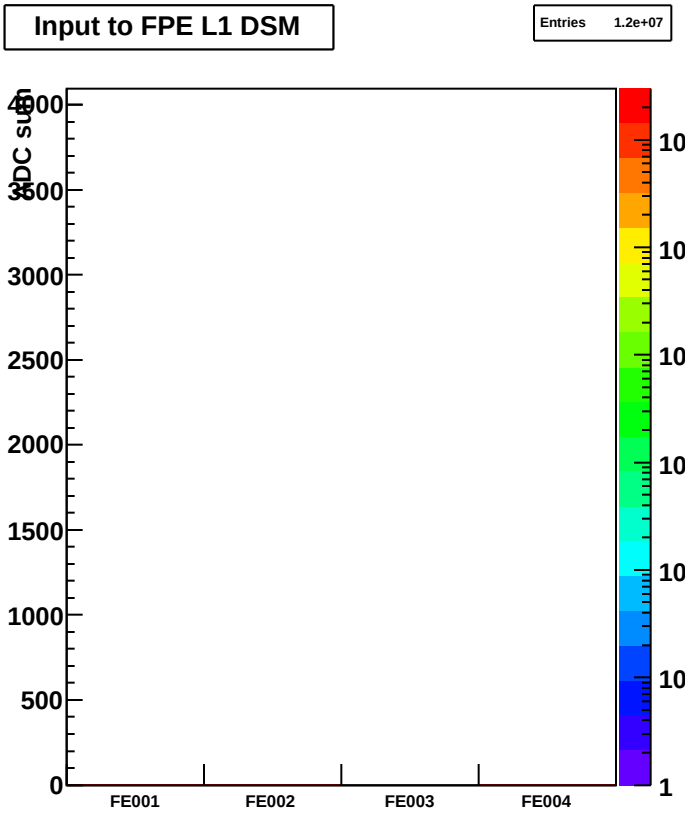

Input to FPD L2 DSM
Entries 1.799999e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

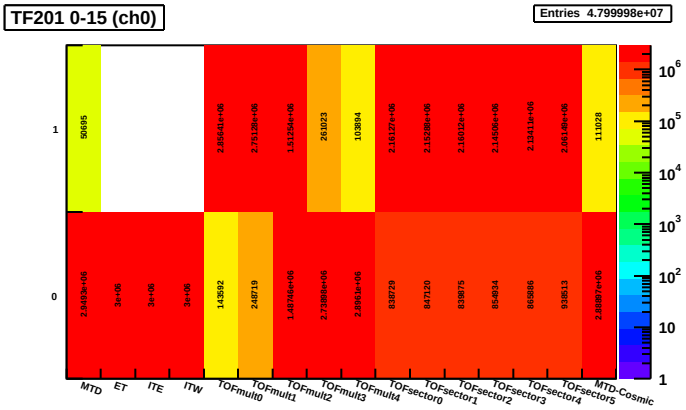

Input to FE001 QT board
Entries 9.599997e+07
ADC
channel

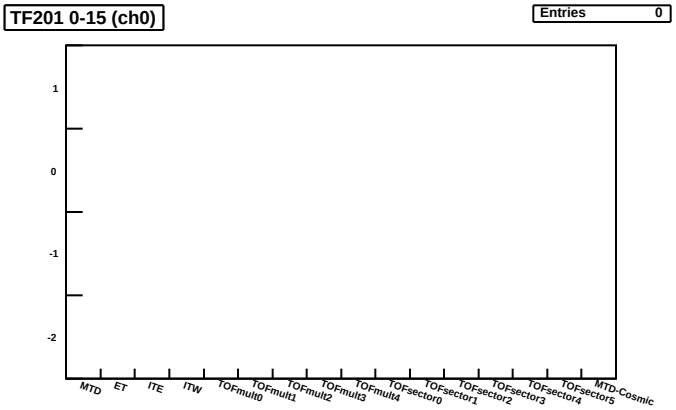

Input to FE002 QT board
Entries 9.599997e+07
ADC
channel

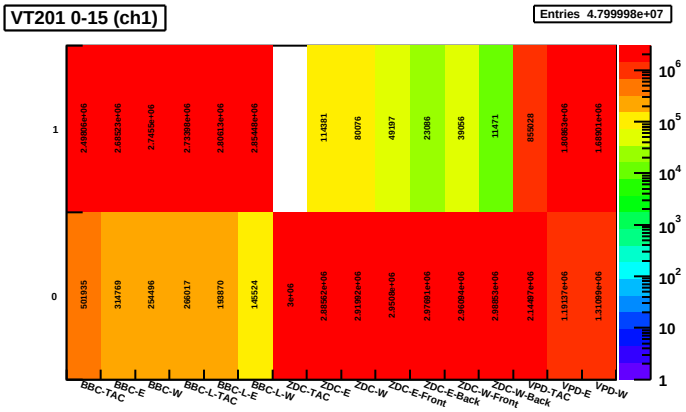

Input to FE003 QT board
Entries 9.599997e+07
ADC
channel


Input to FE004 QT board
Entries 9.599997e+07
ADC
channel


Input to FPE L1 DSM
Entries 1.2e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 4.799998e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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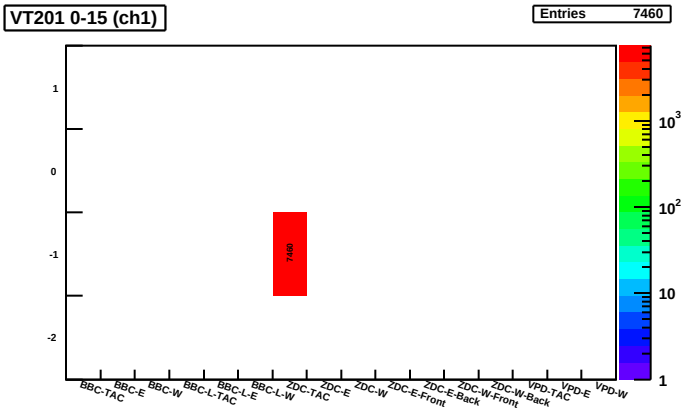
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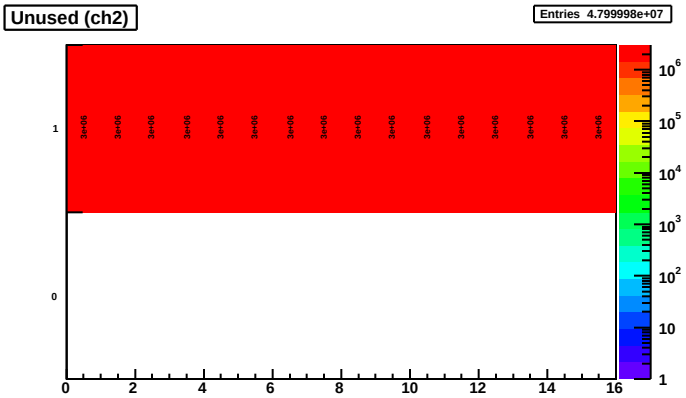
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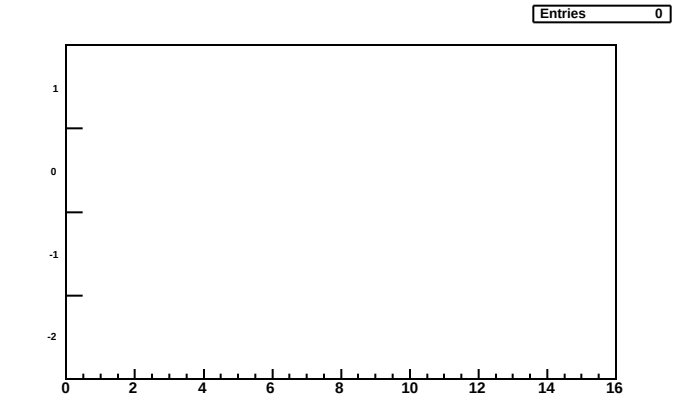
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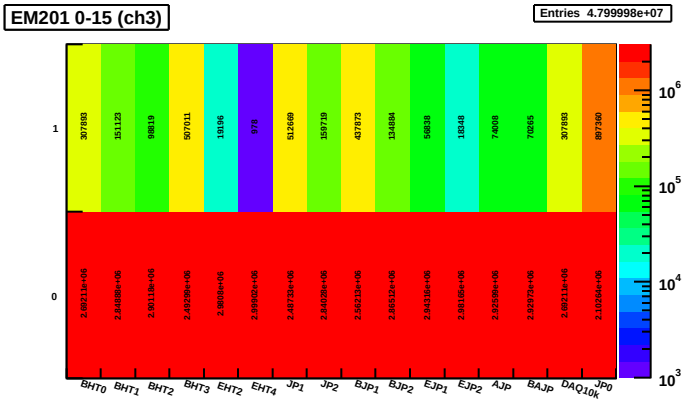
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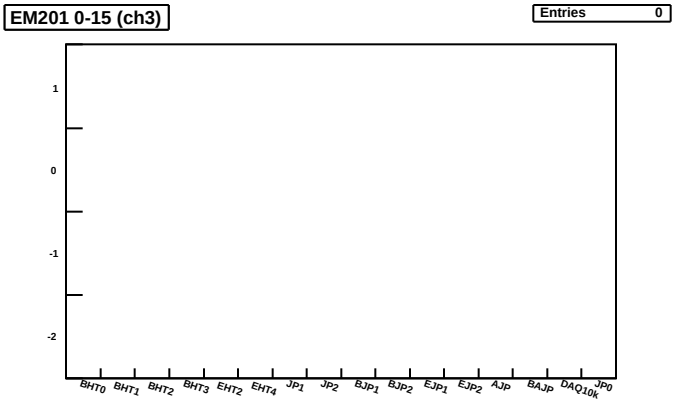
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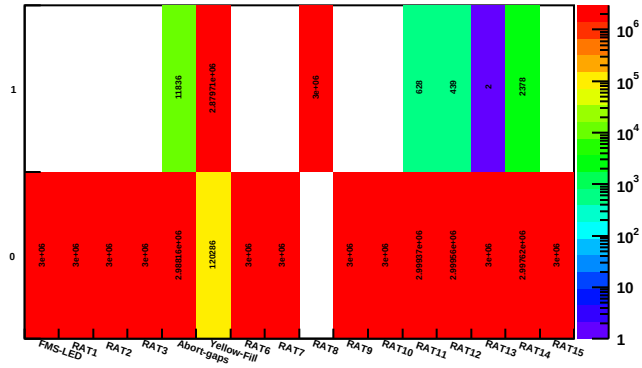
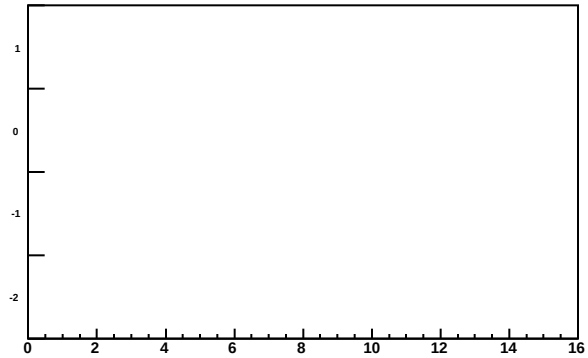
RAT board (ch4)





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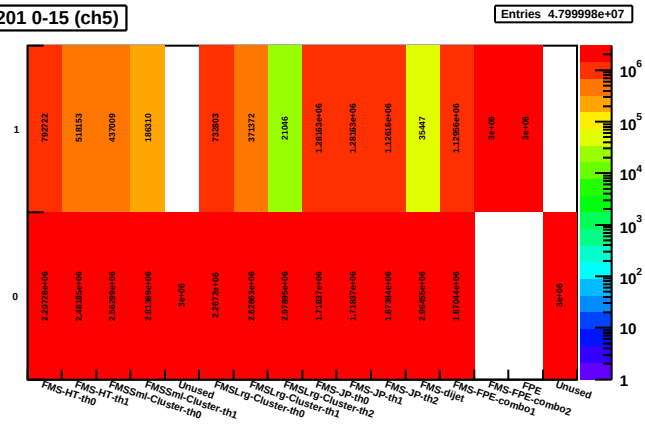

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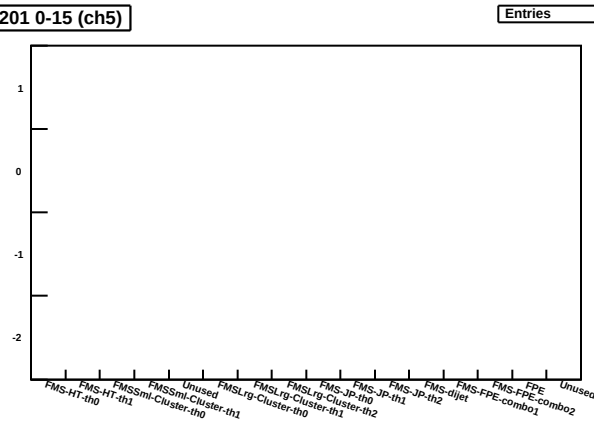
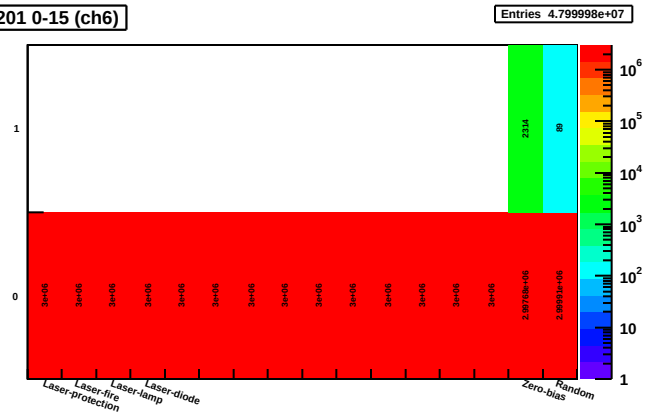
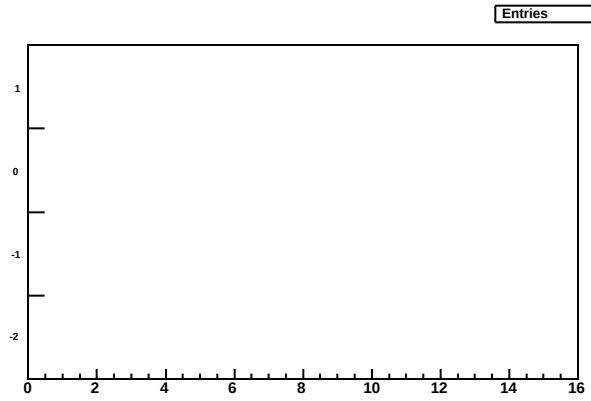
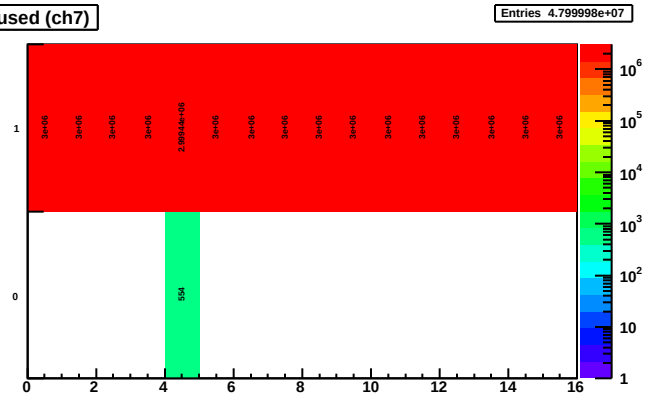
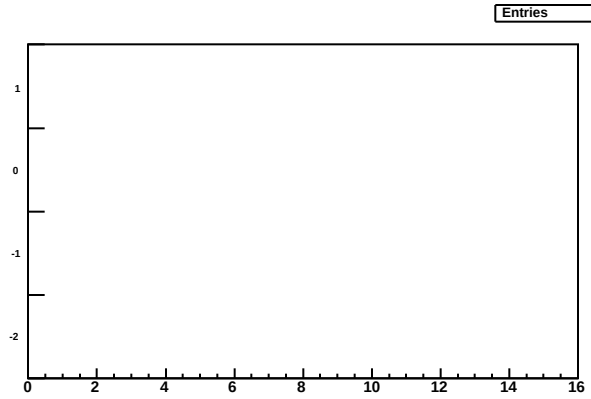


ST201 0-15 (ch6)





Unused (ch7)