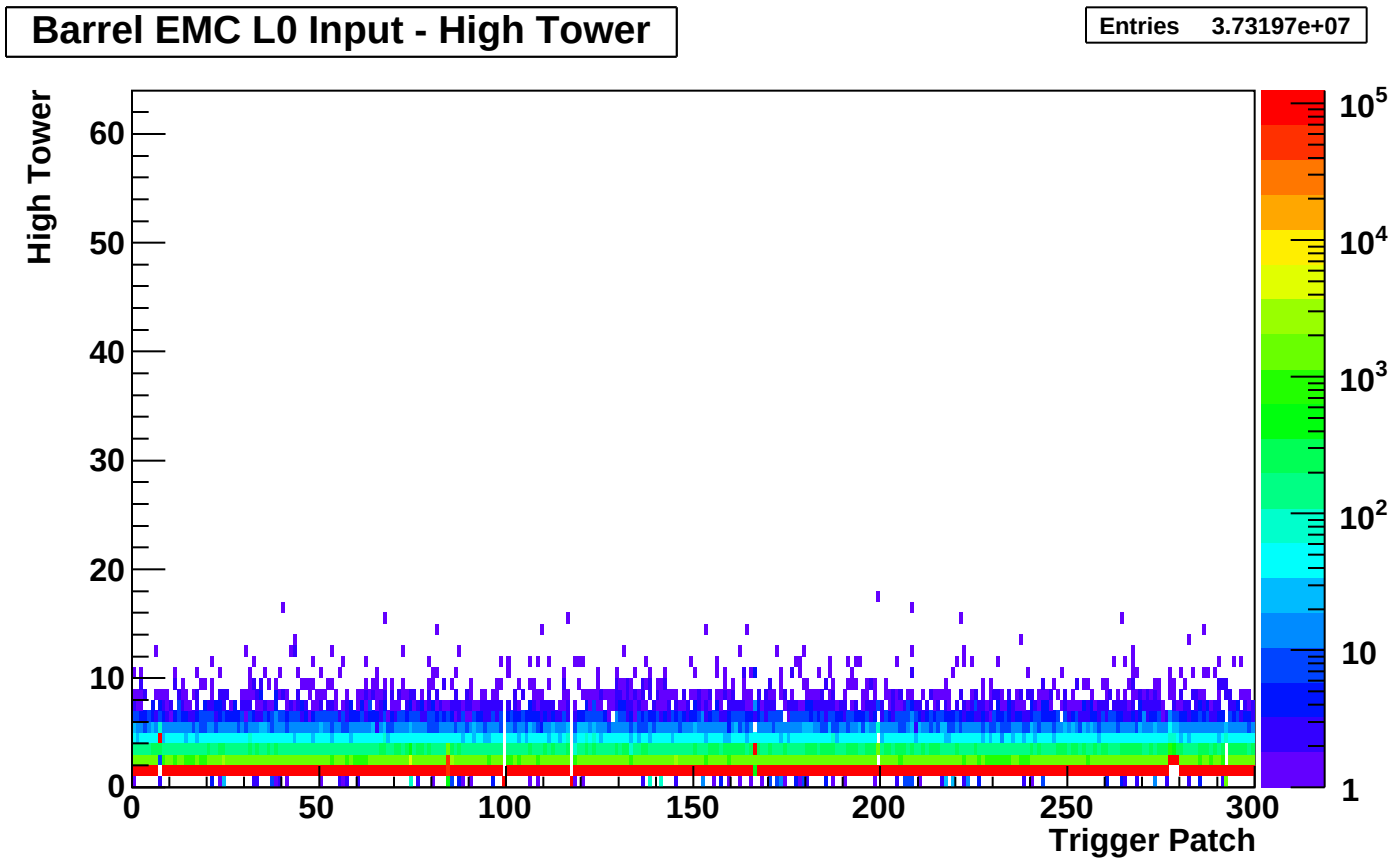
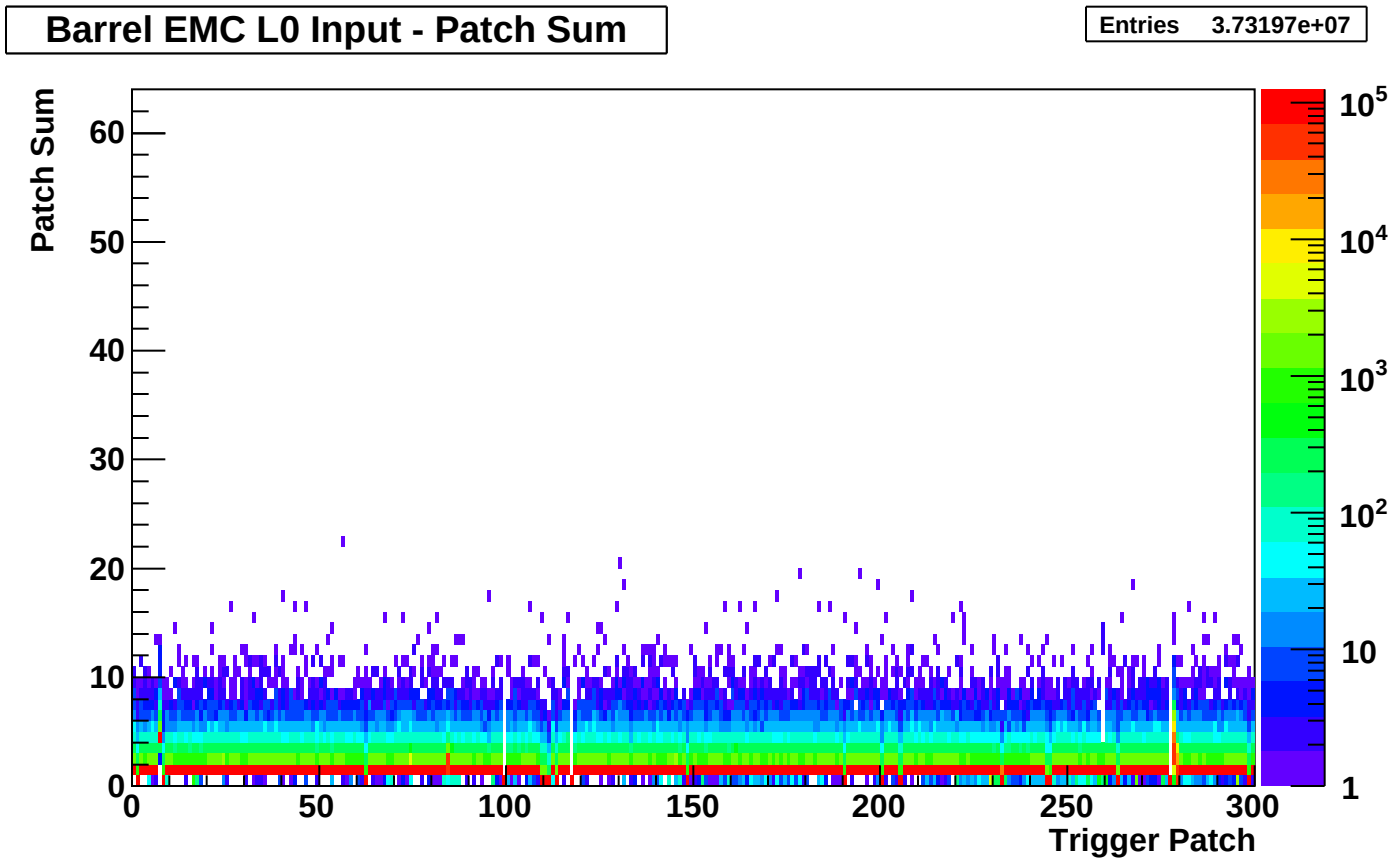
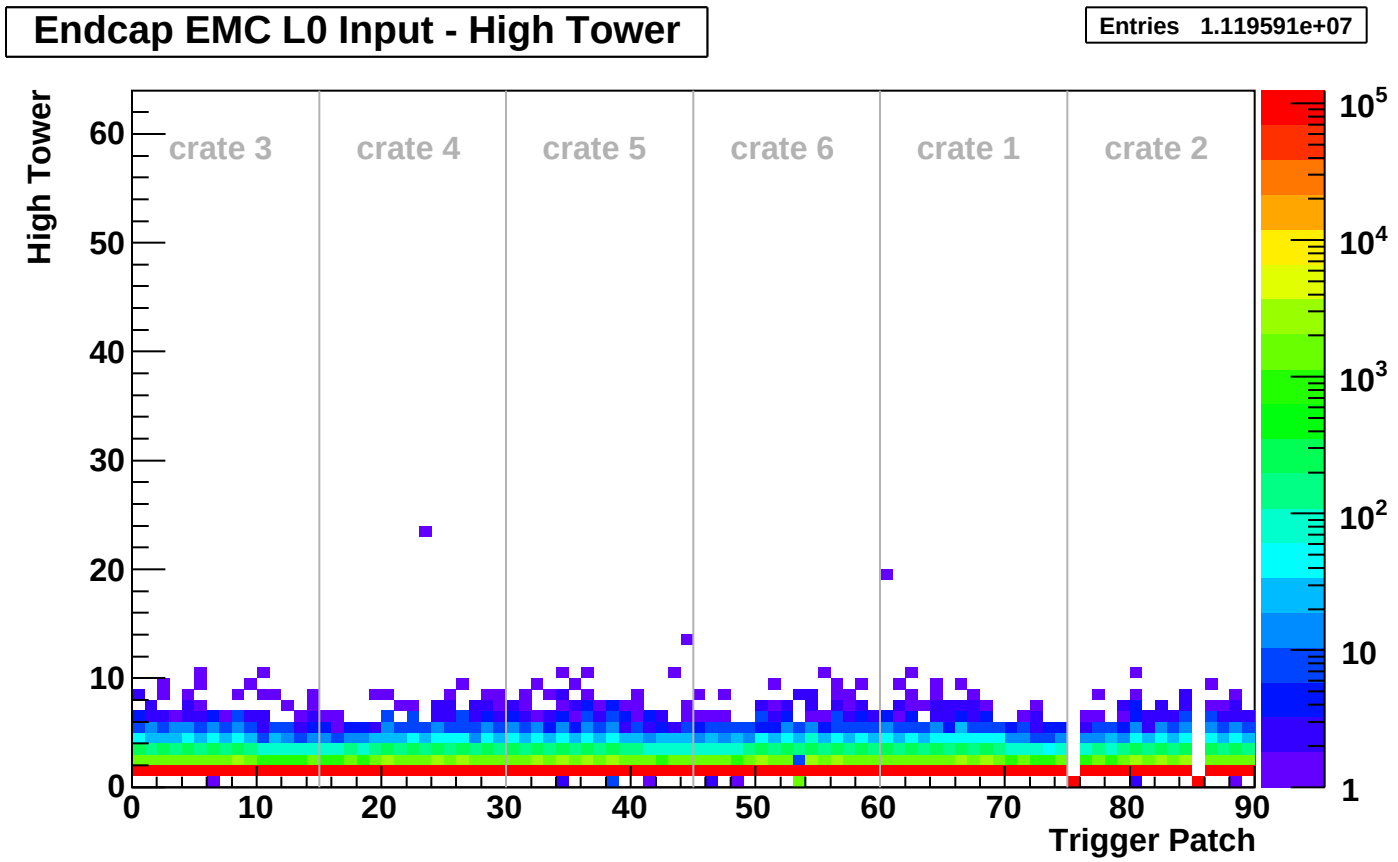
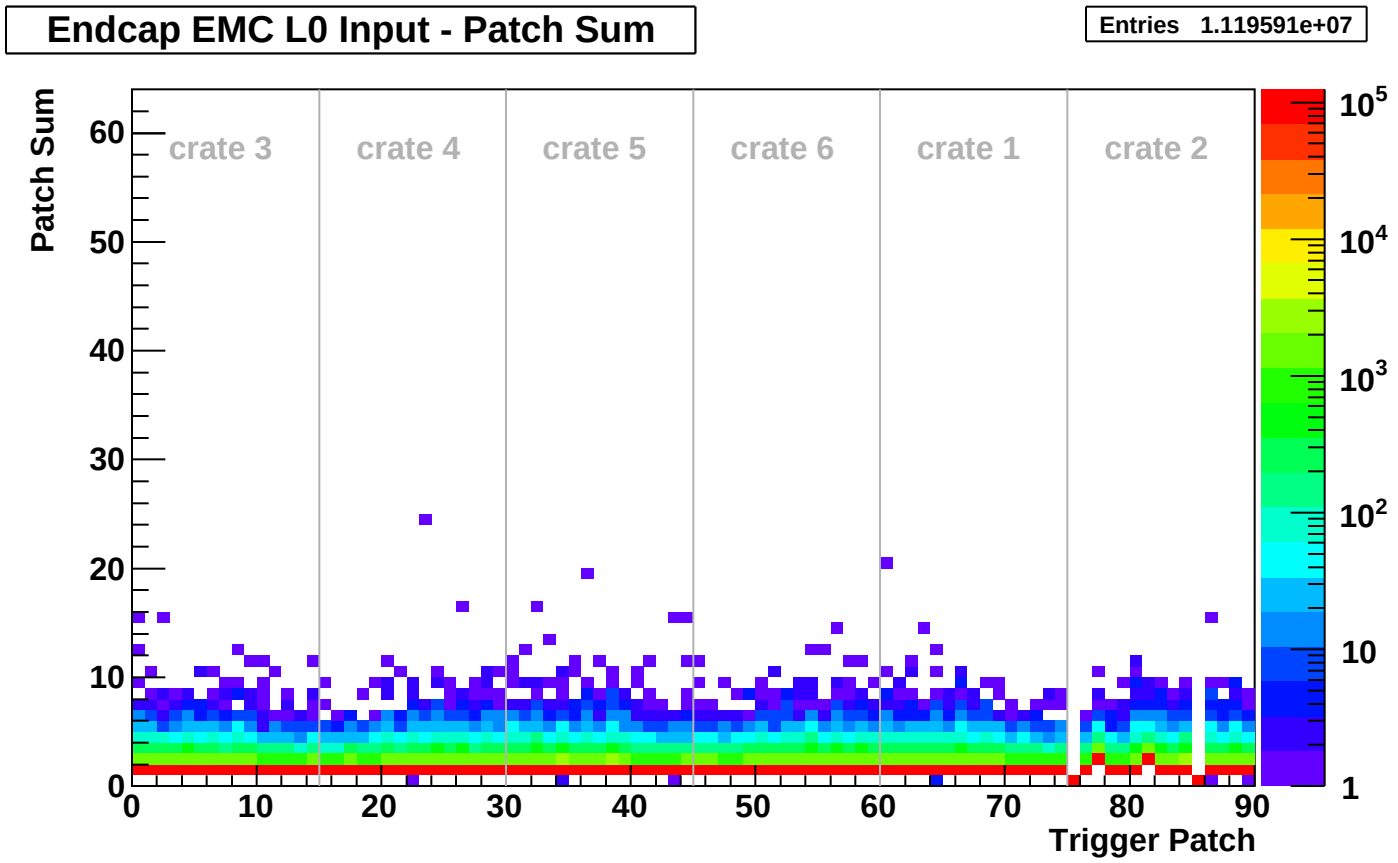
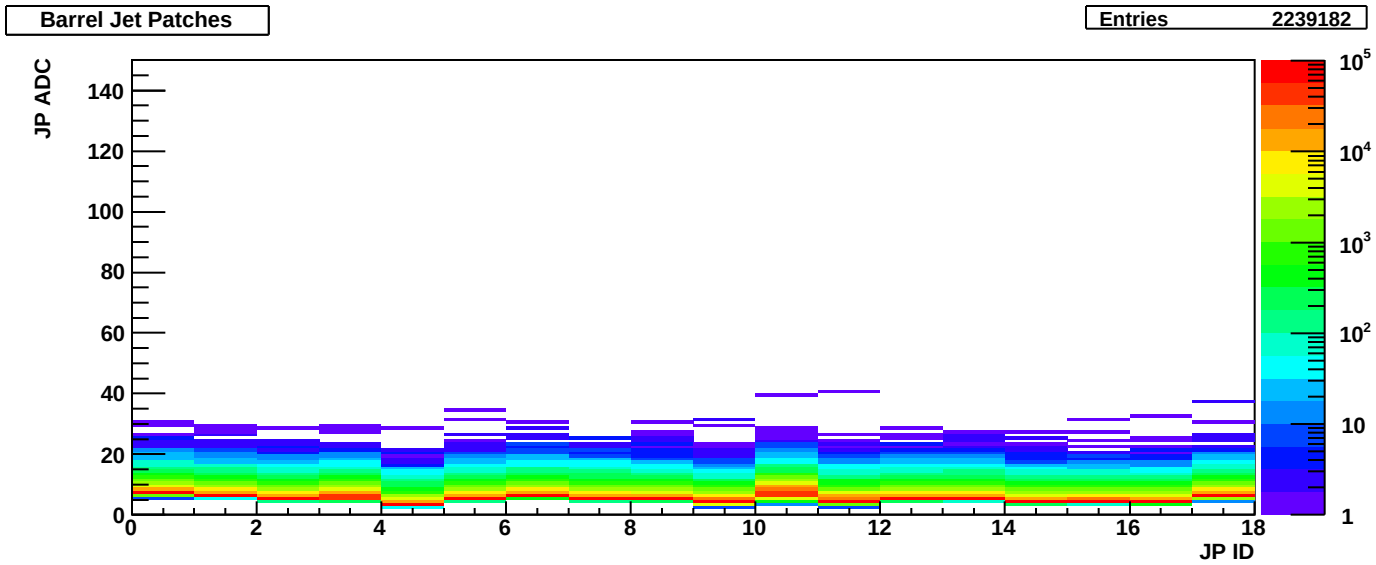
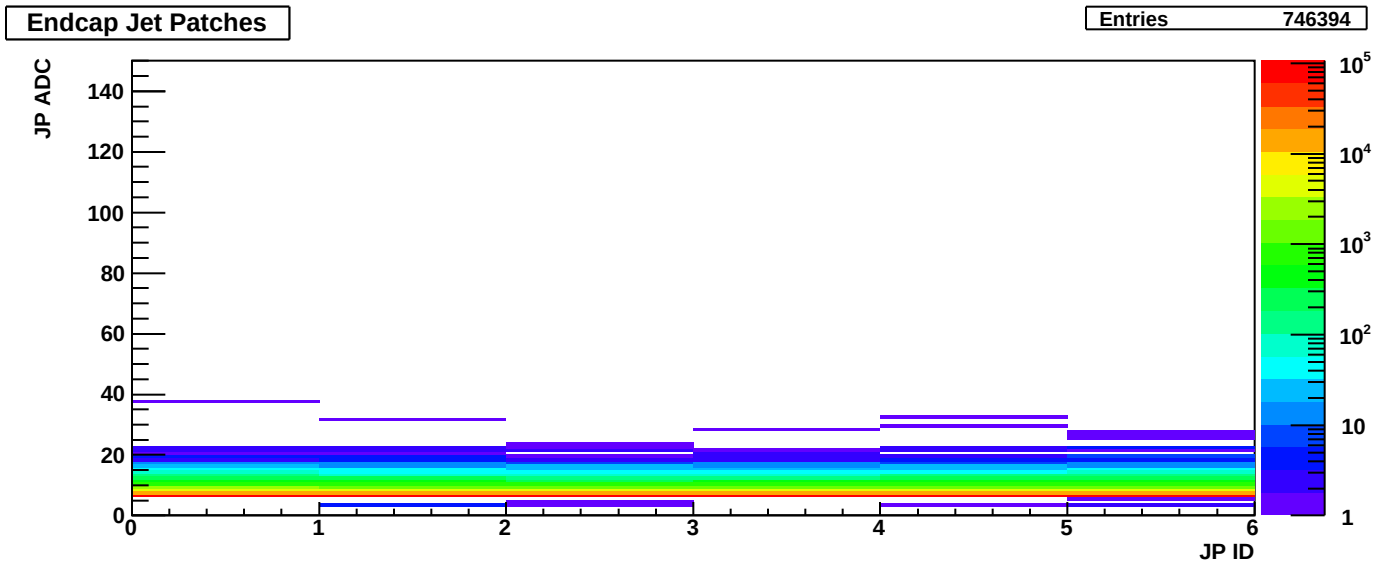

Barrel EMC L0 Input - High Tower
Entries 3.73197e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

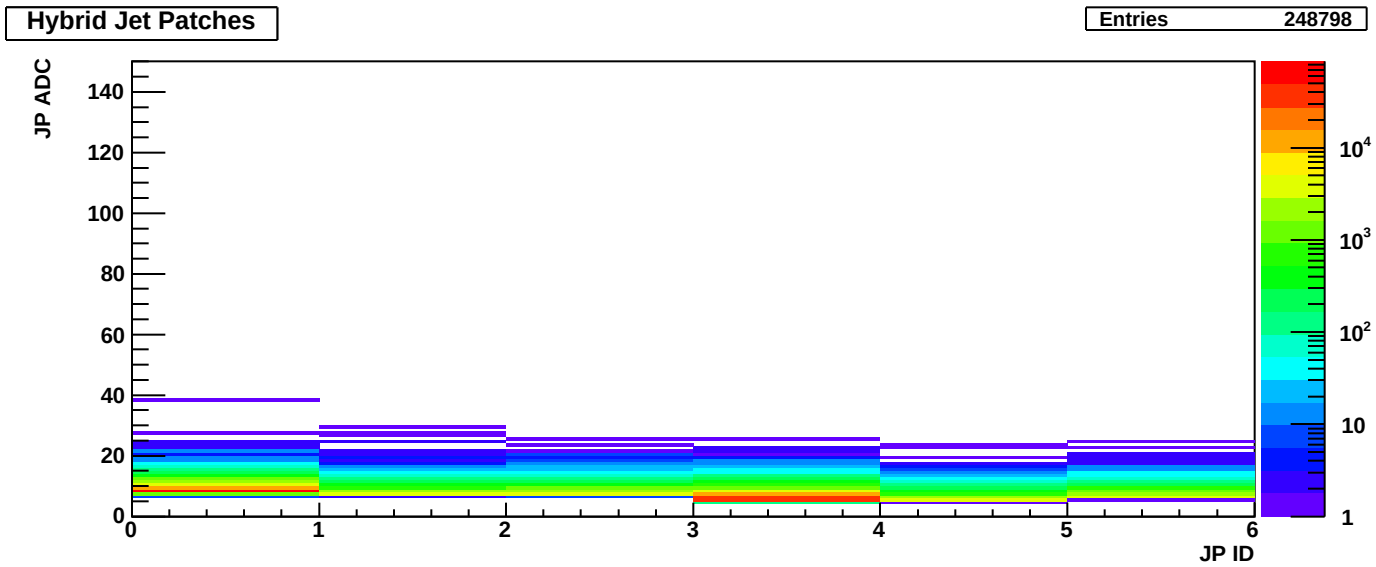

Barrel EMC L0 Input - Patch Sum
Entries 3.73197e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

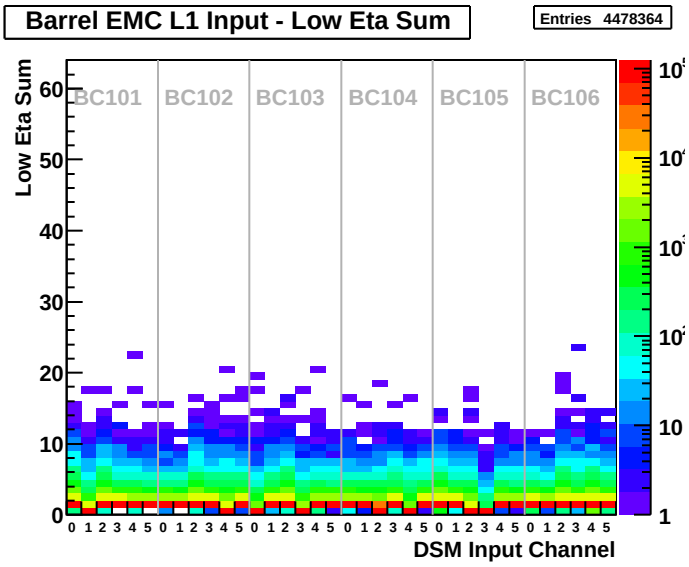

Endcap EMC L0 Input - High Tower
Entries 1.119591e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

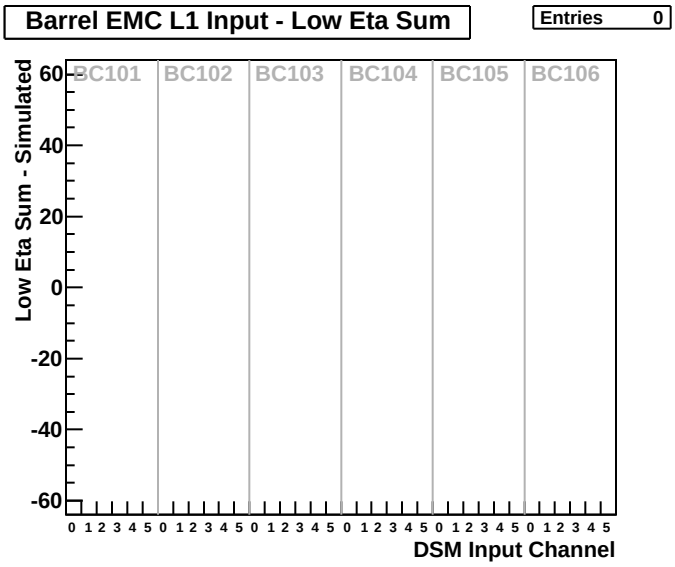

Endcap EMC L0 Input - Patch Sum
Entries 1.119591e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

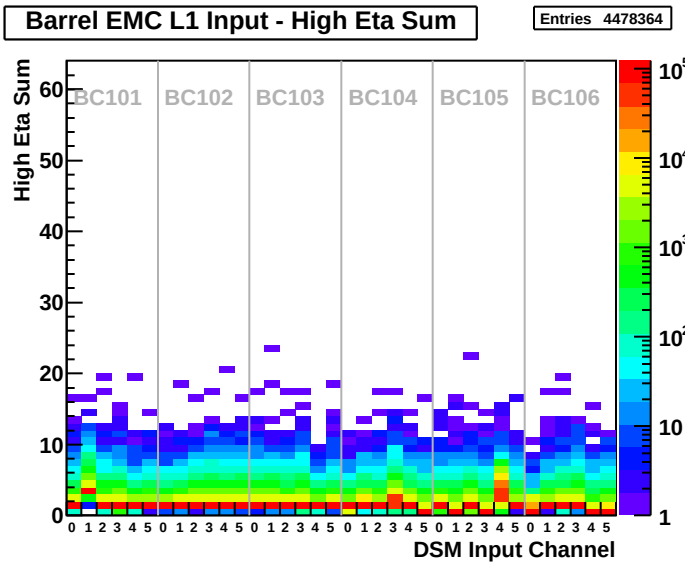

Barrel Jet Patches
Entries 2239182
JP ADC
JP ID

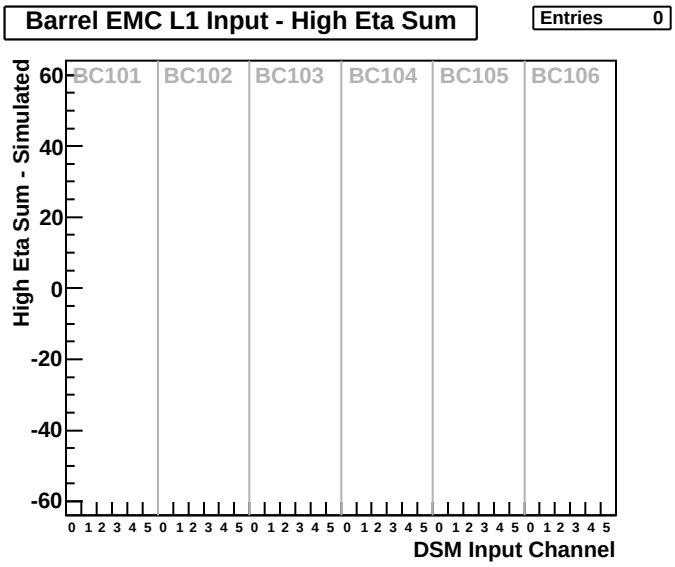

Endcap Jet Patches
Entries 746394
JP ADC
JP ID

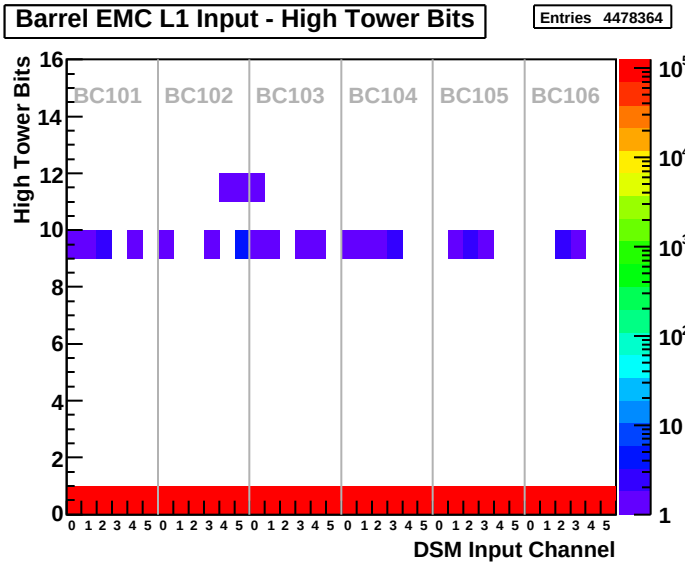

Hybrid Jet Patches
Entries 248798
JP ADC
JP ID

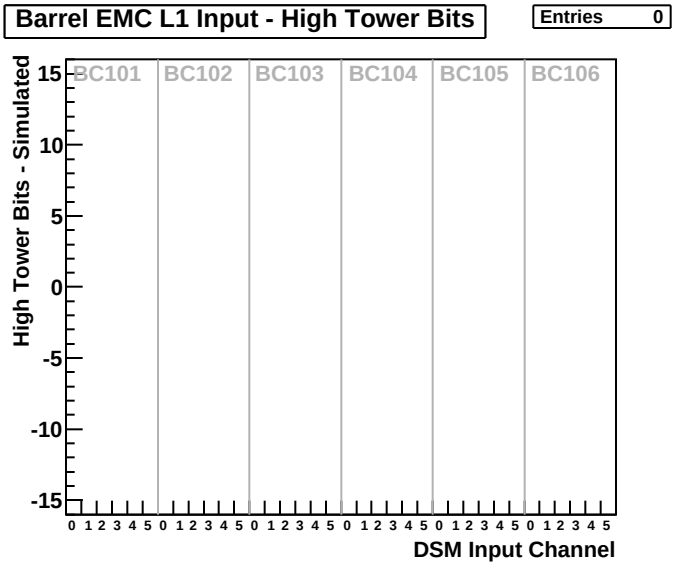

Barrel EMC L1 Input - Low Eta Sum
Entries 4478364
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

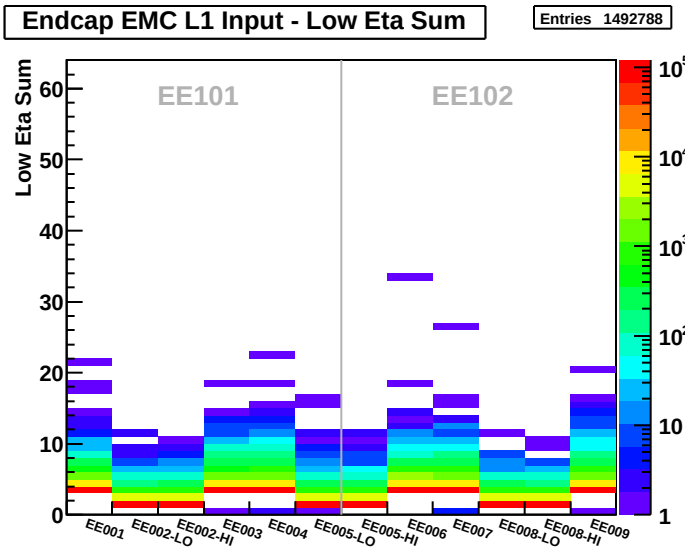

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

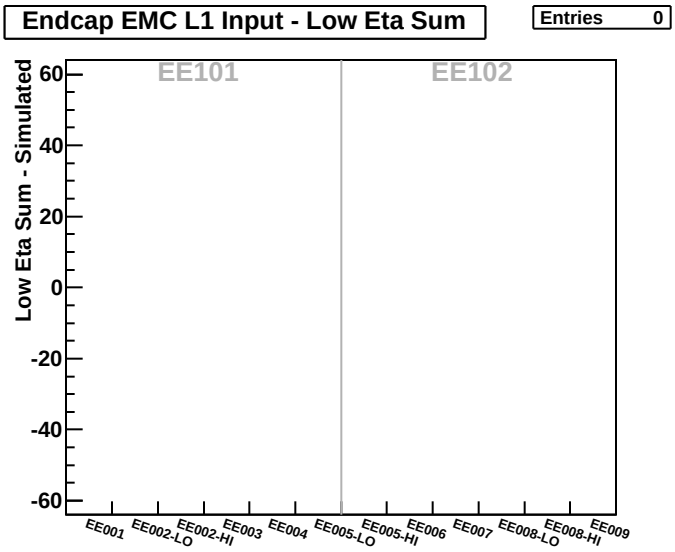

Barrel EMC L1 Input - High Eta Sum
Entries 4478364
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

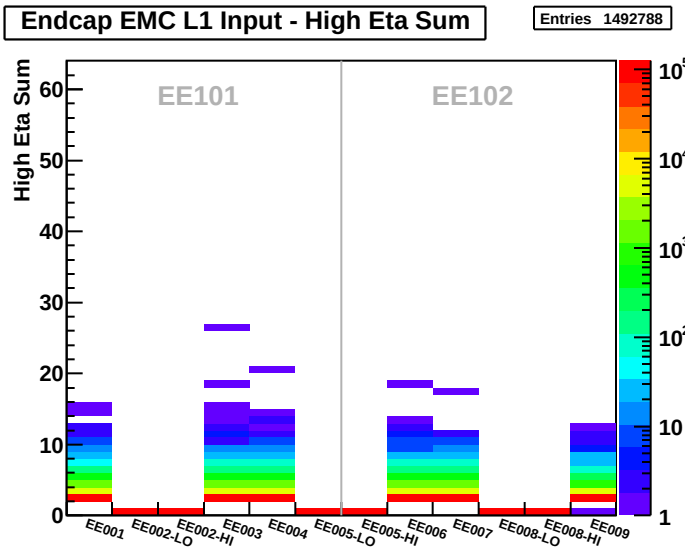

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

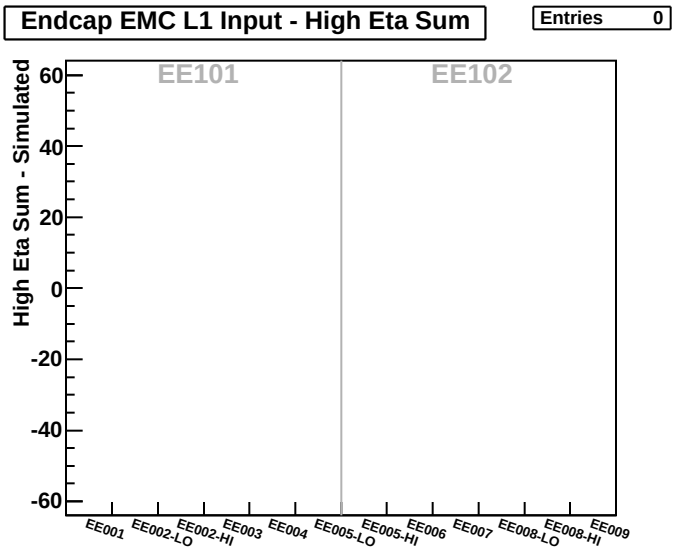

Barrel EMC L1 Input - High Tower Bits
Entries 4478364
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

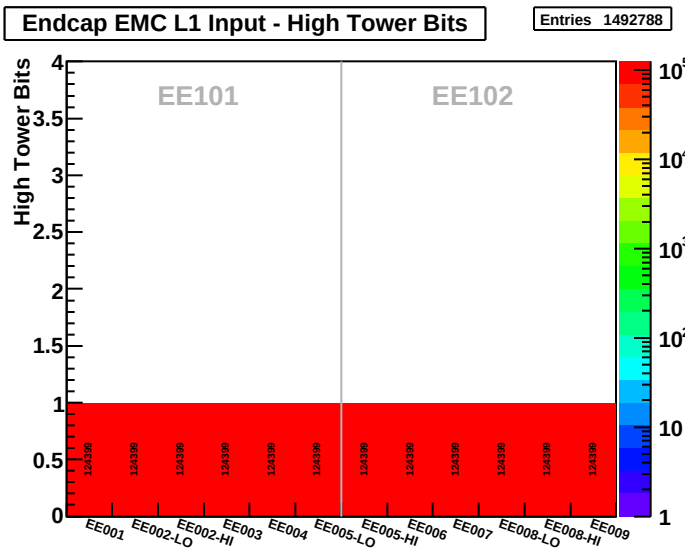

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

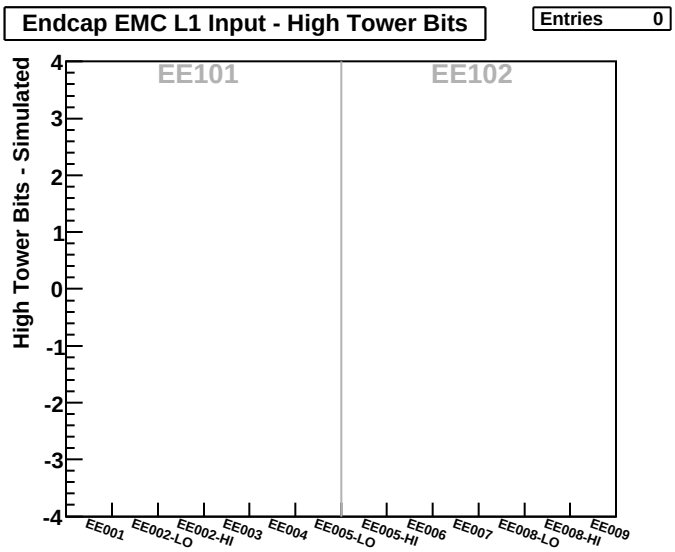

Endcap EMC L1 Input - Low Eta Sum
Entries 1492788
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

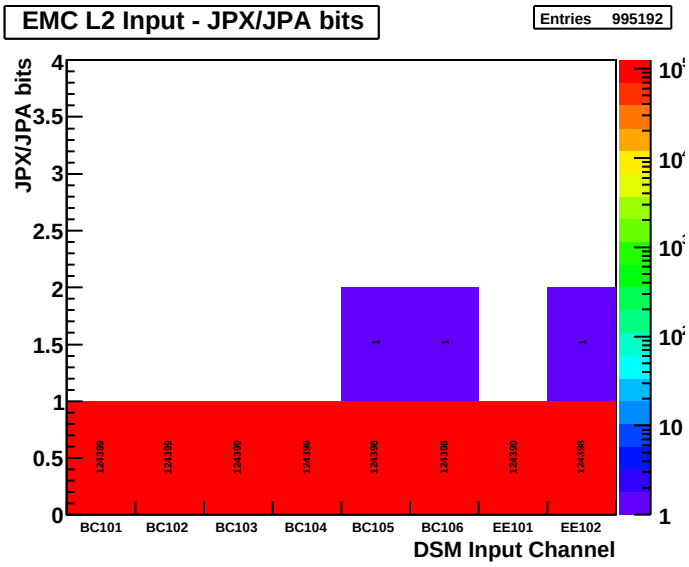

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

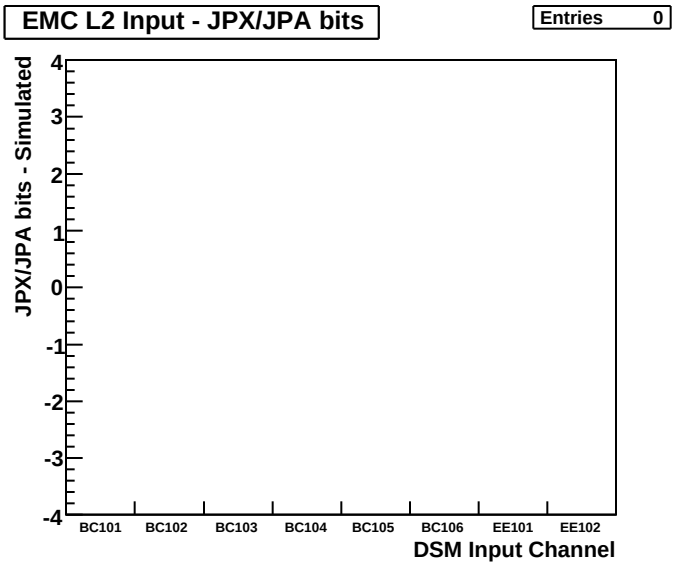

Endcap EMC L1 Input - High Eta Sum
Entries 1492788
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

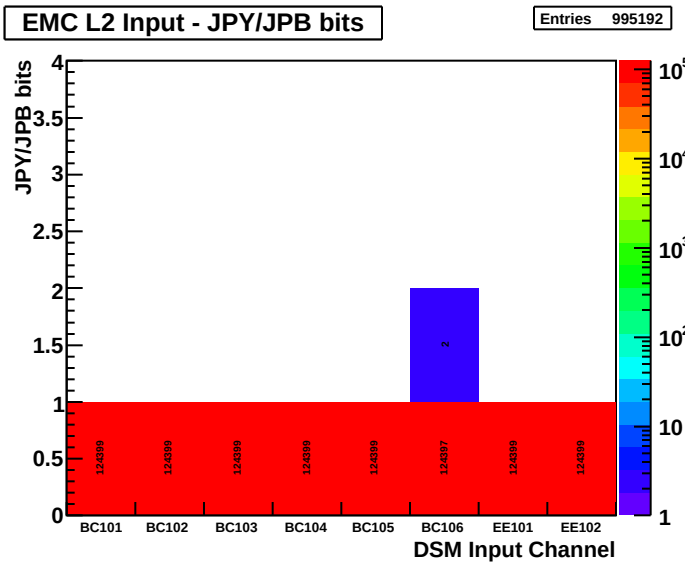

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

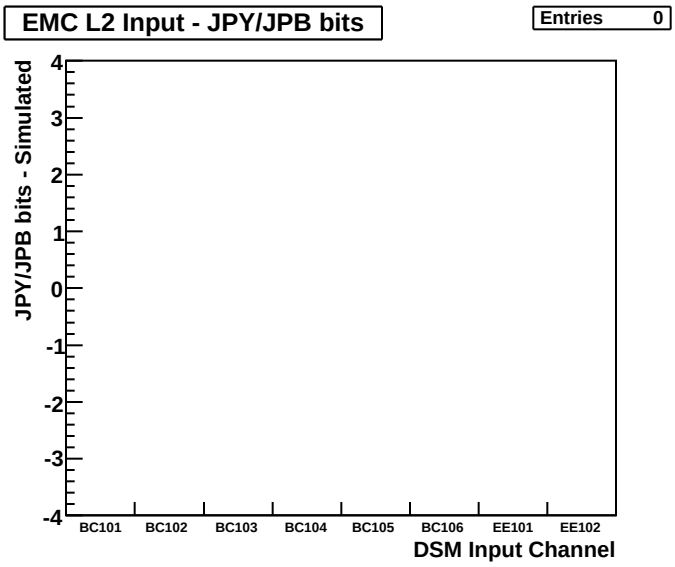

Endcap EMC L1 Input - High Tower Bits
Entries 1492788
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

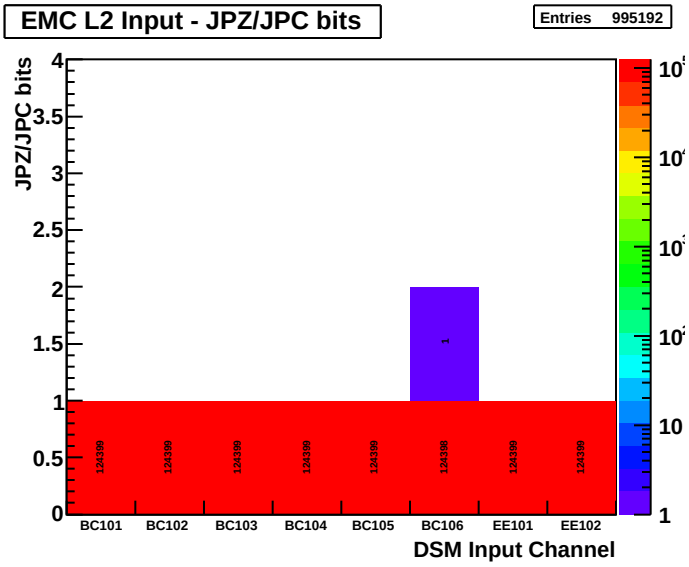

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

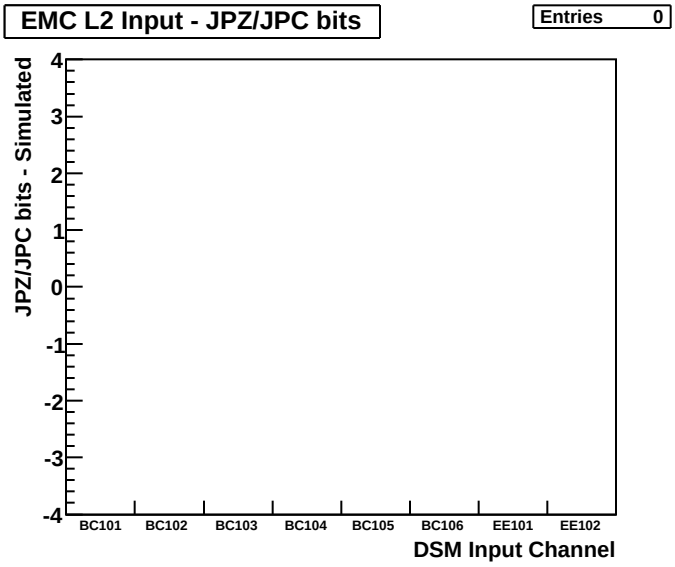

EMC L2 Input - JPX/JPA bits
Entries 995192
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

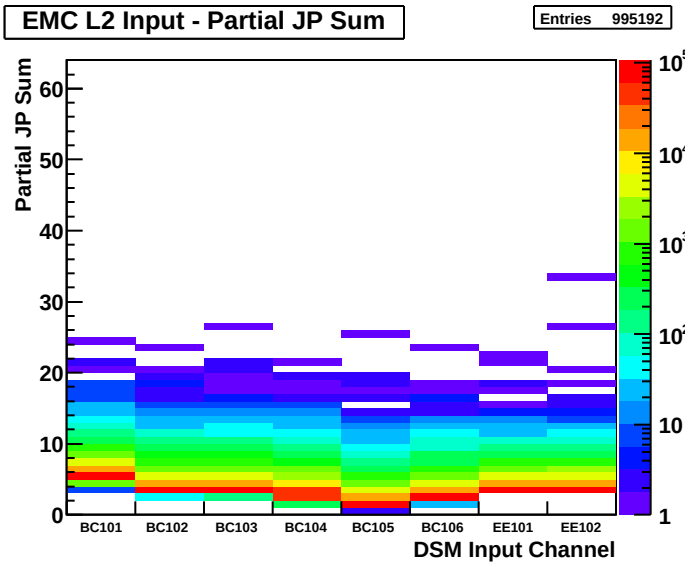

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

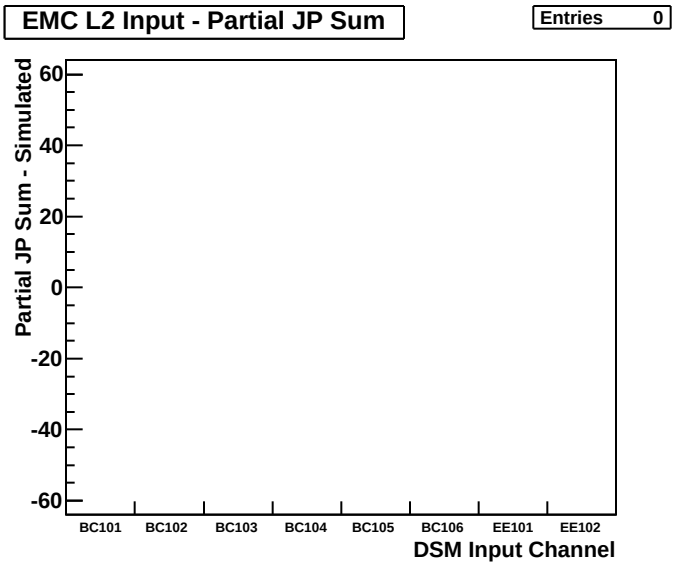

EMC L2 Input - JPY/JPB bits
Entries 995192
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

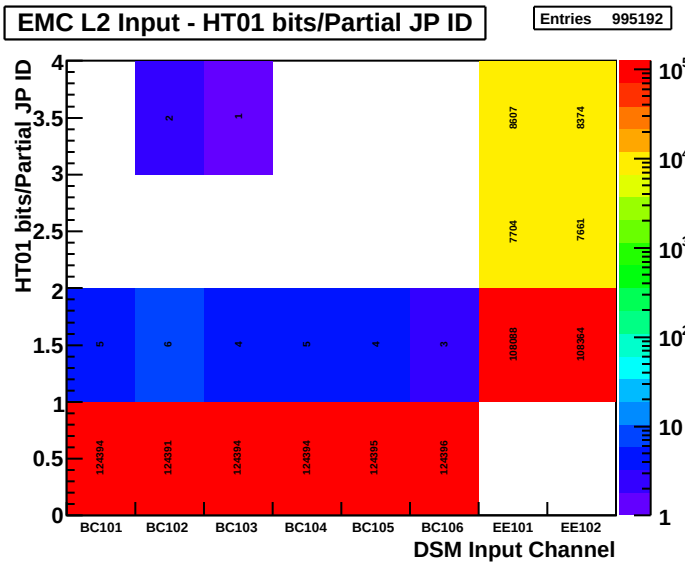

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

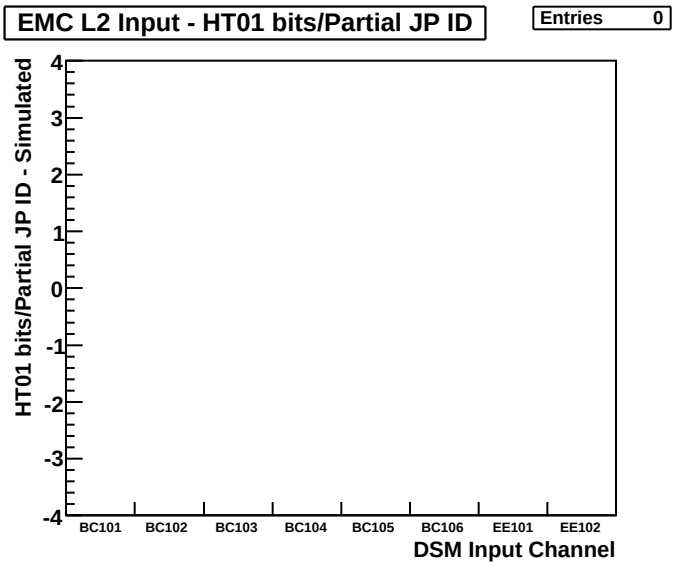

EMC L2 Input - JPZ/JPC bits
Entries 995192
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

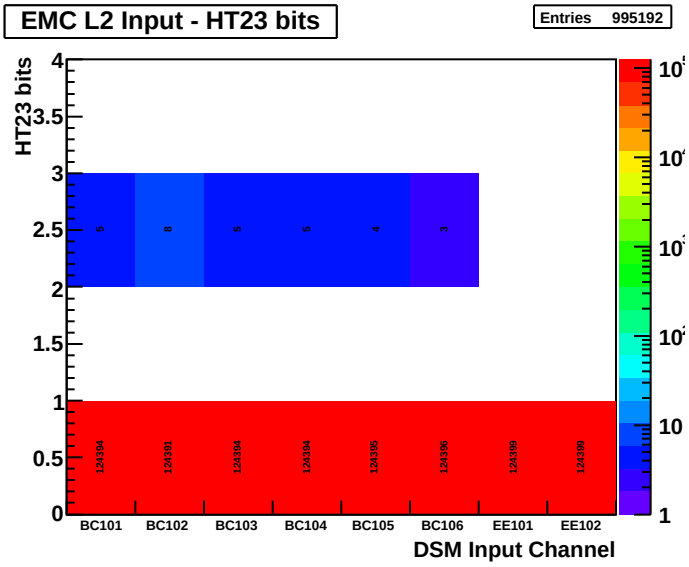

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

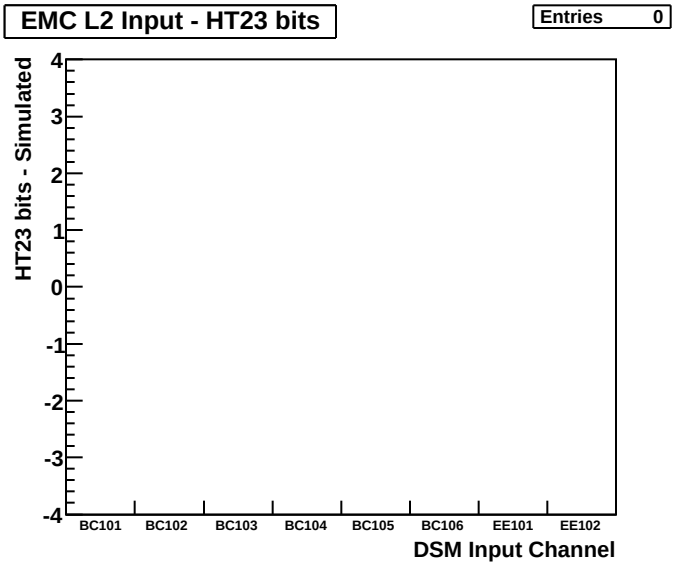

EMC L2 Input - Partial JP Sum
Entries 995192
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

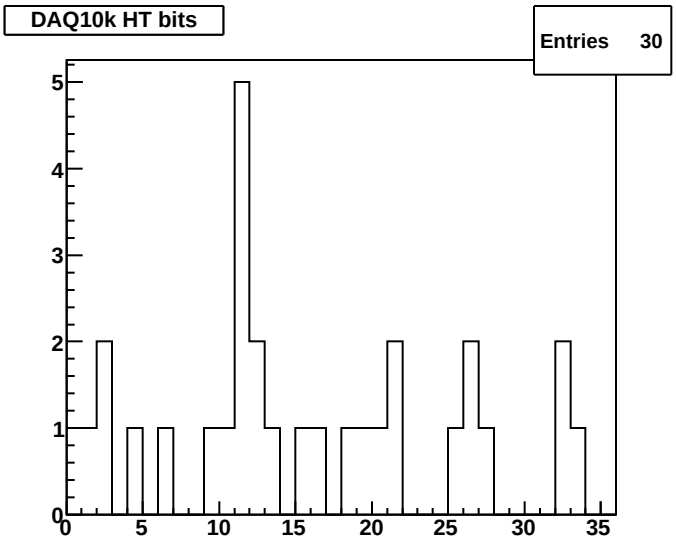

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

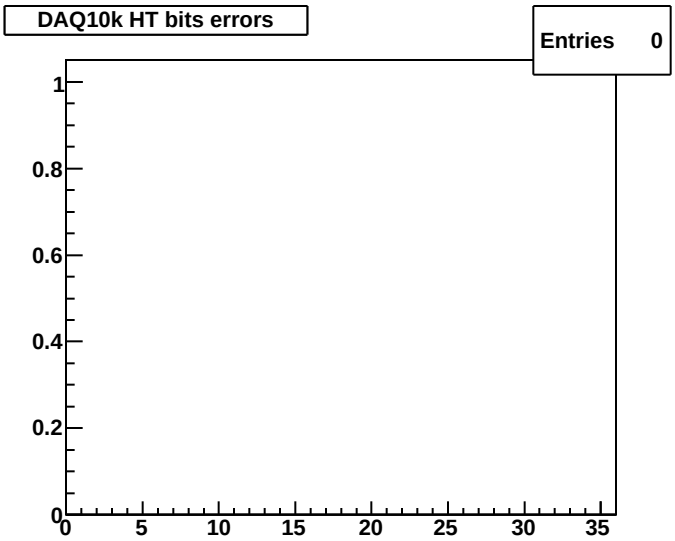

EMC L2 Input - HT01 bits/Partial JP ID
Entries 995192
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

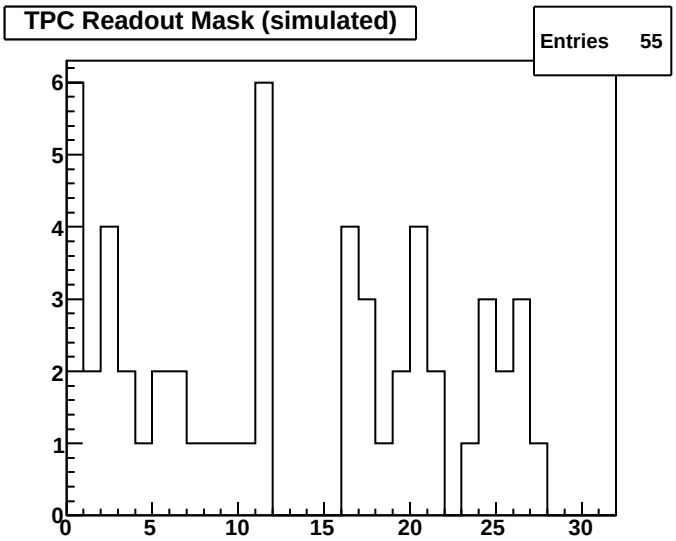

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

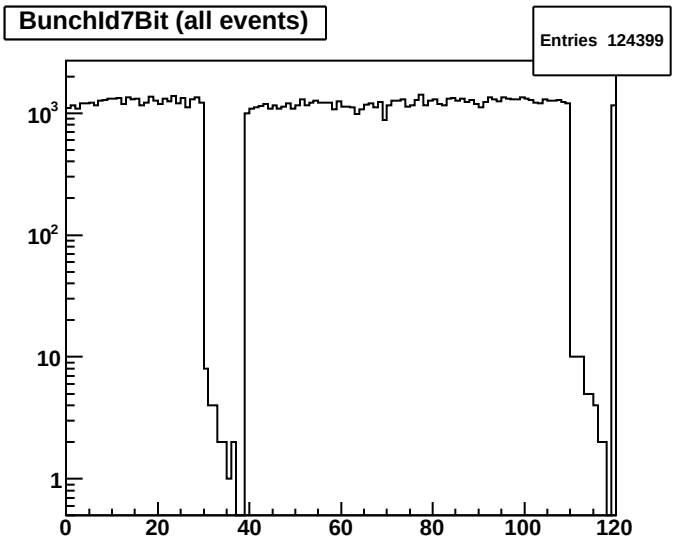

EMC L2 Input - HT23 bits
Entries 995192
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

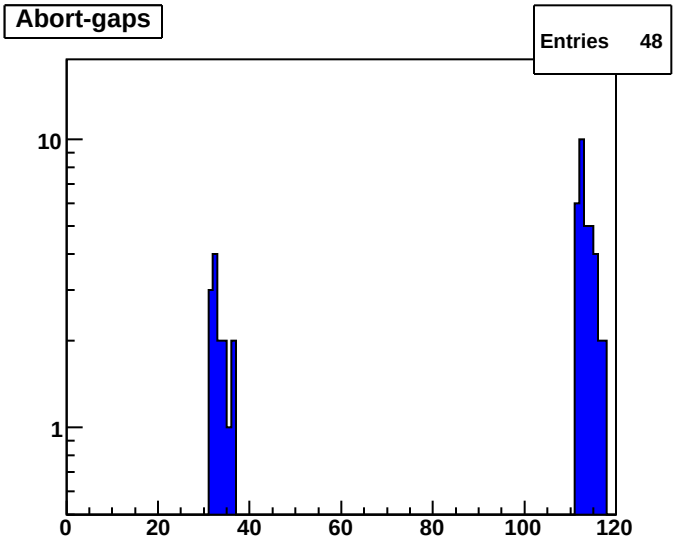

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

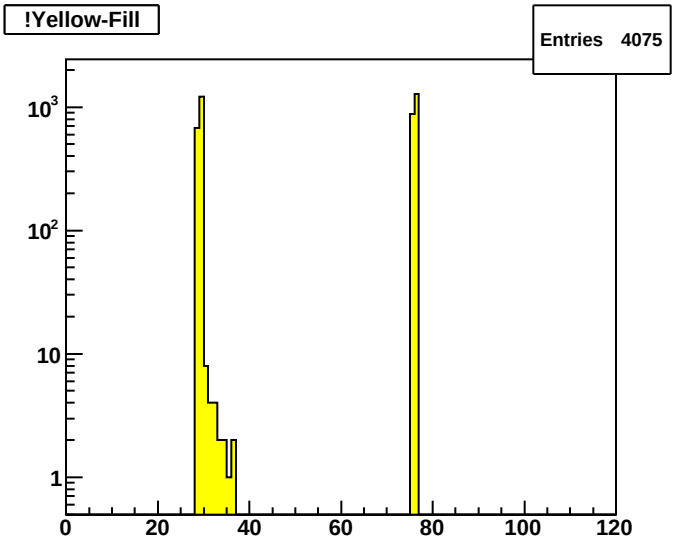

DAQ10k HT bits
Entries 30

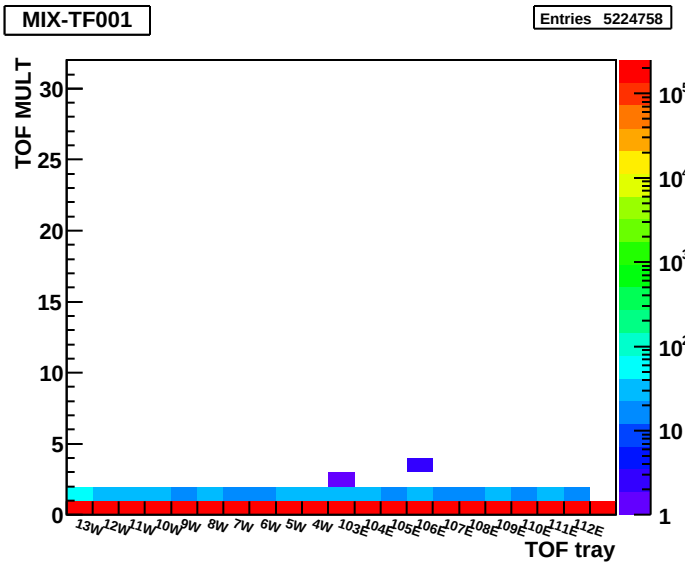

DAQ10k HT bits errors
Entries 0

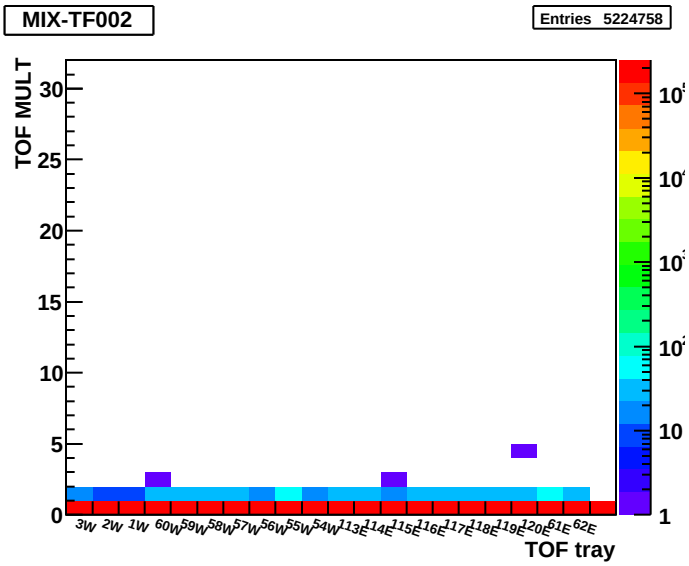

TPC Readout Mask (simulated)
Entries 55

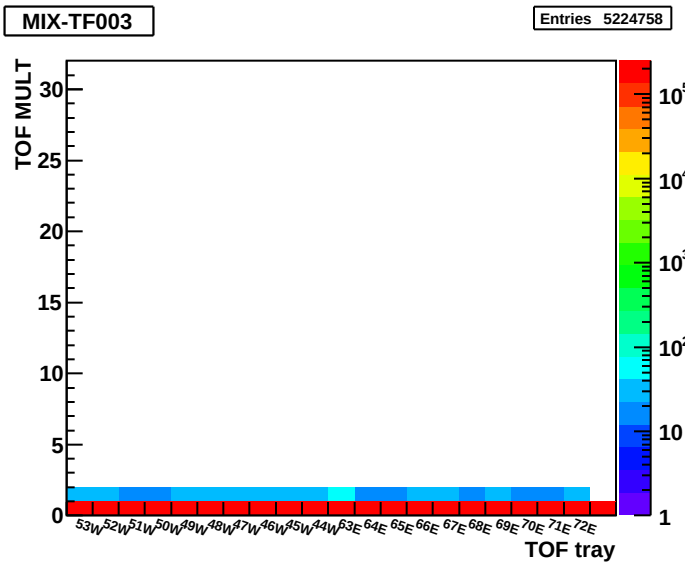

BunchId7Bit (all events)
Entries 124399

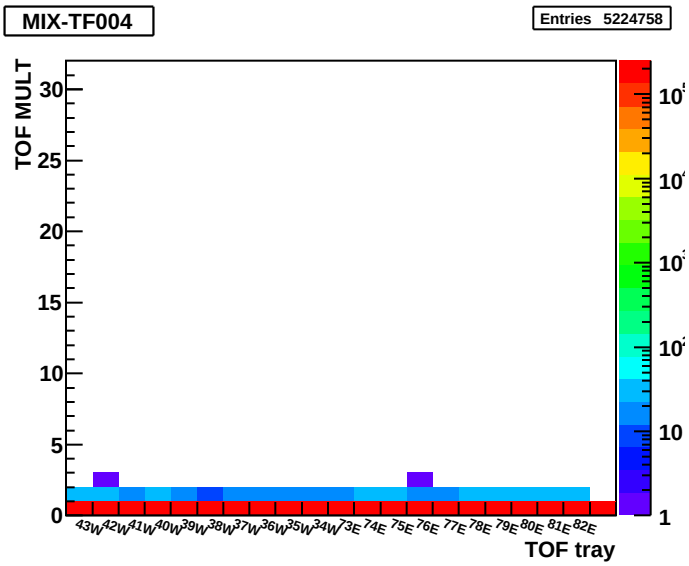

Abort-gaps
Entries 48

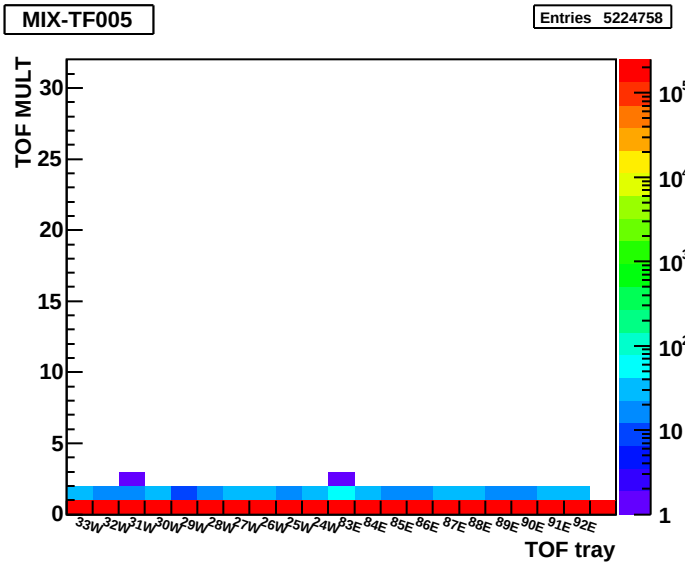

!Yellow-Fill
Entries 4075

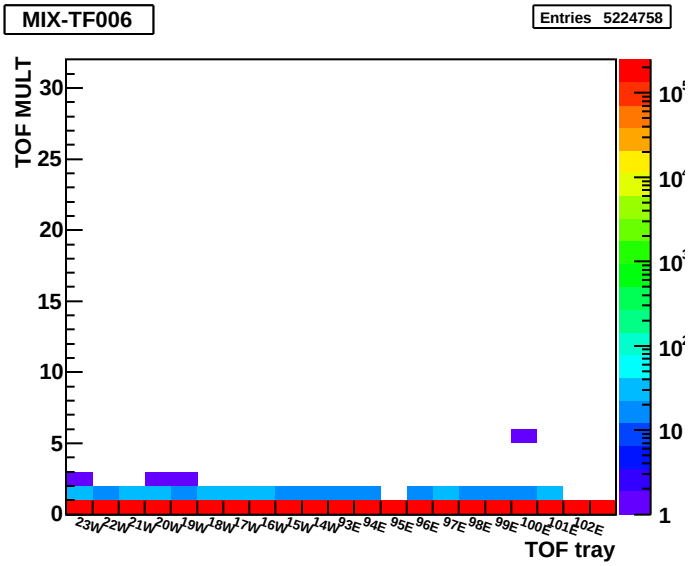

MIX-TF001
Entries 5224758
TOF MULT
TOF tray

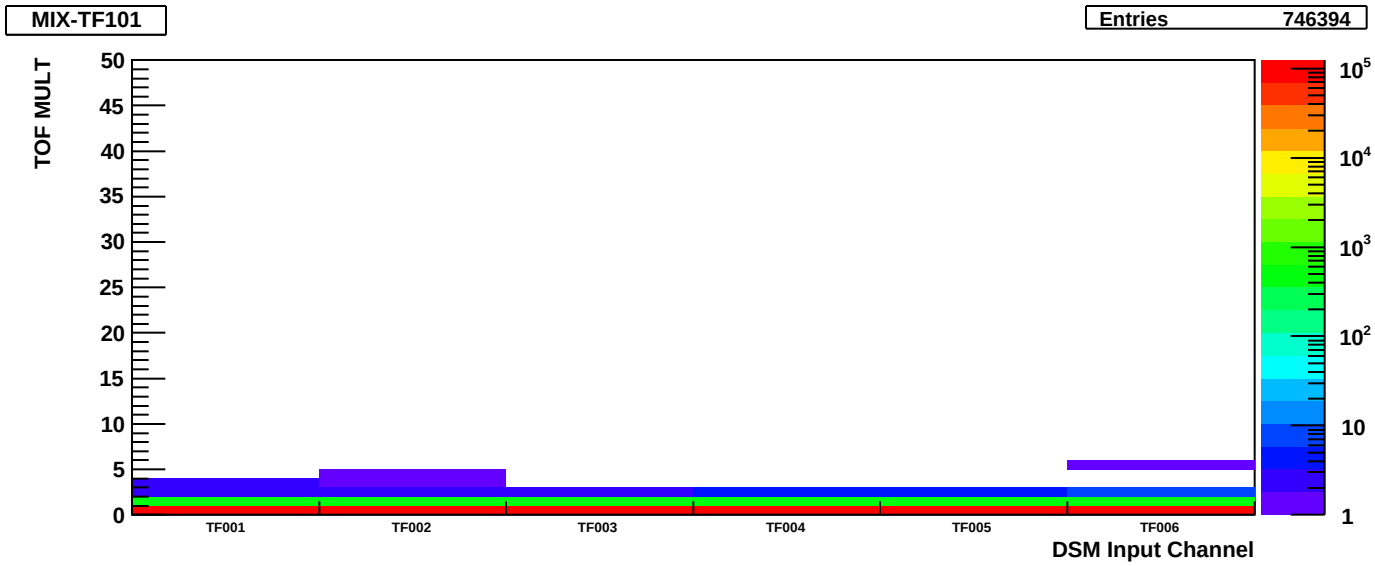

MIX-TF002
Entries 5224758
TOF MULT
TOF tray

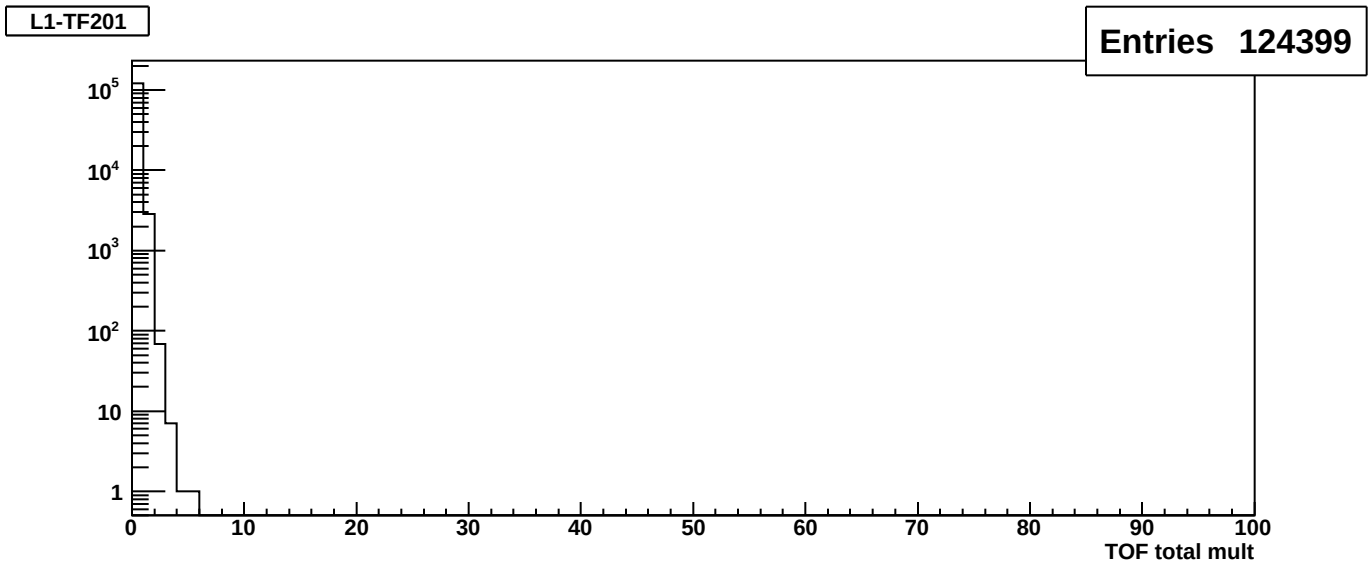

MIX-TF003
Entries 5224758
TOF MULT
TOF tray

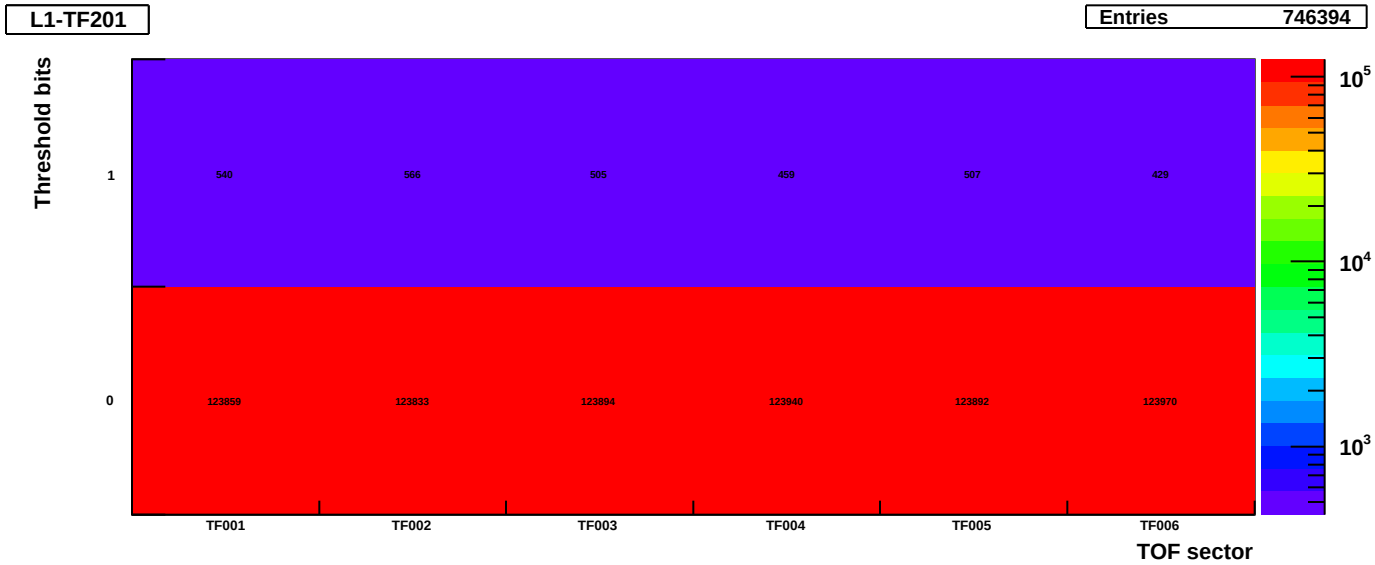

MIX-TF004
Entries 5224758
TOF MULT
TOF tray

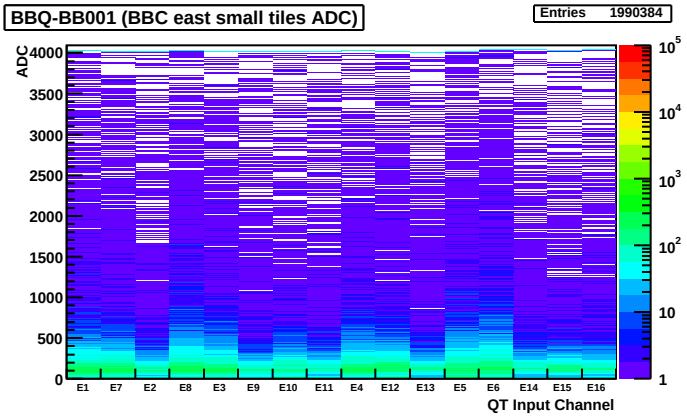

MIX-TF005
Entries 5224758
TOF MULT
TOF tray

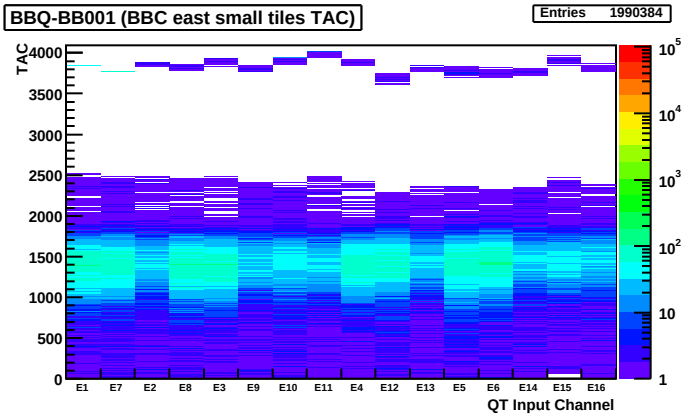

MIX-TF006
Entries 5224758
TOF MULT
TOF tray

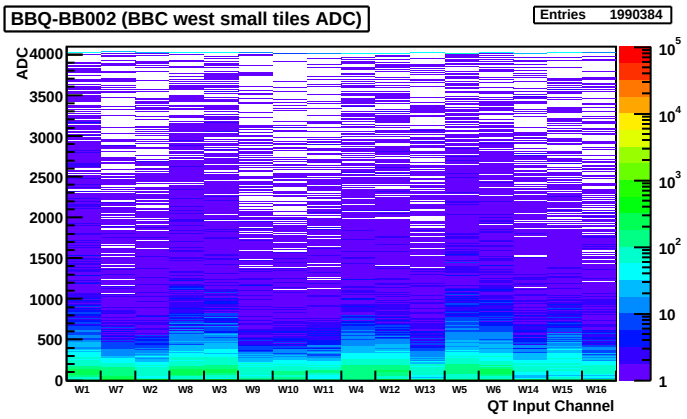

MIX-TF101
Entries 746394
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

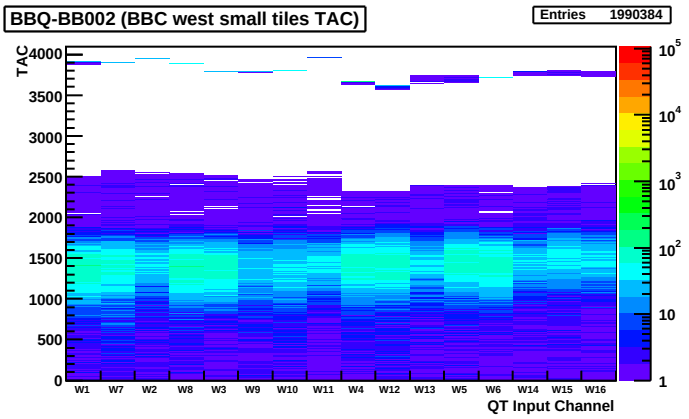

L1-TF201
Entries 124399
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

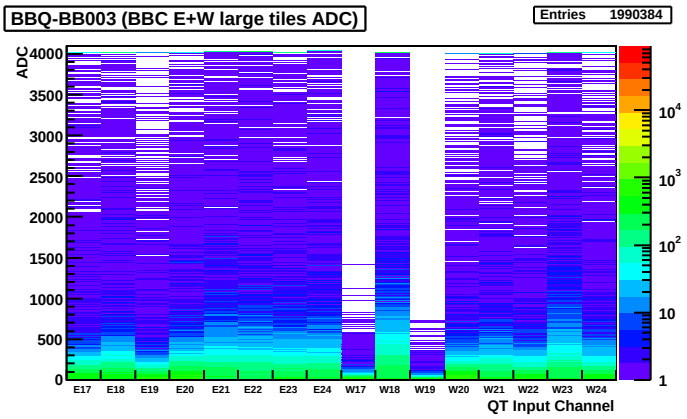

L1-TF201
Entries 746394
Threshold bits
1
0
540
566
505
459
507
429
123859
123833
123894
123940
123892
123970
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4
10^3

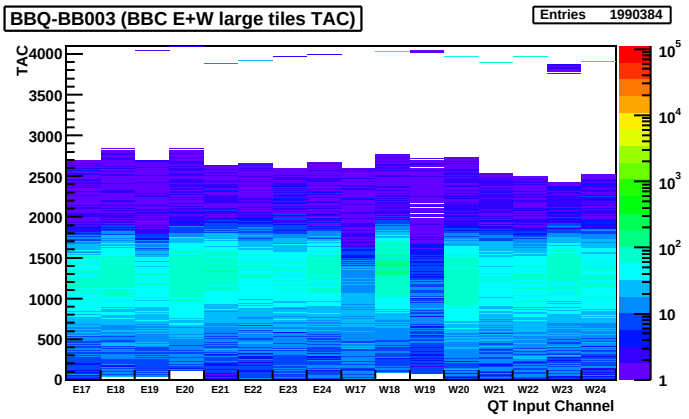

BBQ-BB001 (BBC east small tiles ADC)
Entries 1990384
ADC
QT Input Channel

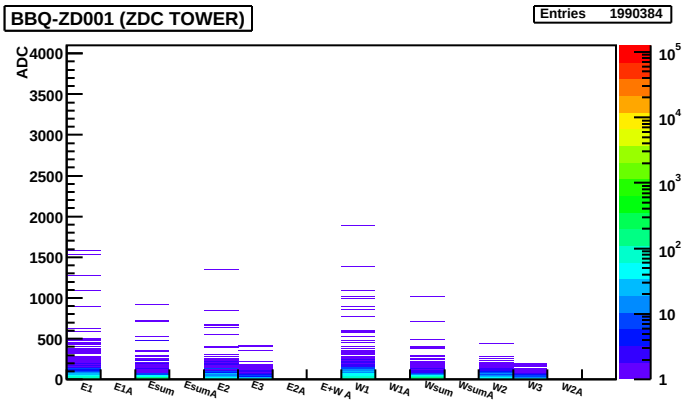

BBQ-BB001 (BBC east small tiles TAC)
Entries 1990384
TAC
QT Input Channel

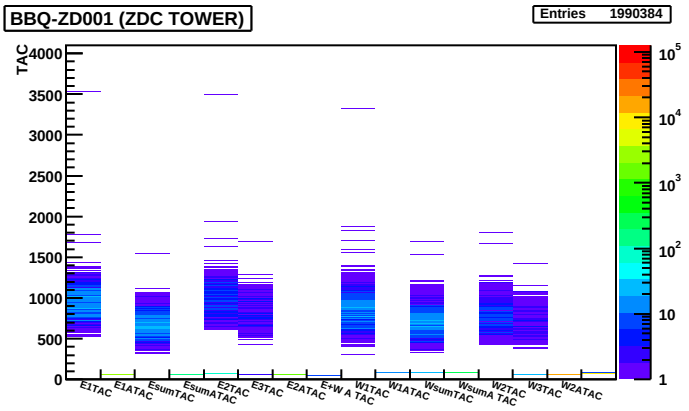

BBQ-BB002 (BBC west small tiles ADC)
Entries 1990384
ADC
QT Input Channel

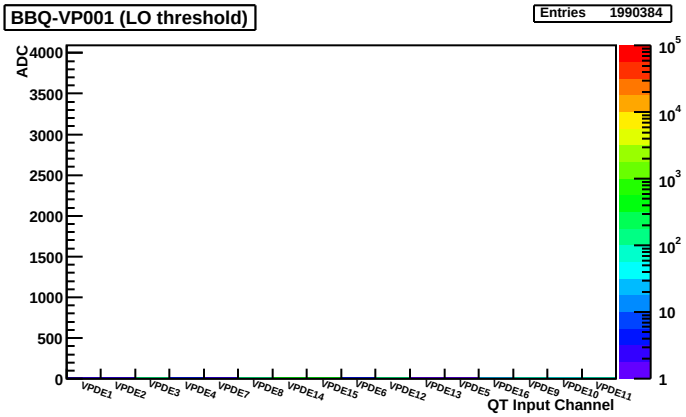

BBQ-BB002 (BBC west small tiles TAC)
Entries 1990384
TAC
QT Input Channel

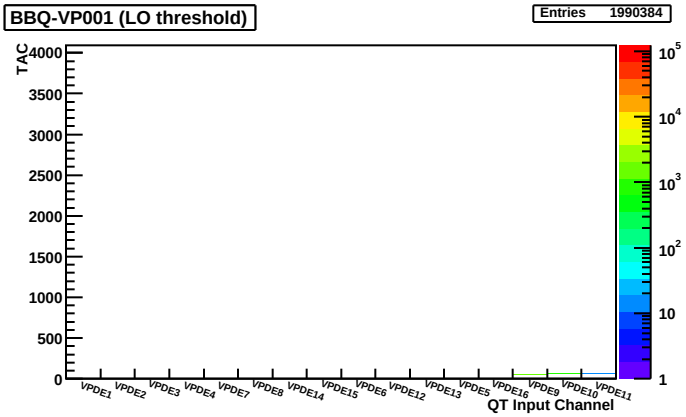

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1990384
ADC
QT Input Channel

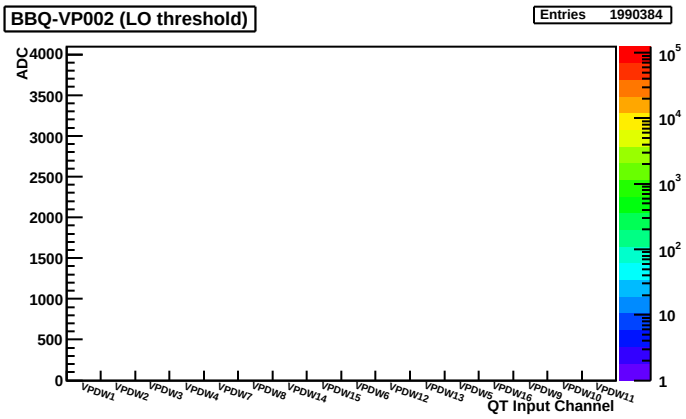

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1990384
TAC
QT Input Channel

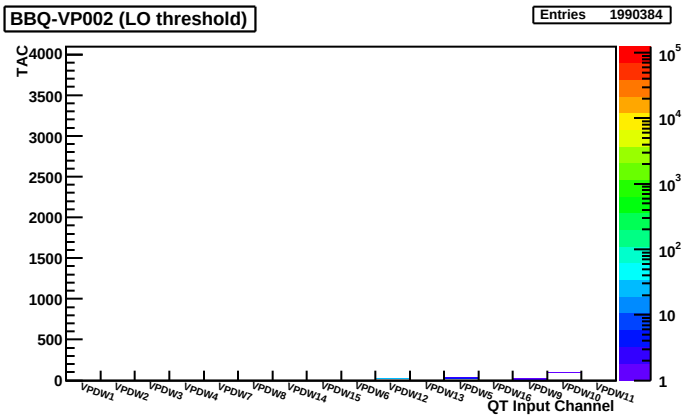

BBQ-ZD001 (ZDC TOWER)
Entries 1990384
ADC

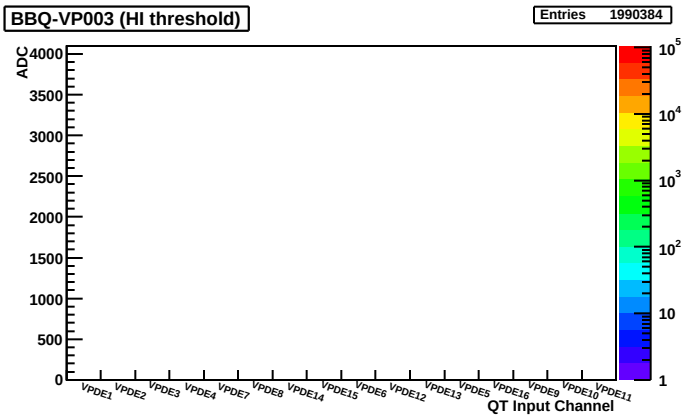

BBQ-ZD001 (ZDC TOWER)
Entries 1990384
TAC

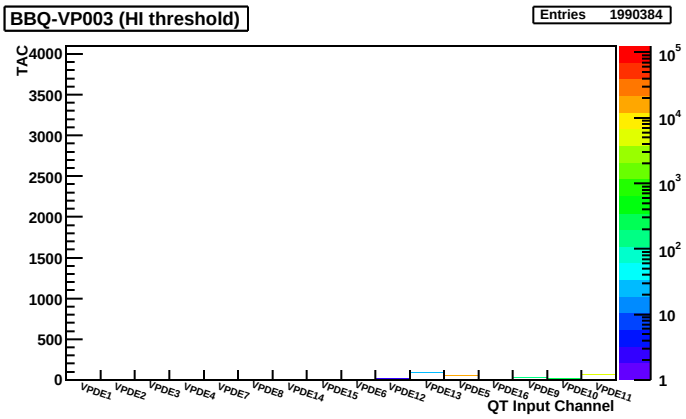

BBQ-VP001 (LO threshold)
Entries 1990384
ADC
QT Input Channel

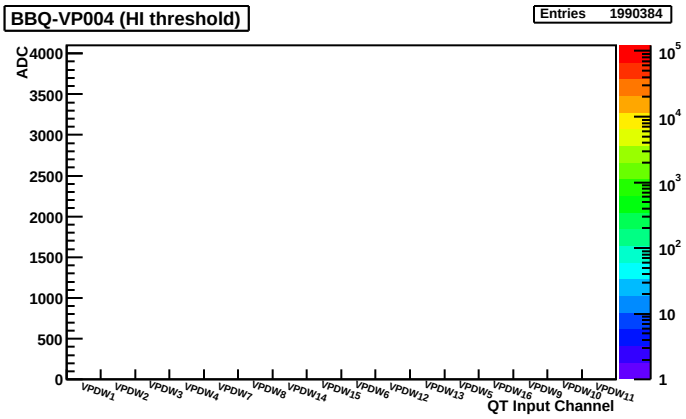

BBQ-VP001 (LO threshold)
Entries 1990384
TAC
QT Input Channel

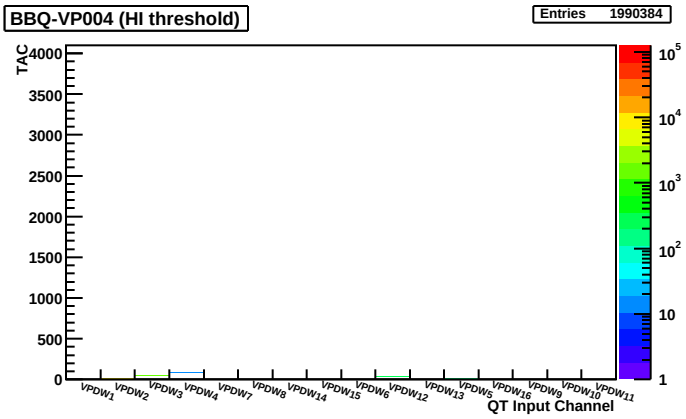

BBQ-VP002 (LO threshold)
Entries 1990384
ADC
QT Input Channel

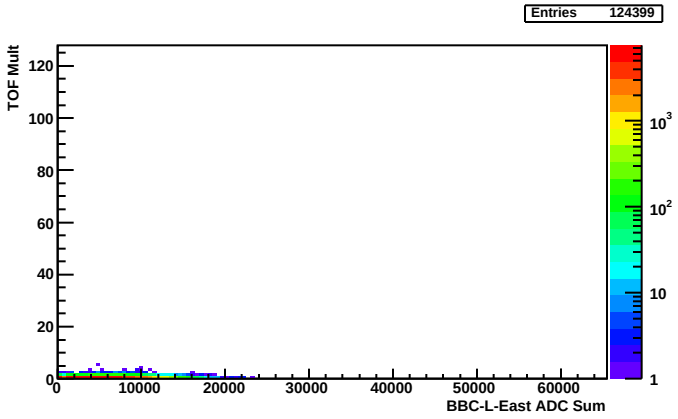

BBQ-VP002 (LO threshold)
Entries 1990384
TAC
QT Input Channel

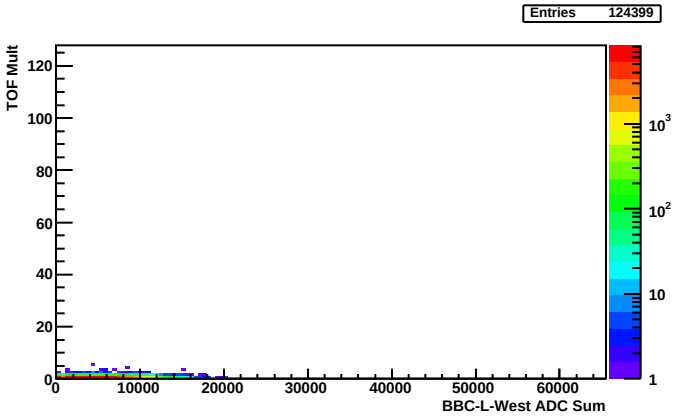

BBQ-VP003 (HI threshold)
Entries 1990384
ADC
QT Input Channel

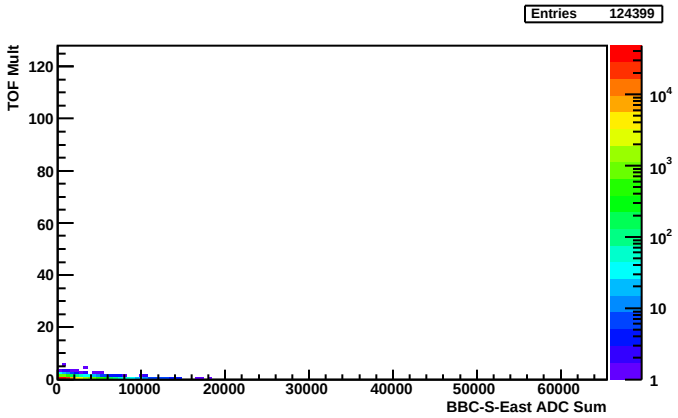

BBQ-VP003 (HI threshold)
Entries 1990384
TAC
QT Input Channel

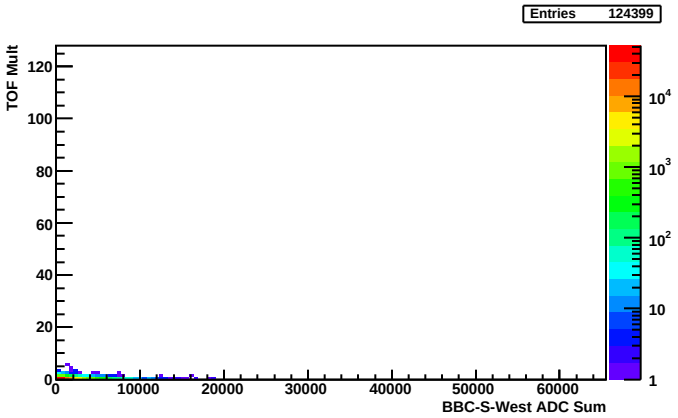

BBQ-VP004 (HI threshold)
Entries 1990384
ADC
QT Input Channel

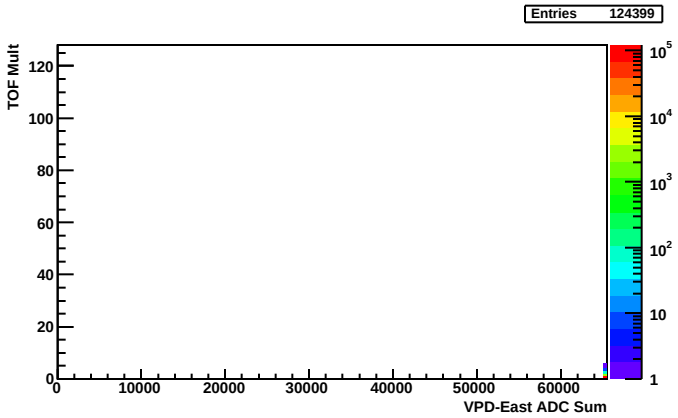

BBQ-VP004 (HI threshold)
Entries 1990384
TAC
QT Input Channel

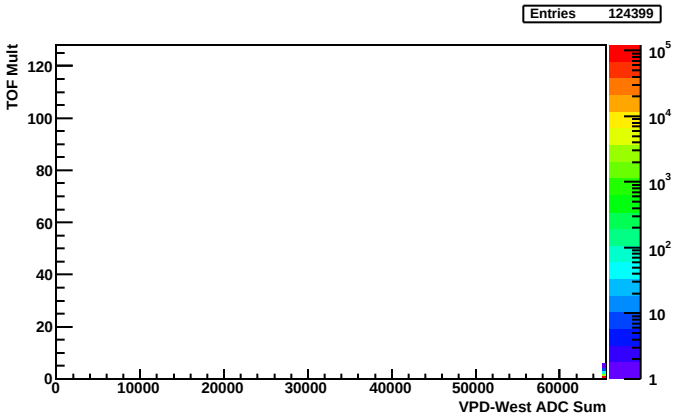

Entries 124399
TOF Mult
BBC-L-East ADC Sum

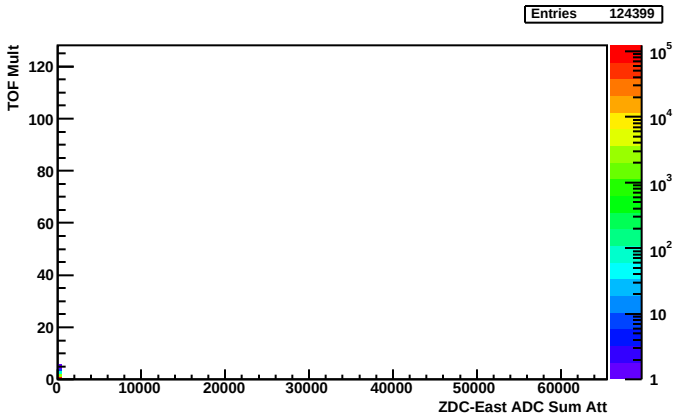

Entries 124399
TOF Mult
BBC-L-West ADC Sum

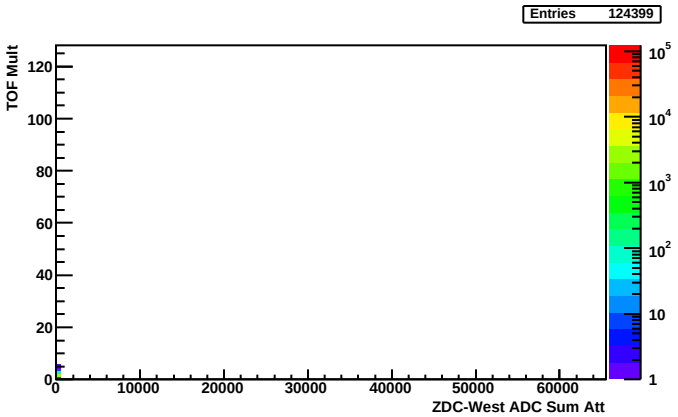

Entries 124399
TOF Mult
BBC-S-East ADC Sum

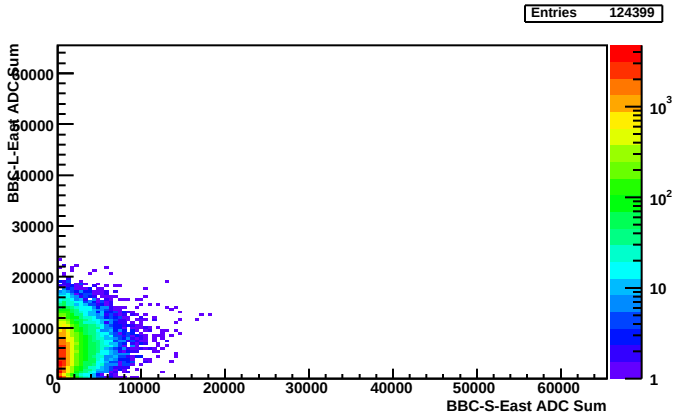

Entries 124399
TOF Mult
BBC-S-West ADC Sum

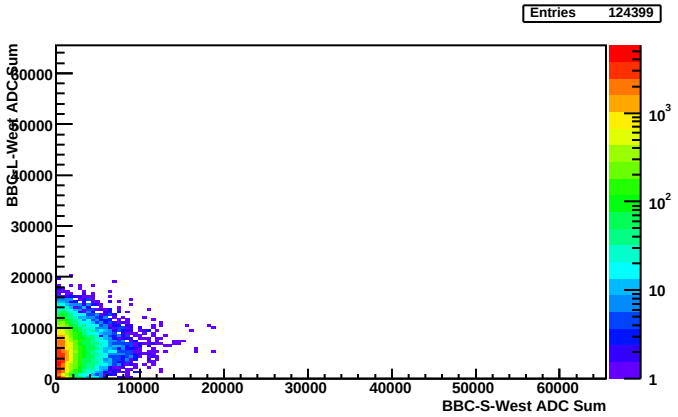

Entries 124399
TOF Mult
VPD-East ADC Sum

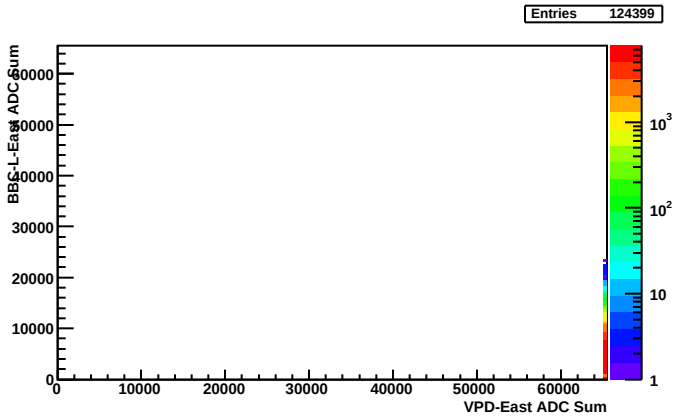

Entries 124399
TOF Mult
VPD-West ADC Sum

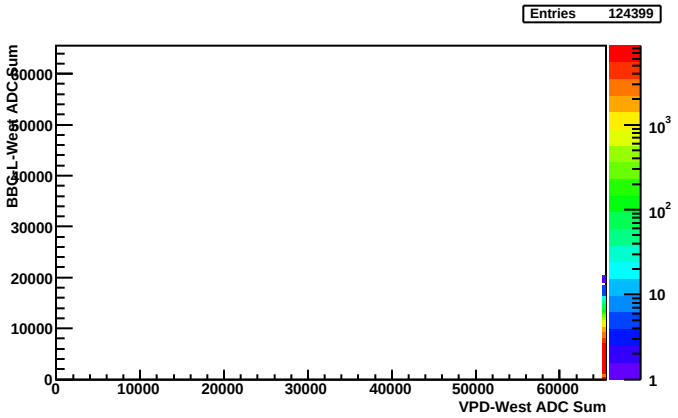

Entries 124399
TOF Mult
ZDC-East ADC Sum Att

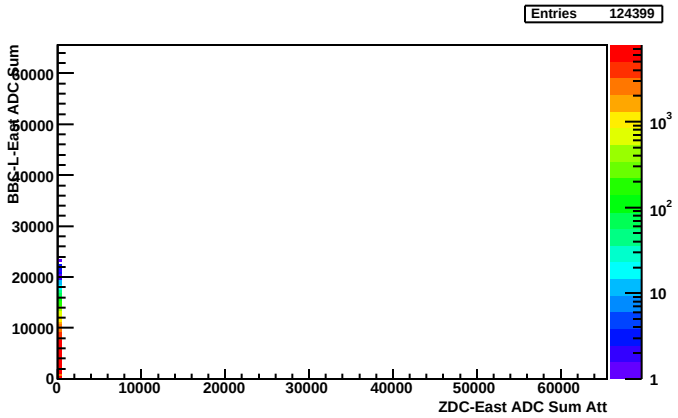

Entries 124399
TOF Mult
ZDC-West ADC Sum Att

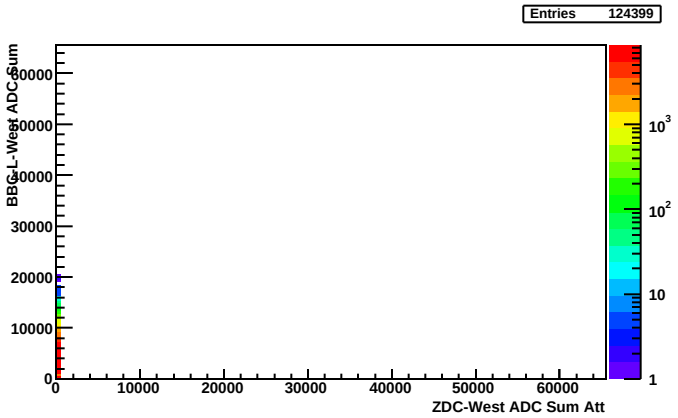

Entries 124399
BBC-L-East ADC Sum
BBC-S-East ADC Sum

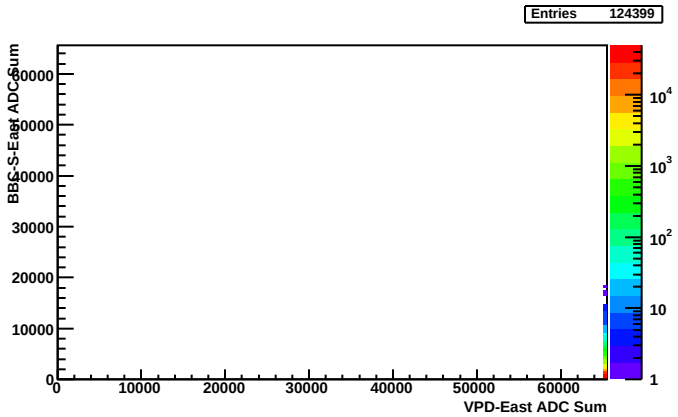

Entries 124399
BBC-L-West ADC Sum
BBC-S-West ADC Sum

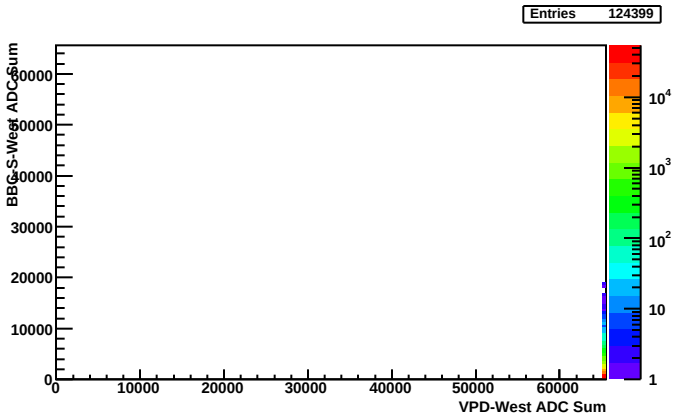

Entries 124399
BBC-L-East ADC Sum
VPD-East ADC Sum

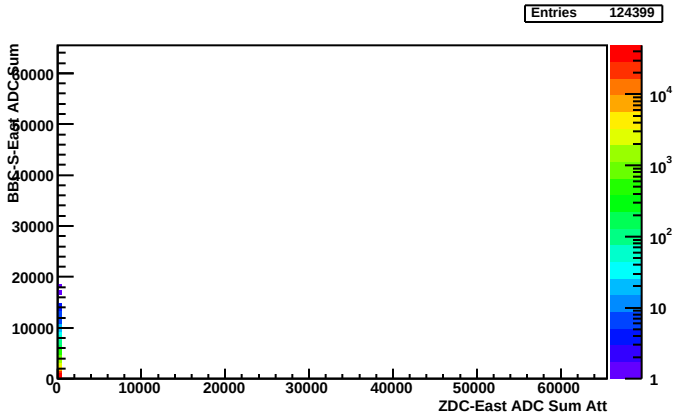

Entries 124399
BBC-L-West ADC Sum
VPD-West ADC Sum

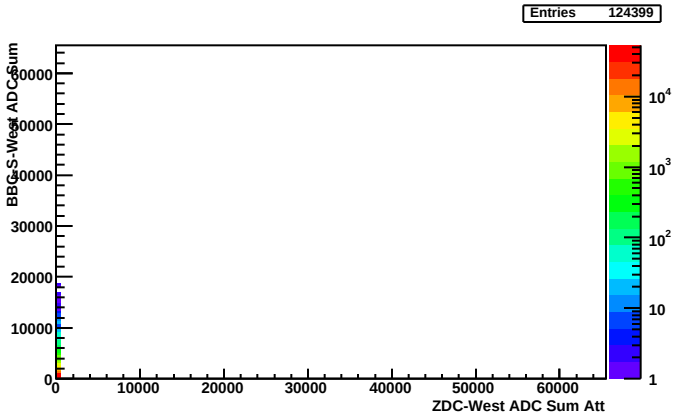

Entries 124399
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

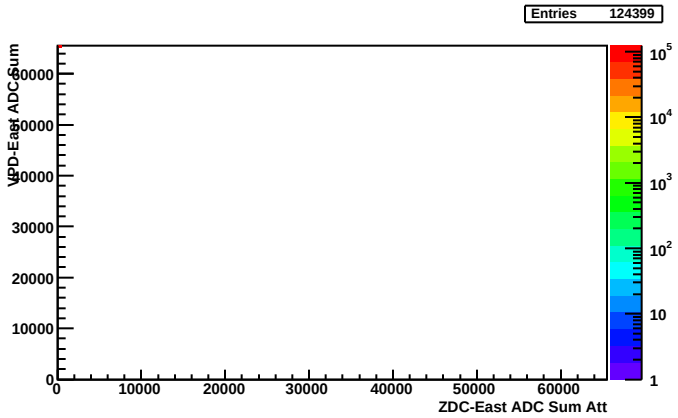

Entries 124399
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

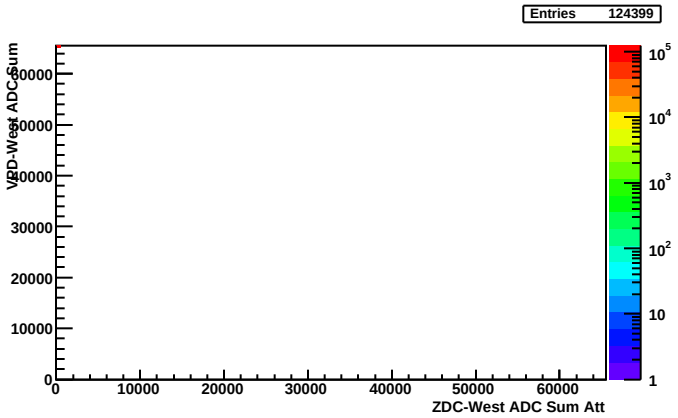

Entries 124399
BBC-S-East ADC Sum
VPD-East ADC Sum

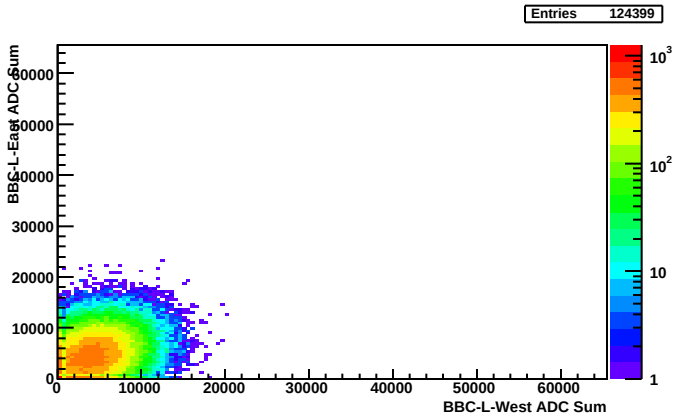

Entries 124399
BBC-S-West ADC Sum
VPD-West ADC Sum

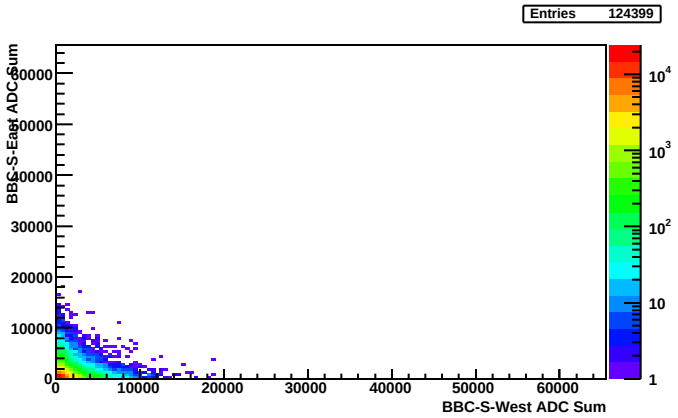

Entries 124399
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

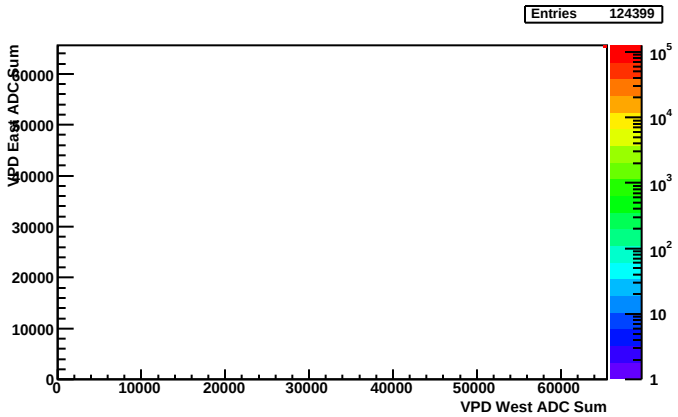

Entries 124399
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

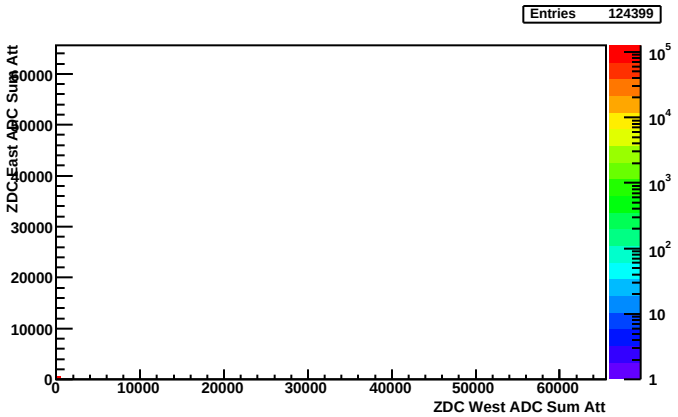

Entries 124399
VPD-East ADC Sum
ZDC-East ADC Sum Att

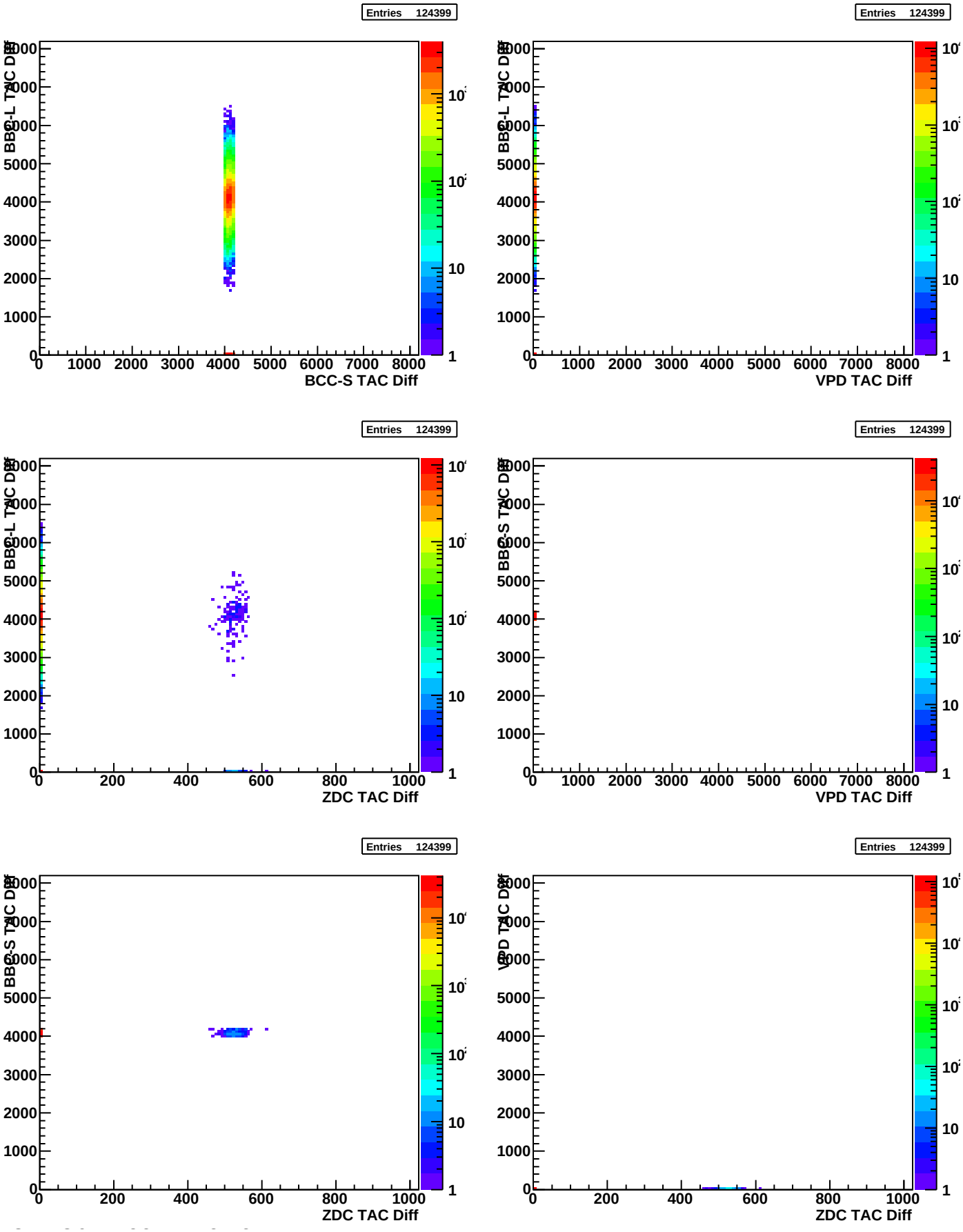

Entries 124399
VPD-West ADC Sum
ZDC-West ADC Sum Att

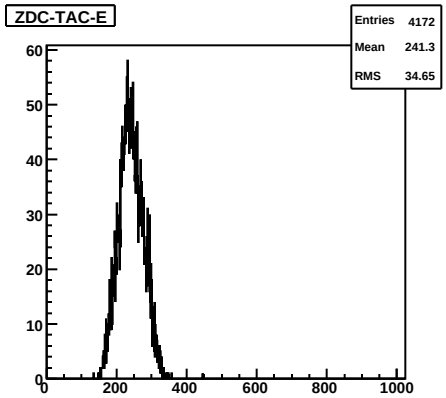

Entries 124399
BBC-L-East ADC Sum
BBC-L-West ADC Sum

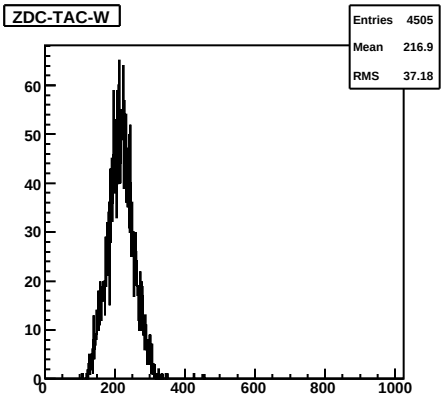

Entries 124399
BBC-S-East ADC Sum
BBC-S-West ADC Sum

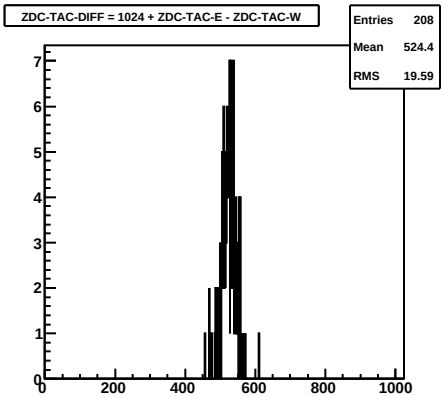

Entries 124399
VPD East ADC Sum
VPD West ADC Sum

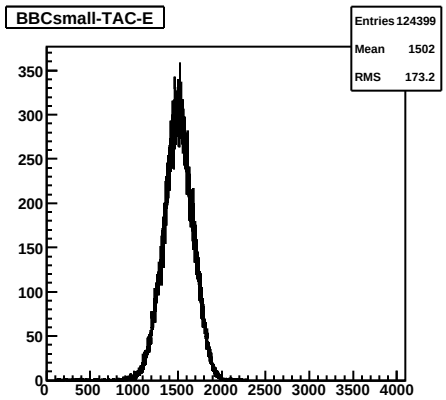

Entries 124399
ZDC East ADC Sum Att
ZDC West ADC Sum Att

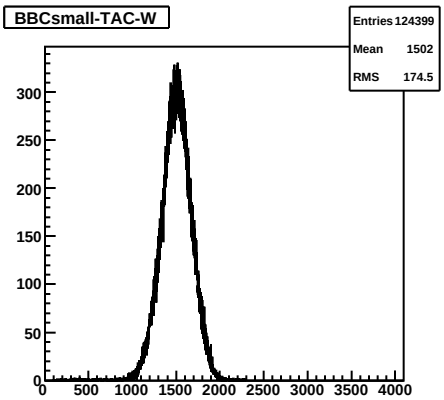

Entries 124399
BBC-L TAC Diff
BCC-S TAC Diff
Entries 124399
BBC-L TAC Diff
VPD TAC Diff
Entries 124399
BBC-L TAC Diff
ZDC TAC Diff
Entries 124399
BBC-S TAC Diff
VPD TAC Diff
Entries 124399
BBC-S TAC Diff
ZDC TAC Diff
Entries 124399
VPD TAC Diff
ZDC TAC Diff

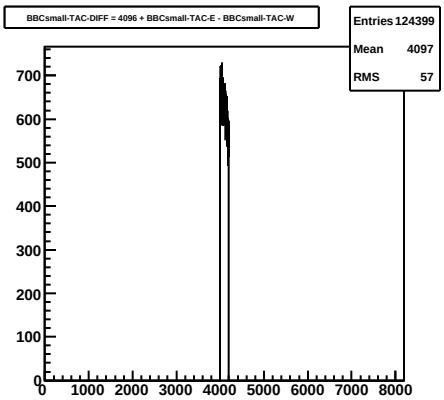

ZDC-TAC-E
Entries 4172
Mean 241.3
RMS 34.65

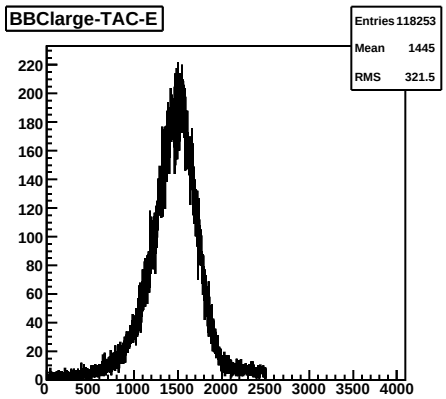

ZDC-TAC-W
Entries 4505
Mean 216.9
RMS 37.18

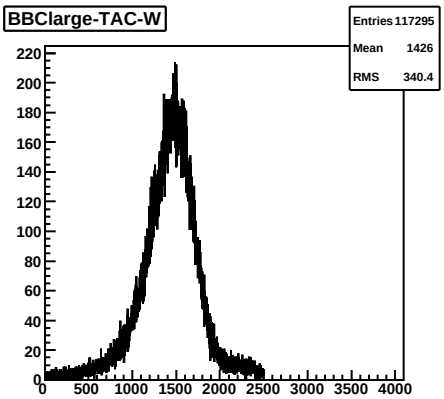

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 208
Mean 524.4
RMS 19.59

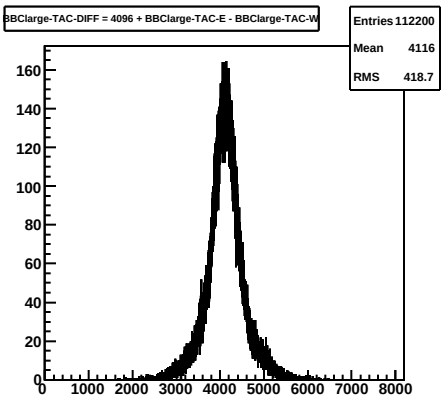

BBCsmall-TAC-E
Entries 124399
Mean 1502
RMS 173.2

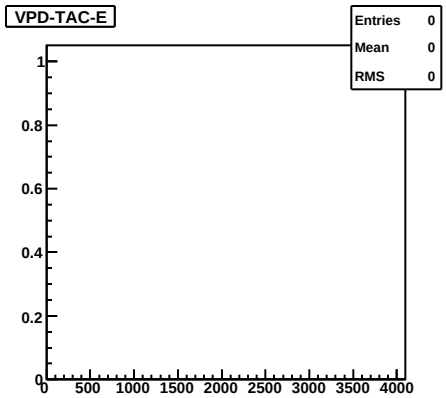

BBCsmall-TAC-W
Entries 124399
Mean 1502
RMS 174.5

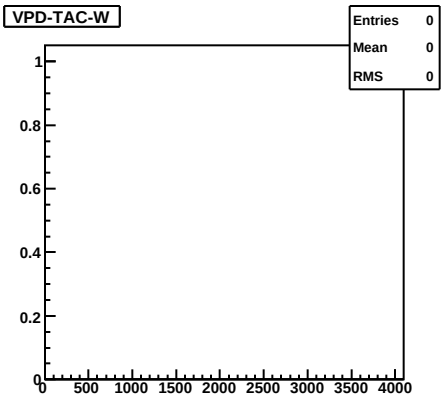

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 124399
Mean 4097
RMS 57

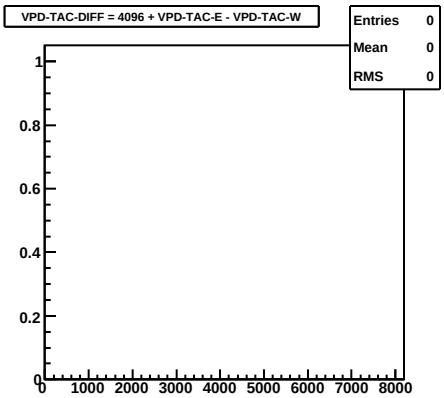

BBClarge-TAC-E
Entries 118253
Mean 1445
RMS 321.5

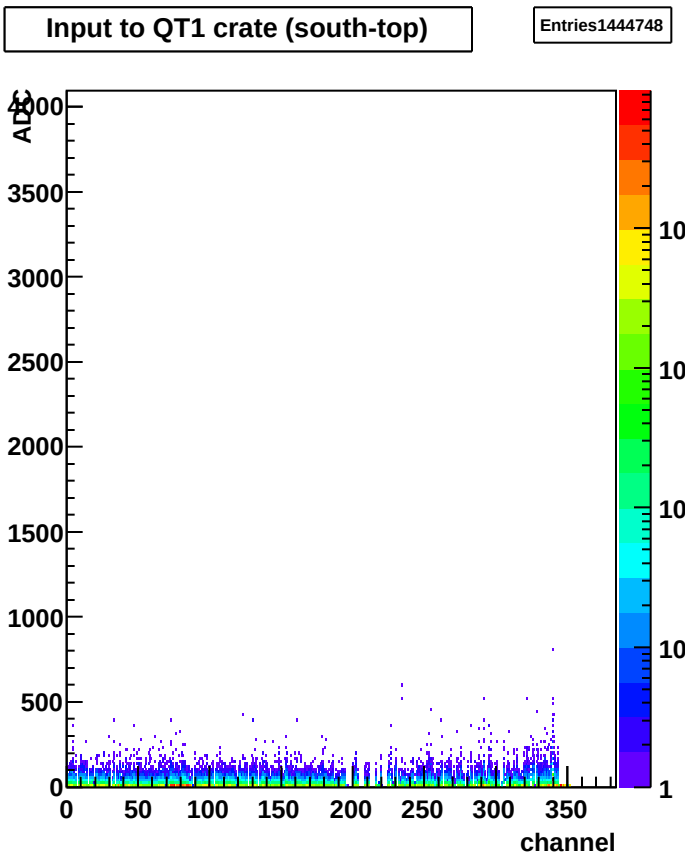

BBClarge-TAC-W
Entries 117295
Mean 1426
RMS 340.4

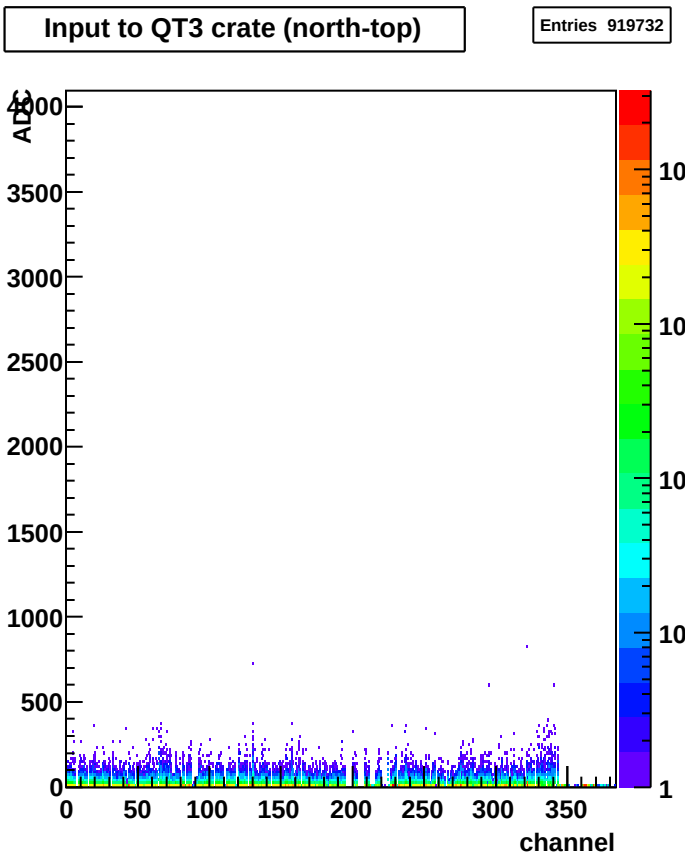

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 112200
Mean 4116
RMS 418.7

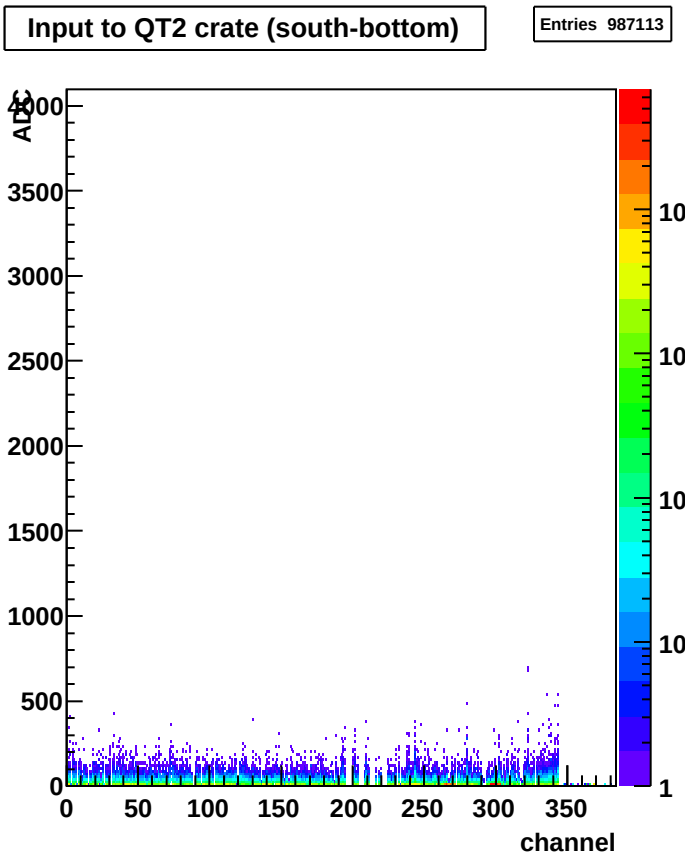

VPD-TAC-E
Entries 0
Mean 0
RMS 0

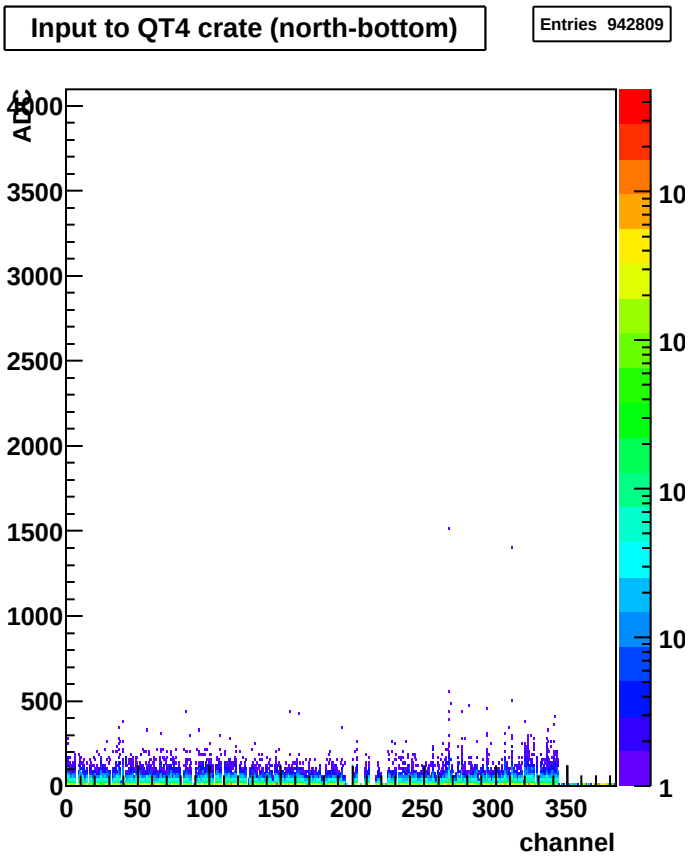

VPD-TAC-W
Entries 0
Mean 0
RMS 0

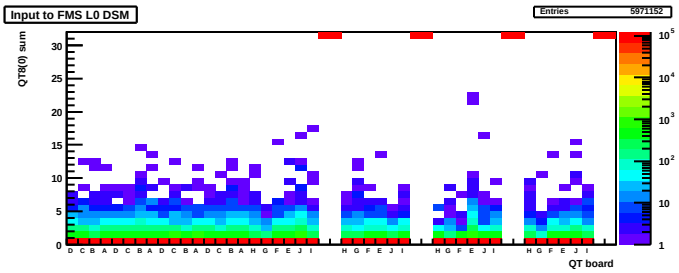

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 0
Mean 0
RMS 0

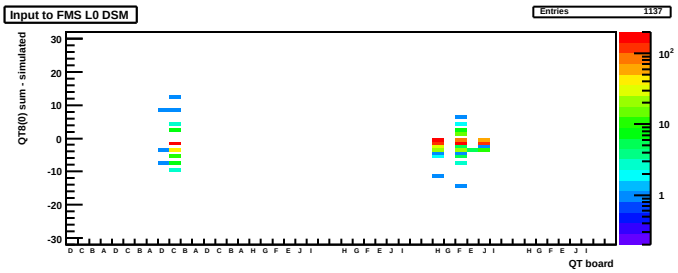

Input to QT1 crate (south-top)
Entries1444748
ADC
channel

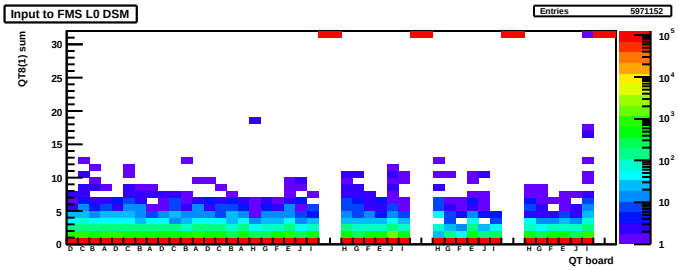

Input to QT3 crate (north-top)
Entries 919732
ADC
channel

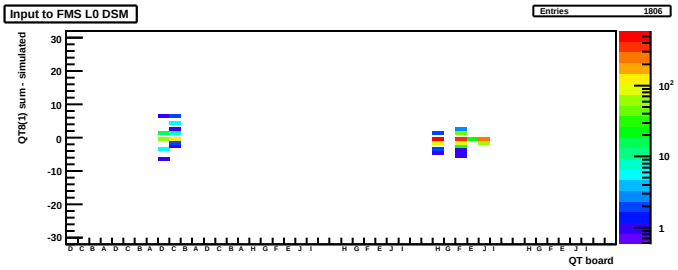

Input to QT2 crate (south-bottom)
Entries 987113
ADC
channel

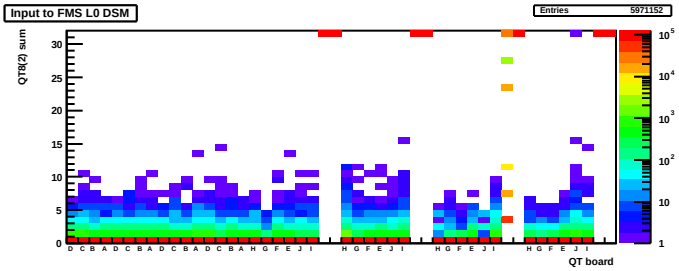

Input to QT4 crate (north-bottom)
Entries 942809
ADC
channel

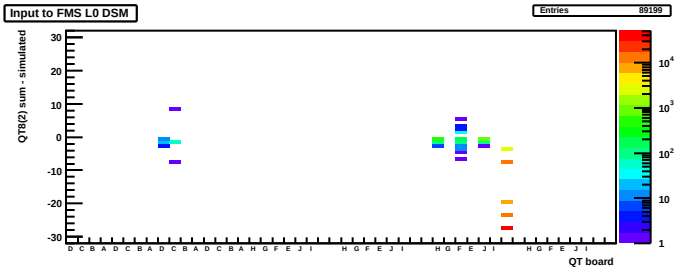

Input to FMS L0 DSM
Entries 5971152
QT8(0) sum
QT board

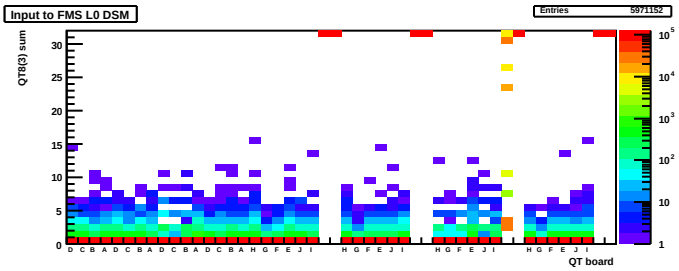

Input to FMS L0 DSM
Entries 1137
QT8(0) sum - simulated
QT board

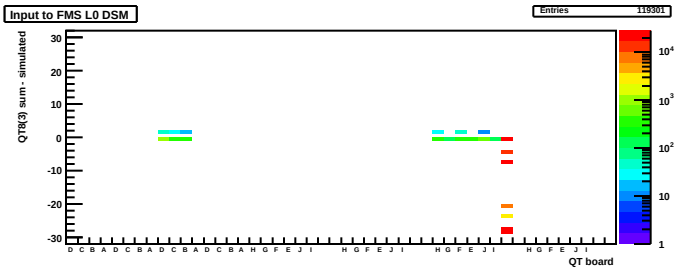

Input to FMS L0 DSM
Entries 5971152
QT8(1) sum
QT board

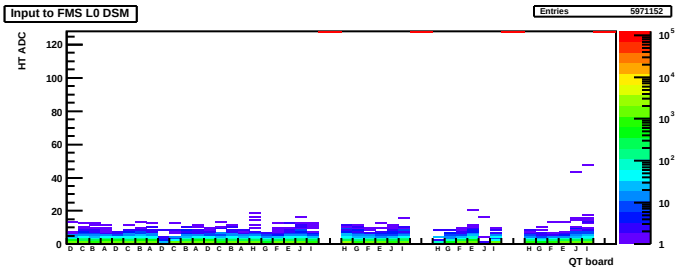

Input to FMS L0 DSM
Entries 1806
QT8(1) sum - simulated
QT board

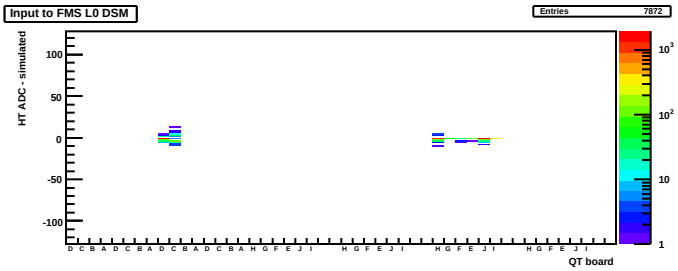

Input to FMS L0 DSM
Entries 5971152
QT8(2) sum
QT board

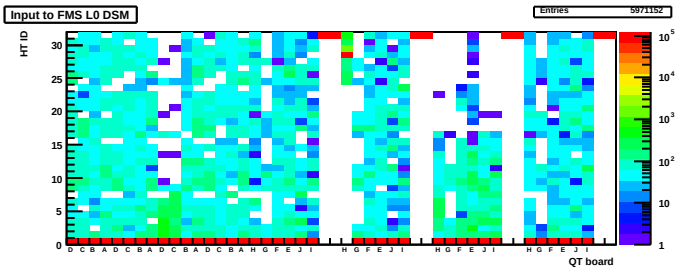

Input to FMS L0 DSM
Entries 89199
QT8(2) sum - simulated
QT board

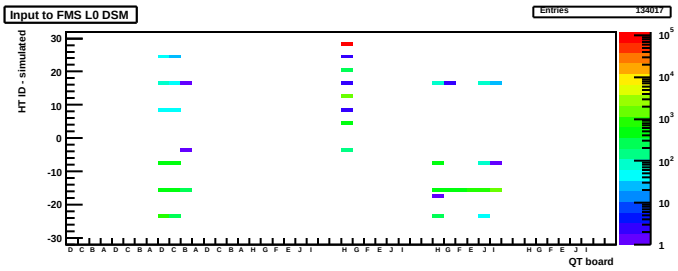

Input to FMS L0 DSM
Entries 5971152
QT8(3) sum
QT board

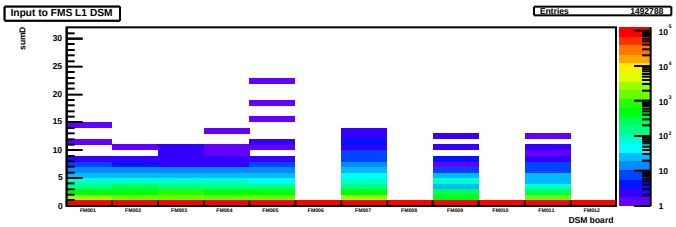

Input to FMS L0 DSM
Entries 119301
QT8(3) sum - simulated
QT board

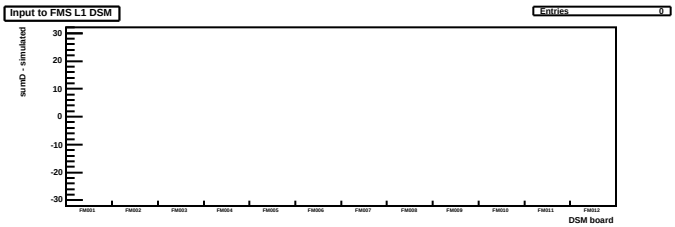

Input to FMS L0 DSM
Entries 5971152
HT ADC
QT board

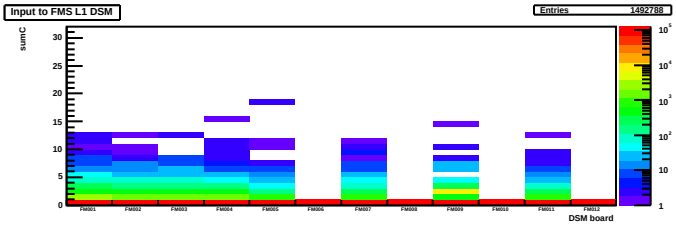

Input to FMS L0 DSM
Entries 7872
HT ADC - simulated
QT board

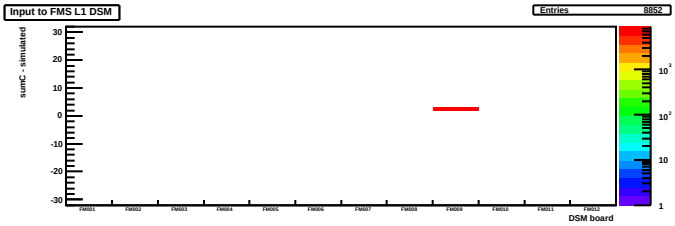

Input to FMS L0 DSM
Entries 5971152
HT ID
QT board

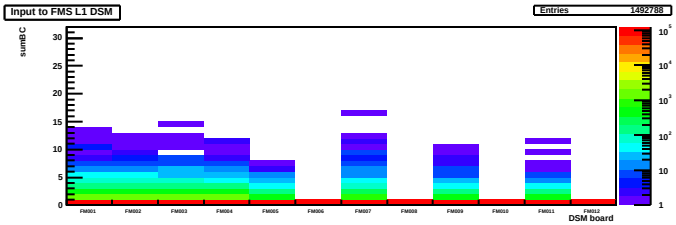

Input to FMS L0 DSM
Entries 134017
HT ID - simulated
QT board

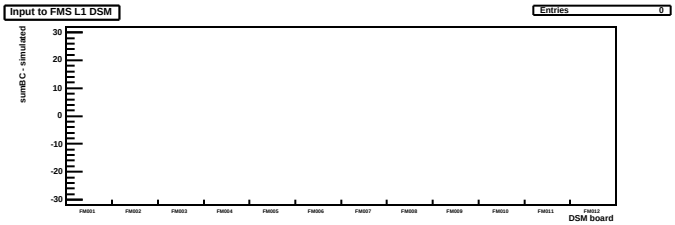

Input to FMS L1 DSM
Entries 1492788
sumD
DSM board

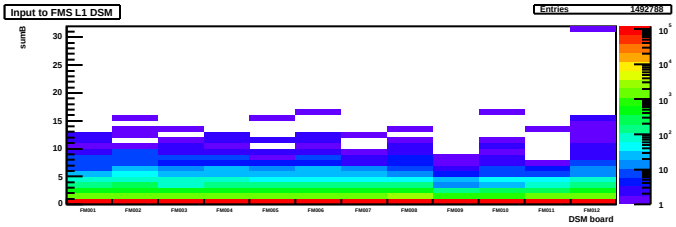

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

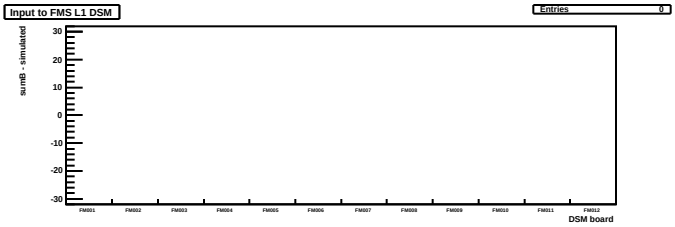

Input to FMS L1 DSM
Entries 1492788
sumC
DSM board

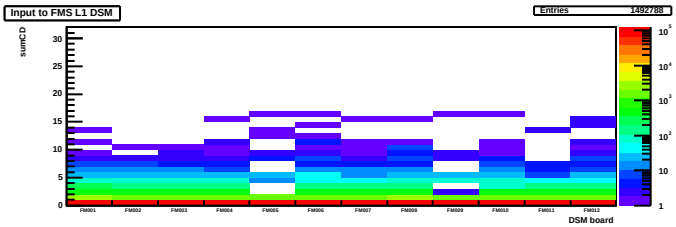

Input to FMS L1 DSM
Entries 8852
sumC - simulated
DSM board

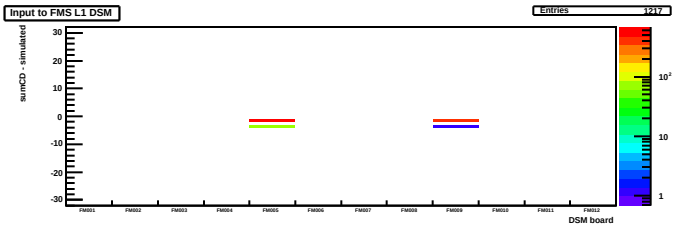

Input to FMS L1 DSM
Entries 1492788
sumBC
DSM board

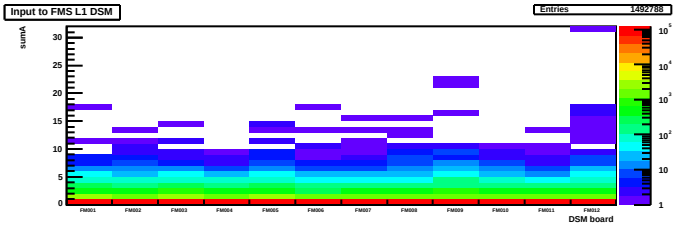

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

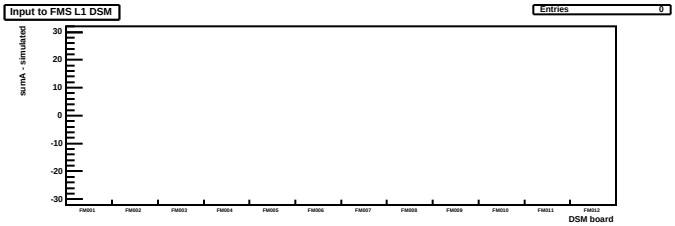

Input to FMS L1 DSM
Entries 1492788
sumB
DSM board

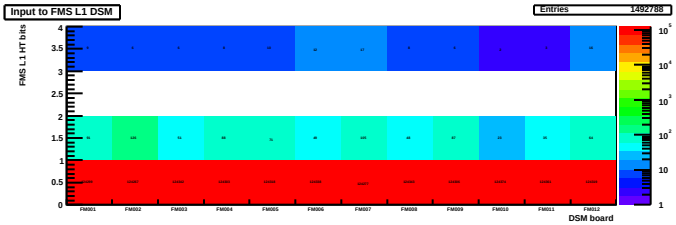

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

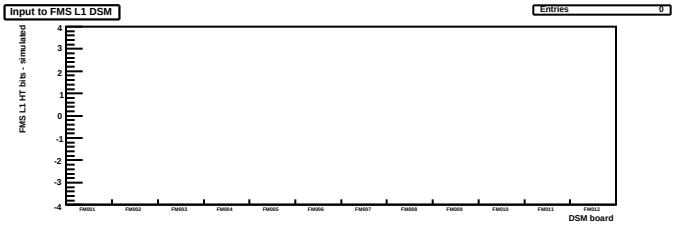

Input to FMS L1 DSM
Entries 1492788
sumCD
DSM board

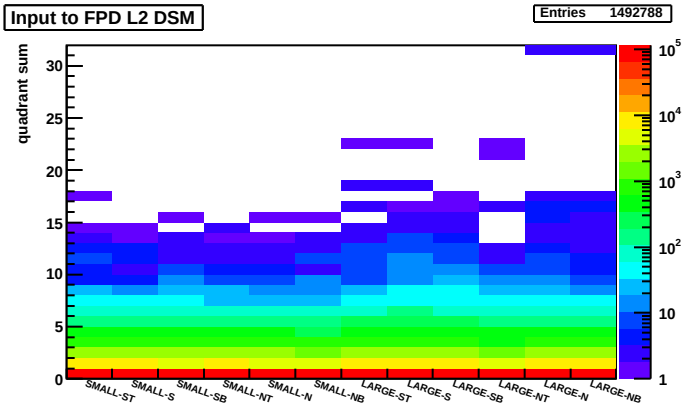

Input to FMS L1 DSM
Entries 1217
sumCD - simulated
DSM board

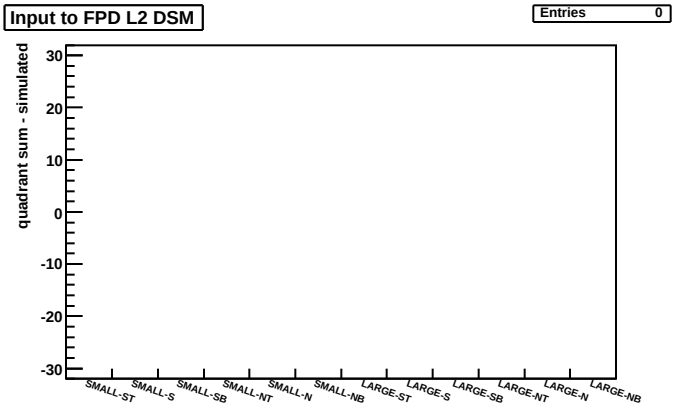

Input to FMS L1 DSM
Entries 1492788
sumA
DSM board

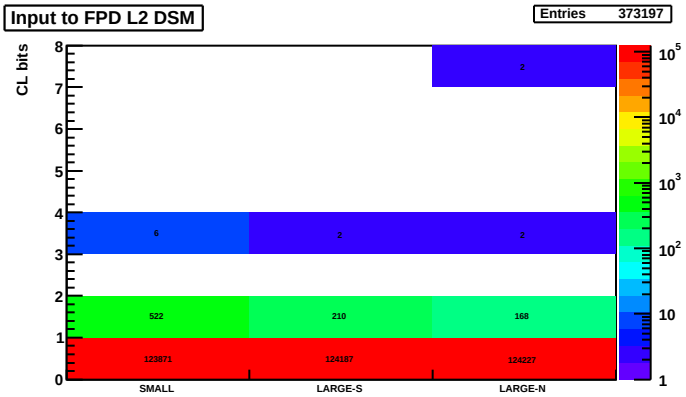

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

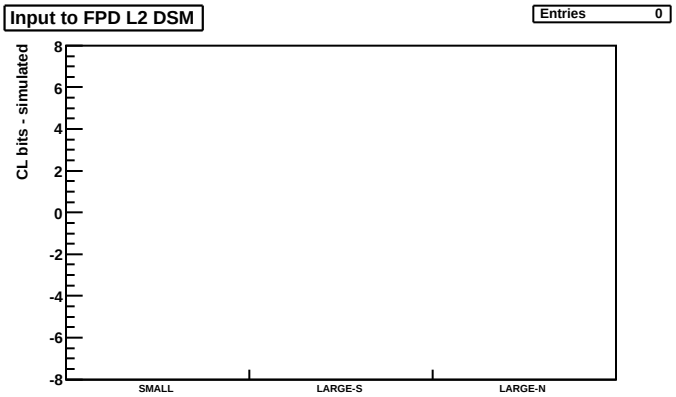

Input to FMS L1 DSM
Entries 1492788
FMS L1 HT bits
DSM board

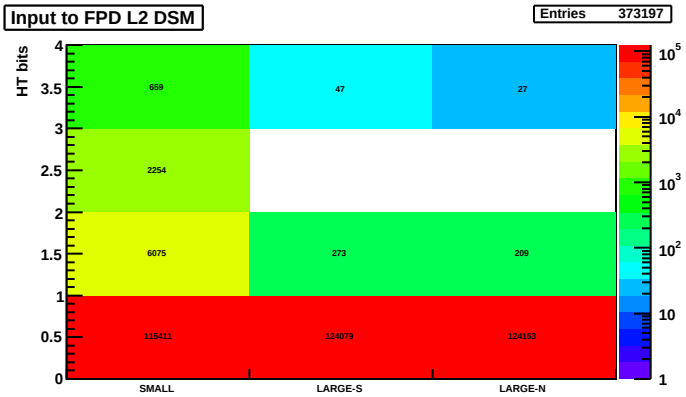

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

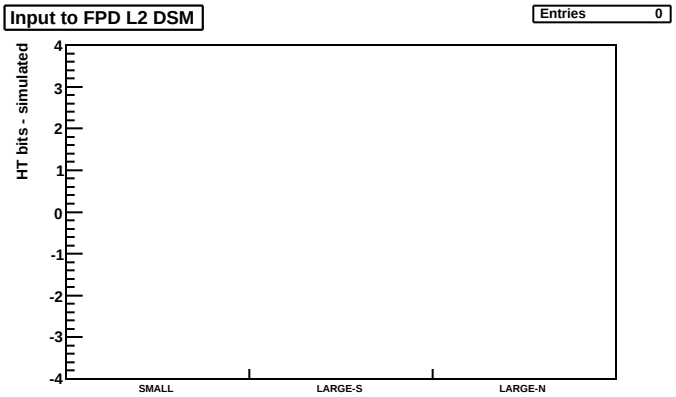

Input to FPD L2 DSM
Entries 1492788
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

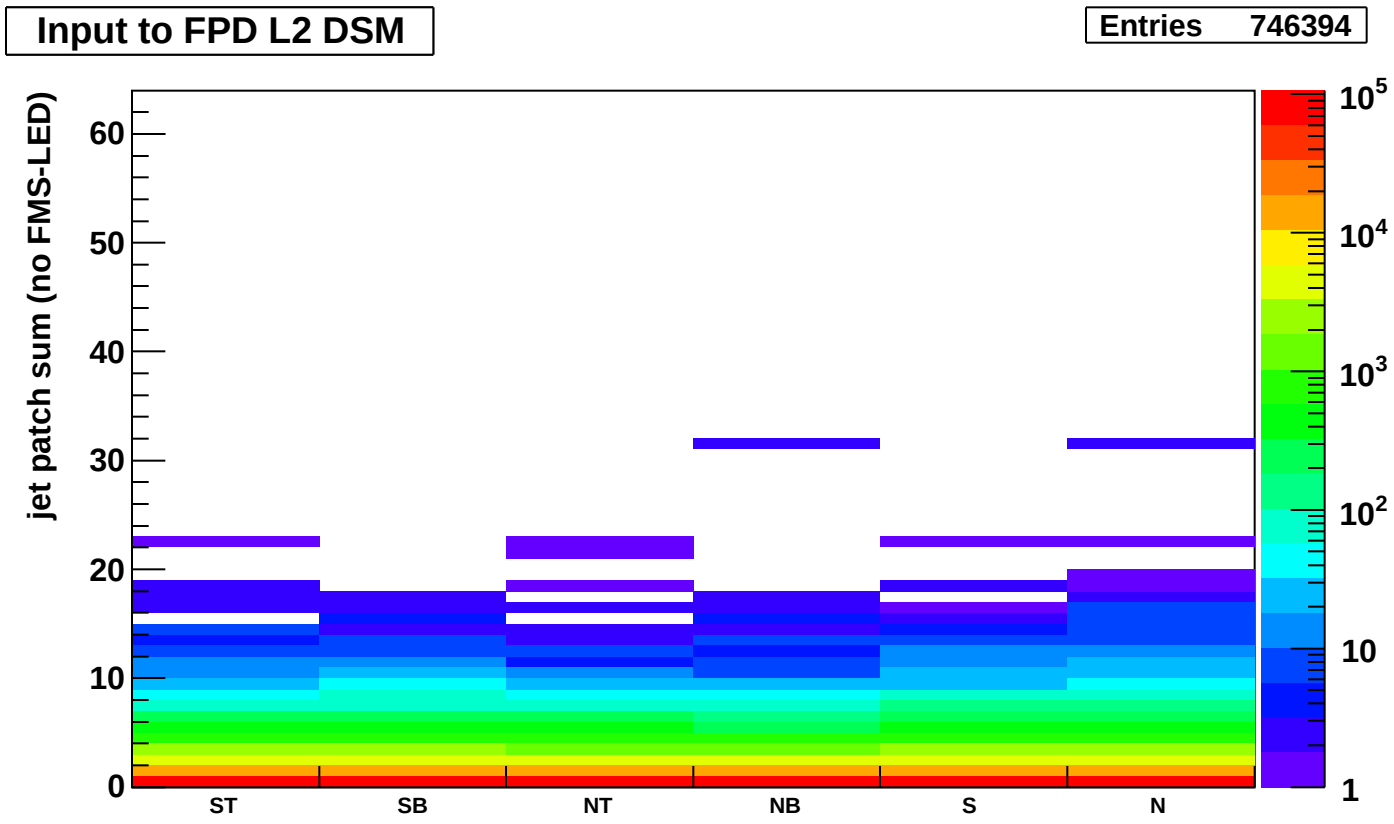

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

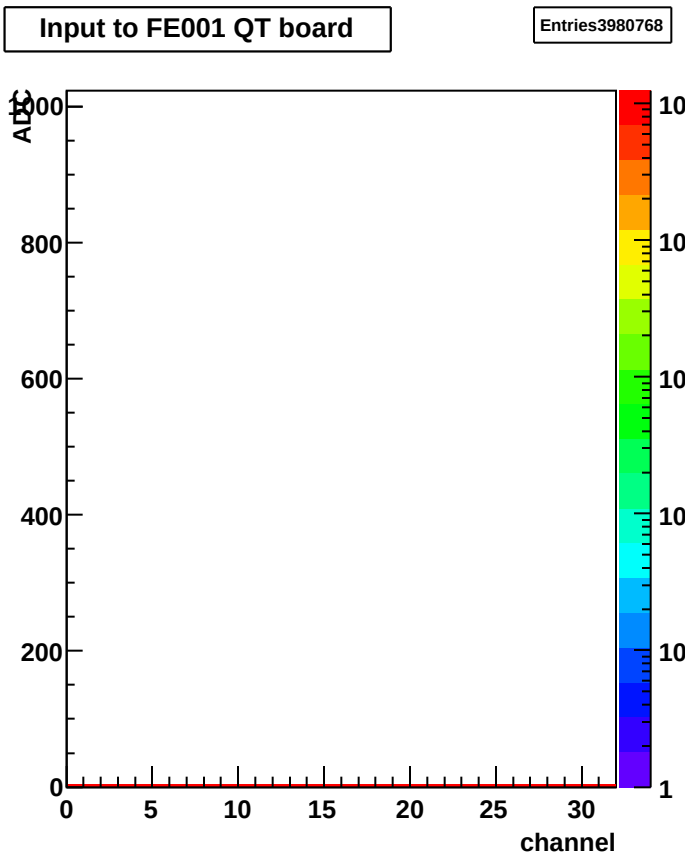

Input to FPD L2 DSM
Entries 373197
CL bits
2
6
2
2
522
210
168
123871
124187
124227
SMALL
LARGE-S
LARGE-N

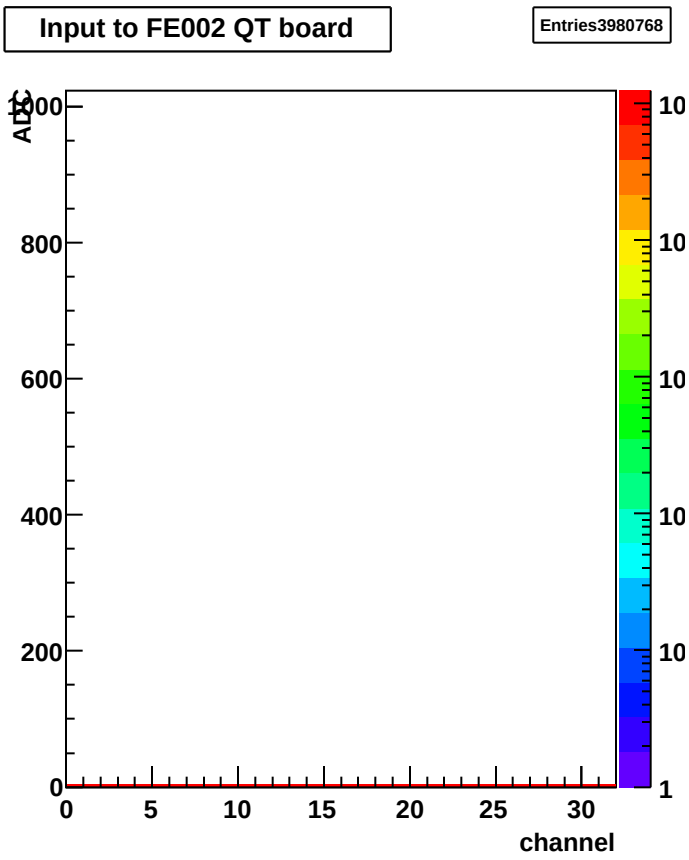

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

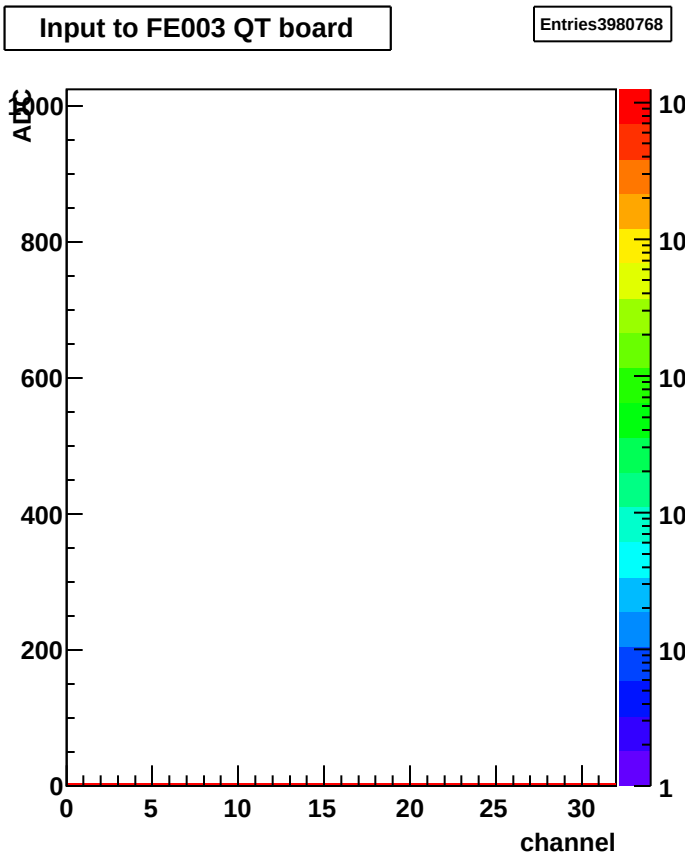

Input to FPD L2 DSM
Entries 373197
HT bits
659
47
27
2254
6075
273
209
115411
124079
124163
SMALL
LARGE-S
LARGE-N

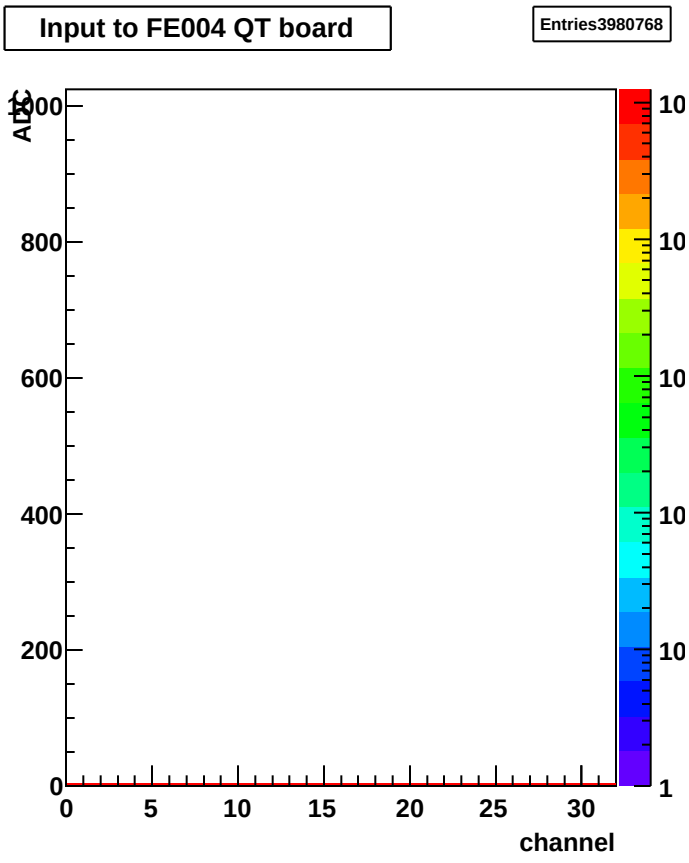

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

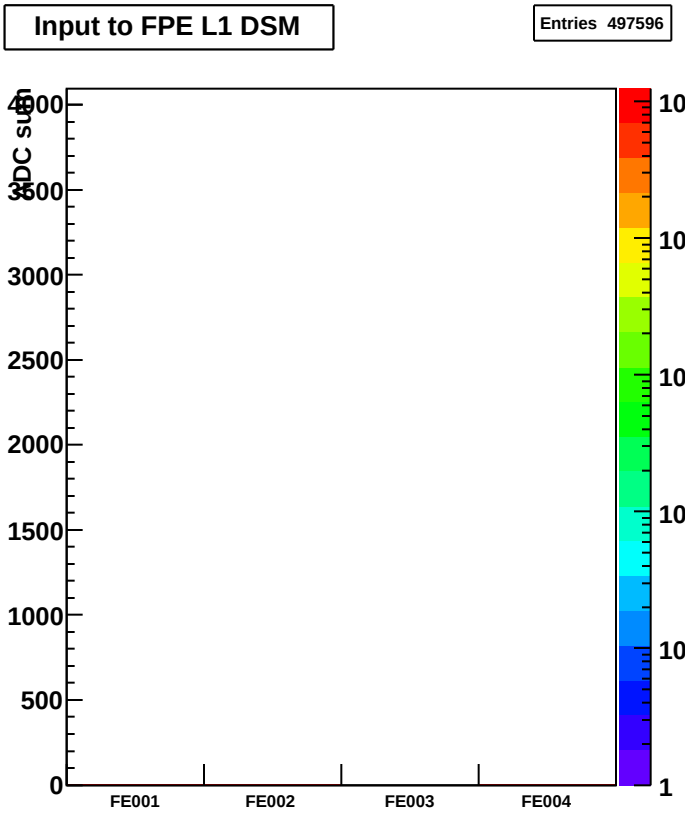

Input to FPD L2 DSM
Entries 746394
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

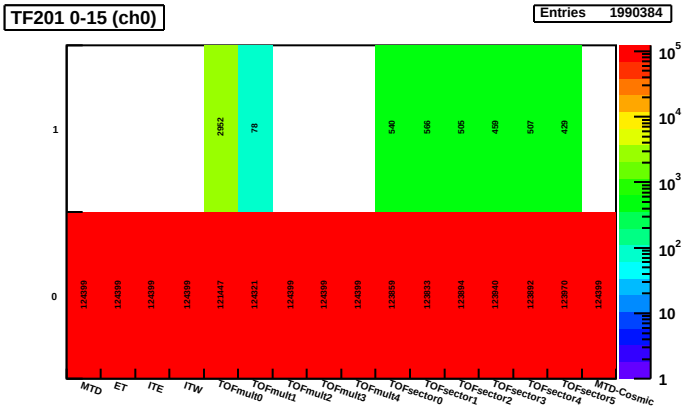

Input to FE001 QT board
Entries3980768
ADC
channel

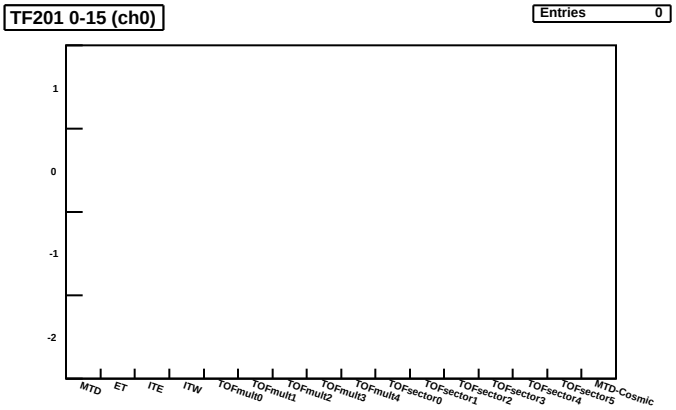

Input to FE002 QT board
Entries3980768
ADC
channel

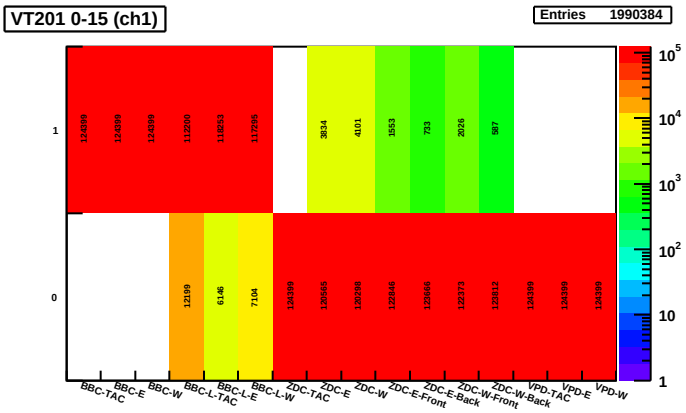

Input to FE003 QT board
Entries3980768
ADC
channel

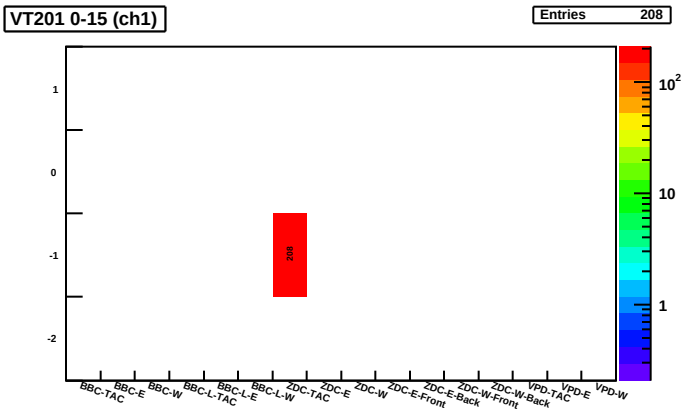

Input to FE004 QT board
Entries3980768
ADC
channel

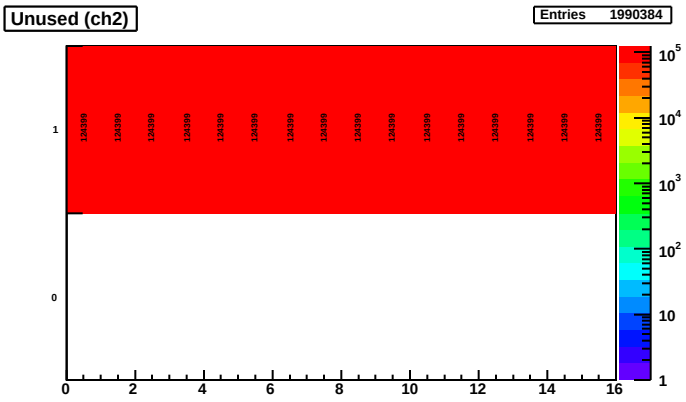

Input to FPE L1 DSM
Entries 497596
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

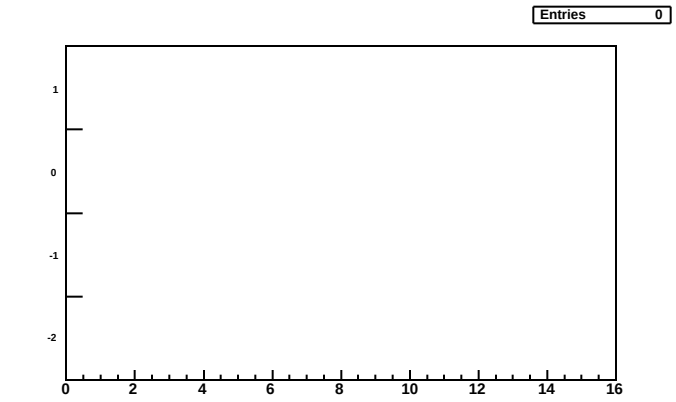

TF201 0-15 (ch0)
Entries 1990384
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

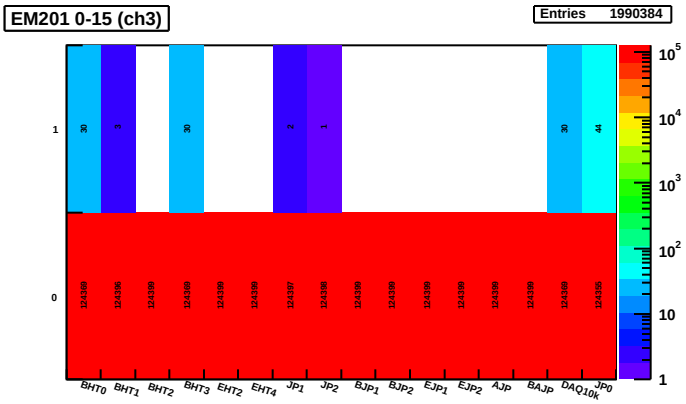

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 1990384
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 208
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 1990384


Entries 0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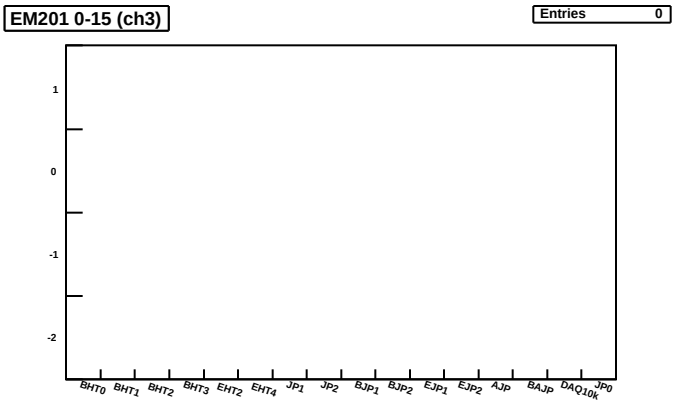
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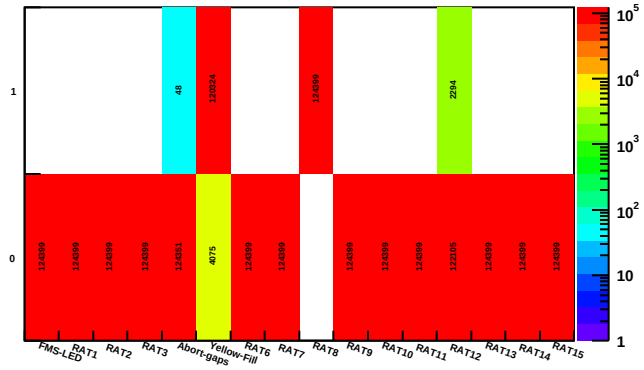
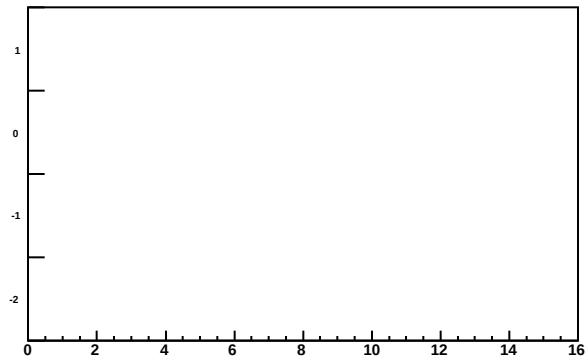
RAT board (ch4)





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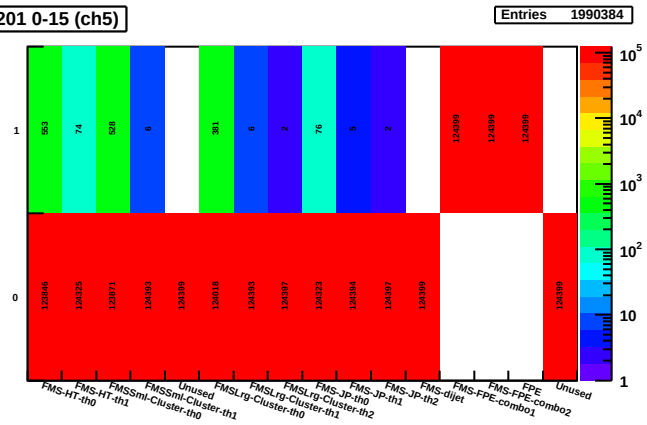

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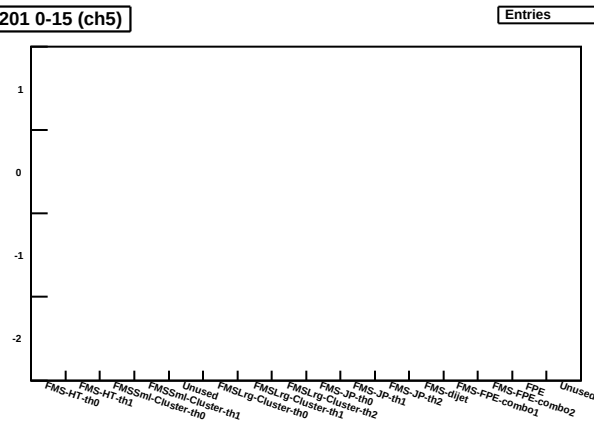

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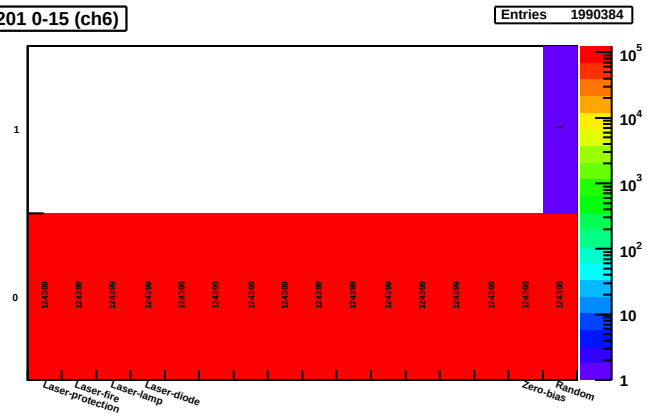

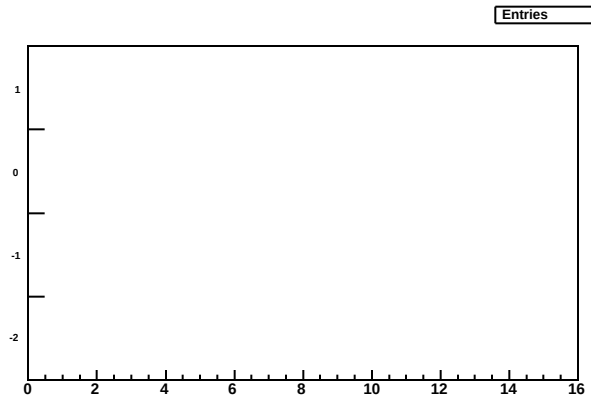
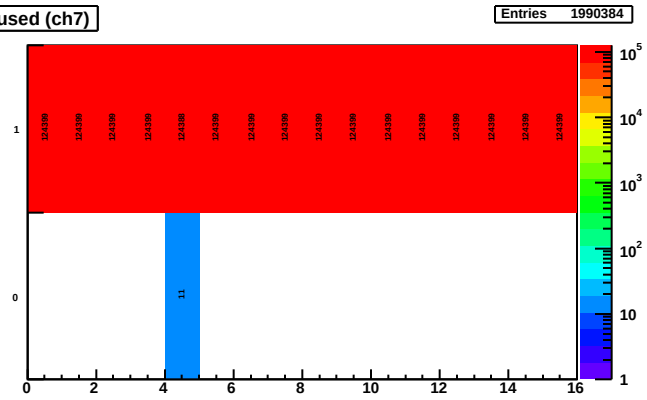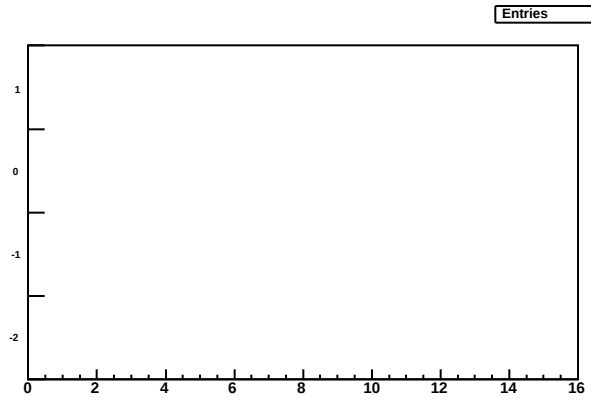
Unused (ch7)