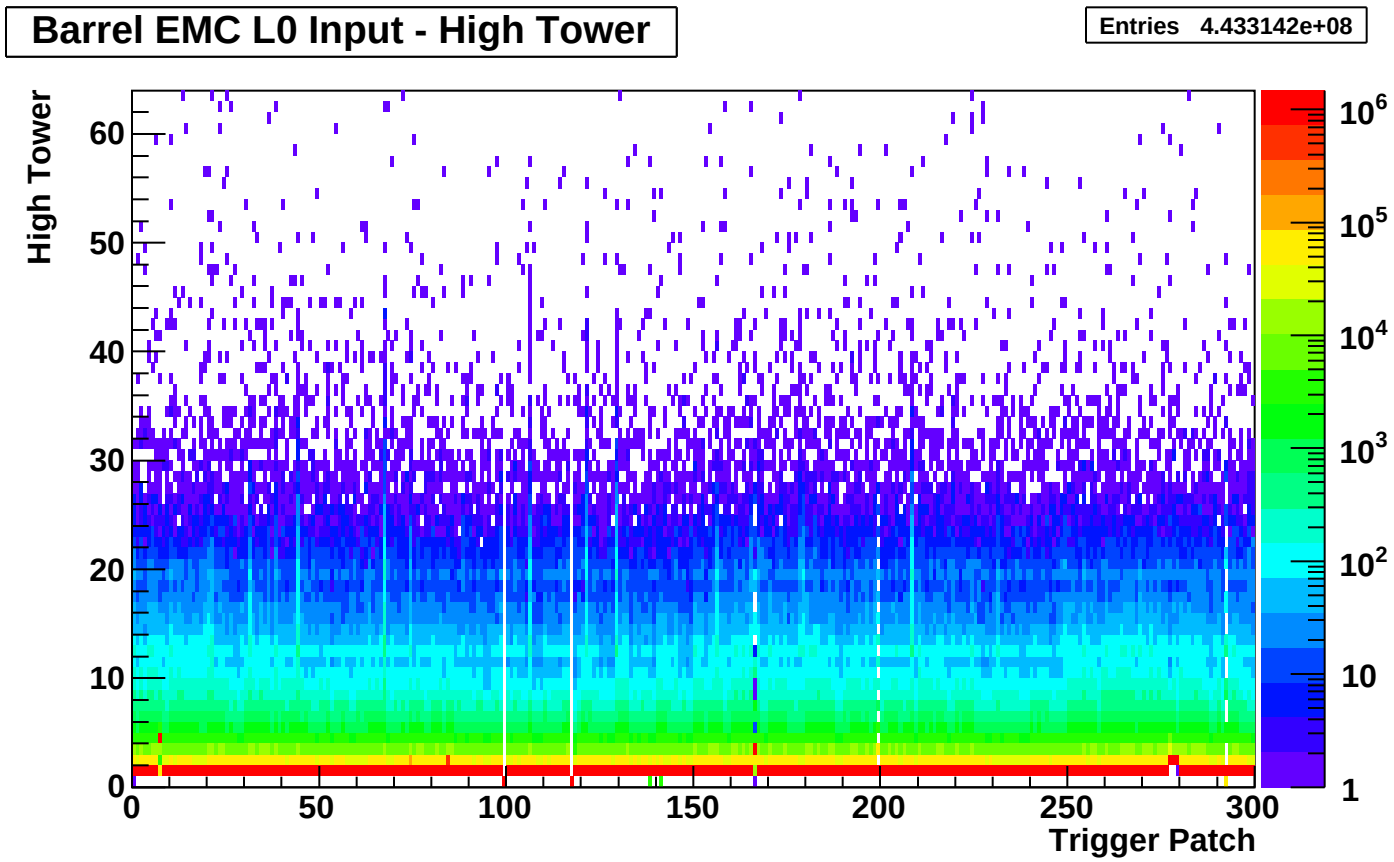
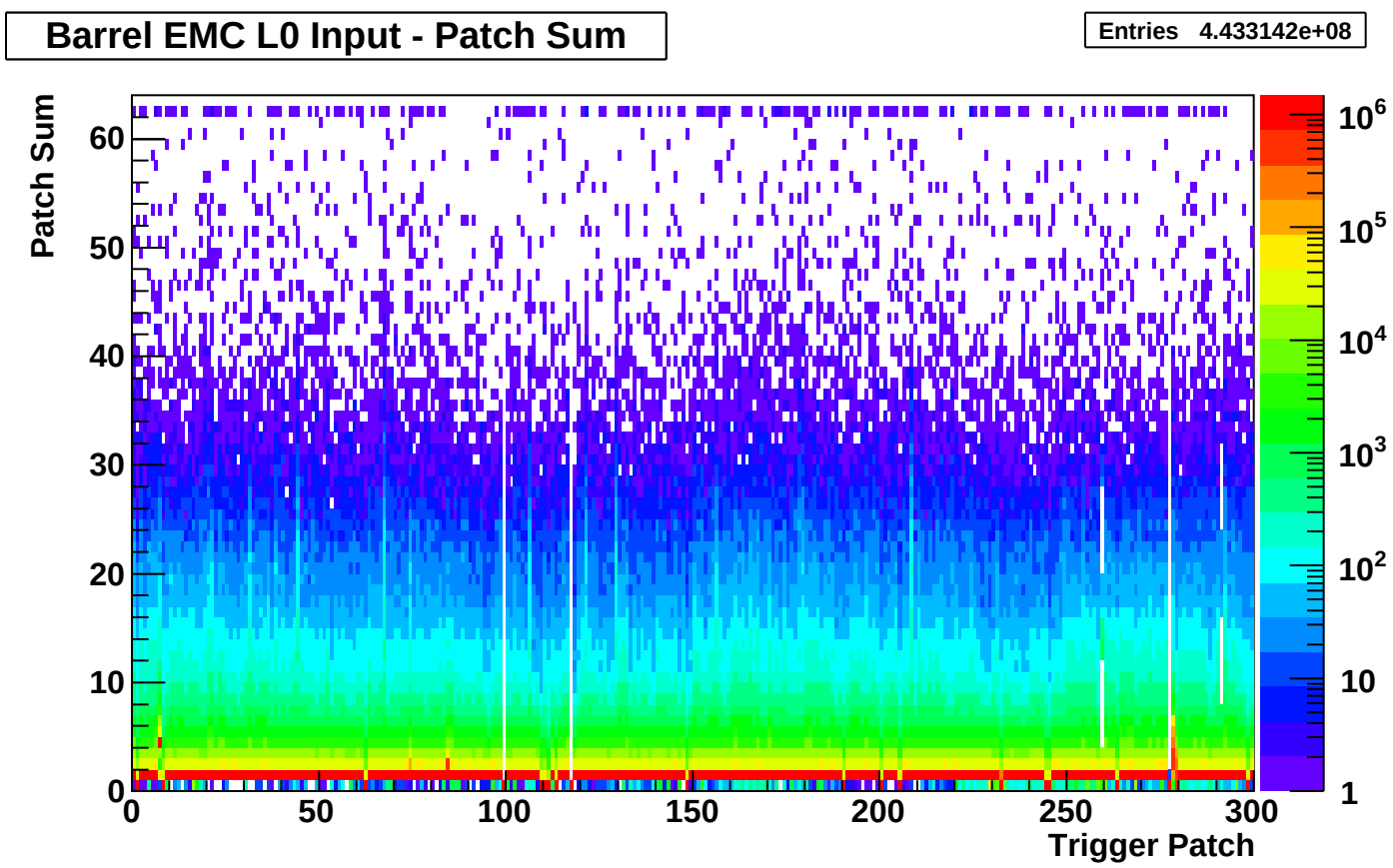
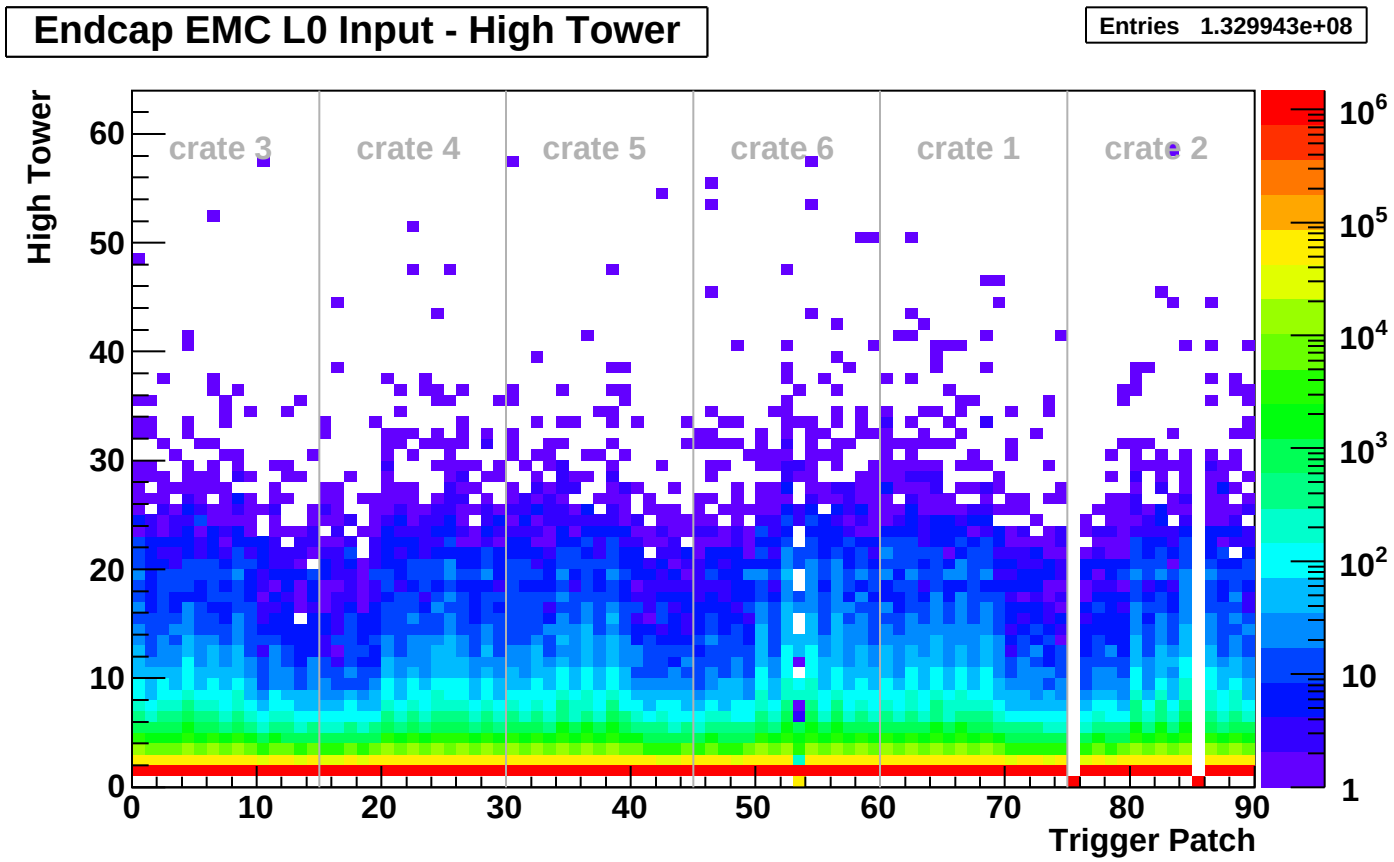
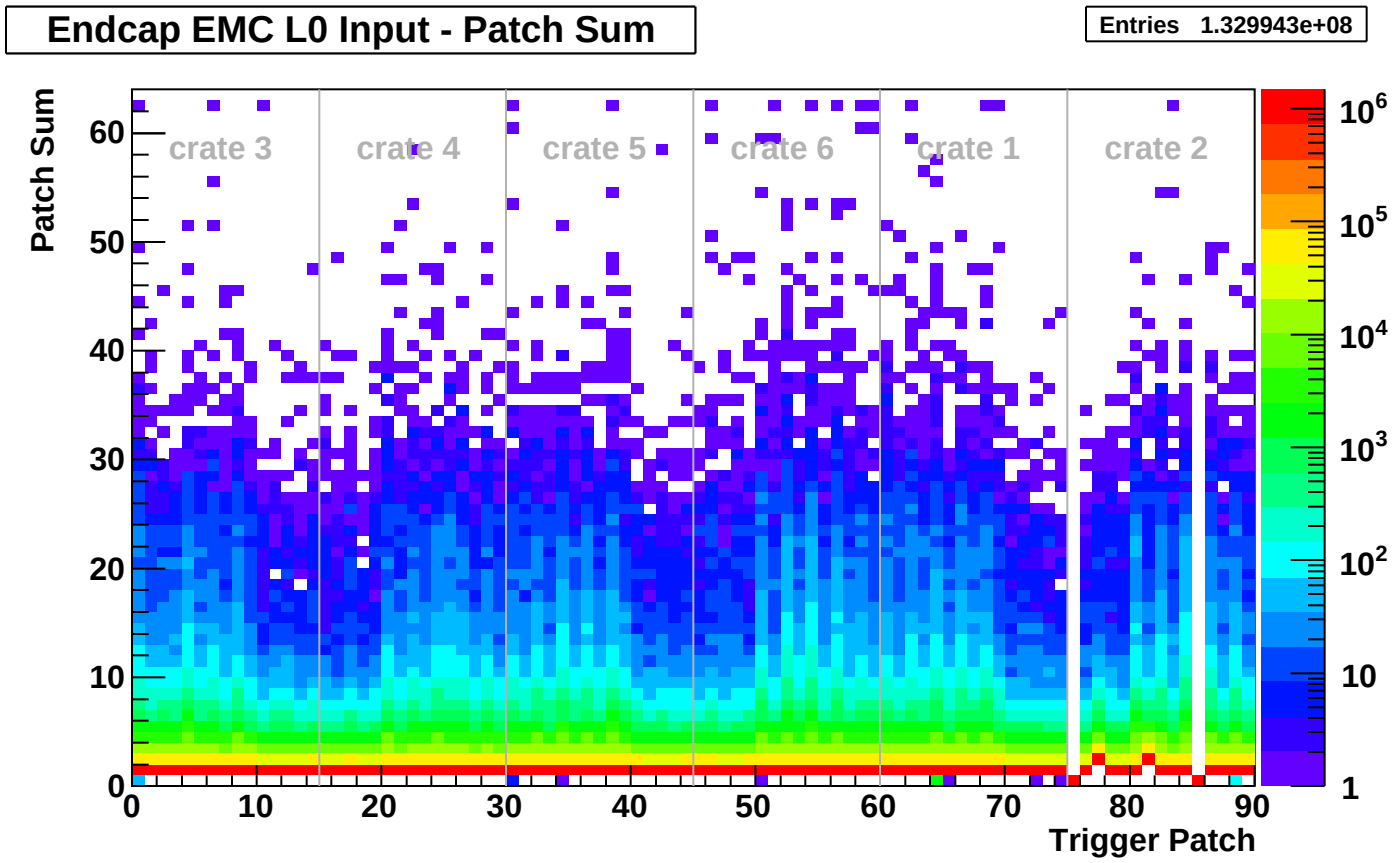
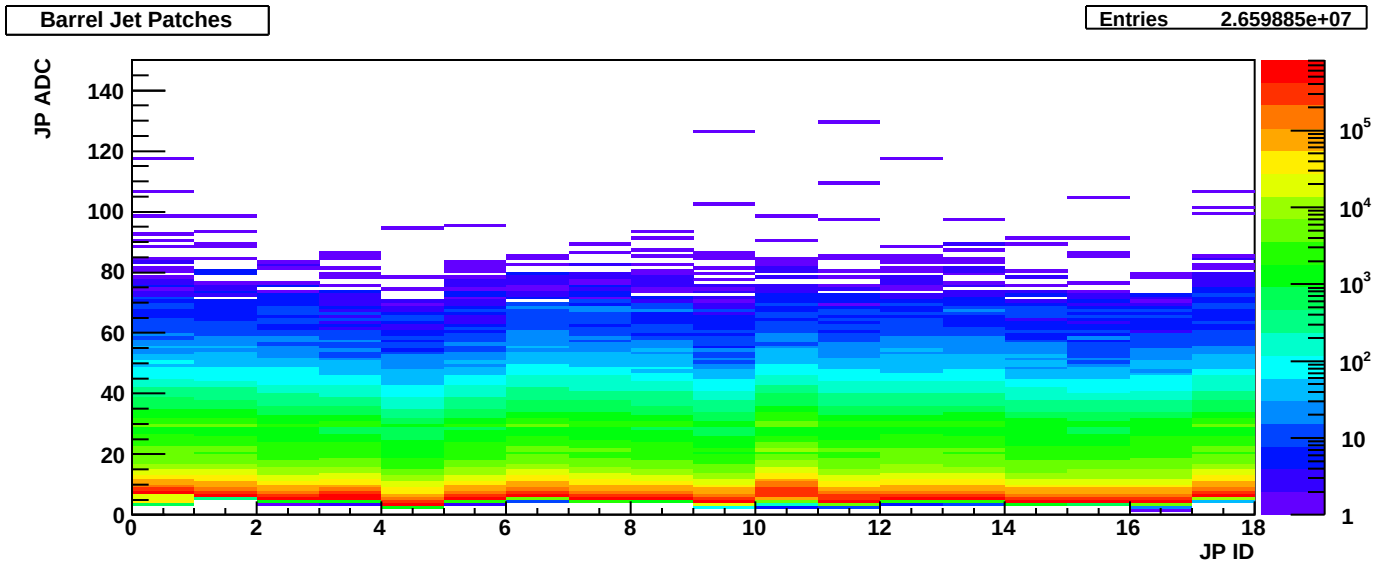
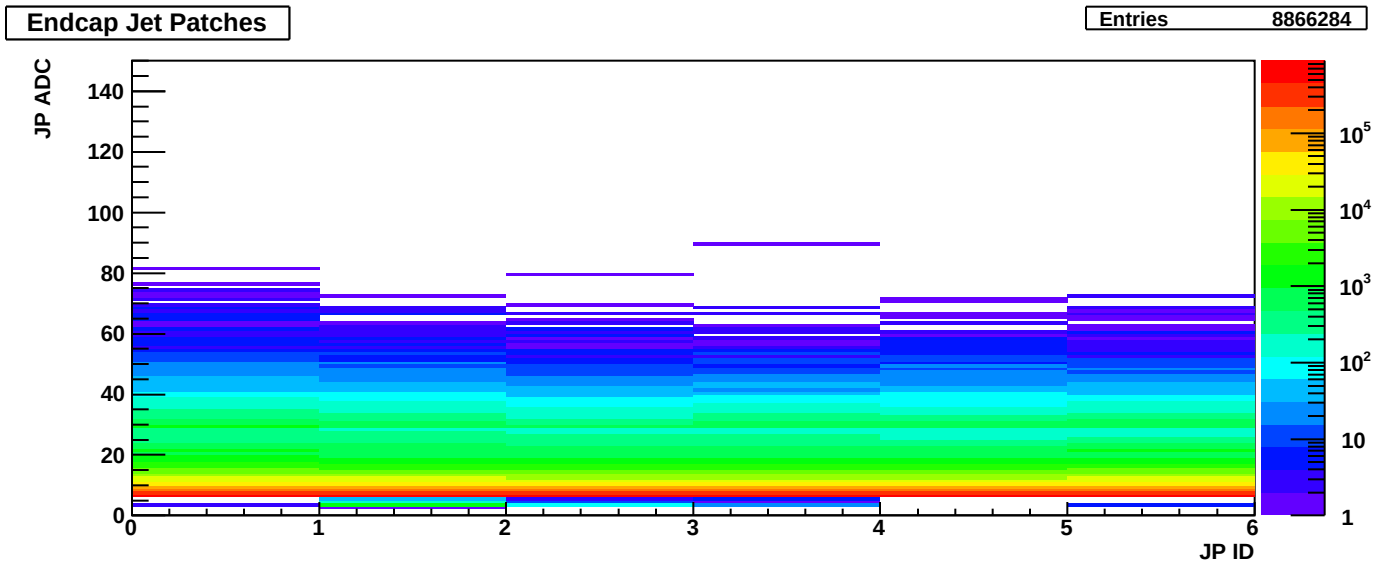

Barrel EMC L0 Input - High Tower
Entries 4.433142e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

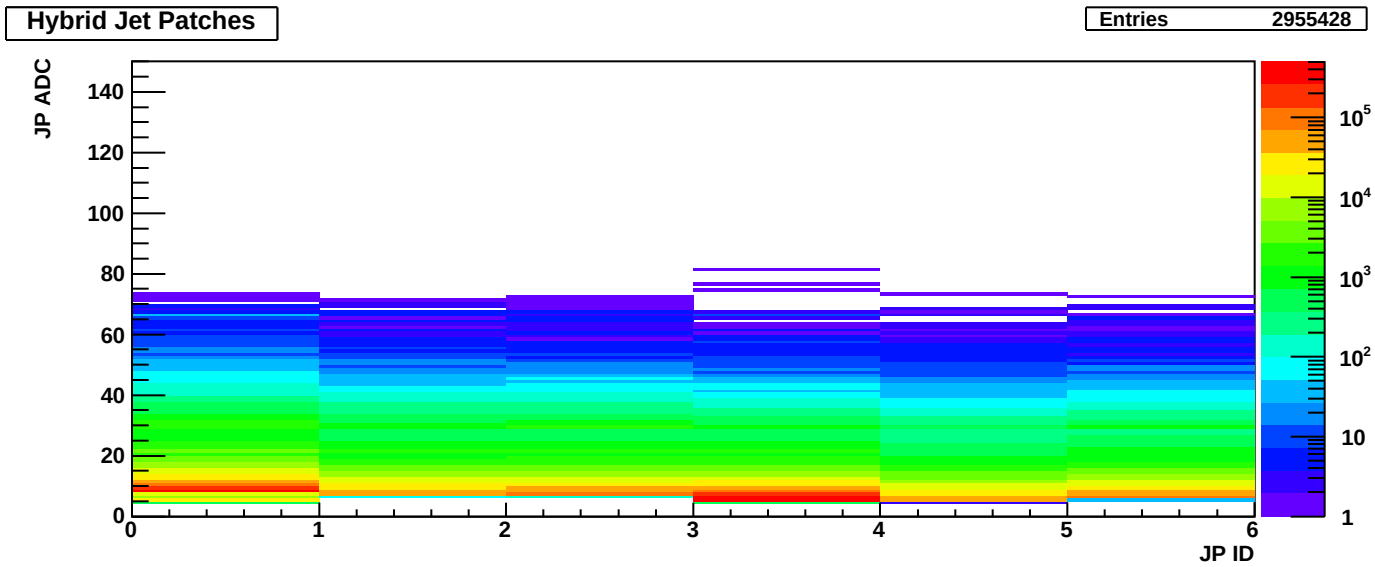

Barrel EMC L0 Input - Patch Sum
Entries 4.433142e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

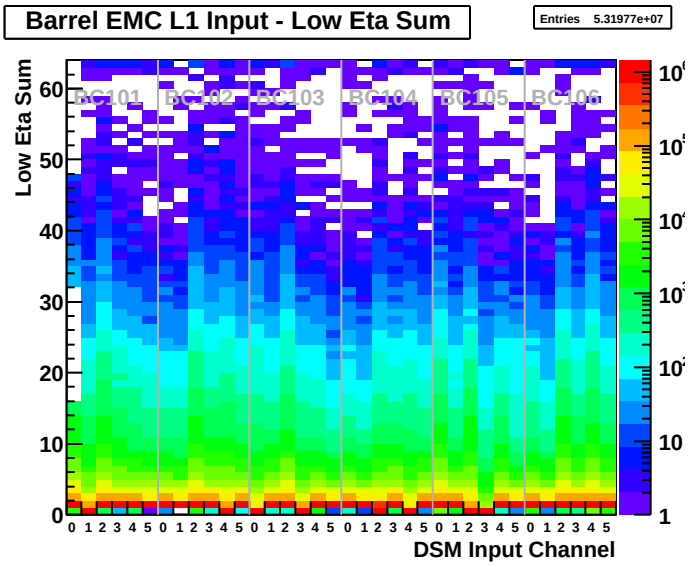

Endcap EMC L0 Input - High Tower
Entries 1.329943e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

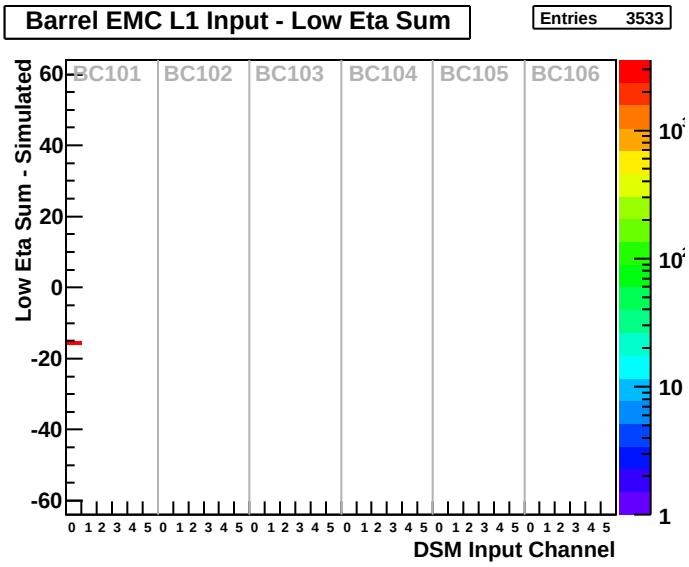

Endcap EMC L0 Input - Patch Sum
Entries 1.329943e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

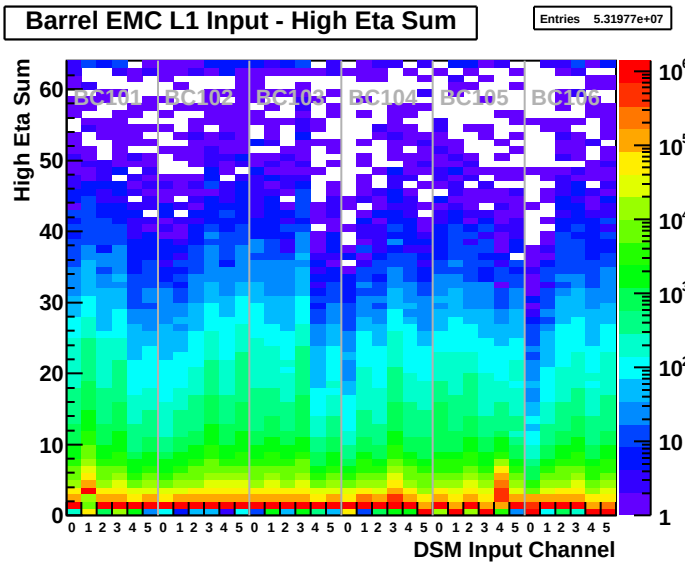

Barrel Jet Patches
Entries 2.659885e+07
JP ADC
JP ID

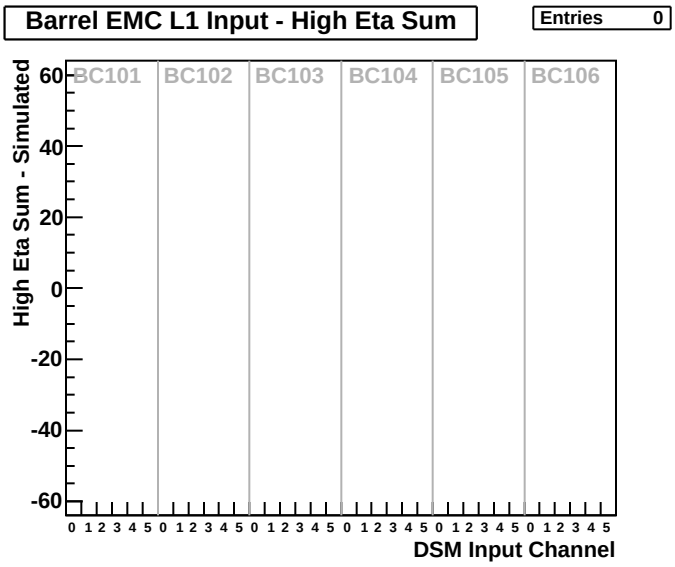

Endcap Jet Patches
Entries 8866284
JP ADC
JP ID

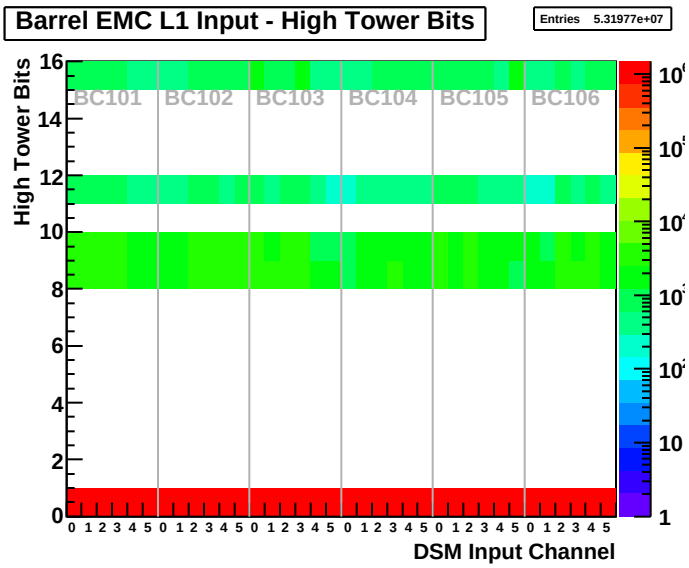

Hybrid Jet Patches
Entries 2955428
JP ADC
JP ID

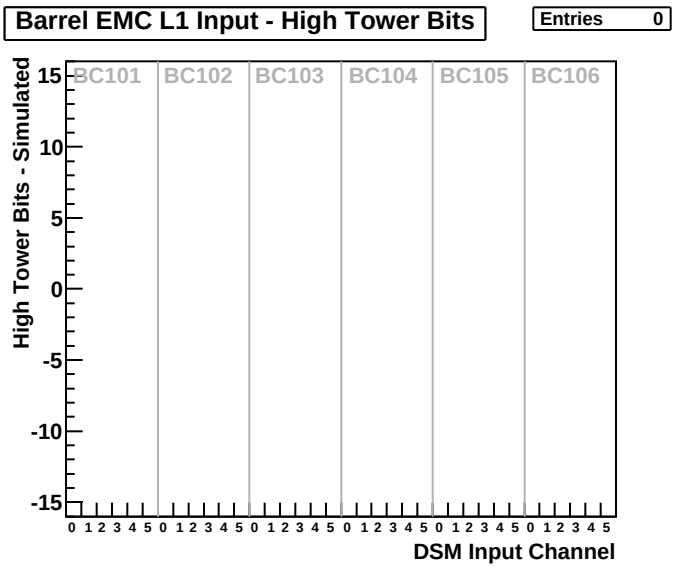

Barrel EMC L1 Input - Low Eta Sum
Entries 5.31977e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

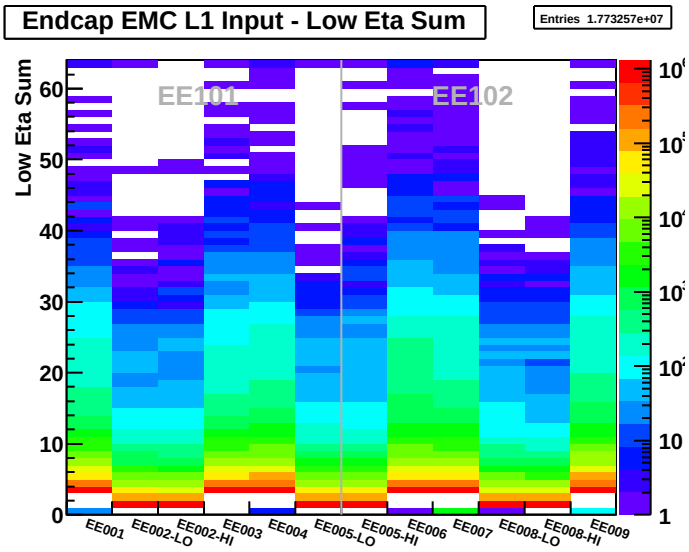

Barrel EMC L1 Input - Low Eta Sum
Entries 3533
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

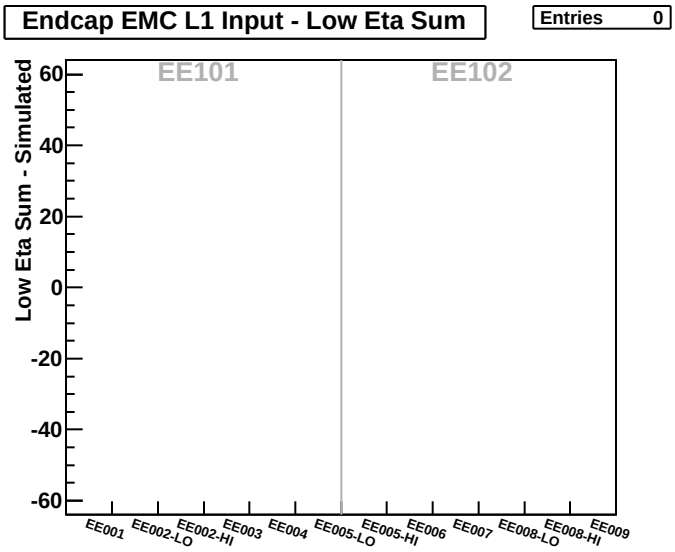

Barrel EMC L1 Input - High Eta Sum
Entries 5.31977e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

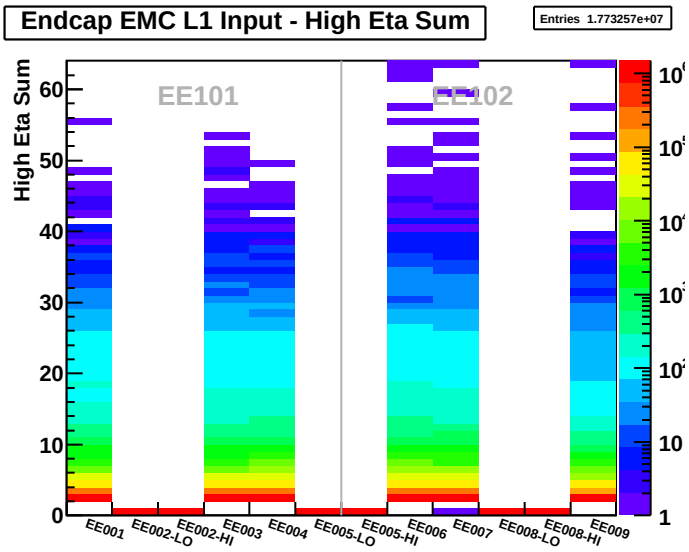

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

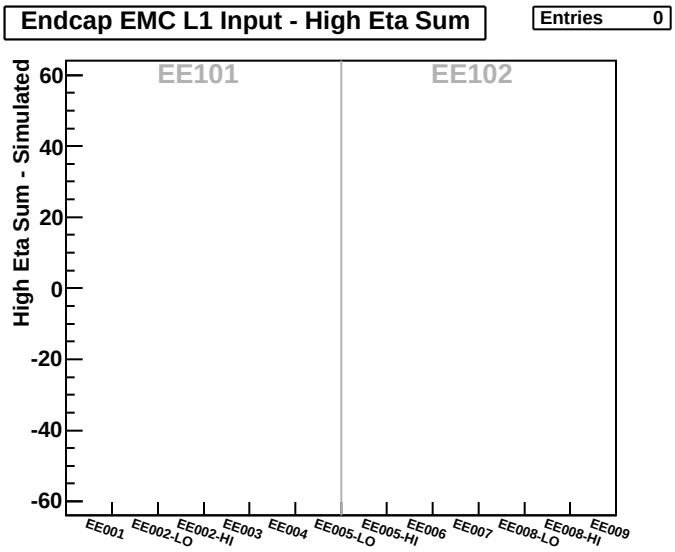

Barrel EMC L1 Input - High Tower Bits
Entries 5.31977e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

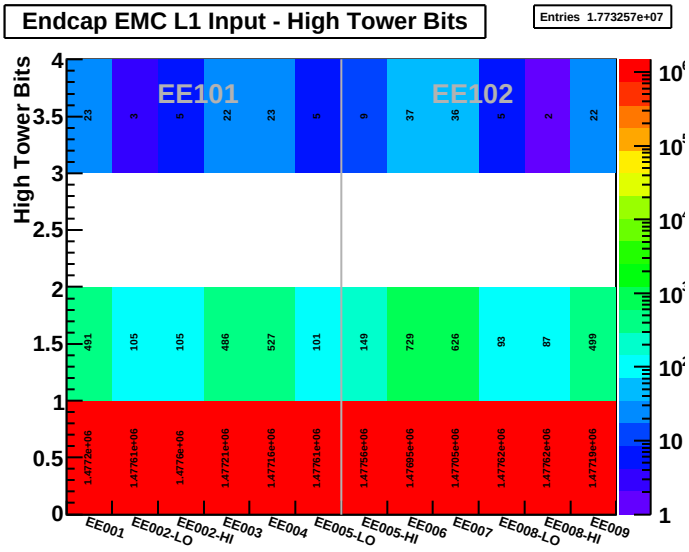

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

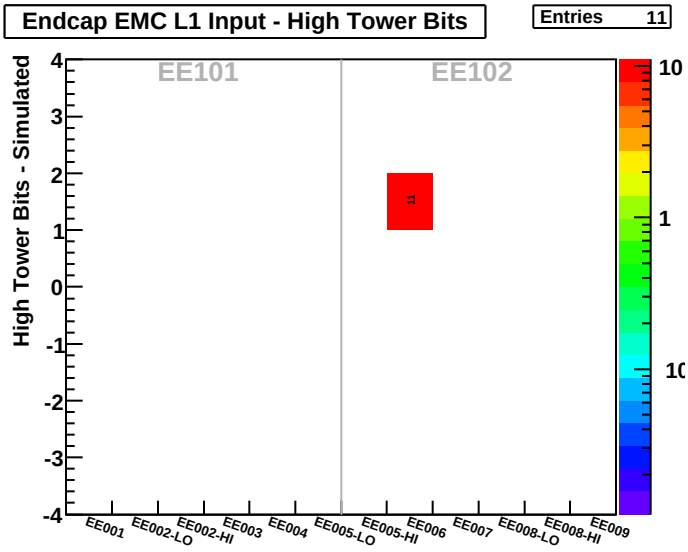

Endcap EMC L1 Input - Low Eta Sum
Entries 1.773257e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

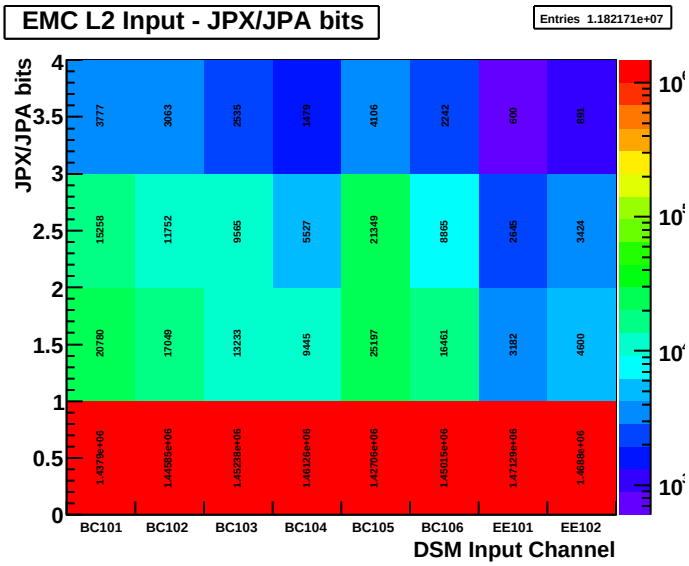

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

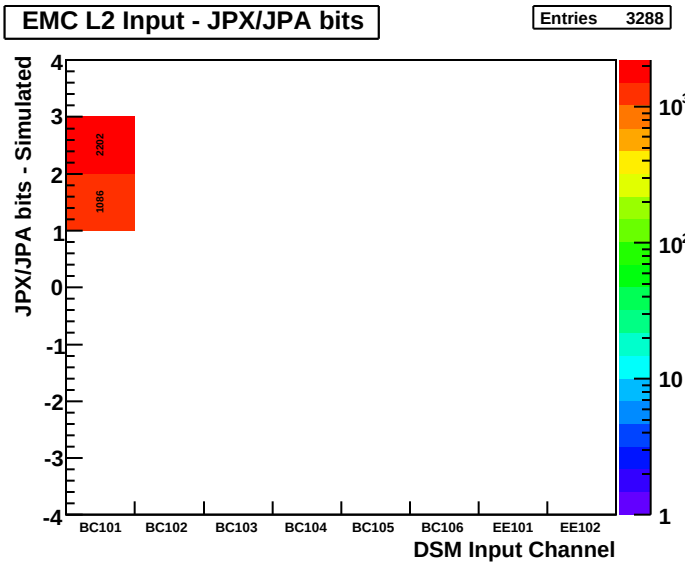

Endcap EMC L1 Input - High Eta Sum
Entries 1.773257e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

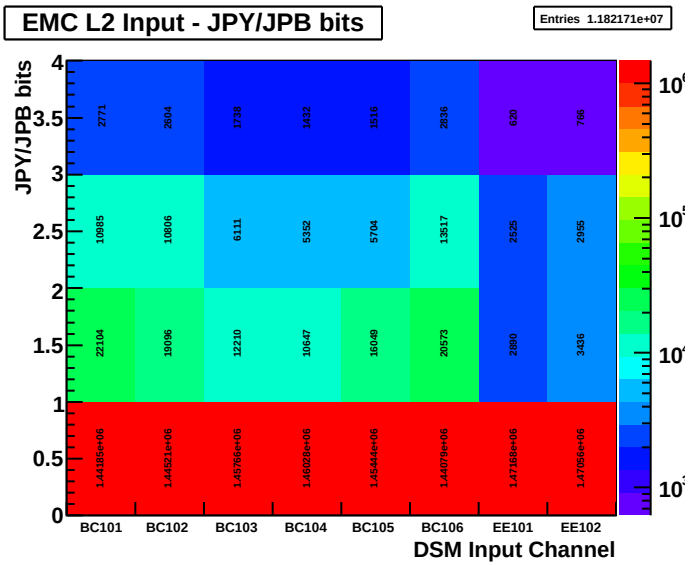

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

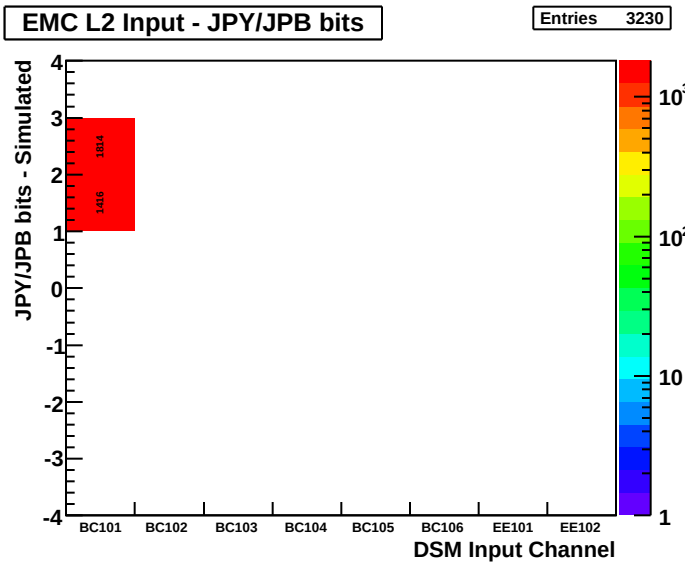

Endcap EMC L1 Input - High Tower Bits
Entries 1.773257e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

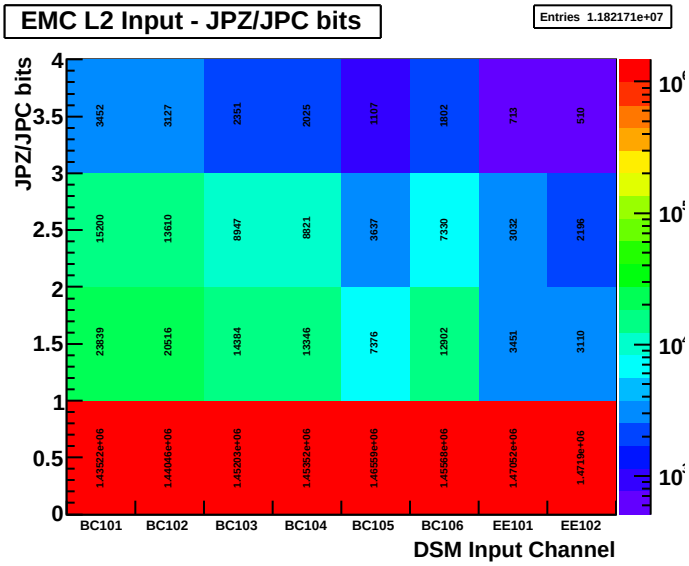

Endcap EMC L1 Input - High Tower Bits
Entries 11
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

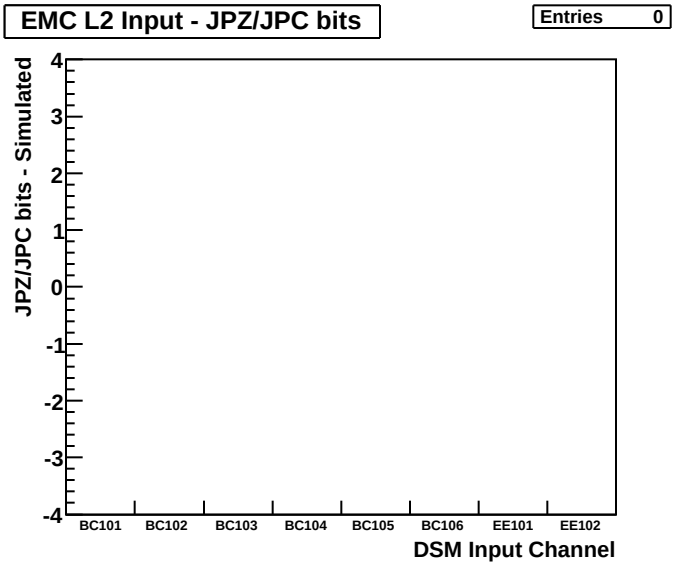

EMC L2 Input - JPX/JPA bits
Entries 1.182171e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3777 3063 2535 1479 4106 2242 600 891
15258 11752 9565 5527 21349 8865 2645 3424
20780 17049 13233 9445 25197 16461 3182 4600
1.4379e+06 1.44985e+06 1.45238e+06 1.46126e+06 1.42706e+06 1.45015e+06 1.47129e+06 1.4698e+06

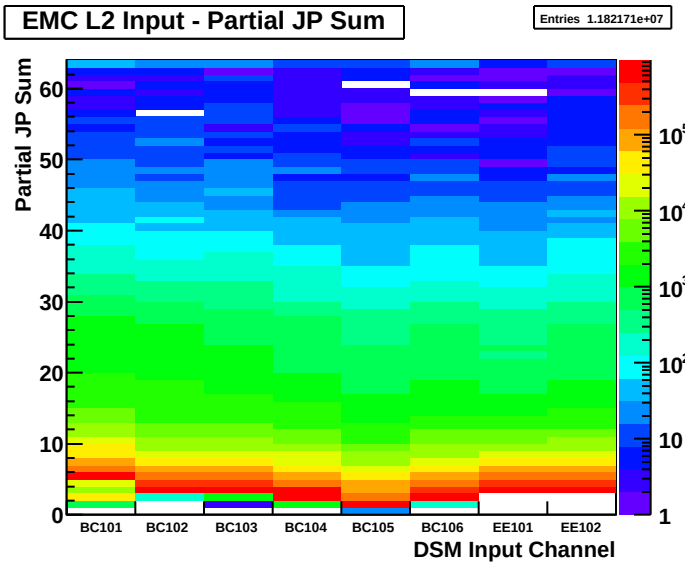

EMC L2 Input - JPX/JPA bits
Entries 3288
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2202
1086

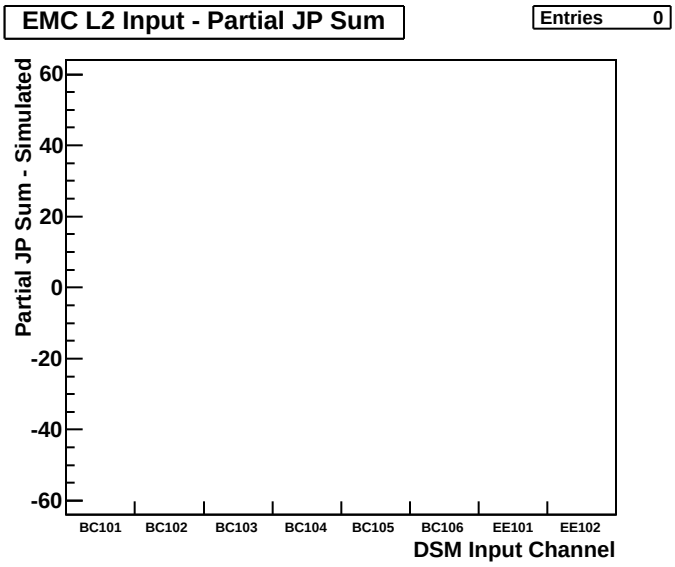

EMC L2 Input - JPY/JPB bits
Entries 1.182171e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2771 2604 1738 1432 1516 2836 620 766
10985 10806 6111 5352 5704 13517 2525 2955
22104 19096 12210 10647 16049 20573 2890 3436
1.44185e+06 1.44521e+06 1.45766e+06 1.46028e+06 1.45444e+06 1.44079e+06 1.47198e+06 1.47056e+06

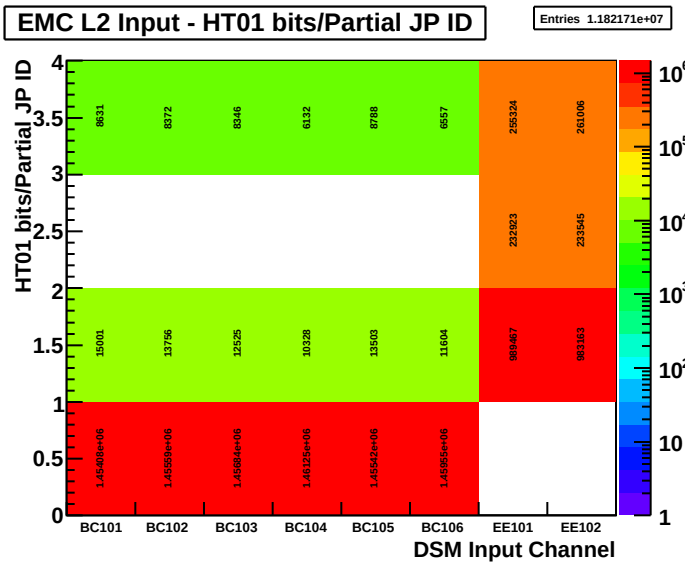

EMC L2 Input - JPY/JPB bits
Entries 3230
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1814
1416

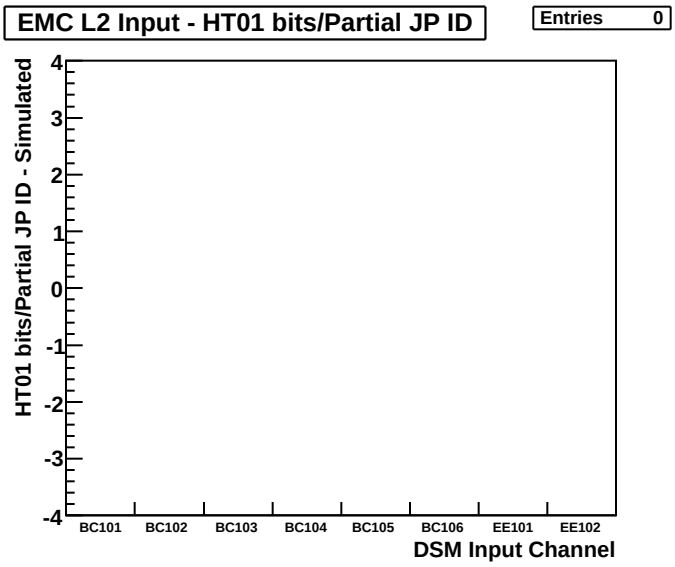

EMC L2 Input - JPZ/JPC bits
Entries 1.182171e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3452 3127 2351 2025 1107 1002 713 510
15200 13610 8947 8821 3637 7330 3032 2196
23839 20516 14384 13346 7376 12902 3451 3110
1.43522e+06 1.44046e+06 1.45203e+06 1.45352e+06 1.46559e+06 1.45568e+06 1.47052e+06 1.4719e+06

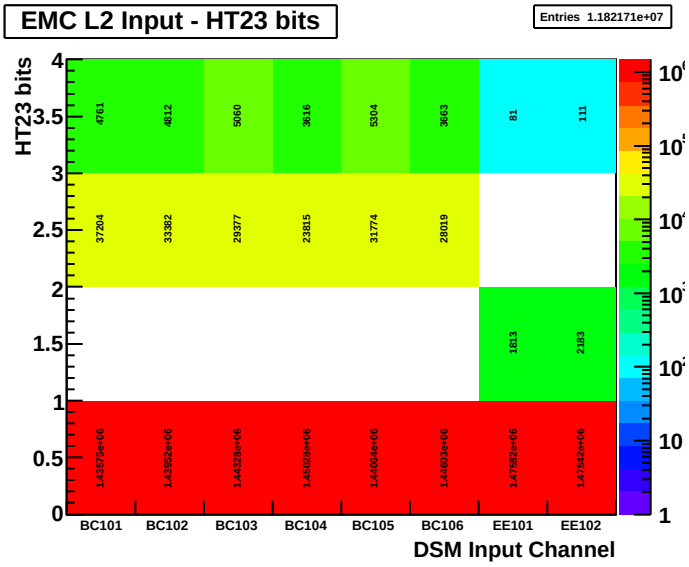

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

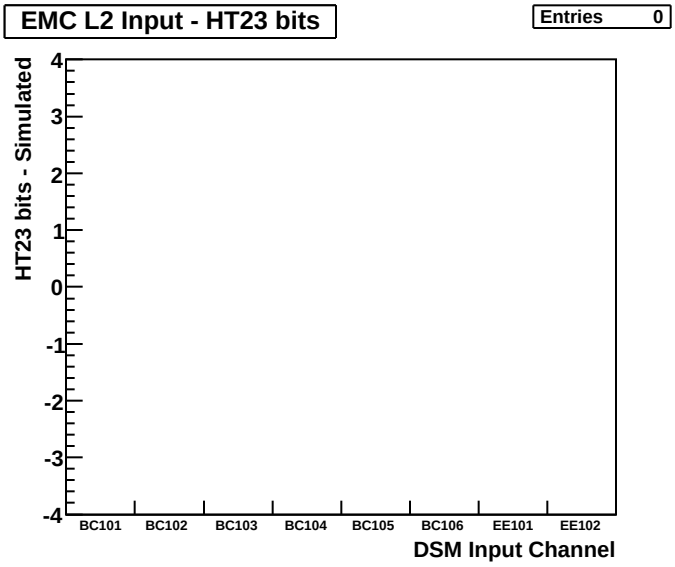

EMC L2 Input - Partial JP Sum
Entries 1.182171e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

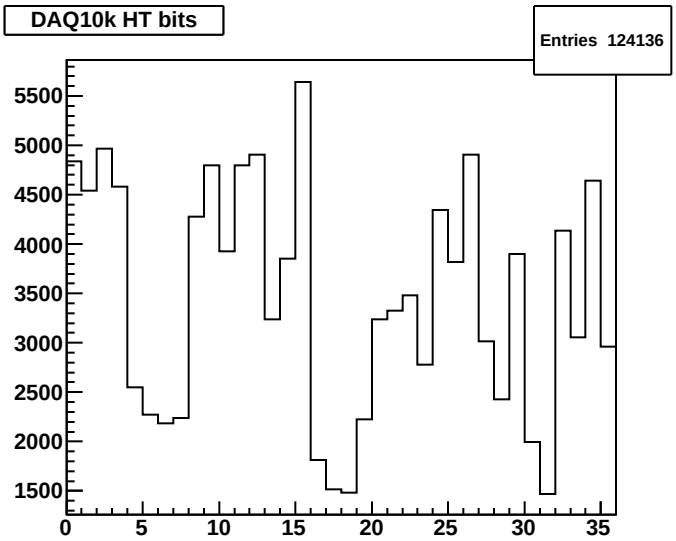

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

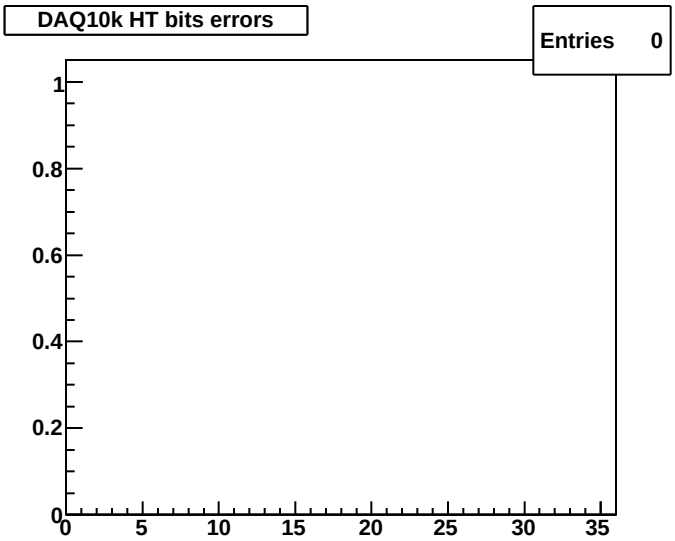

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.182171e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

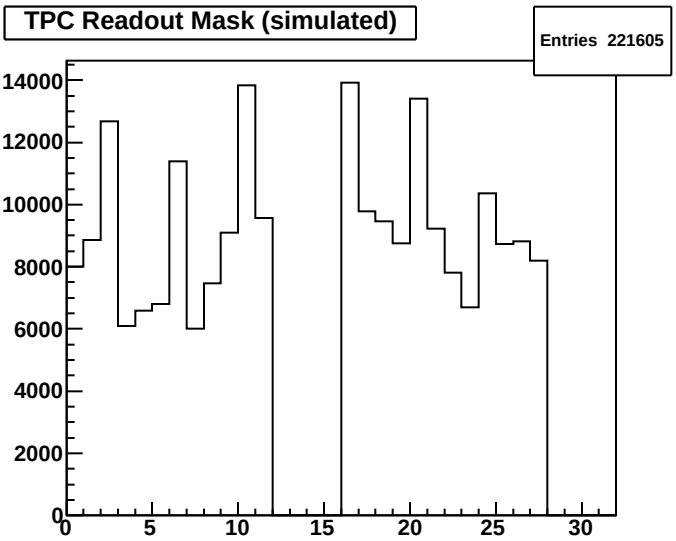

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

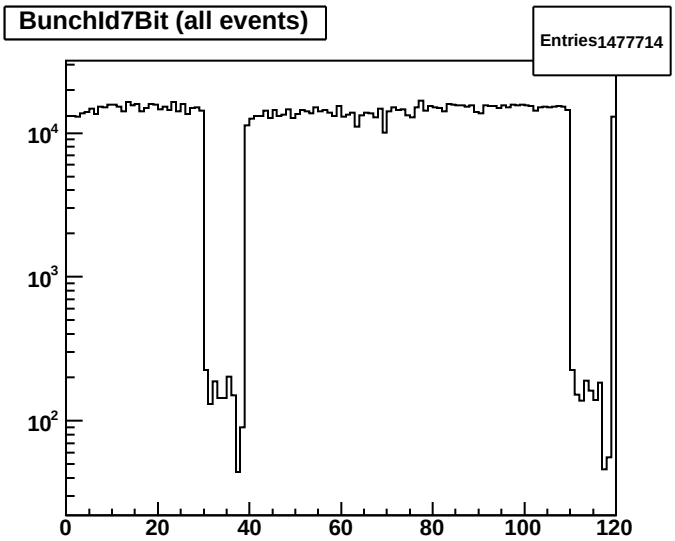

EMC L2 Input - HT23 bits
Entries 1.182171e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

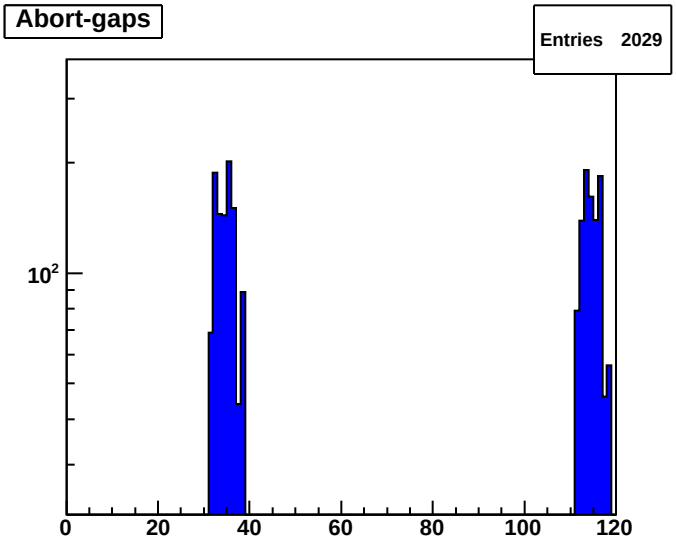

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

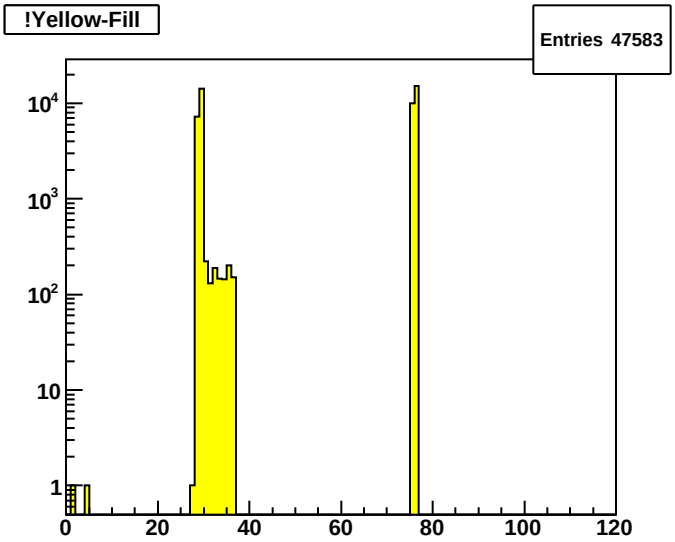

DAQ10k HT bits
Entries 124136

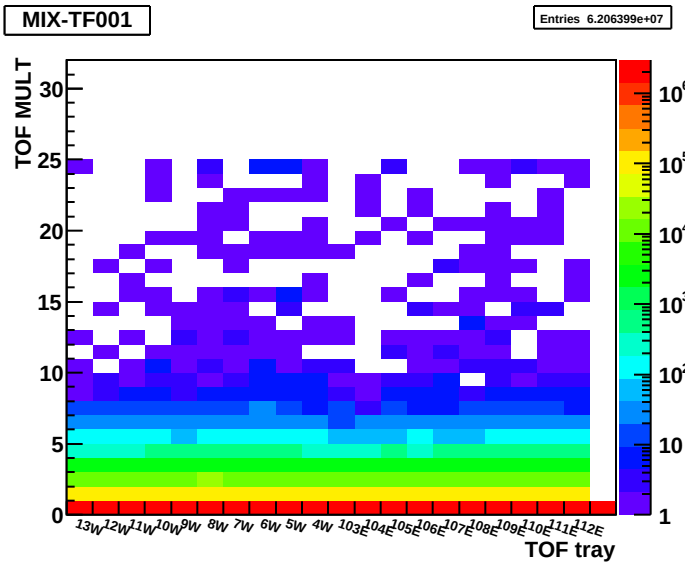

DAQ10k HT bits errors
Entries 0

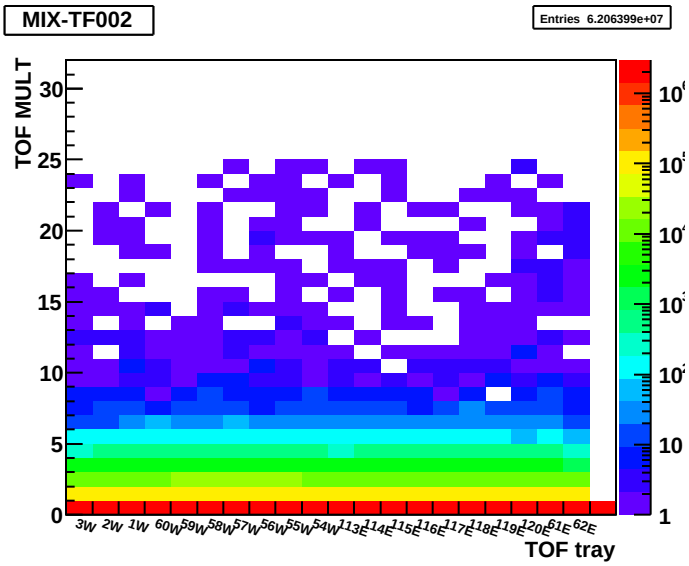

TPC Readout Mask (simulated)
Entries 221605

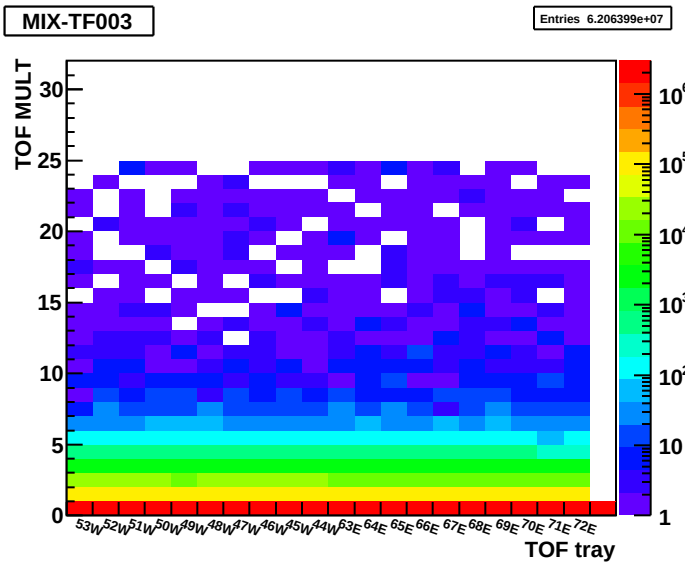

BunchId7Bit (all events)
Entries 1477714

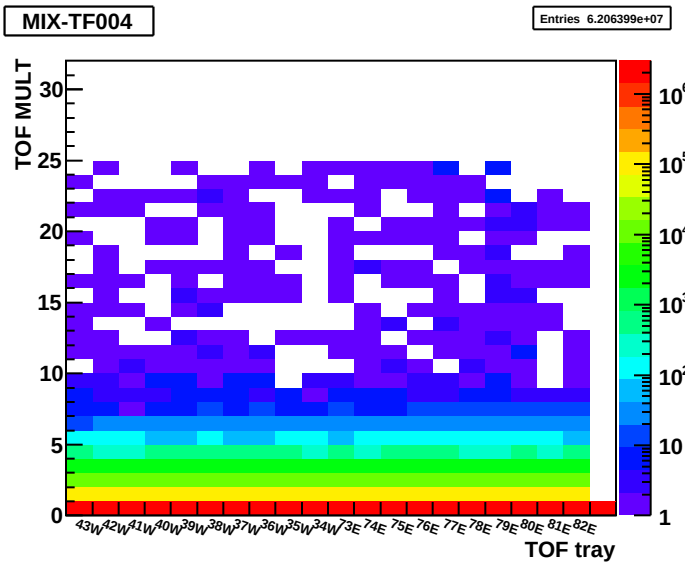

Abort-gaps
Entries 2029

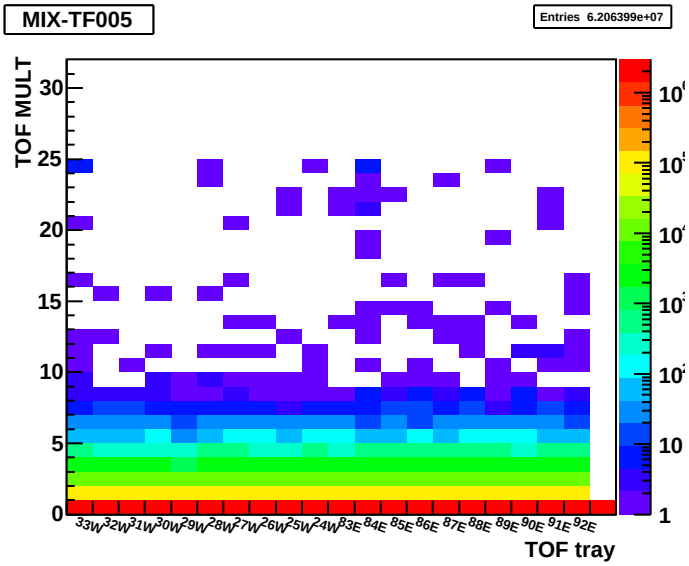

!Yellow-Fill
Entries 47583

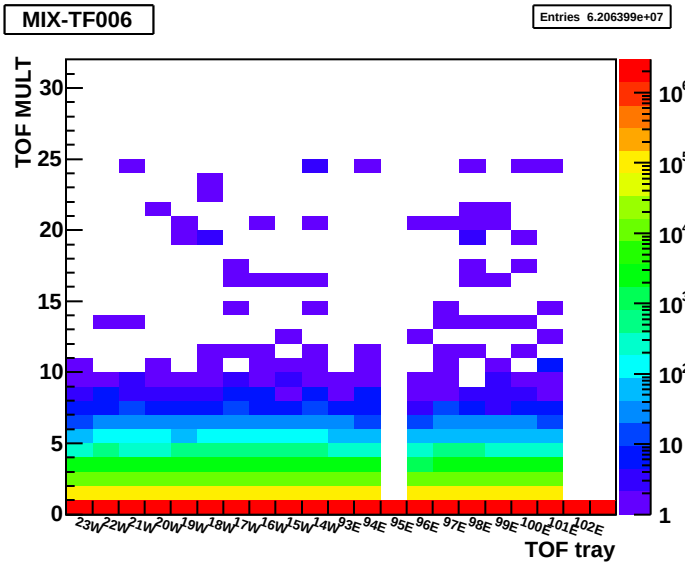

MIX-TF001
Entries 6.206399e+07
TOF MULT
TOF tray

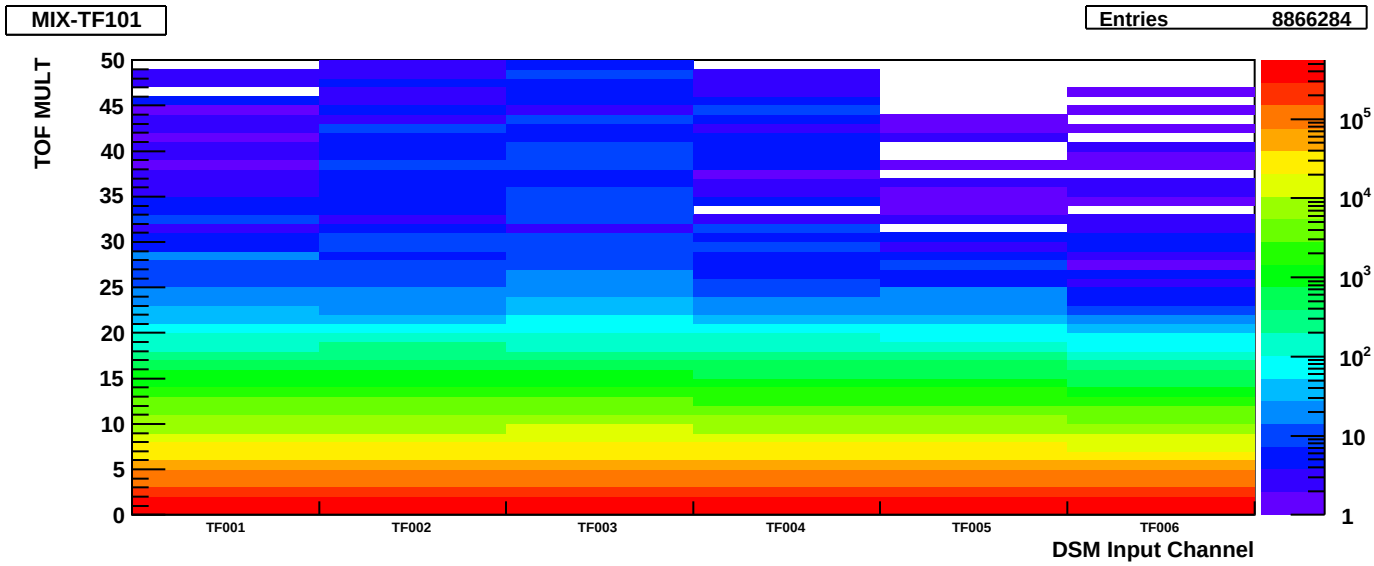

MIX-TF002
Entries 6.206399e+07
TOF MULT
TOF tray

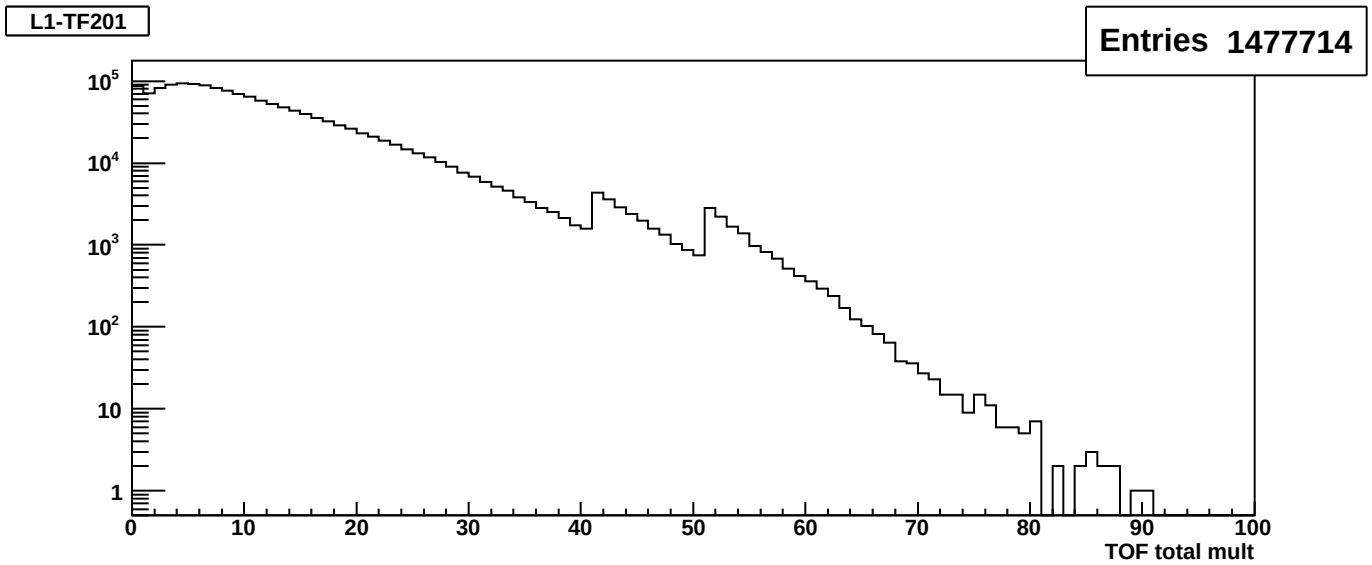

MIX-TF003
Entries 6.206399e+07
TOF MULT
TOF tray

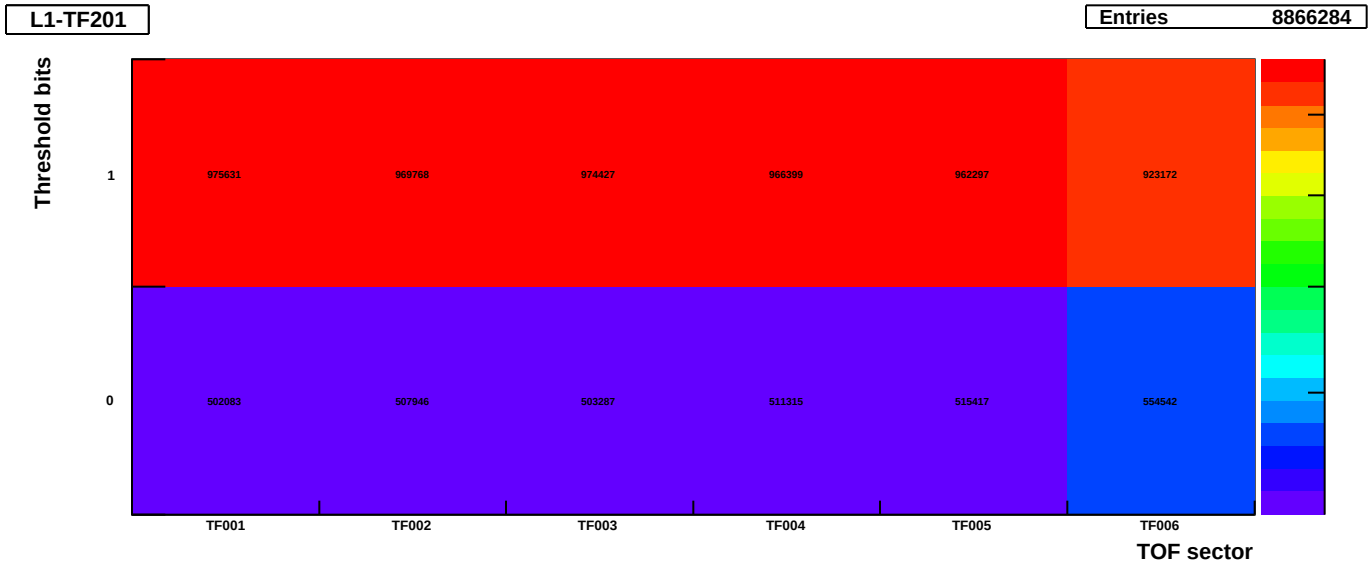

MIX-TF004
Entries 6.206399e+07
TOF MULT
TOF tray

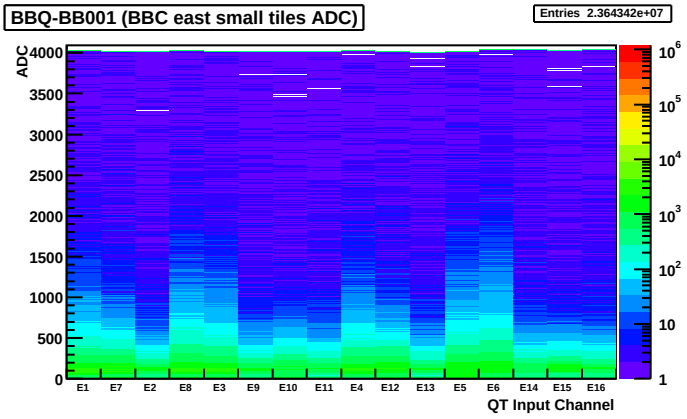

MIX-TF005
Entries 6.206399e+07
TOF MULT
TOF tray

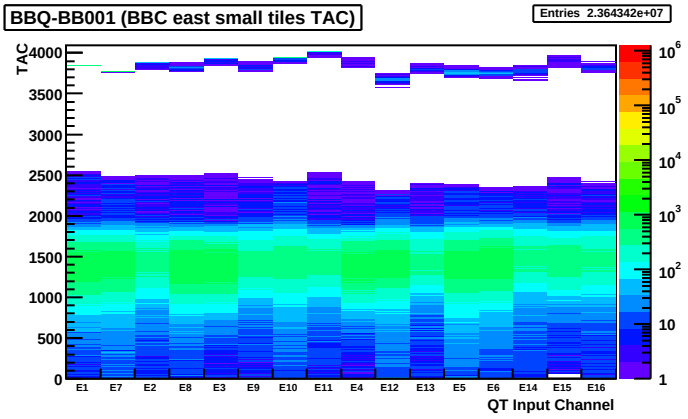

MIX-TF006
Entries 6.206399e+07
TOF MULT
TOF tray

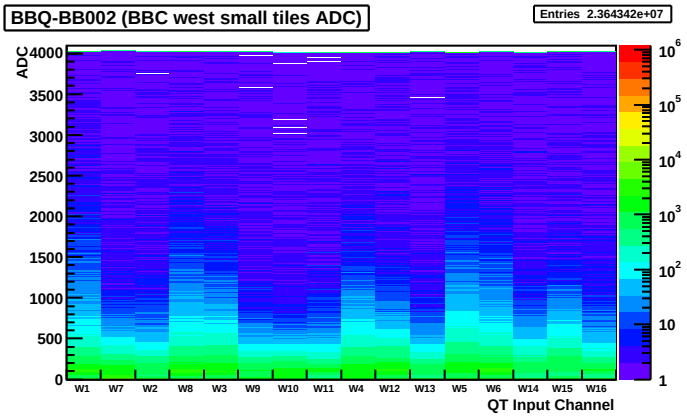

MIX-TF101
Entries 8866284
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

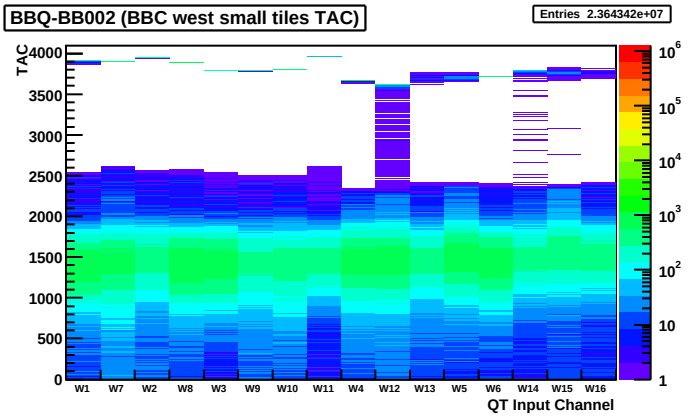

L1-TF201
Entries 1477714
TOF total mult

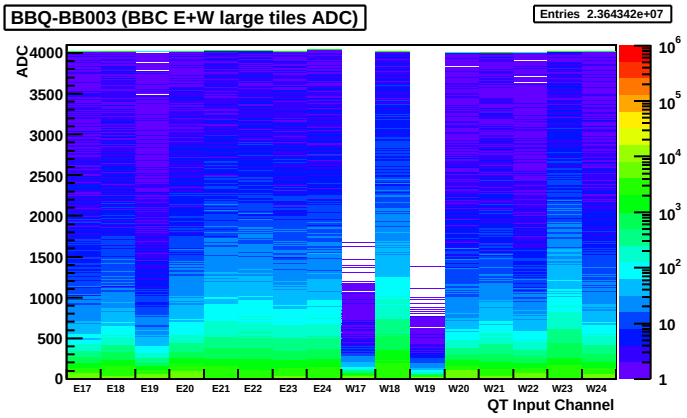

L1-TF201
Entries 8866284
Threshold bits
975631
969768
974427
966399
962297
923172
502083
507946
503287
511315
515417
554542
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

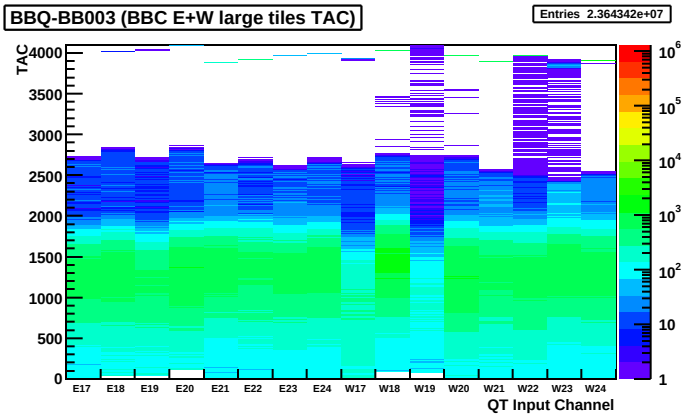

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.364342e+07
ADC
QT Input Channel

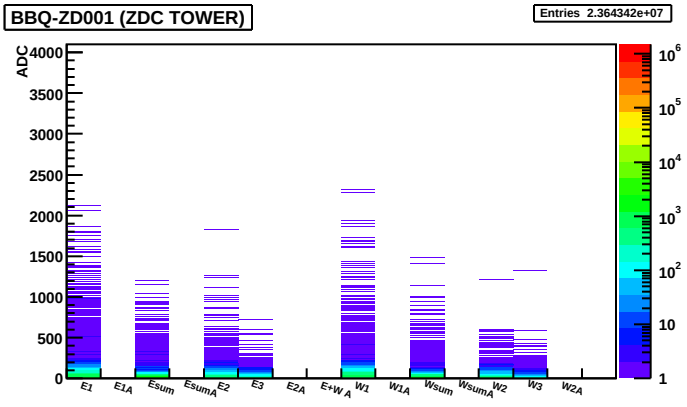

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.364342e+07
TAC
QT Input Channel

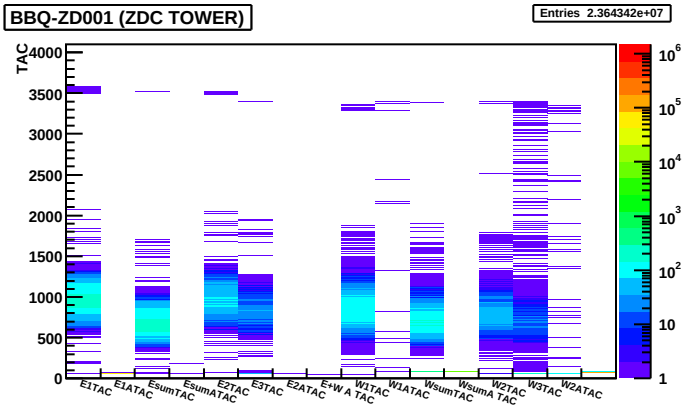

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.364342e+07
ADC
QT Input Channel

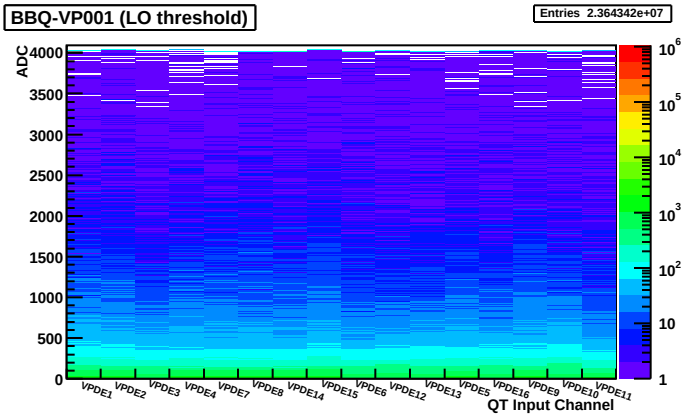

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.364342e+07
TAC
QT Input Channel

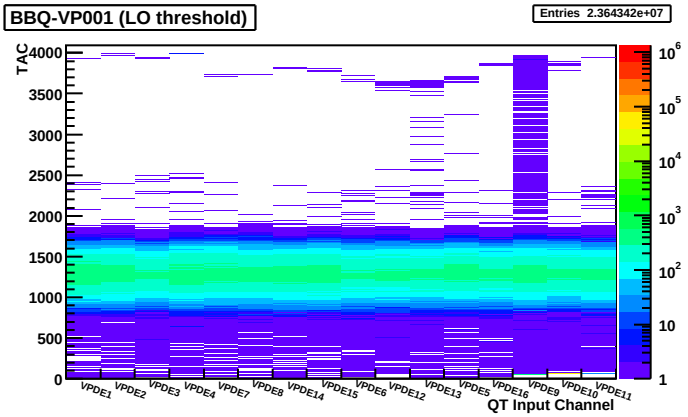

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.364342e+07
ADC
QT Input Channel

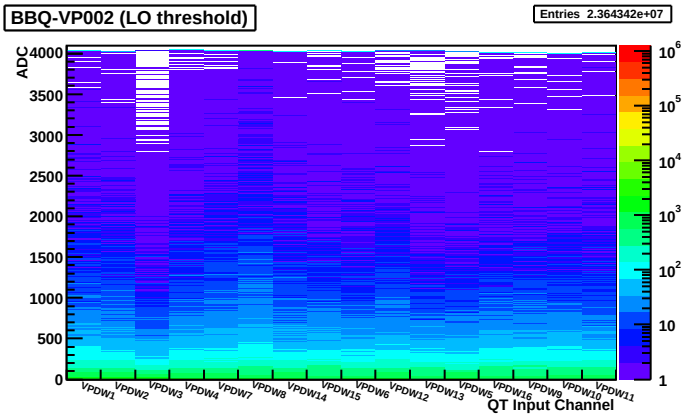

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.364342e+07
TAC
QT Input Channel

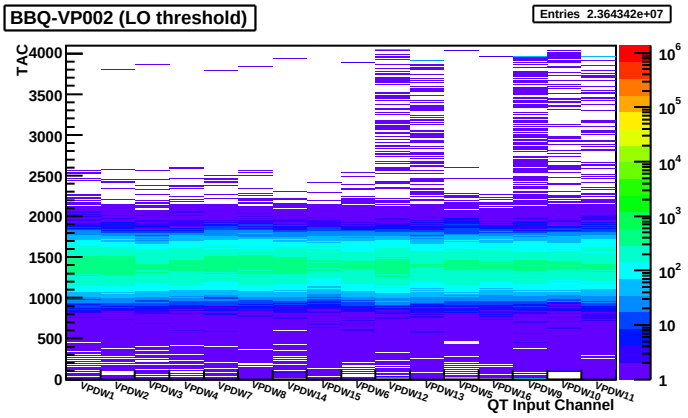

BBQ-ZD001 (ZDC TOWER)
Entries 2.364342e+07
ADC

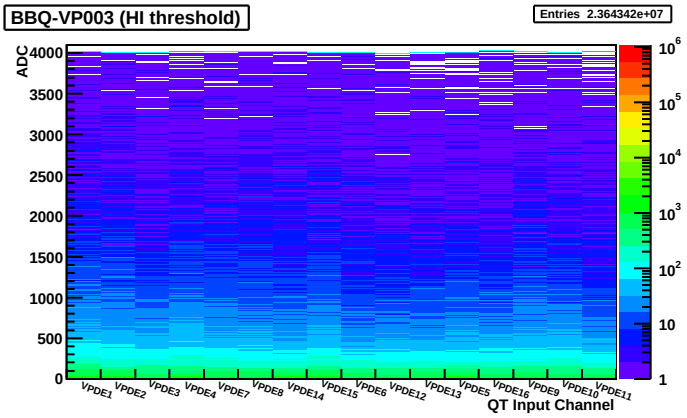

BBQ-ZD001 (ZDC TOWER)
Entries 2.364342e+07
TAC

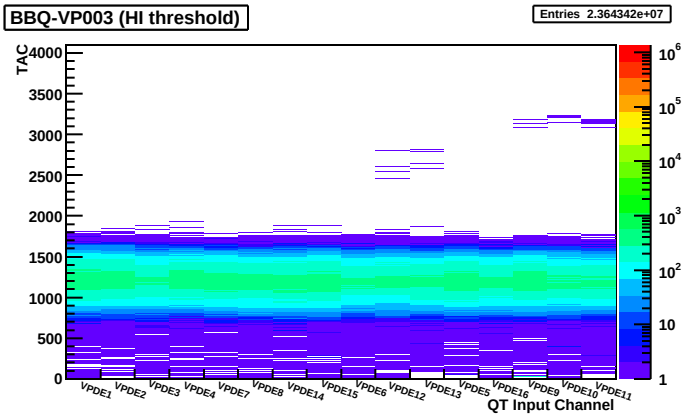

BBQ-VP001 (LO threshold)
Entries 2.364342e+07
ADC
QT Input Channel

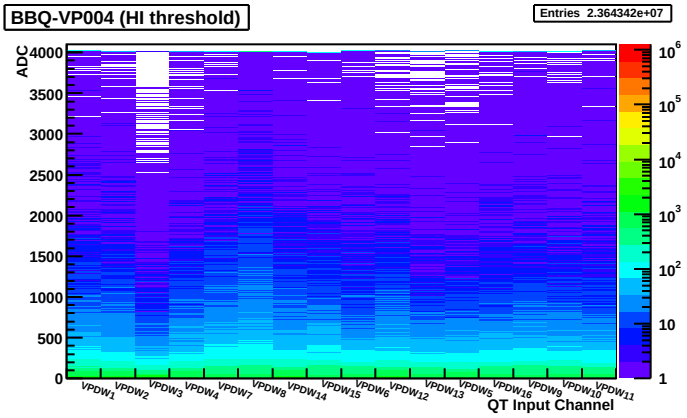

BBQ-VP001 (LO threshold)
Entries 2.364342e+07
TAC
QT Input Channel

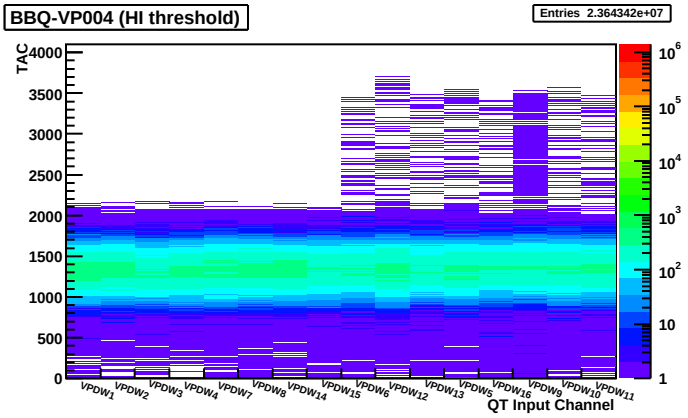

BBQ-VP002 (LO threshold)
Entries 2.364342e+07
ADC
QT Input Channel

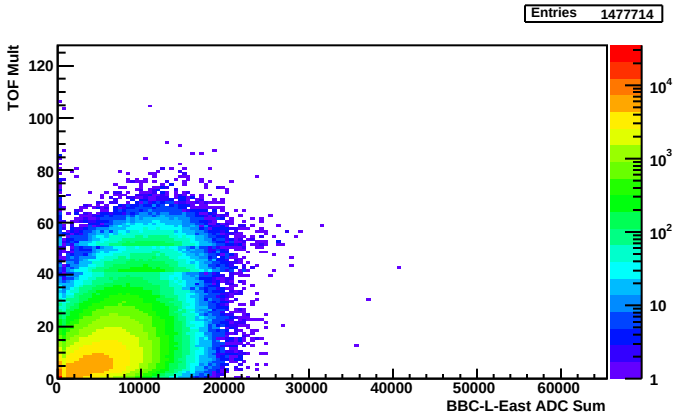

BBQ-VP002 (LO threshold)
Entries 2.364342e+07
TAC
QT Input Channel

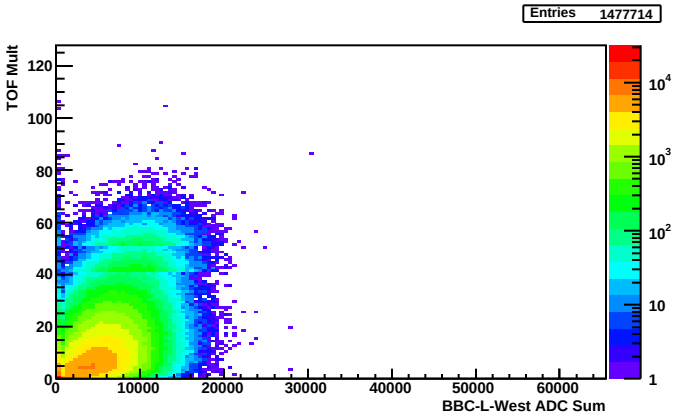

BBQ-VP003 (HI threshold)
Entries 2.364342e+07
ADC
QT Input Channel

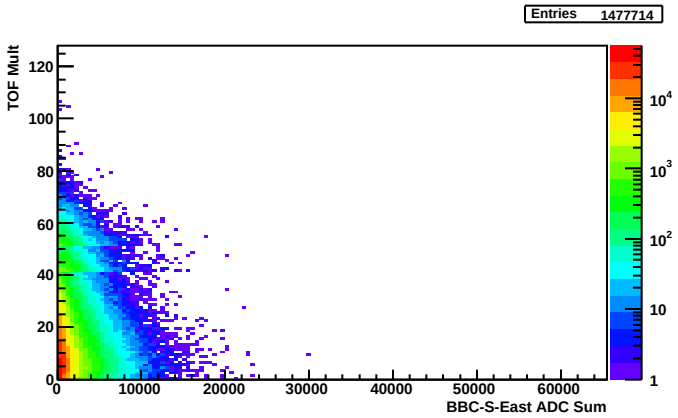

BBQ-VP003 (HI threshold)
Entries 2.364342e+07
TAC
QT Input Channel

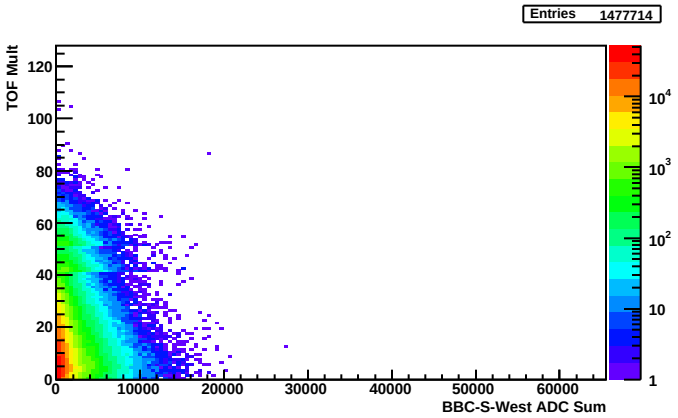

BBQ-VP004 (HI threshold)
Entries 2.364342e+07
ADC
QT Input Channel

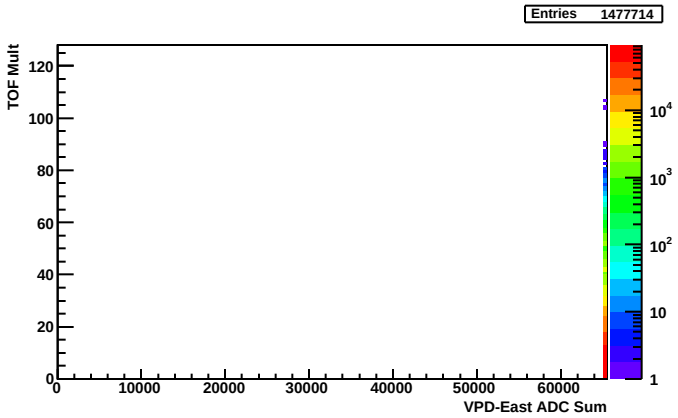

BBQ-VP004 (HI threshold)
Entries 2.364342e+07
TAC
QT Input Channel

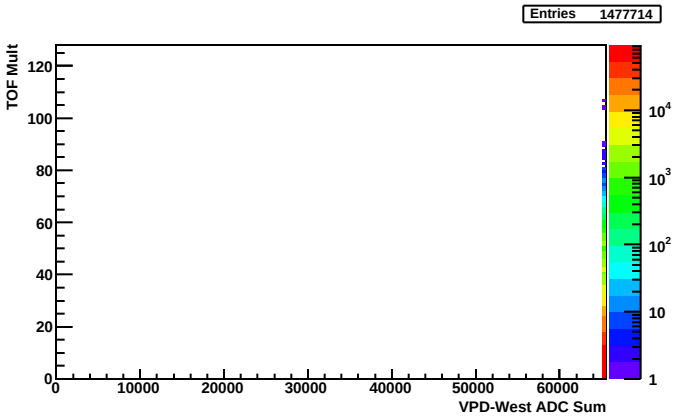

Entries 1477714
TOF Mult
BBC-L-East ADC Sum

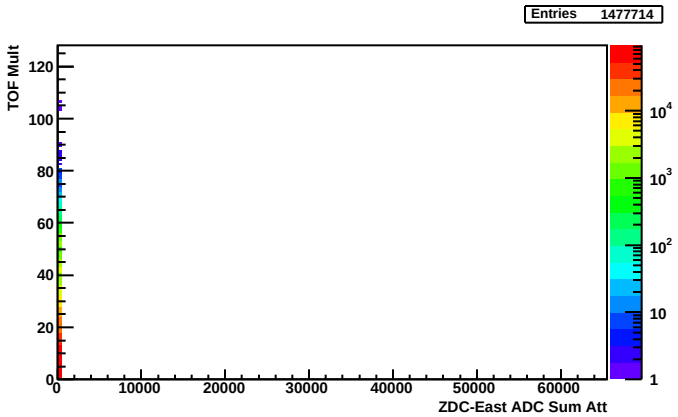

Entries 1477714
TOF Mult
BBC-L-West ADC Sum

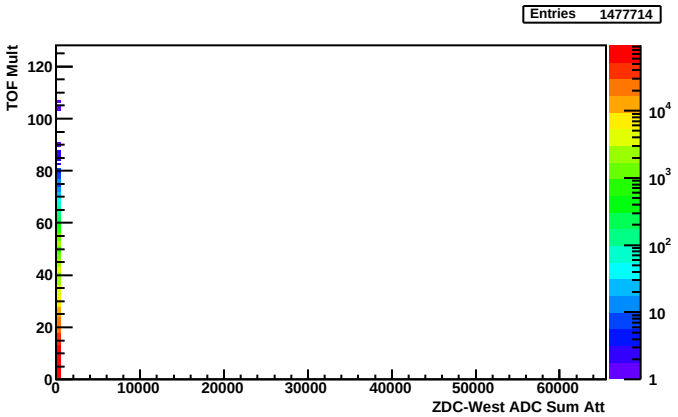

Entries 1477714
TOF Mult
BBC-S-East ADC Sum

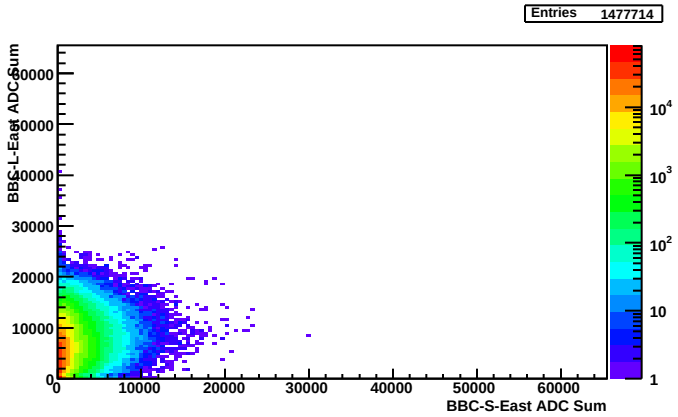

Entries 1477714
TOF Mult
BBC-S-West ADC Sum

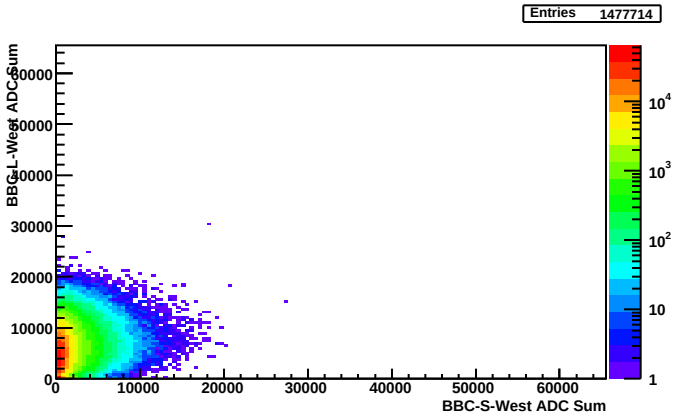

Entries 1477714
TOF Mult
VPD-East ADC Sum

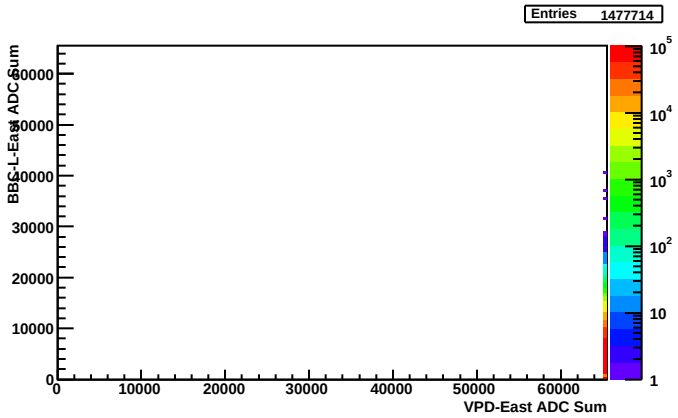

Entries 1477714
TOF Mult
VPD-West ADC Sum

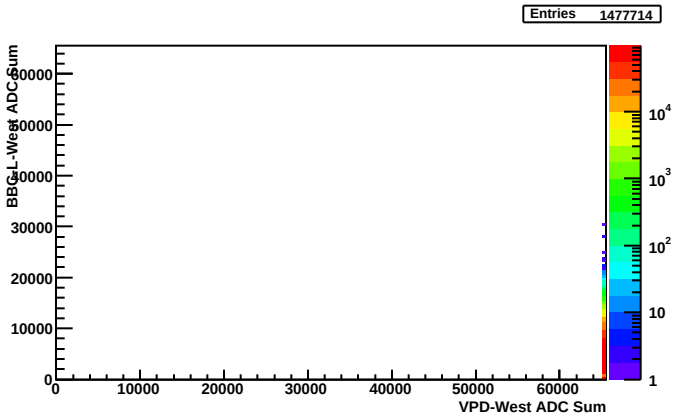

Entries 1477714
TOF Mult
ZDC-East ADC Sum Att

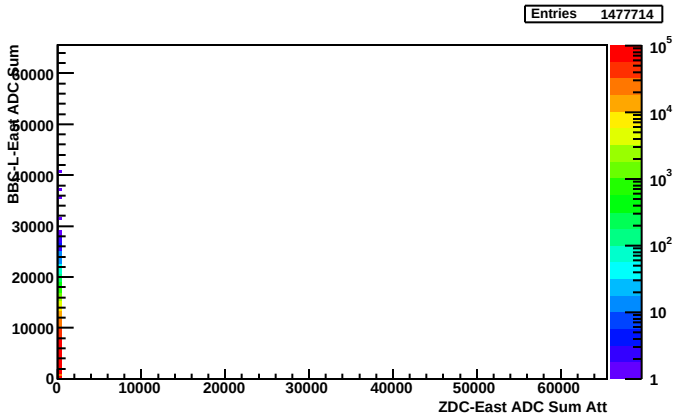

Entries 1477714
TOF Mult
ZDC-West ADC Sum Att

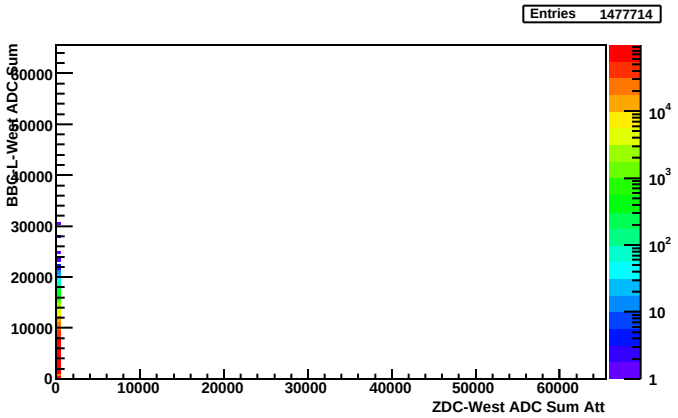

Entries 1477714
BBC-L-East ADC Sum
BBC-S-East ADC Sum

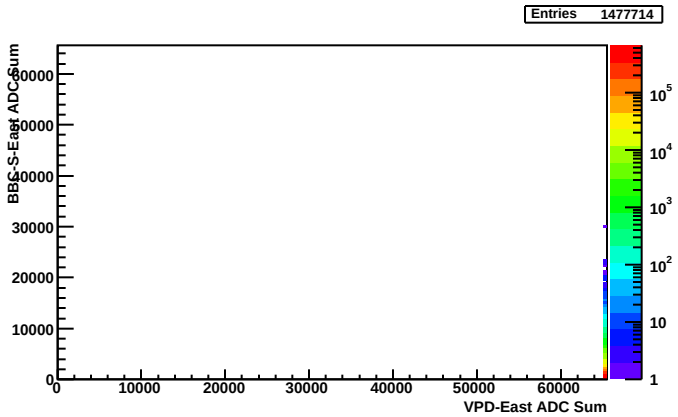

Entries 1477714
BBC-L-West ADC Sum
BBC-S-West ADC Sum

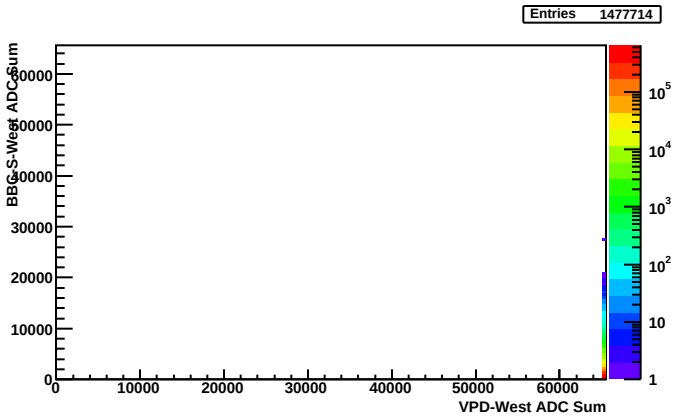

Entries 1477714
BBC-L-East ADC Sum
VPD-East ADC Sum

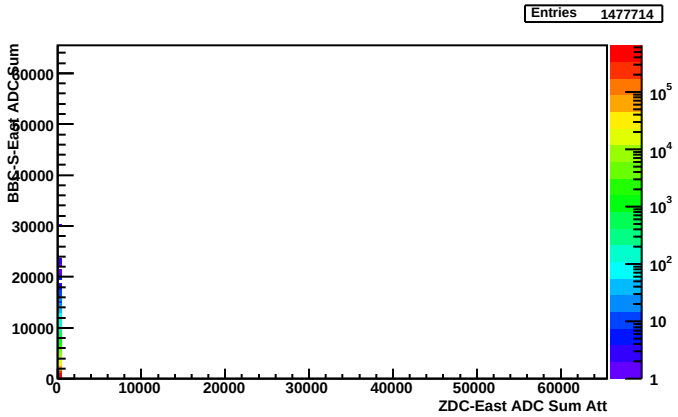

Entries 1477714
BBC-L-West ADC Sum
VPD-West ADC Sum

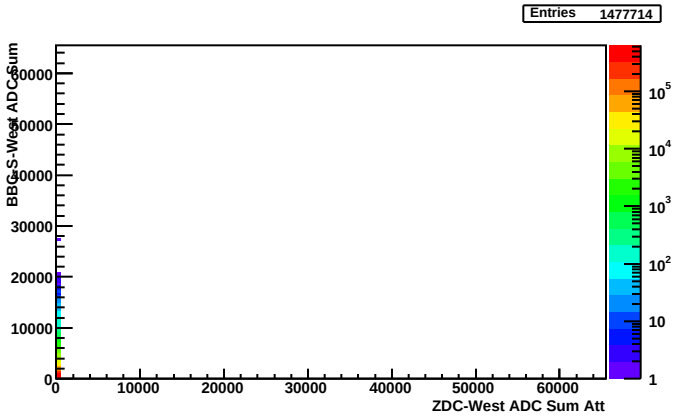

Entries 1477714
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

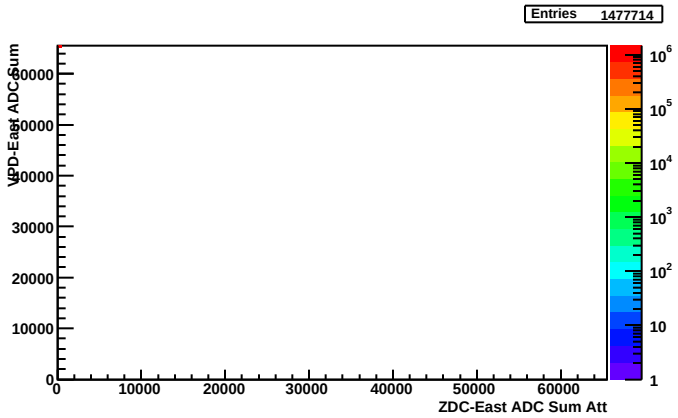

Entries 1477714
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

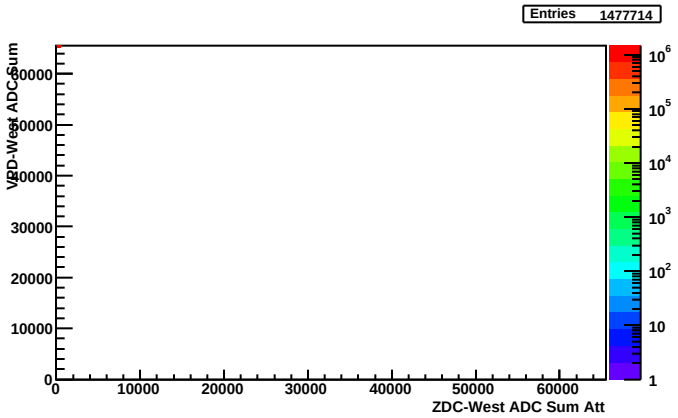

Entries 1477714
BBC-S-East ADC Sum
VPD-East ADC Sum

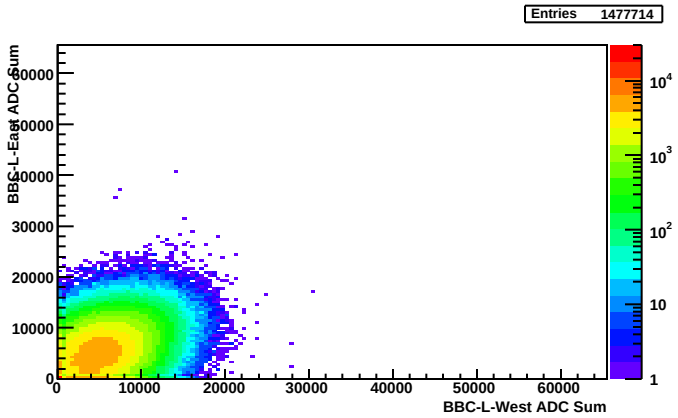

Entries 1477714
BBC-S-West ADC Sum
VPD-West ADC Sum

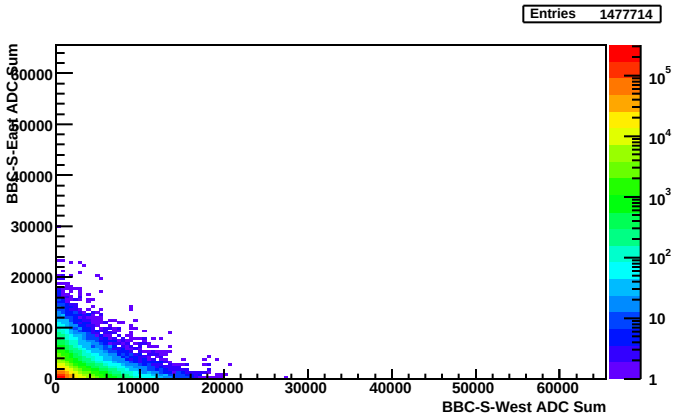

Entries 1477714
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

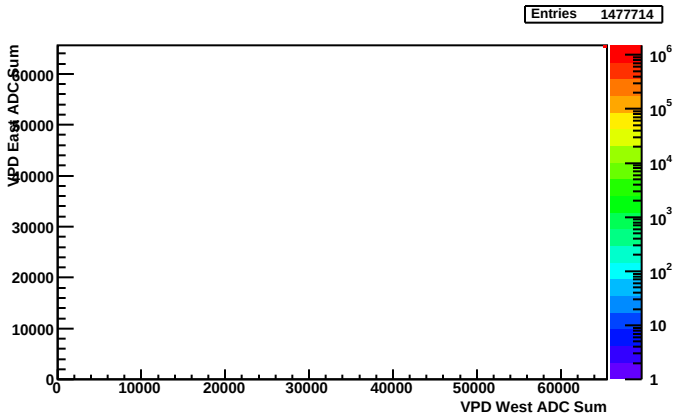

Entries 1477714
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

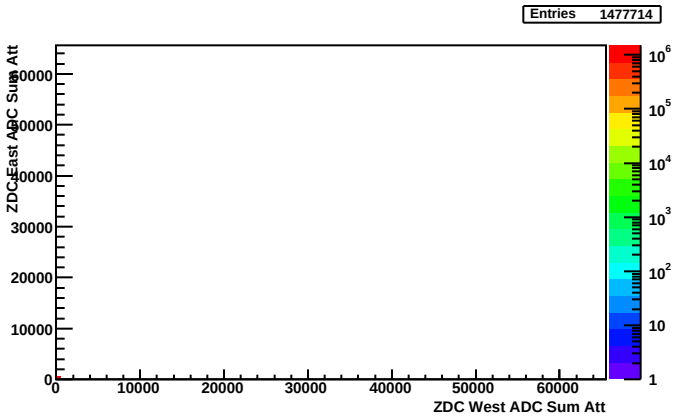

Entries 1477714
VPD-East ADC Sum
ZDC-East ADC Sum Att

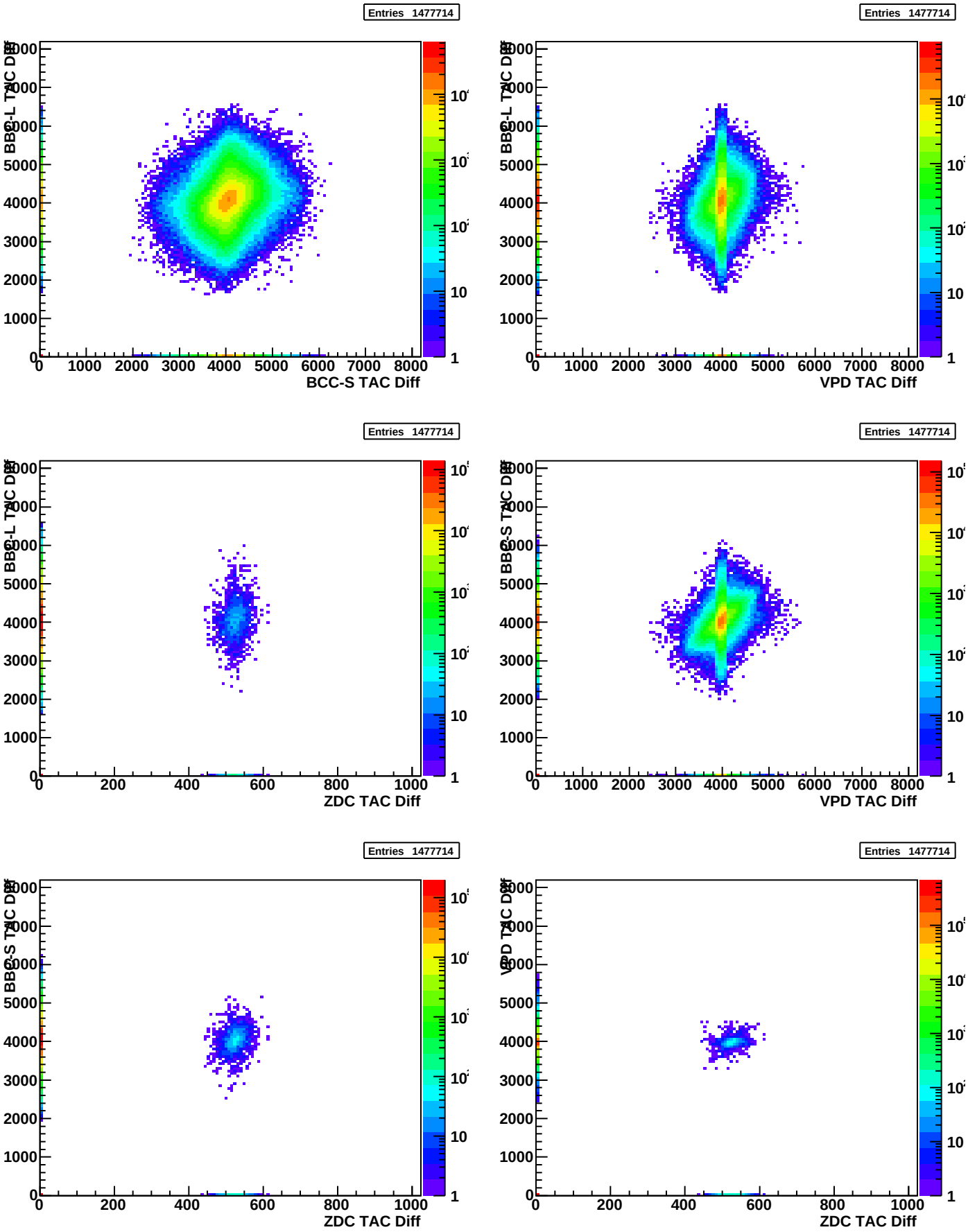

Entries 1477714
VPD-West ADC Sum
ZDC-West ADC Sum Att

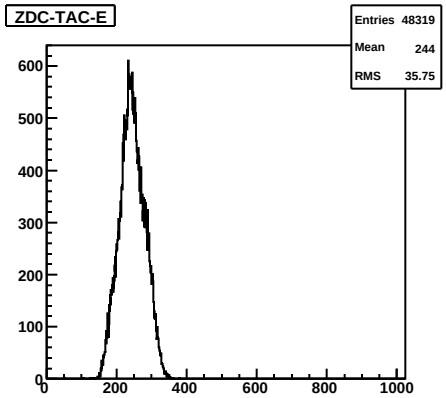

Entries 1477714
BBC-L-East ADC Sum
BBC-L-West ADC Sum

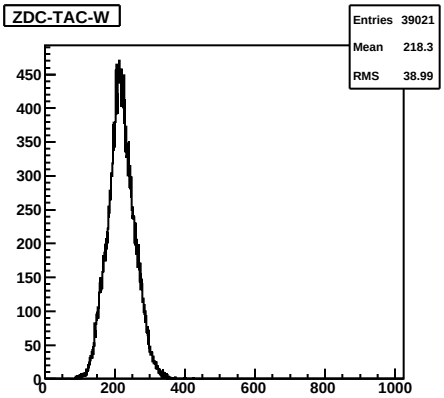

Entries 1477714
BBC-S-East ADC Sum
BBC-S-West ADC Sum

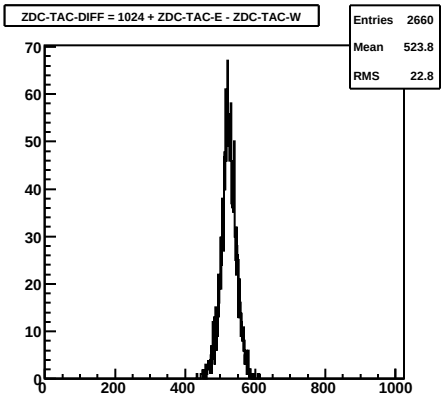

Entries 1477714
VPD East ADC Sum
VPD West ADC Sum

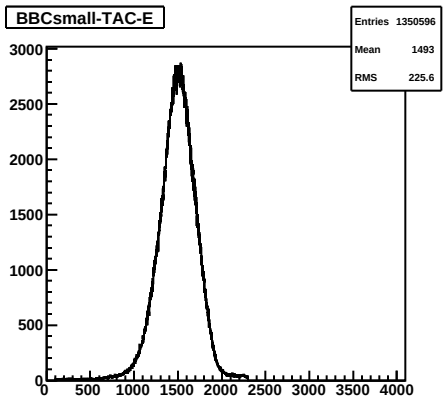

Entries 1477714
ZDC East ADC Sum Att
ZDC West ADC Sum Att

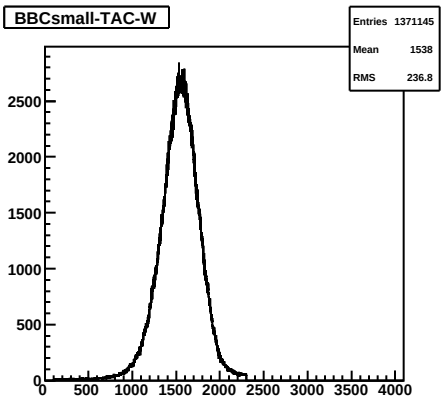

Entries 1477714
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1477714
BBC-L TAC Diff
VPD TAC Diff
Entries 1477714
BBC-L TAC Diff
ZDC TAC Diff
Entries 1477714
BBC-S TAC Diff
VPD TAC Diff
Entries 1477714
BBC-S TAC Diff
ZDC TAC Diff
Entries 1477714
VPD TAC Diff
ZDC TAC Diff

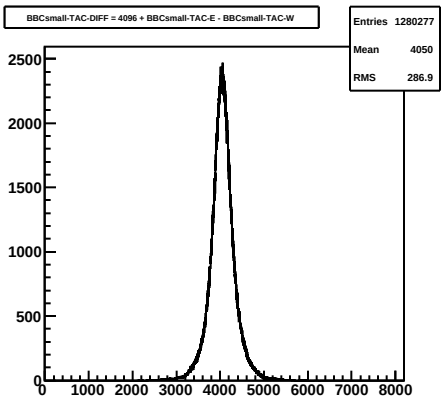

ZDC-TAC-E
Entries 48319
Mean 244
RMS 35.75

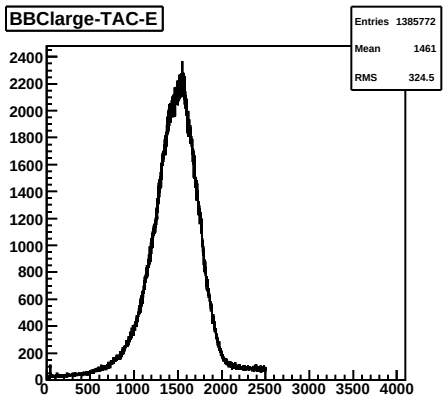

ZDC-TAC-W
Entries 39021
Mean 218.3
RMS 38.99

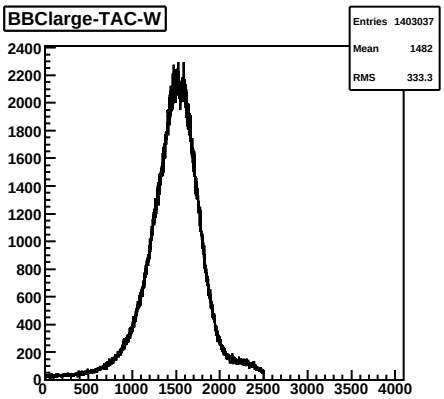

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2660
Mean 523.8
RMS 22.8

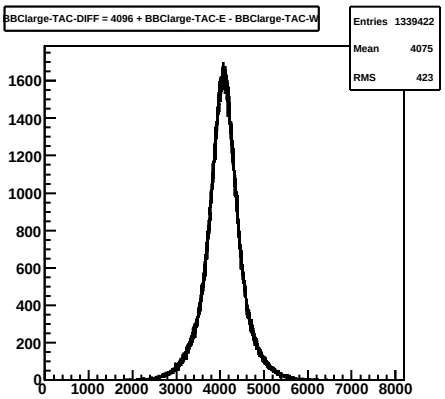

BBCsmall-TAC-E
Entries 1350596
Mean 1493
RMS 225.6

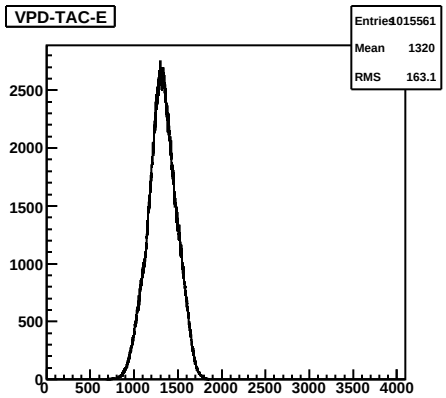

BBCsmall-TAC-W
Entries 1371145
Mean 1538
RMS 236.8

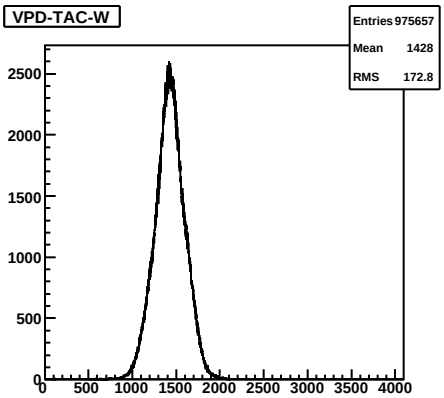

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1280277
Mean 4050
RMS 286.9

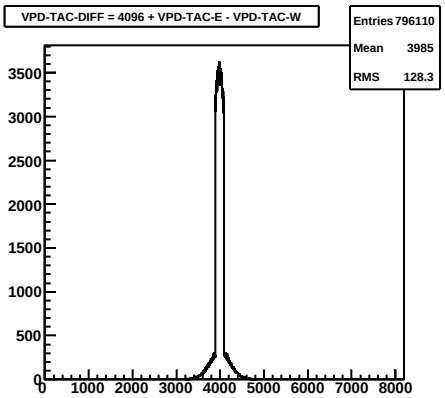

BBClarge-TAC-E
Entries 1385772
Mean 1461
RMS 324.5

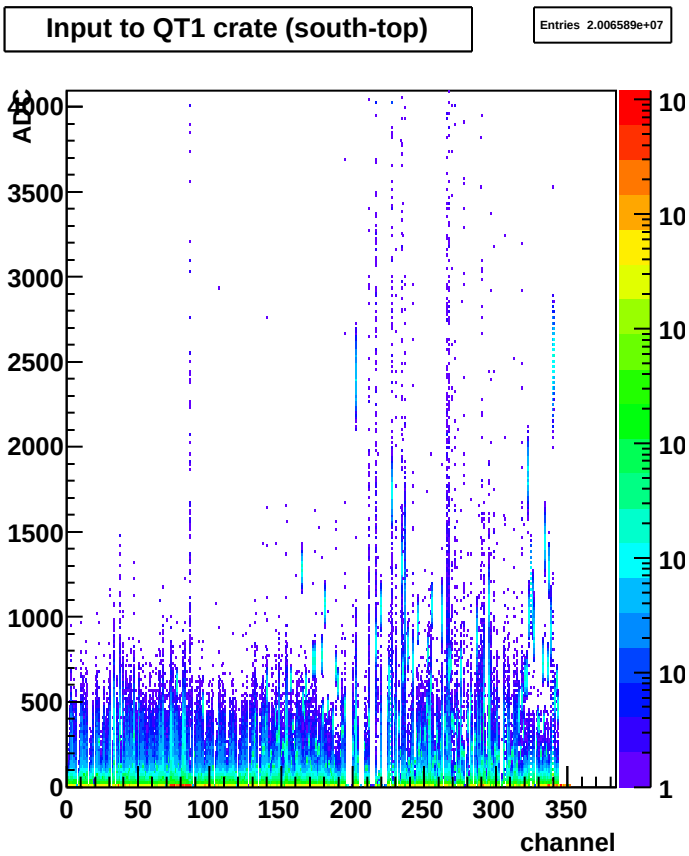

BBClarge-TAC-W
Entries 1403037
Mean 1482
RMS 333.3

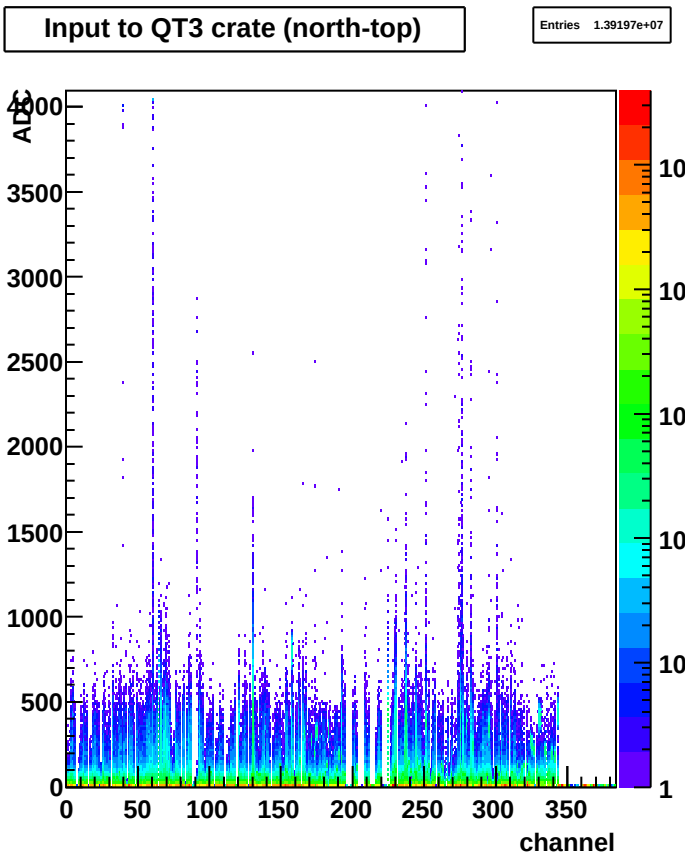

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1339422
Mean 4075
RMS 423

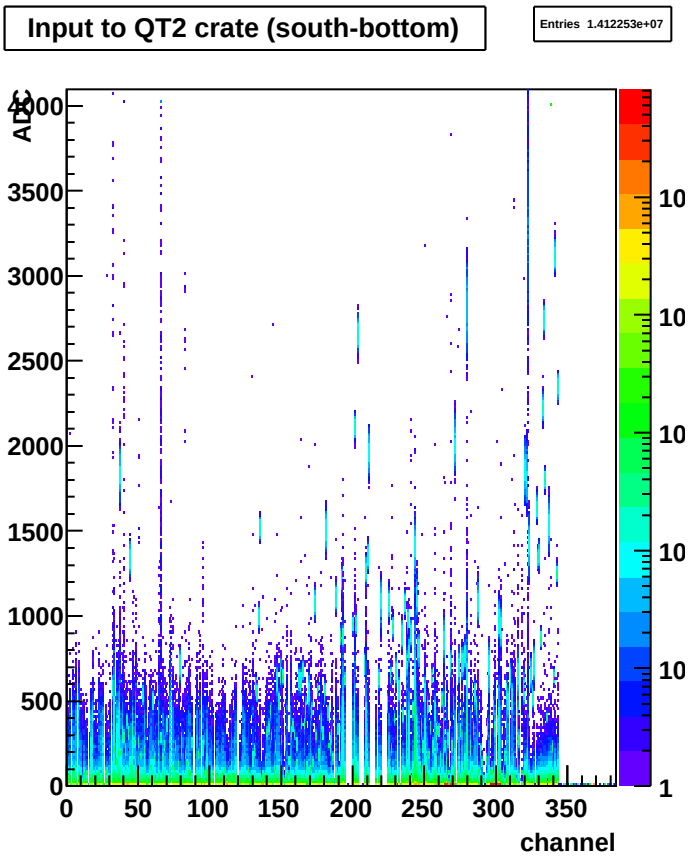

VPD-TAC-E
Entries 1015561
Mean 1320
RMS 163.1

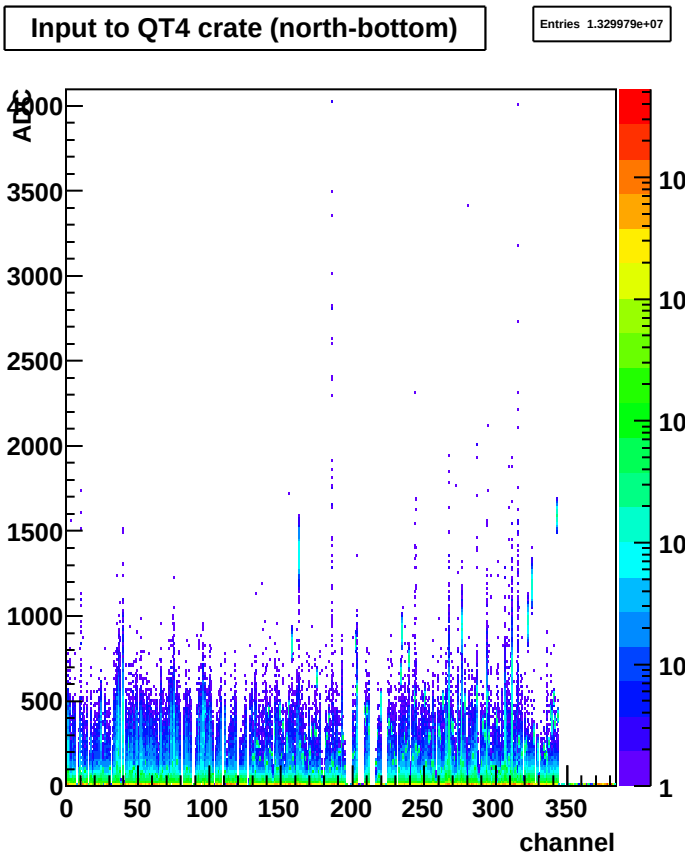

VPD-TAC-W
Entries 975657
Mean 1428
RMS 172.8

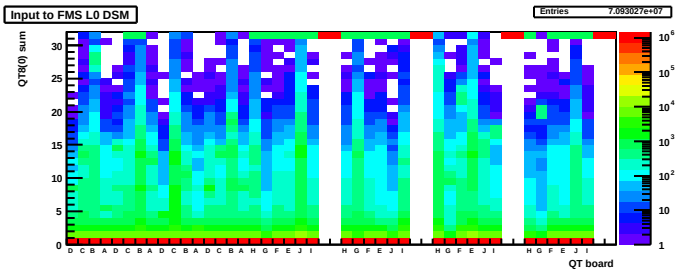

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 796110
Mean 3985
RMS 128.3

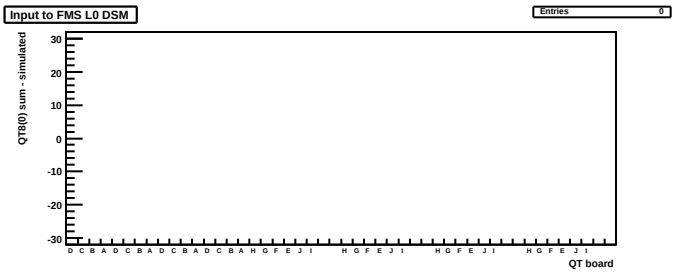

Input to QT1 crate (south-top)
Entries 2.006589e+07
ADC
channel

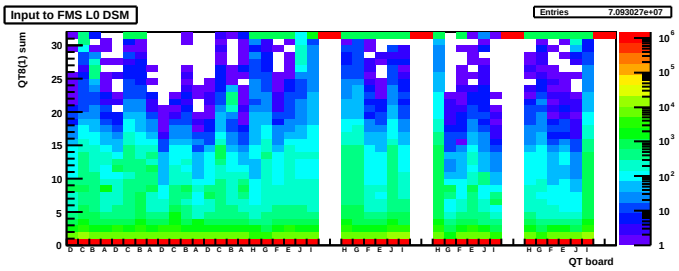

Input to QT3 crate (north-top)
Entries 1.39197e+07
ADC
channel

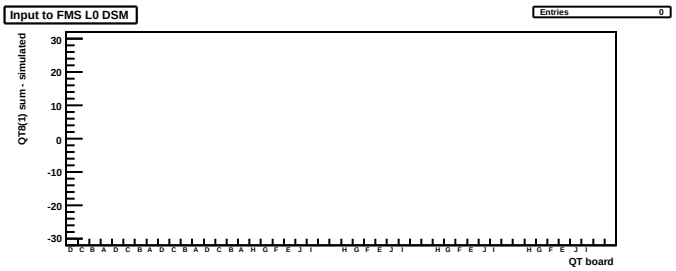

Input to QT2 crate (south-bottom)
Entries 1.412253e+07
ADC
channel

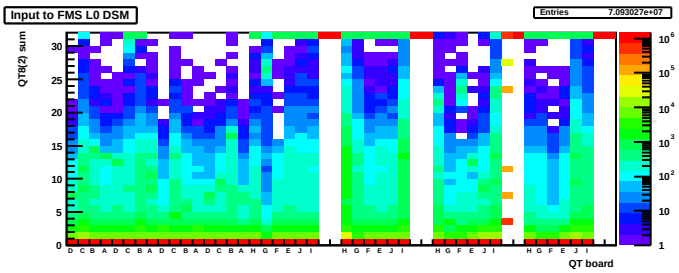

Input to QT4 crate (north-bottom)
Entries 1.329979e+07
ADC
channel

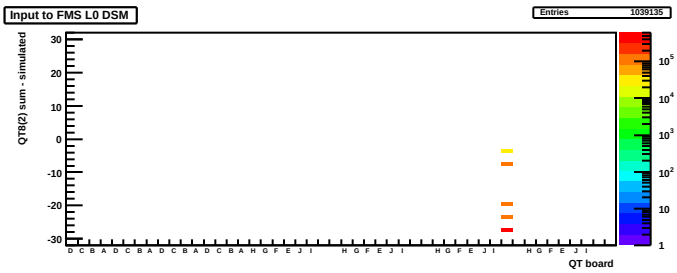

Input to FMS L0 DSM
Entries 7.093027e+07
QT8(0) sum
QT board

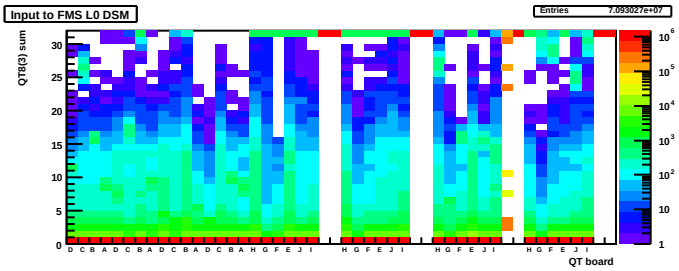

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

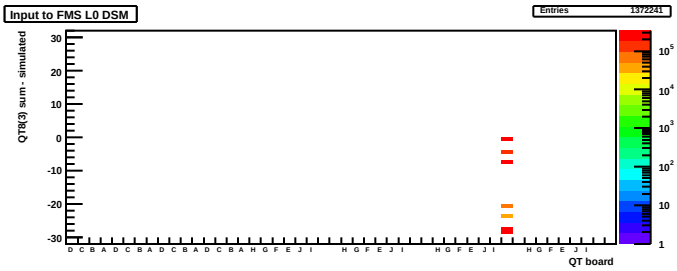

Input to FMS L0 DSM
Entries 7.093027e+07
QT8(1) sum
QT board

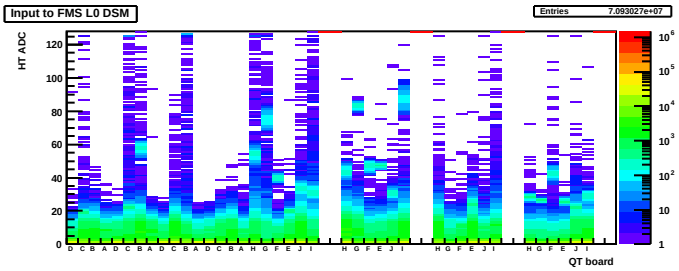

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

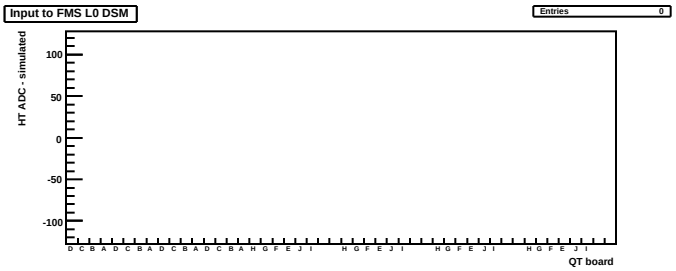

Input to FMS L0 DSM
Entries 7.093027e+07
QT8(2) sum
QT board

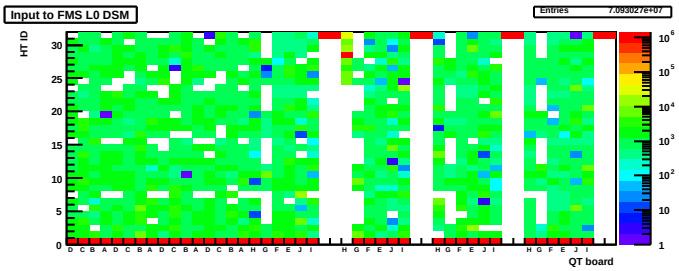

Input to FMS L0 DSM
Entries 1039135
QT8(2) sum - simulated
QT board

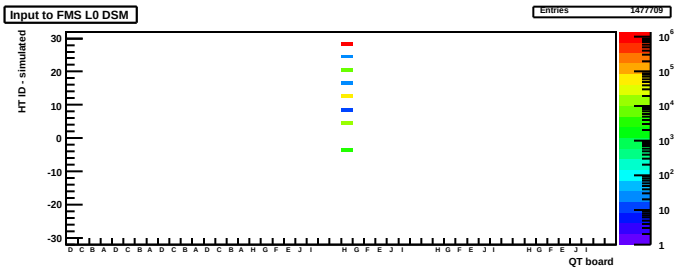

Input to FMS L0 DSM
Entries 7.093027e+07
QT8(3) sum
QT board

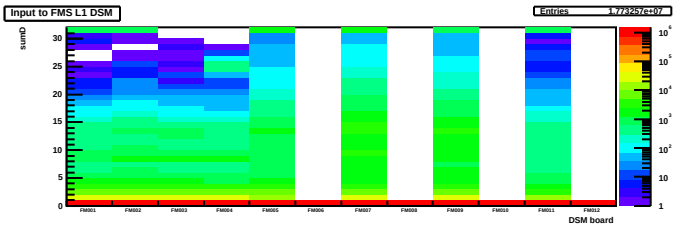

Input to FMS L0 DSM
Entries 1372241
QT8(3) sum - simulated
QT board

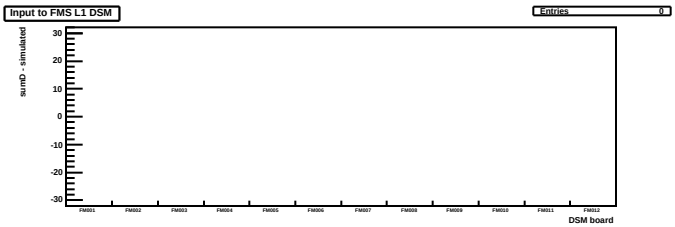

Input to FMS L0 DSM
Entries 7.093027e+07
HT ADC
QT board

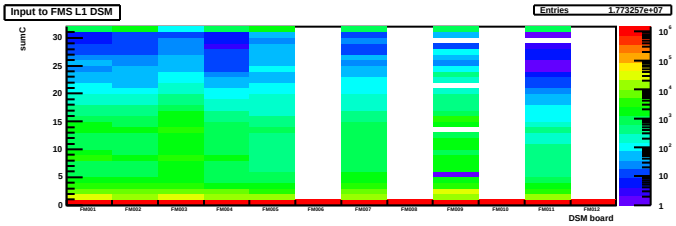

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

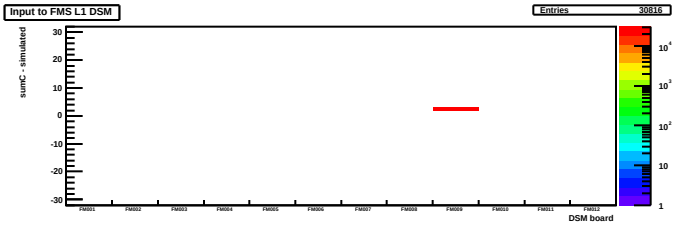

Input to FMS L0 DSM
Entries 7.093027e+07
HT ID
QT board

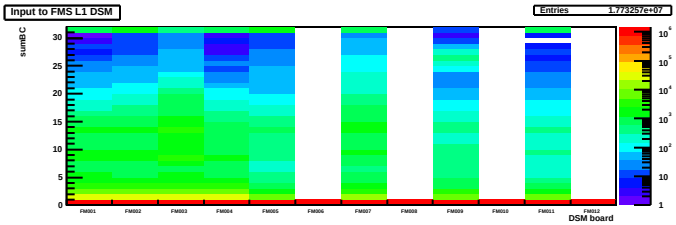

Input to FMS L0 DSM
Entries 1477709
HT ID - simulated
QT board

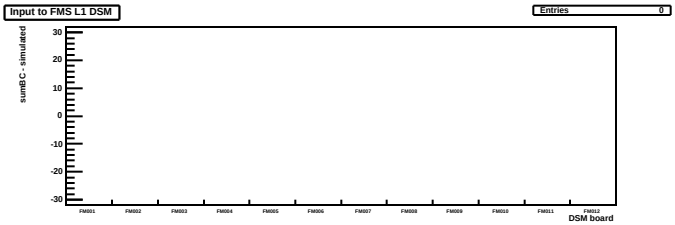

Input to FMS L1 DSM
Entries 1.773257e+07
sumD
DSM board

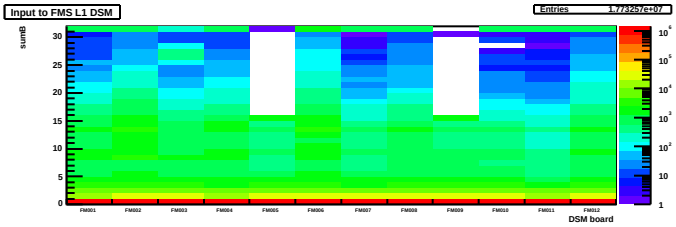

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

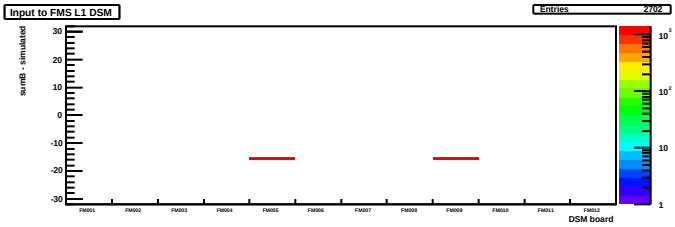

Input to FMS L1 DSM
Entries 1.773257e+07
sumC
DSM board

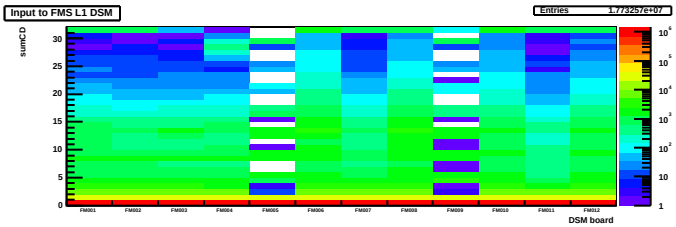

Input to FMS L1 DSM
Entries 30816
sumC - simulated
DSM board

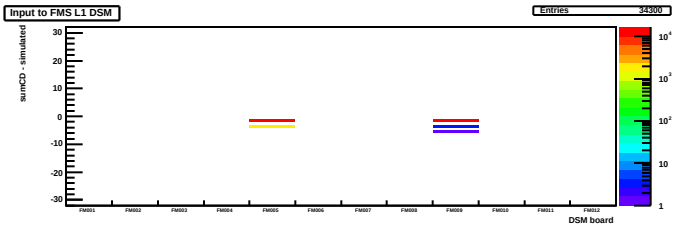

Input to FMS L1 DSM
Entries 1.773257e+07
sumBC
DSM board

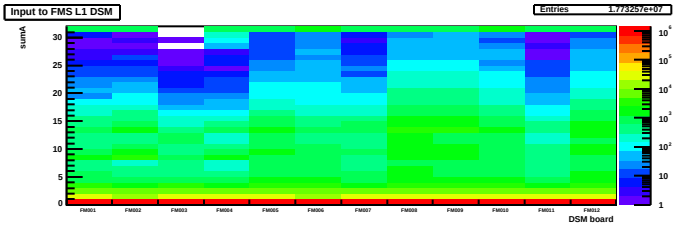

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

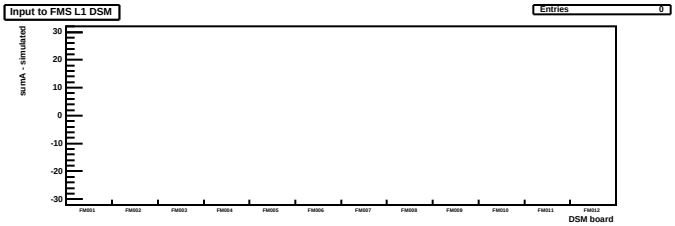

Input to FMS L1 DSM
Entries 1.773257e+07
sumB
DSM board

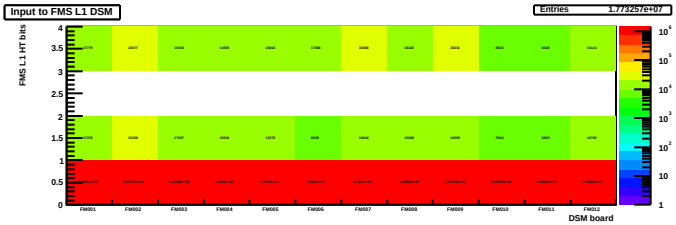

Input to FMS L1 DSM
Entries 2702
sumB - simulated
DSM board

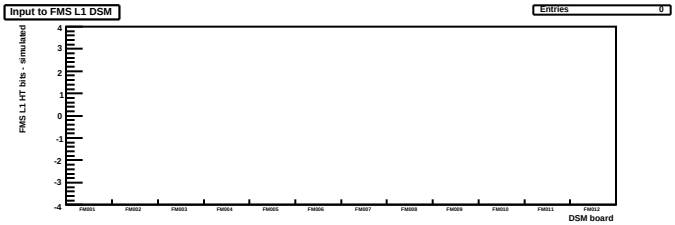

Input to FMS L1 DSM
Entries 1.773257e+07
sumCD
DSM board

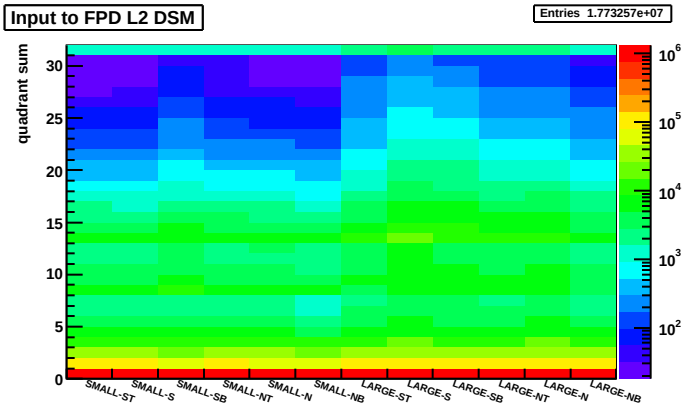

Input to FMS L1 DSM
Entries 34300
sumCD - simulated
DSM board

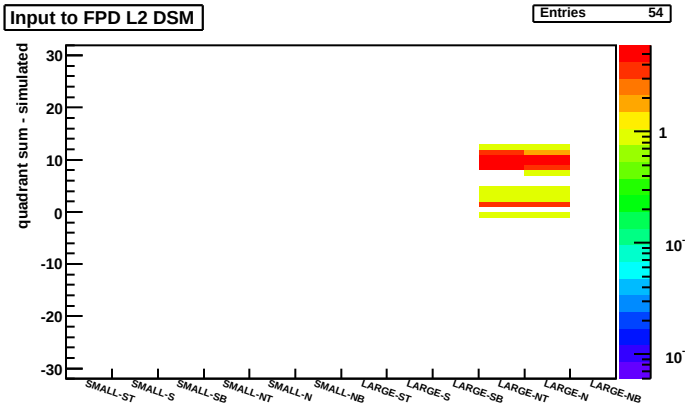

Input to FMS L1 DSM
Entries 1.773257e+07
sumA
DSM board

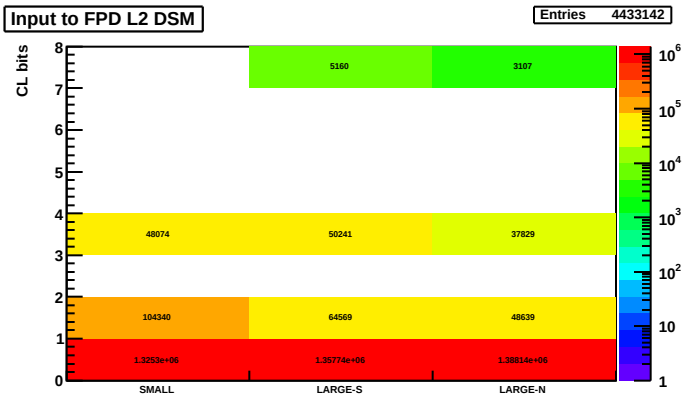

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

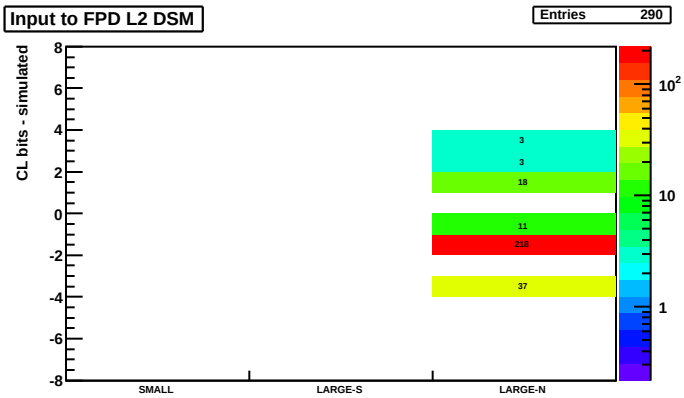

Input to FMS L1 DSM
Entries 1.773257e+07
FMS L1 HT bits
DSM board

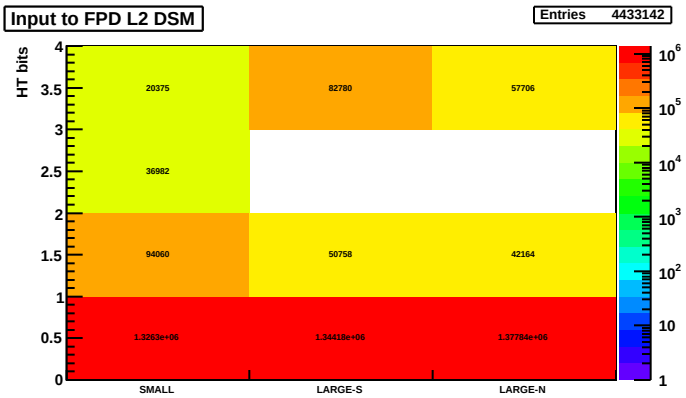

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

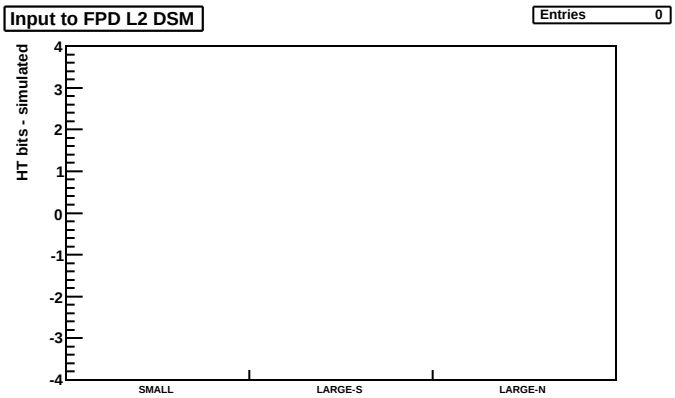

Input to FPD L2 DSM
Entries 1.773257e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

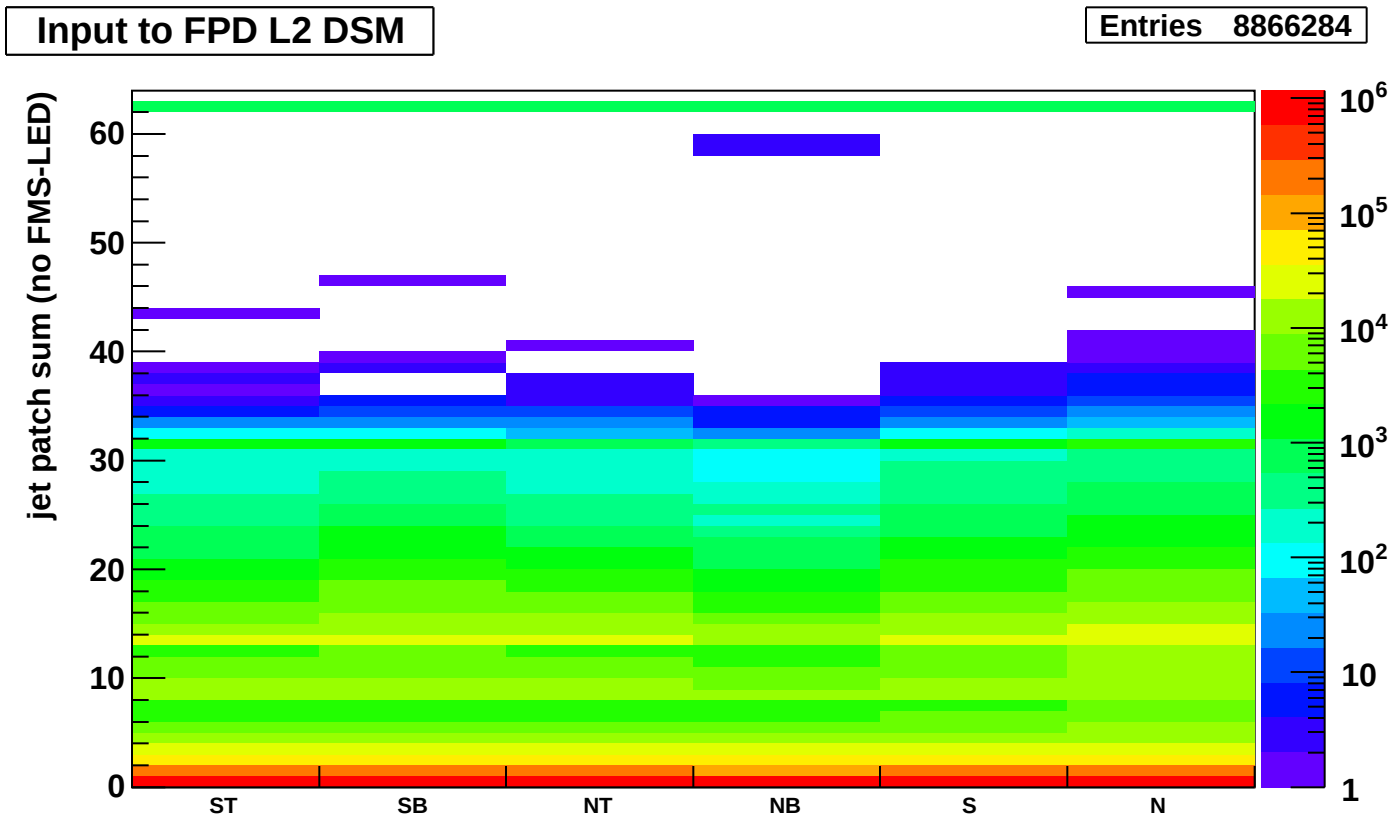

Input to FPD L2 DSM
Entries 54
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

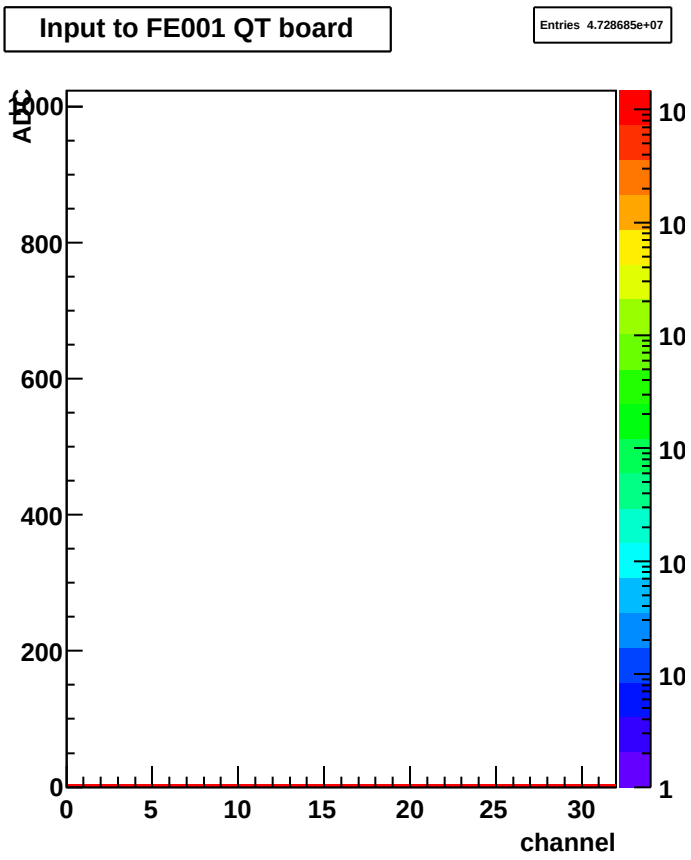

Input to FPD L2 DSM
Entries 4433142
CL bits
5160
3107
48074
50241
37829
104340
64569
48639
1.3253e+06
1.35774e+06
1.38814e+06
SMALL
LARGE-S
LARGE-N

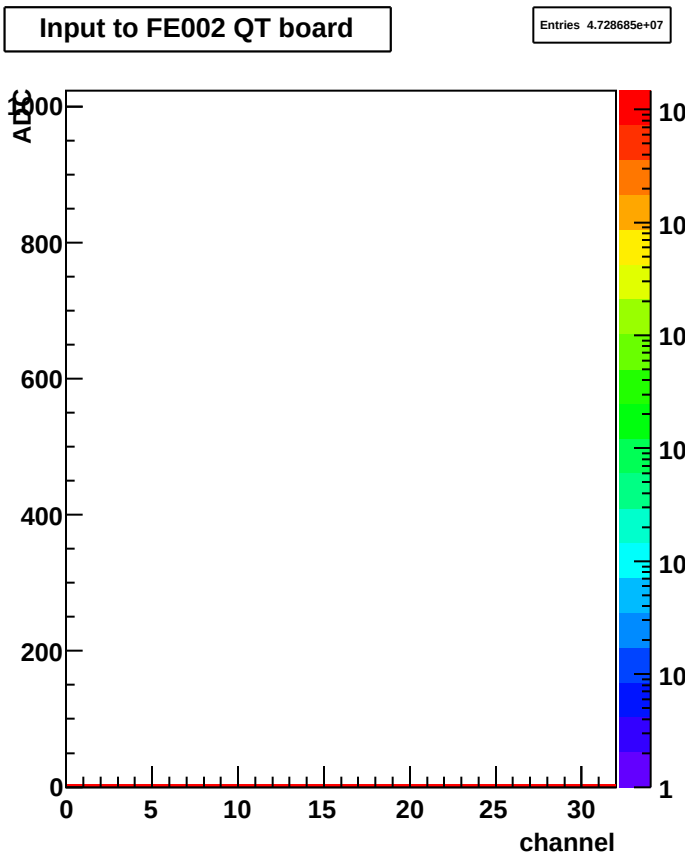

Input to FPD L2 DSM
Entries 290
CL bits - simulated
3
3
18
11
218
37
SMALL
LARGE-S
LARGE-N

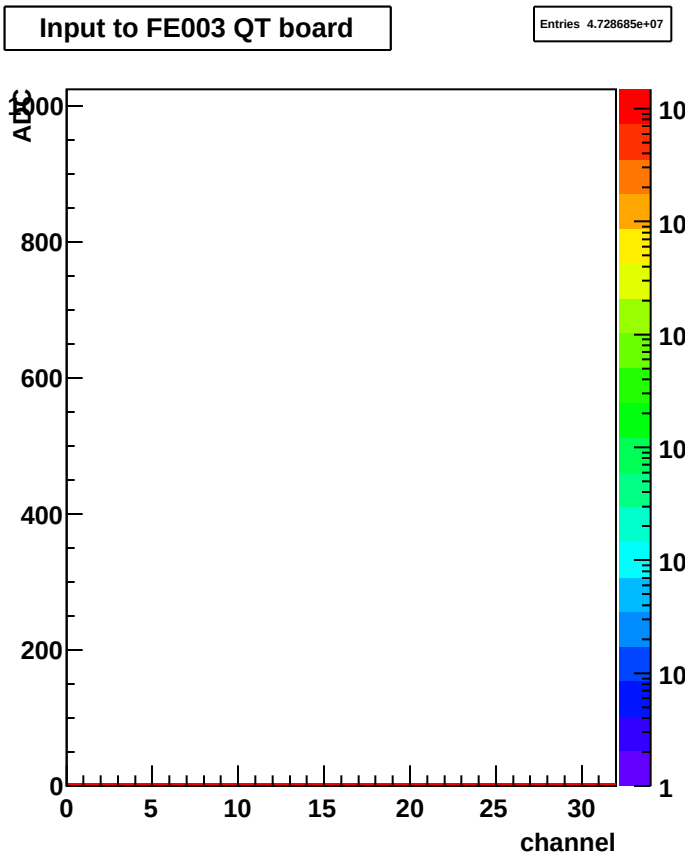

Input to FPD L2 DSM
Entries 4433142
HT bits
20375
82780
57706
36982
94060
50758
42164
1.3263e+06
1.34418e+06
1.37784e+06
SMALL
LARGE-S
LARGE-N

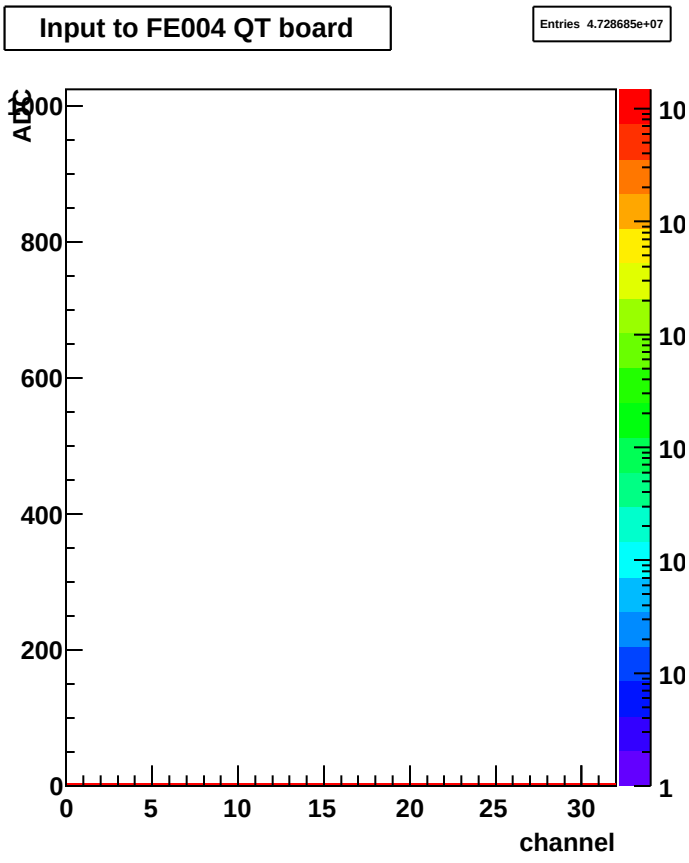

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

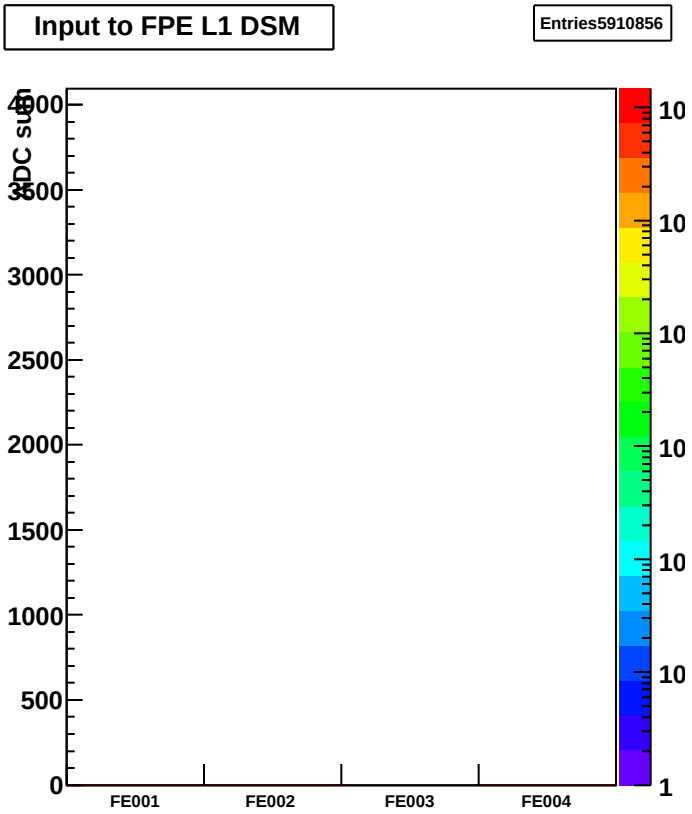

Input to FPD L2 DSM
Entries 8866284
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

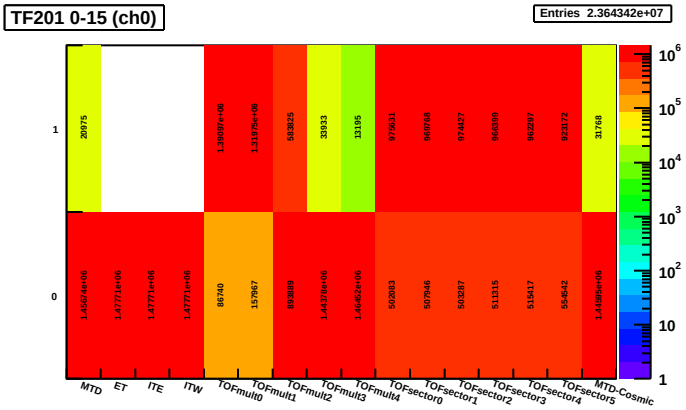

Input to FE001 QT board
Entries 4.728685e+07
ADC
channel

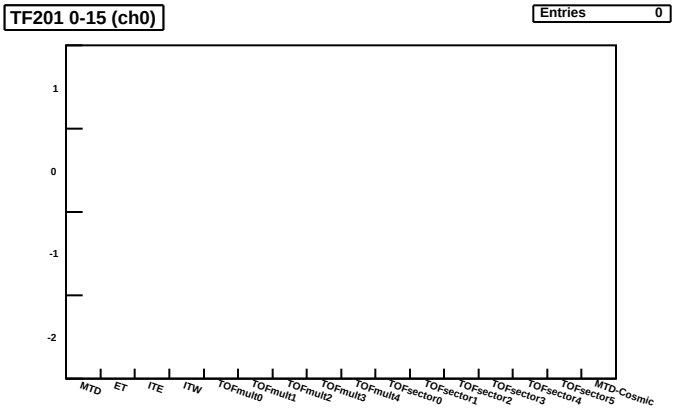

Input to FE002 QT board
Entries 4.728685e+07
ADC
channel

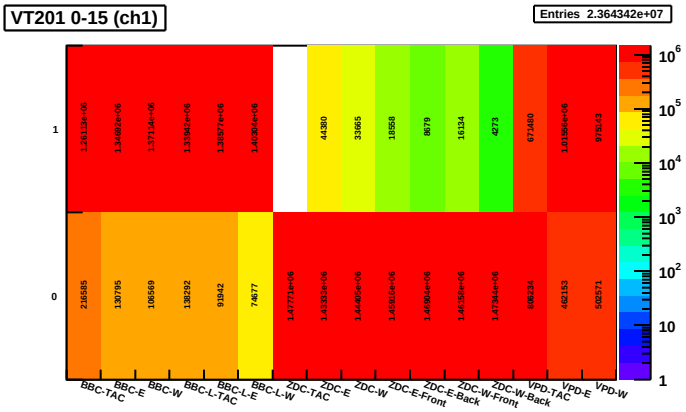

Input to FE003 QT board
Entries 4.728685e+07
ADC
channel


Input to FE004 QT board
Entries 4.728685e+07
ADC
channel


Input to FPE L1 DSM
Entries5910856
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 2.364342e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
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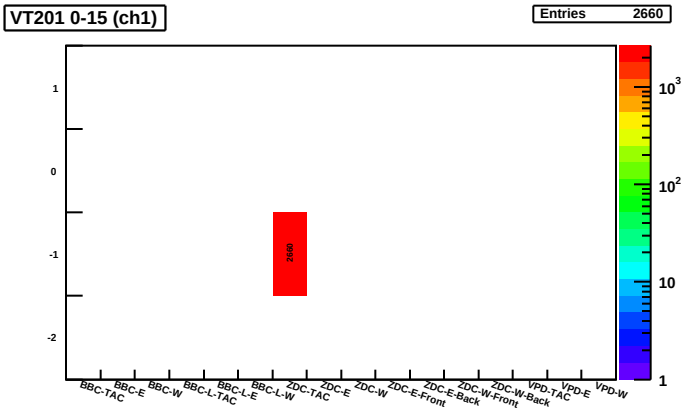
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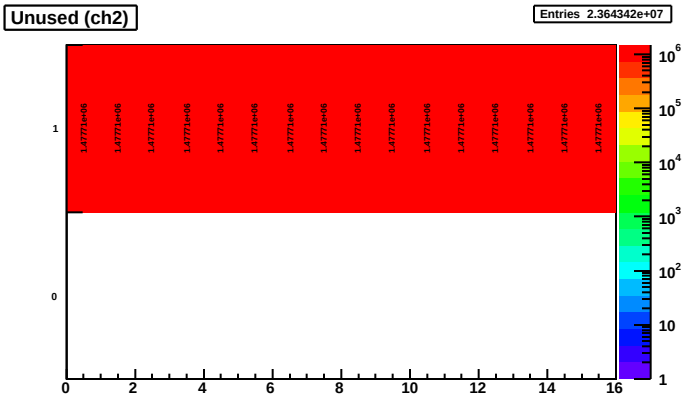
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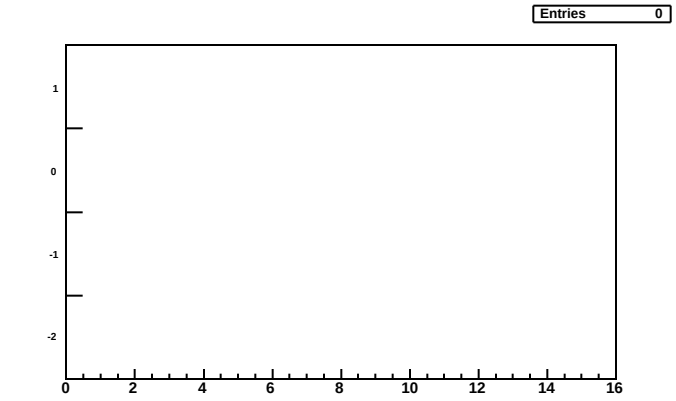
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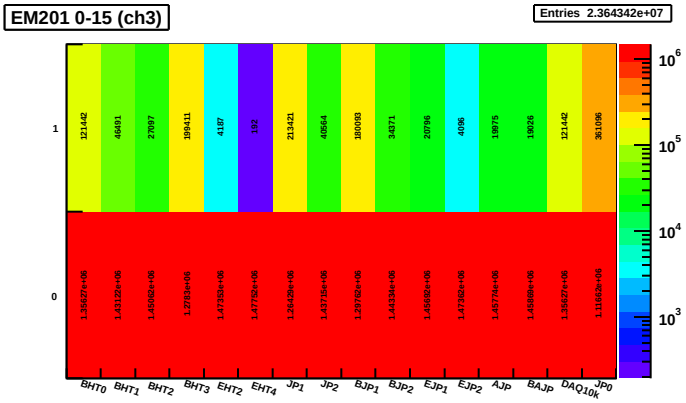
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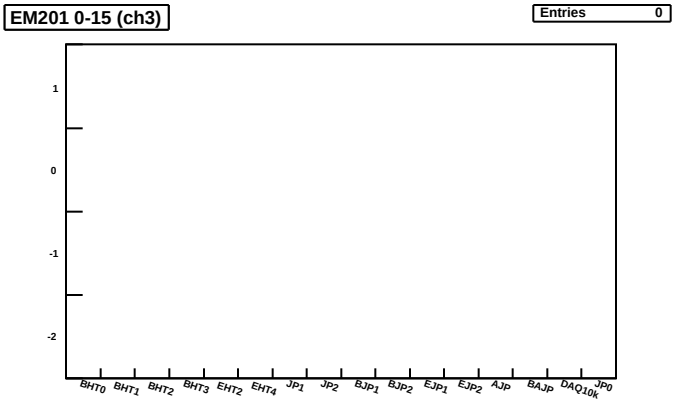
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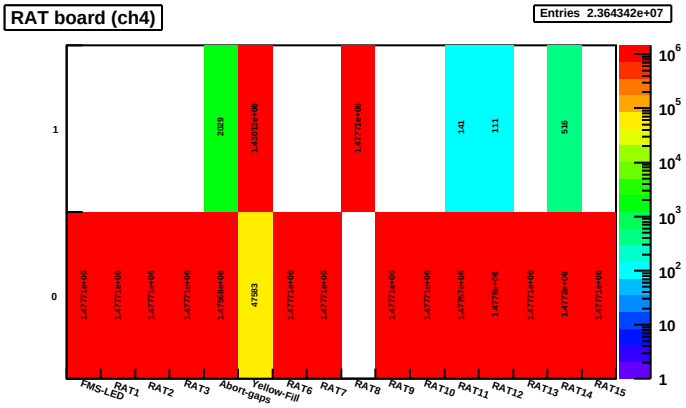
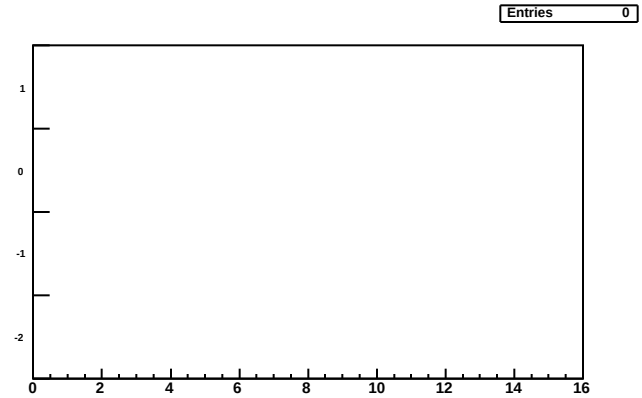
RAT board (ch4)





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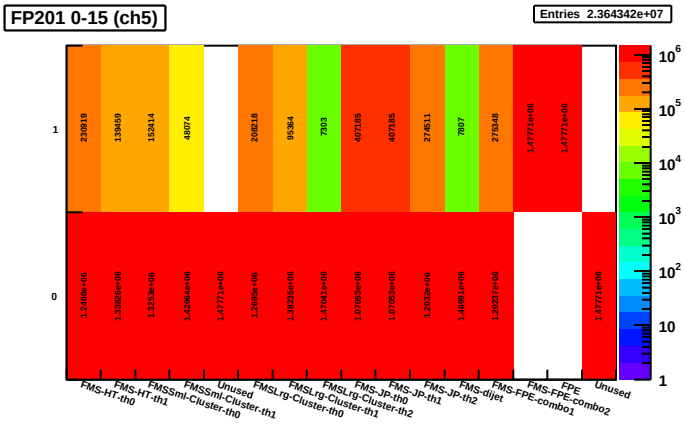

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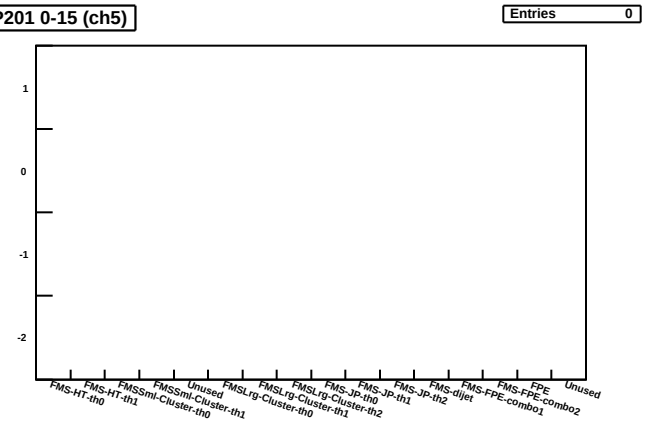
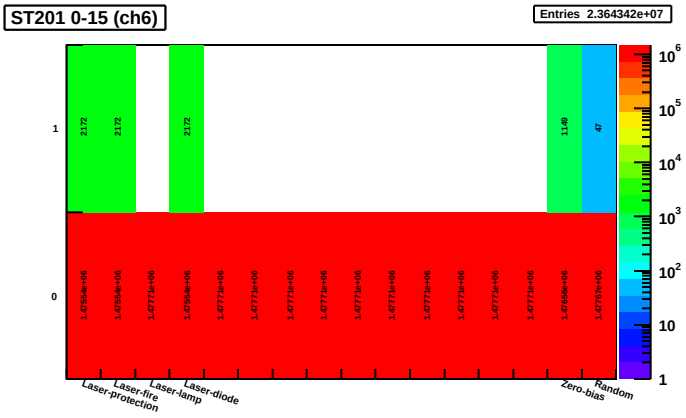
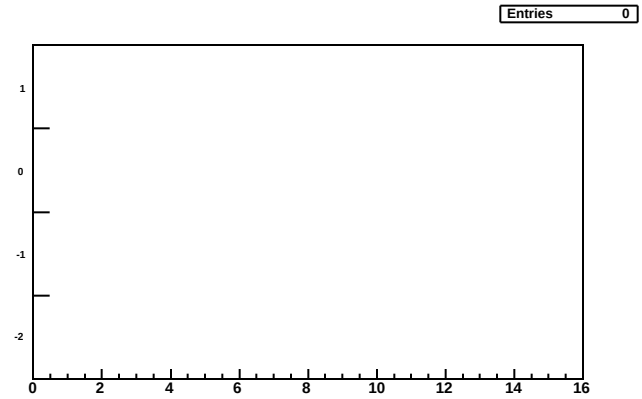
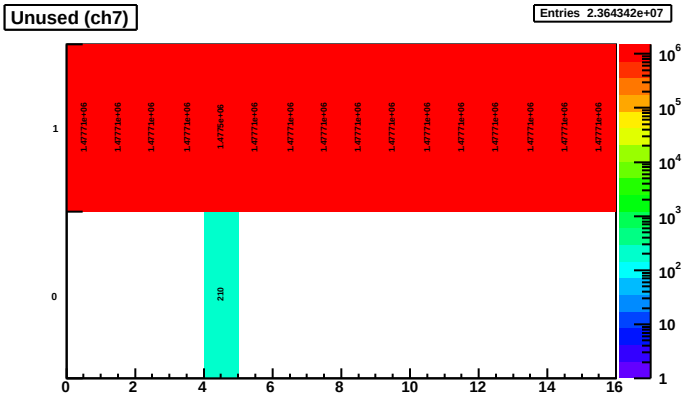
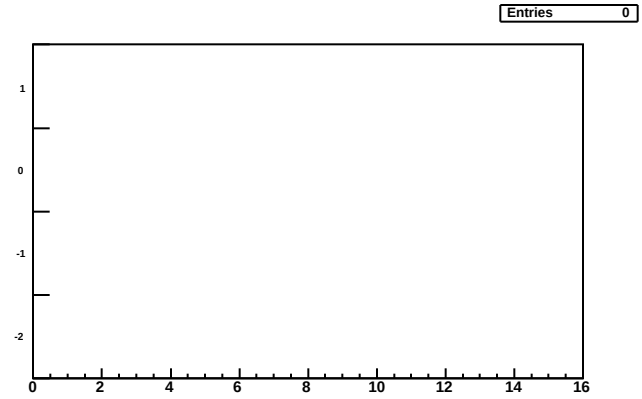


ST201 0-15 (ch6)





Unused (ch7)