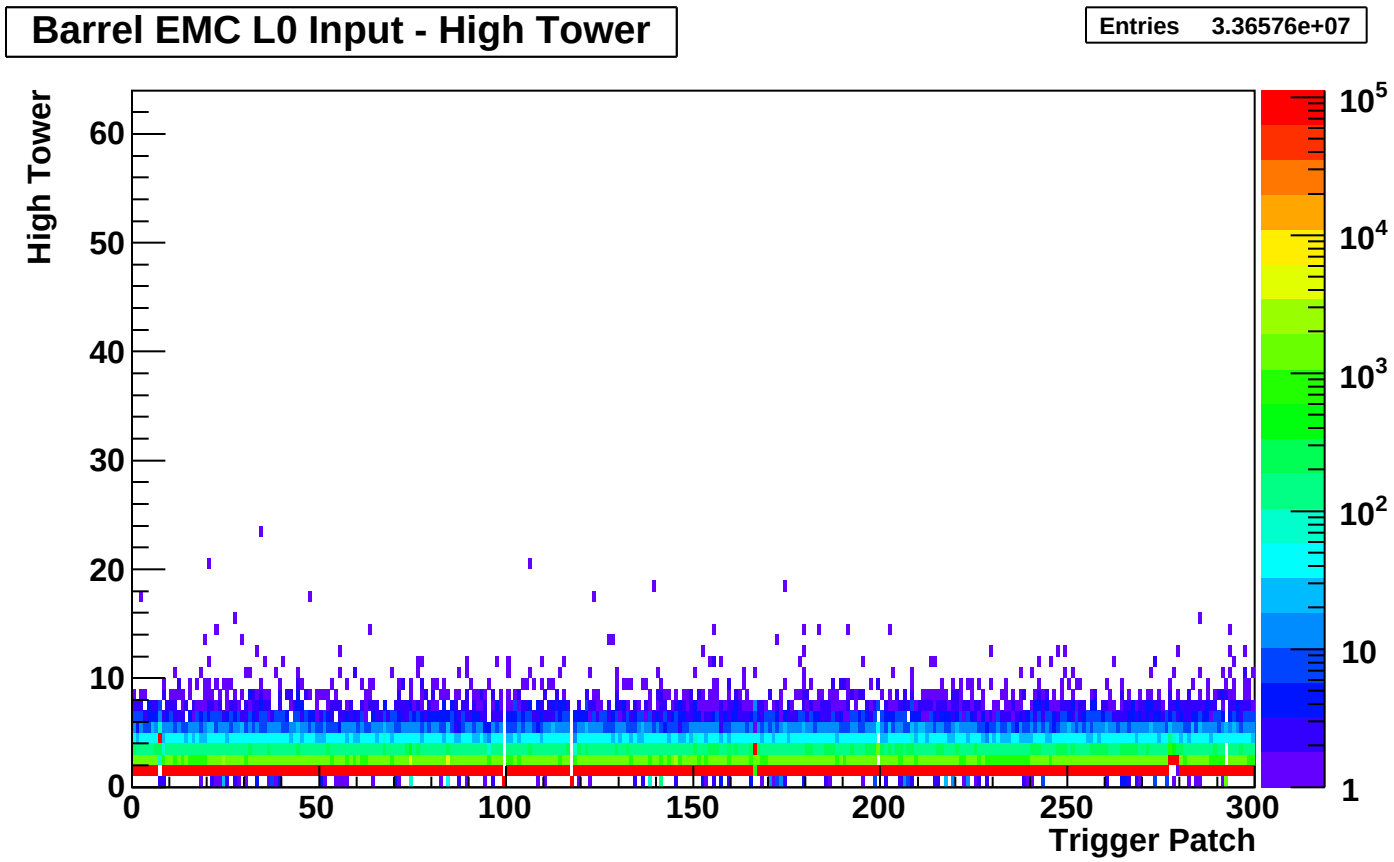
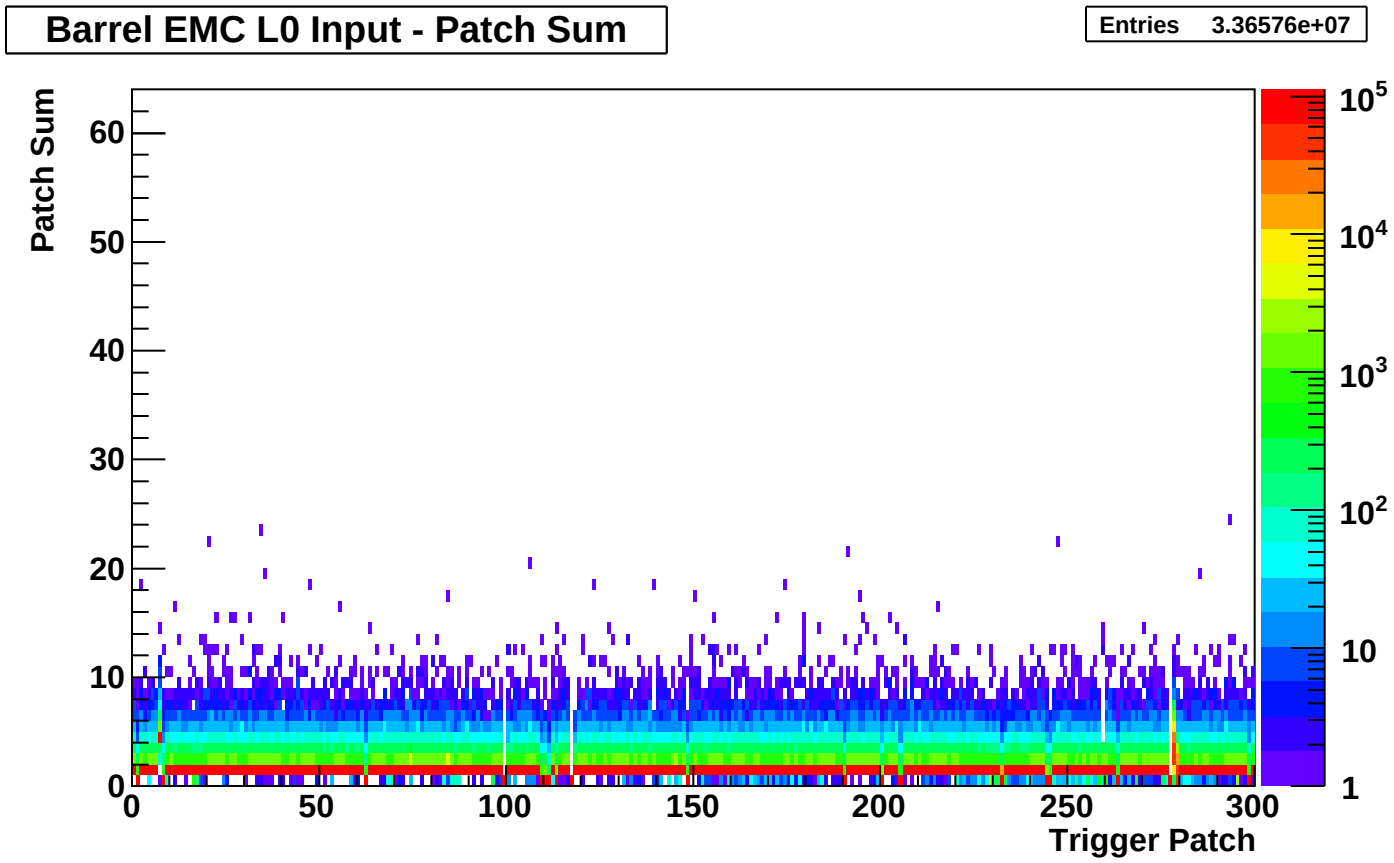
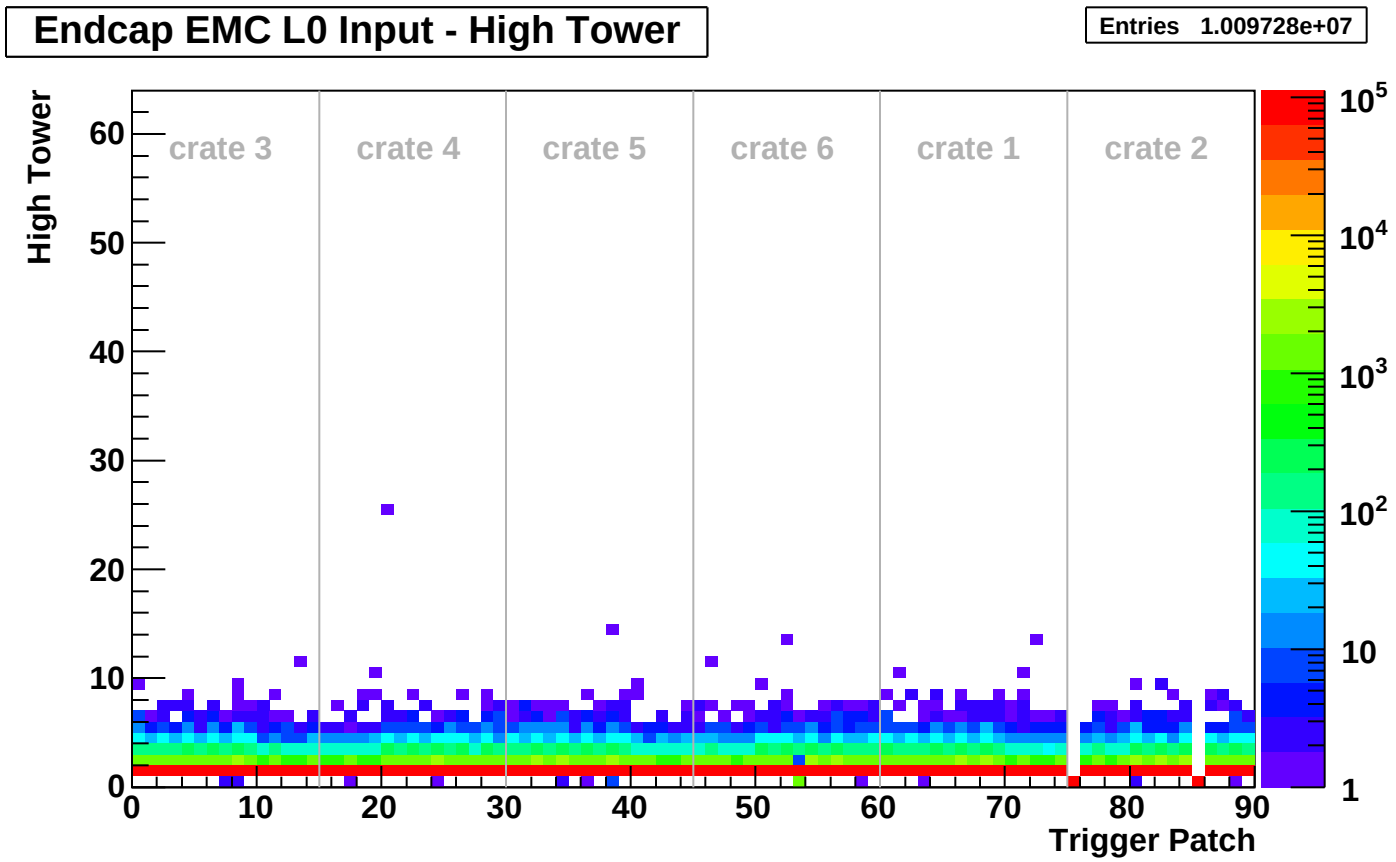
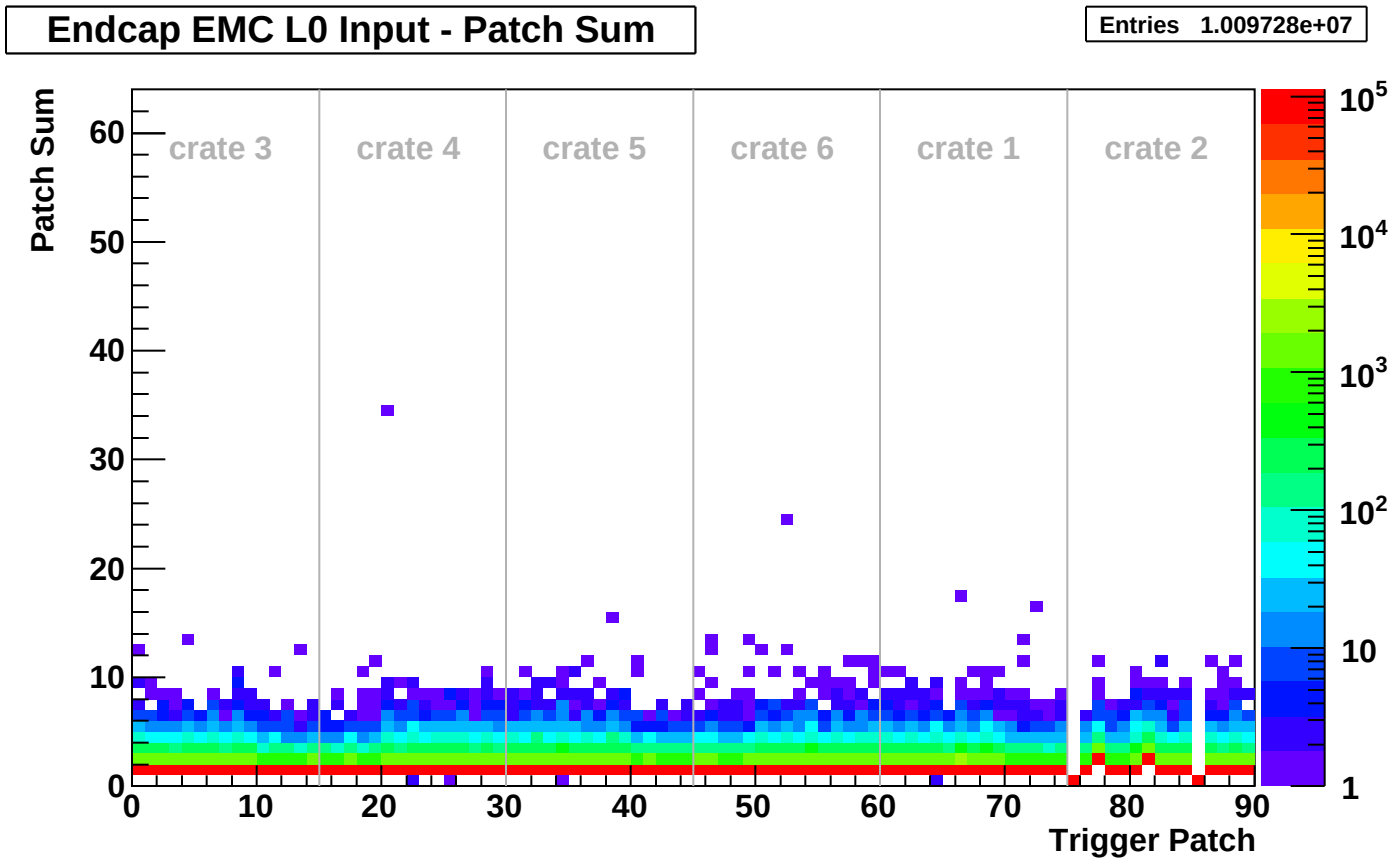
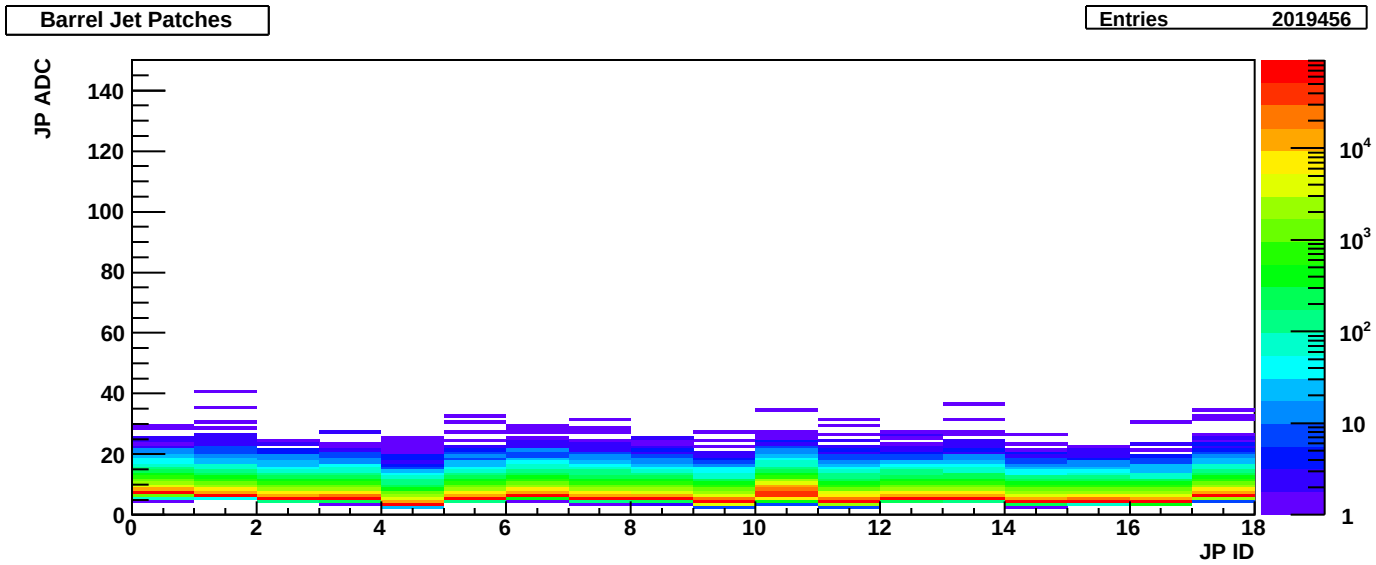
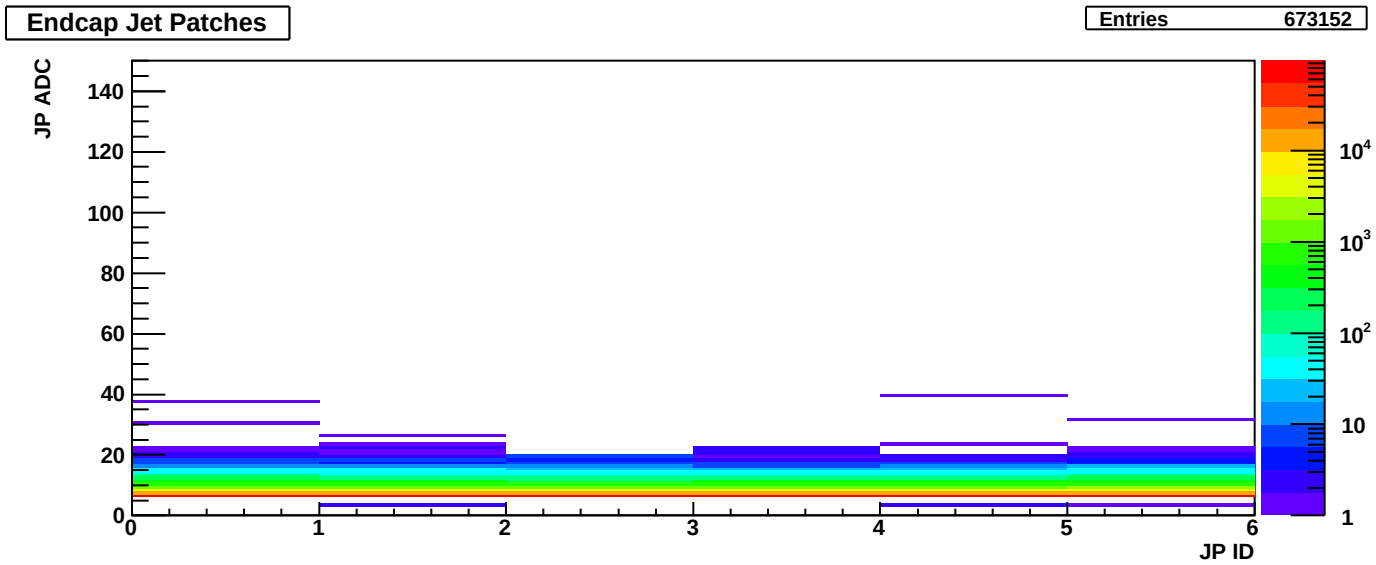

Barrel EMC L0 Input - High Tower
Entries 3.36576e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

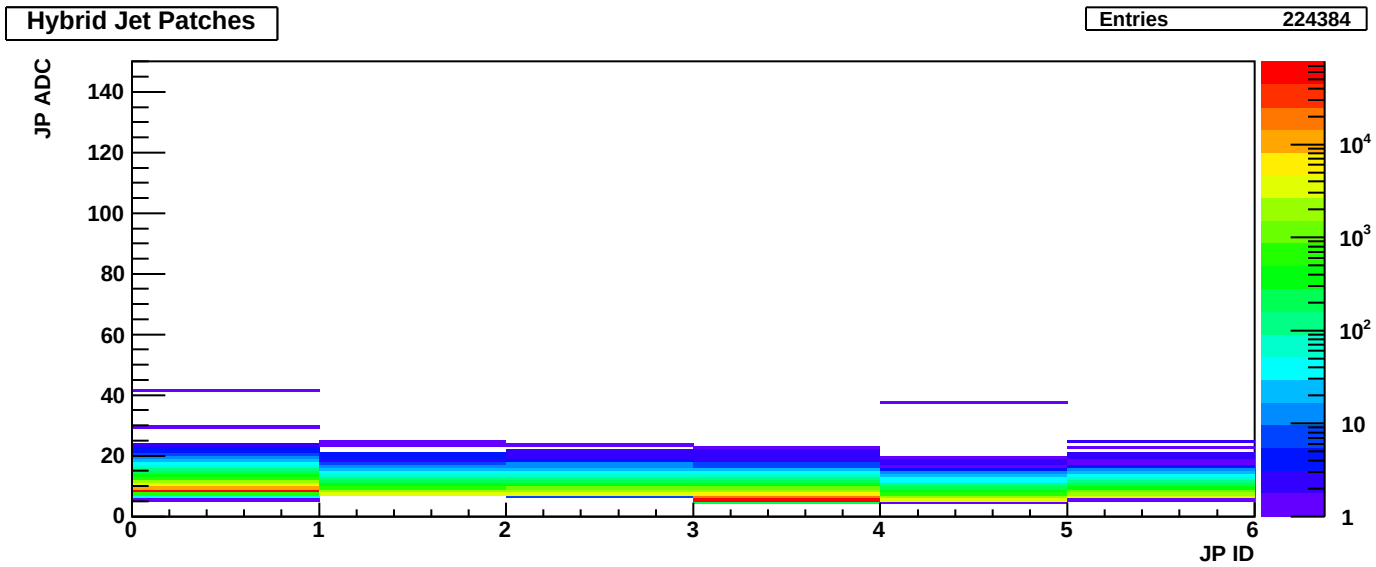

Barrel EMC L0 Input - Patch Sum
Entries 3.36576e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

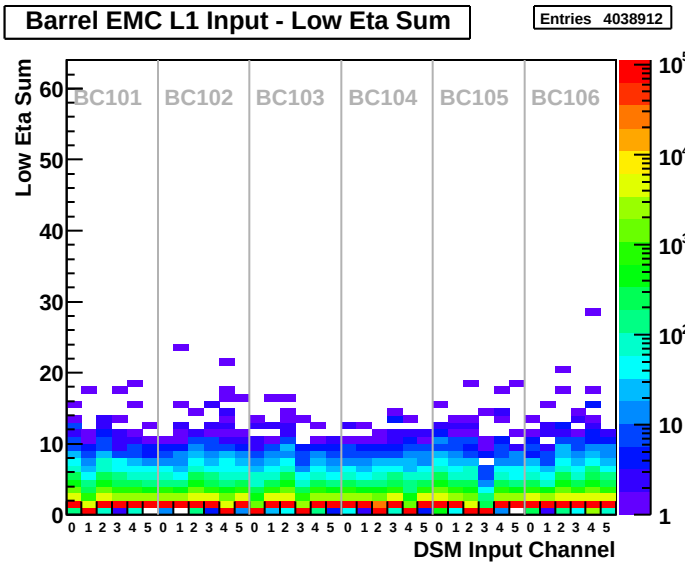

Endcap EMC L0 Input - High Tower
Entries 1.009728e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

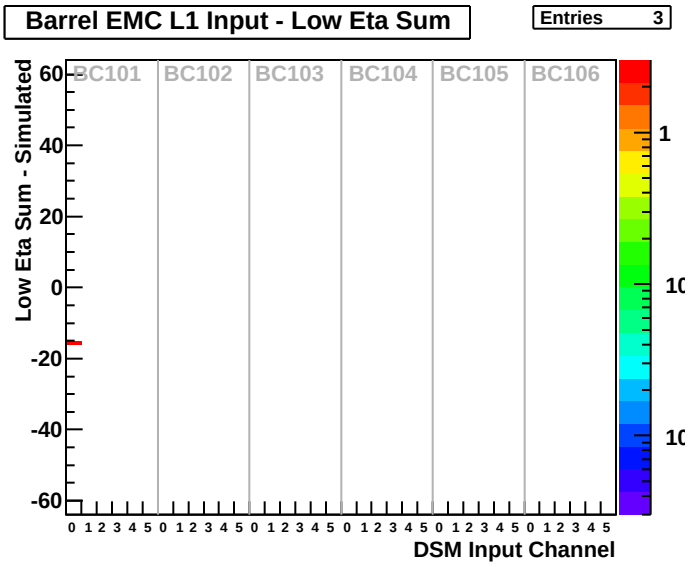

Endcap EMC L0 Input - Patch Sum
Entries 1.009728e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

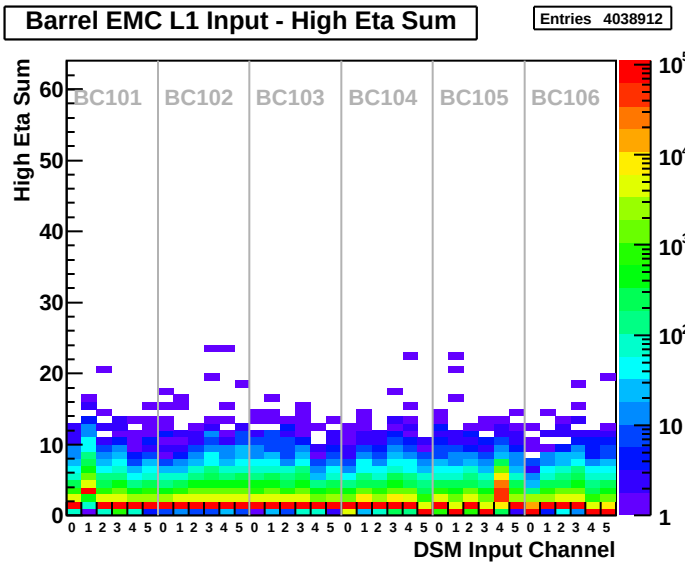

Barrel Jet Patches
Entries 2019456
JP ADC
JP ID

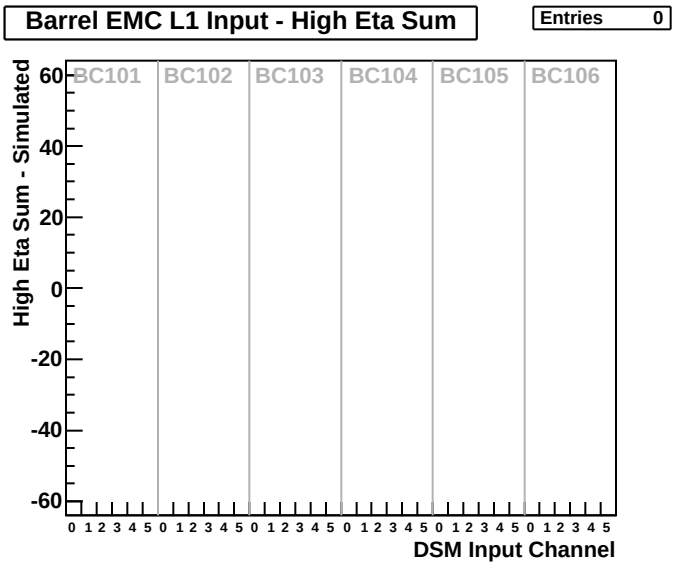

Endcap Jet Patches
Entries 673152
JP ADC
JP ID

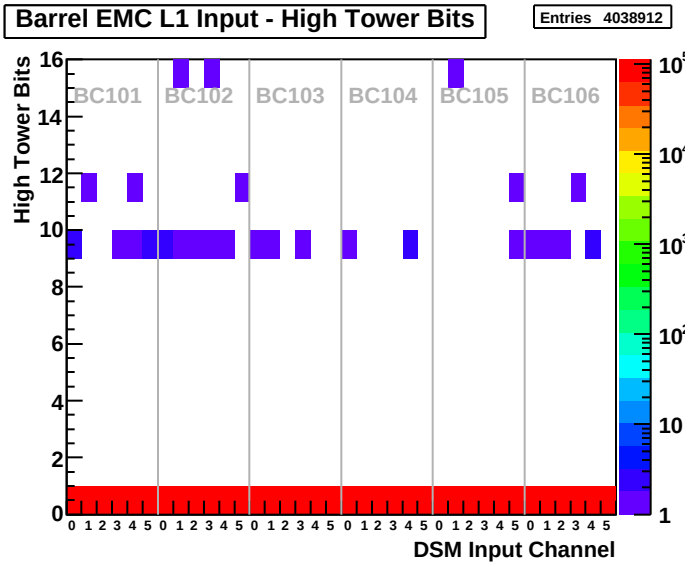

Hybrid Jet Patches
Entries 224384
JP ADC
JP ID

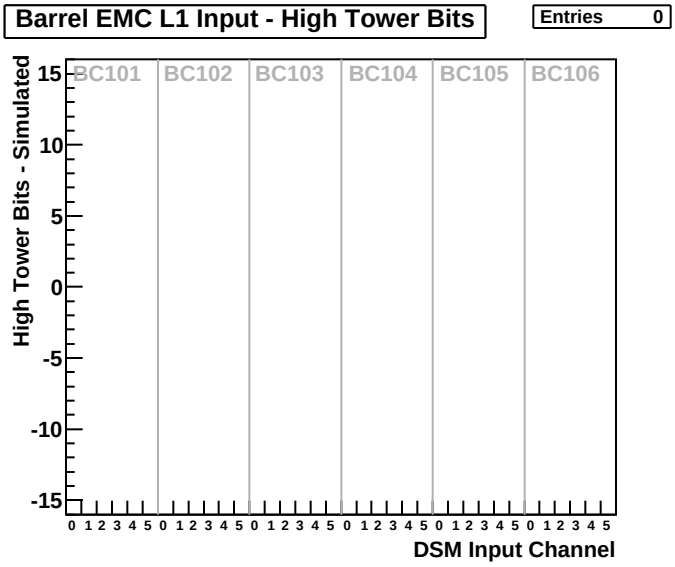

Barrel EMC L1 Input - Low Eta Sum
Entries 4038912
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

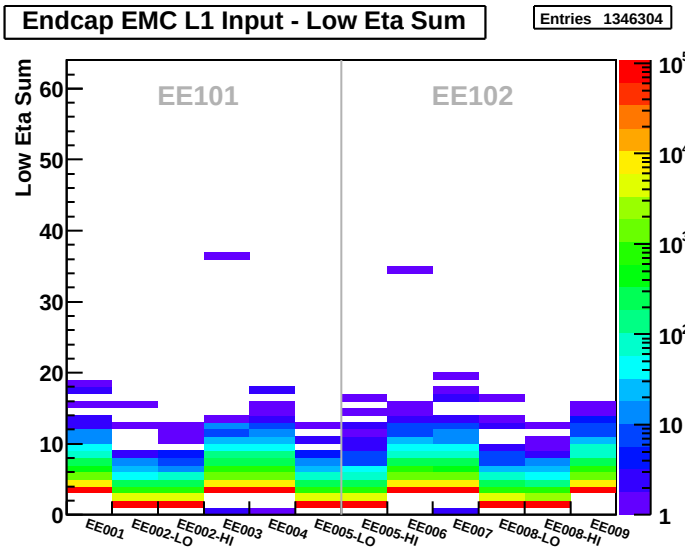

Barrel EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

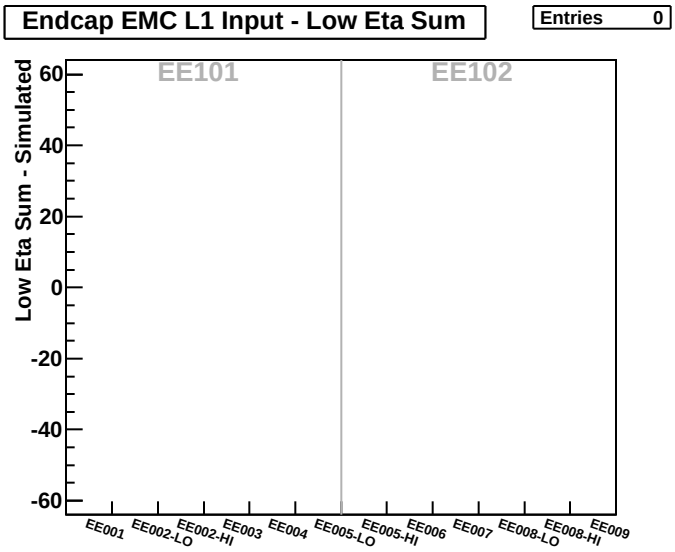

Barrel EMC L1 Input - High Eta Sum
Entries 4038912
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

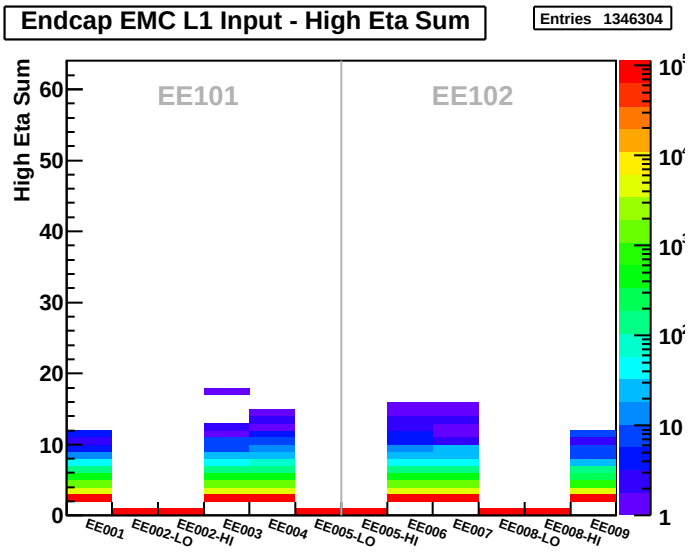

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

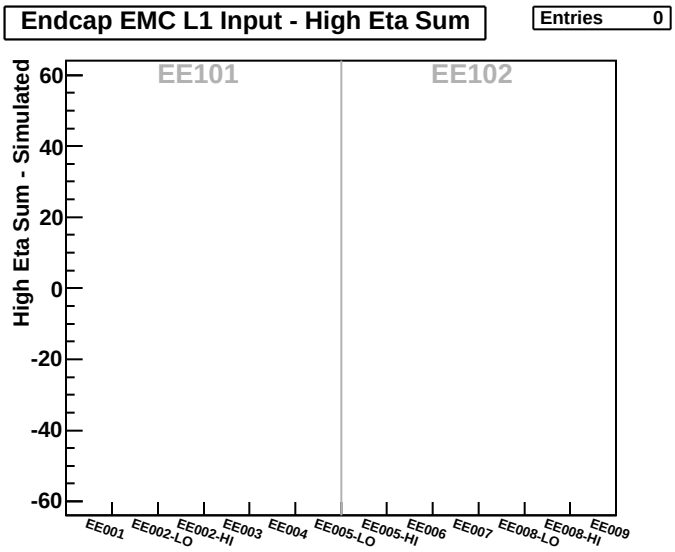

Barrel EMC L1 Input - High Tower Bits
Entries 4038912
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

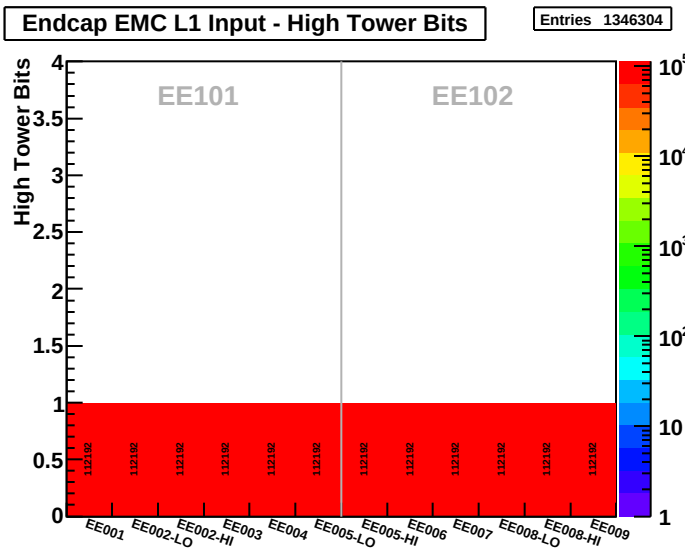

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

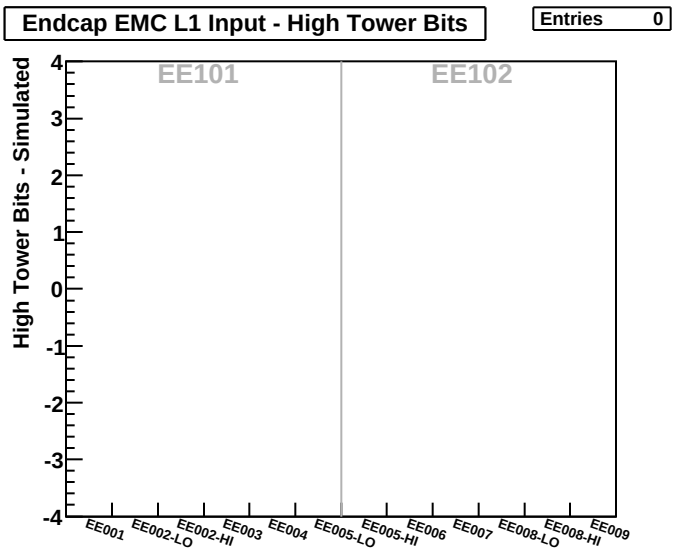

Endcap EMC L1 Input - Low Eta Sum
Entries 1346304
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

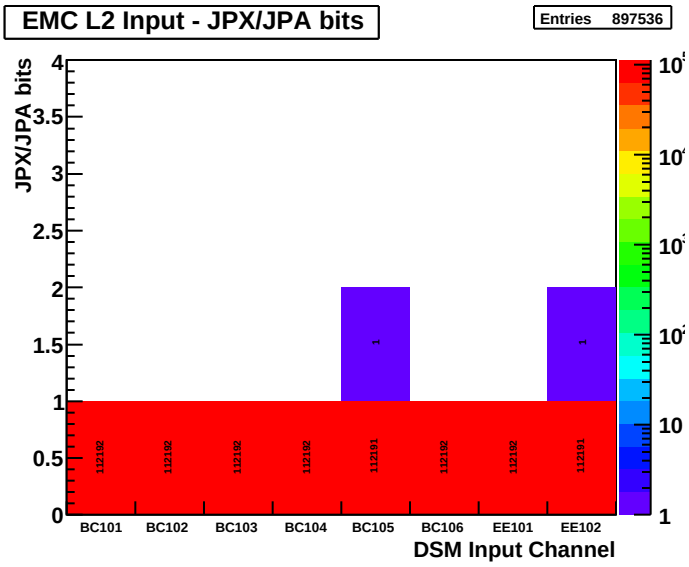

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

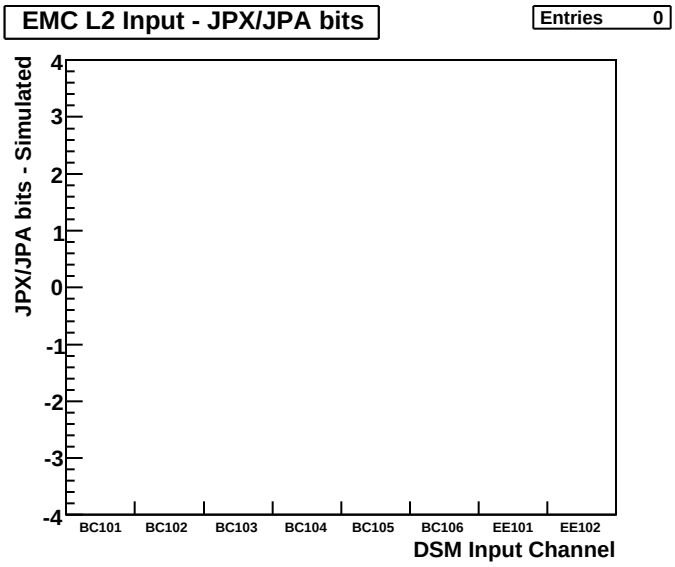

Endcap EMC L1 Input - High Eta Sum
Entries 1346304
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

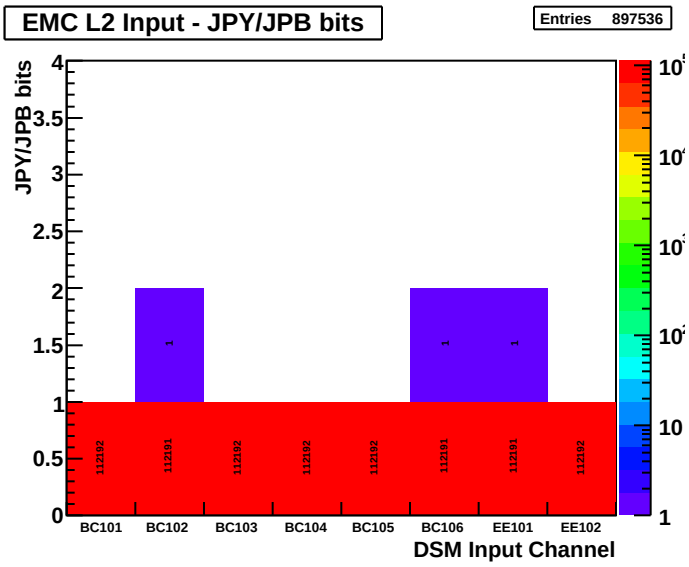

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

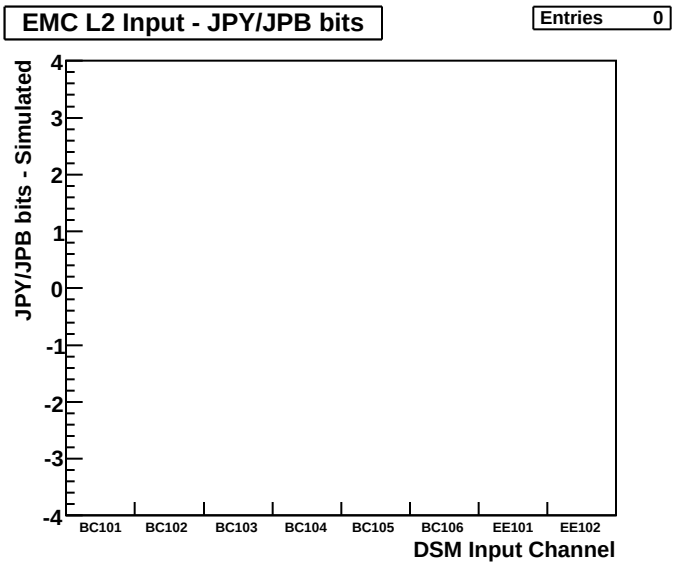

Endcap EMC L1 Input - High Tower Bits
Entries 1346304
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

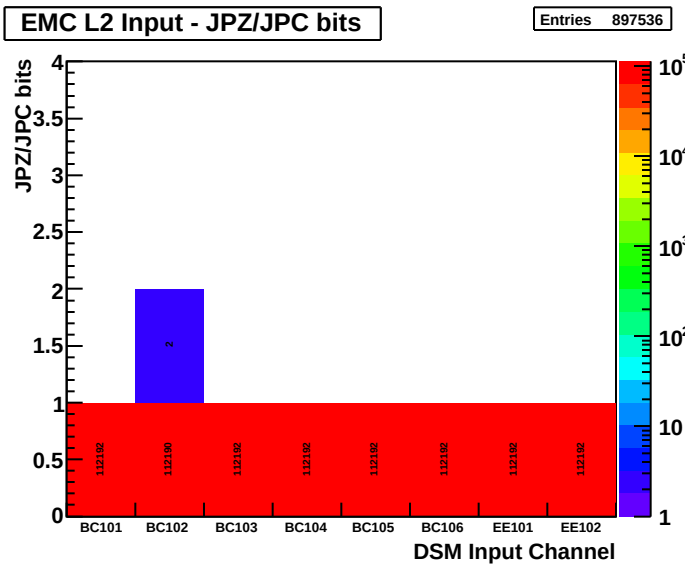

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

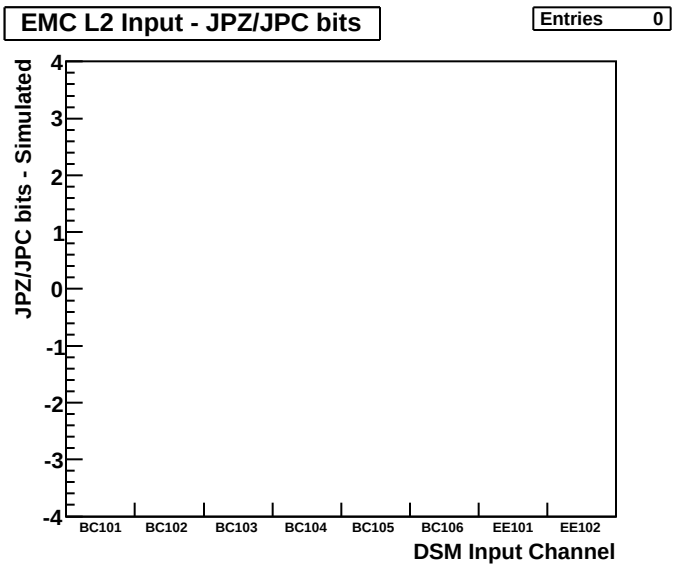

EMC L2 Input - JPX/JPA bits
Entries 897536
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

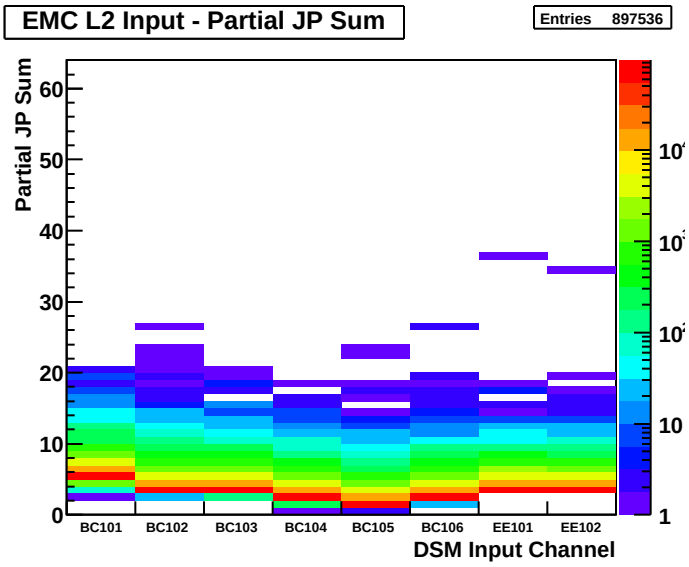

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

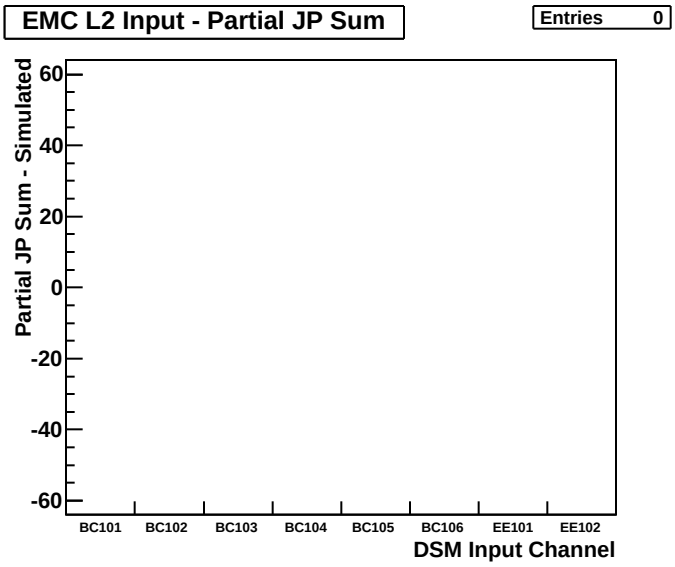

EMC L2 Input - JPY/JPB bits
Entries 897536
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

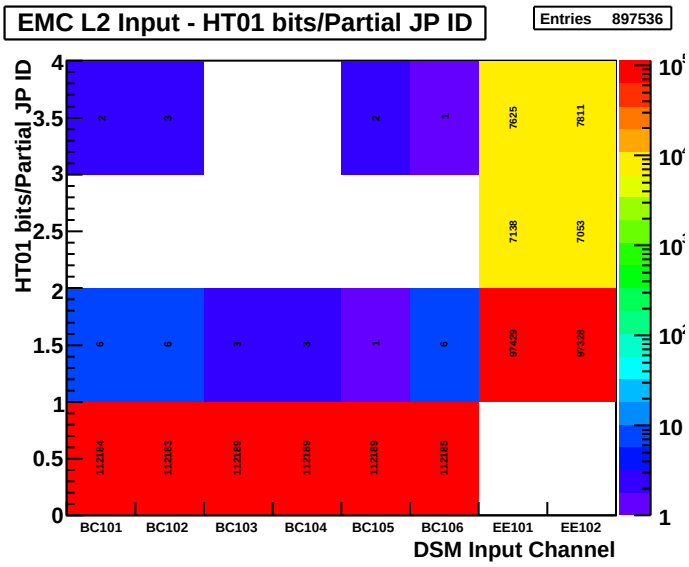

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

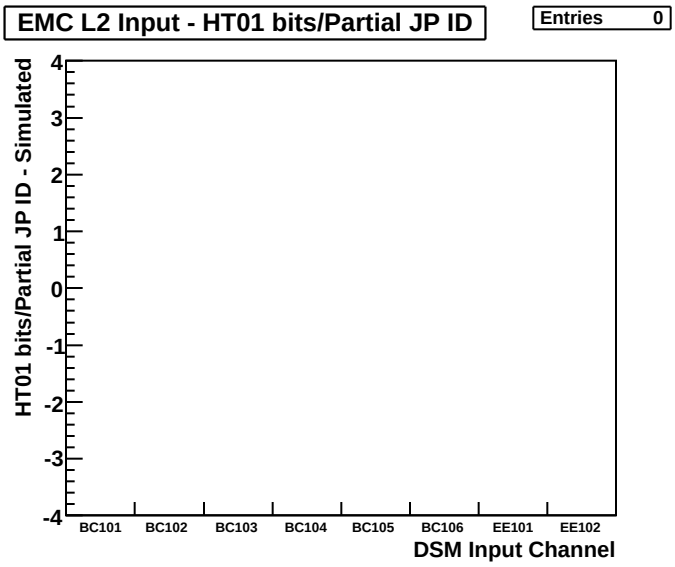

EMC L2 Input - JPZ/JPC bits
Entries 897536
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

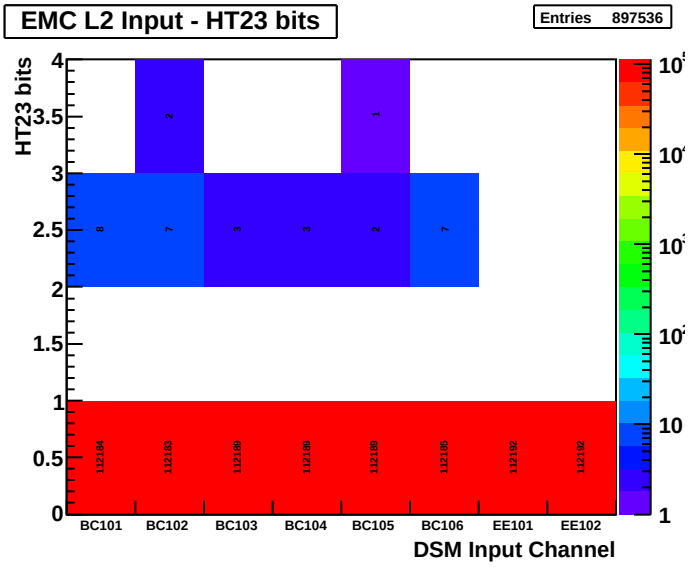

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

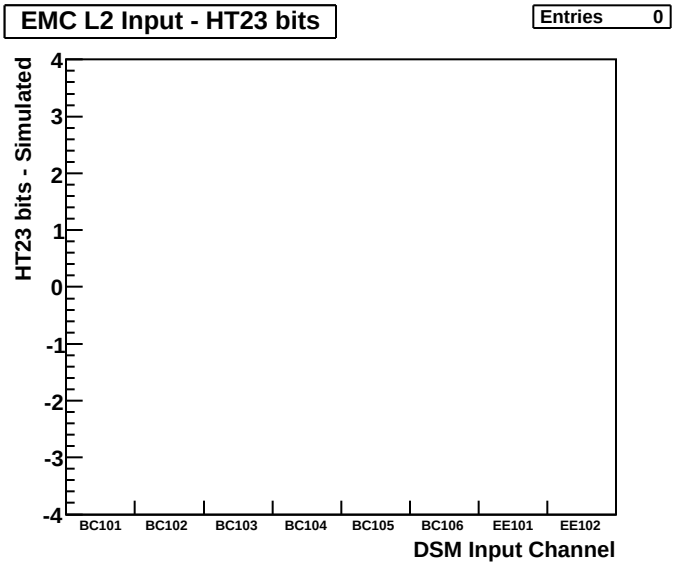

EMC L2 Input - Partial JP Sum
Entries 897536
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

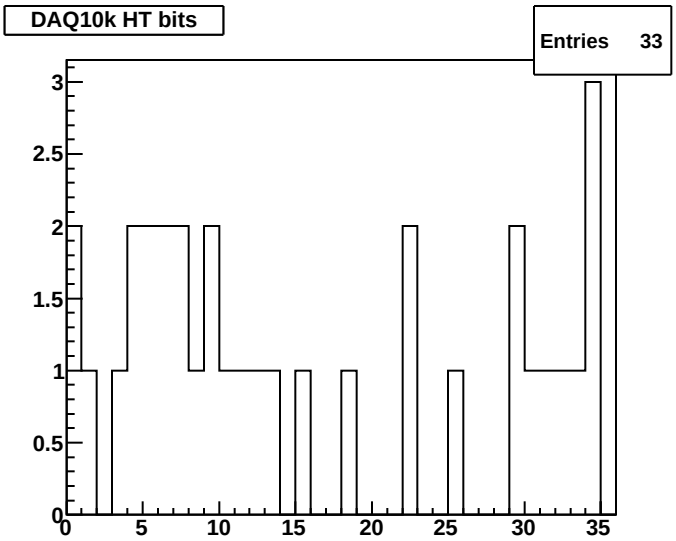

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

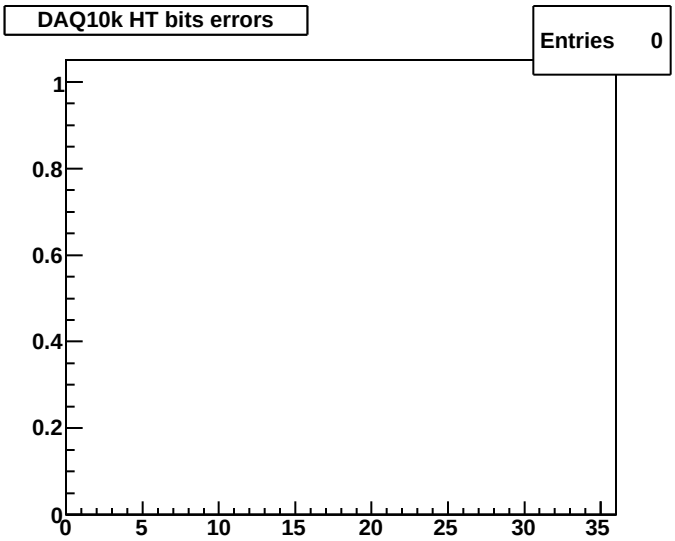

EMC L2 Input - HT01 bits/Partial JP ID
Entries 897536
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

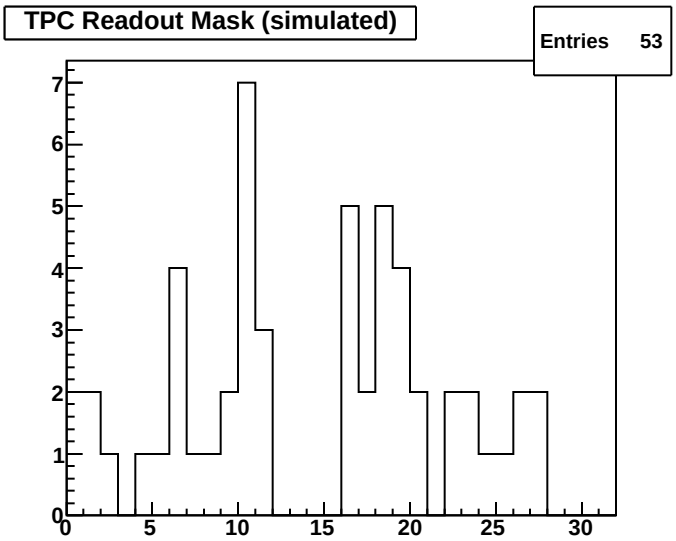

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

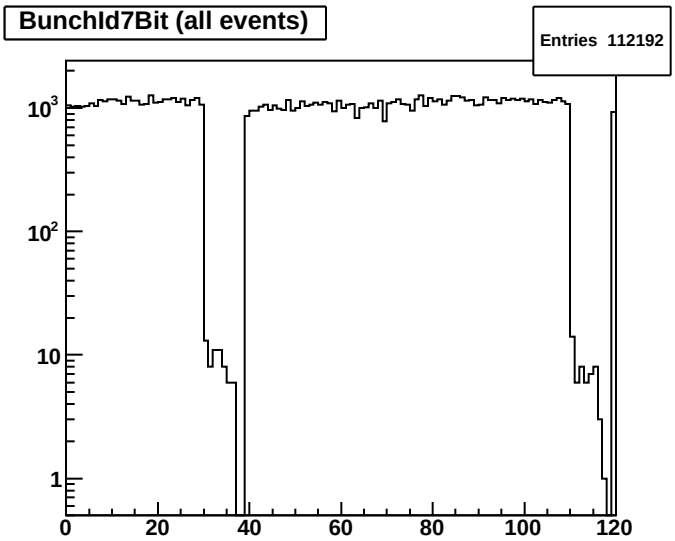

EMC L2 Input - HT23 bits
Entries 897536
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

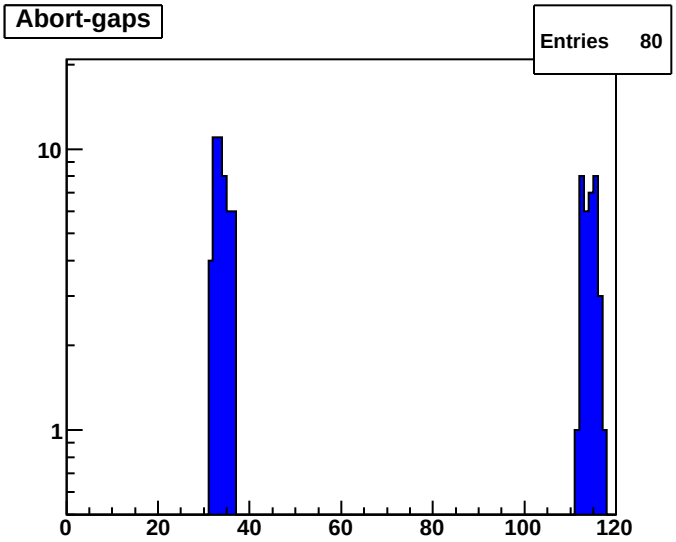

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

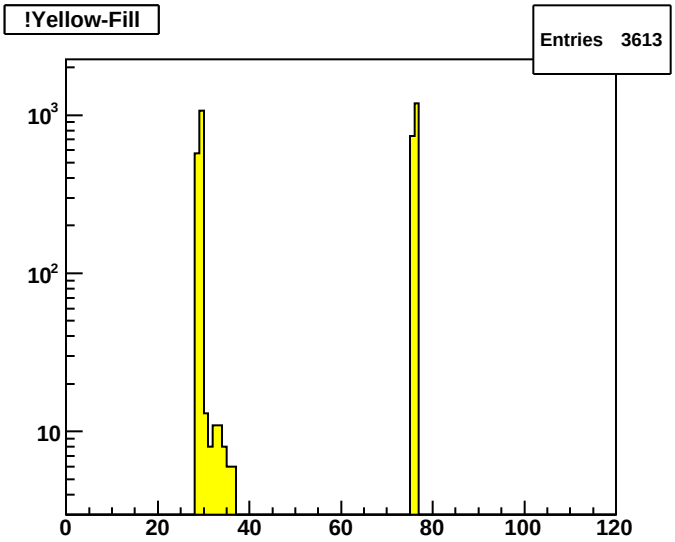

DAQ10k HT bits
Entries 33

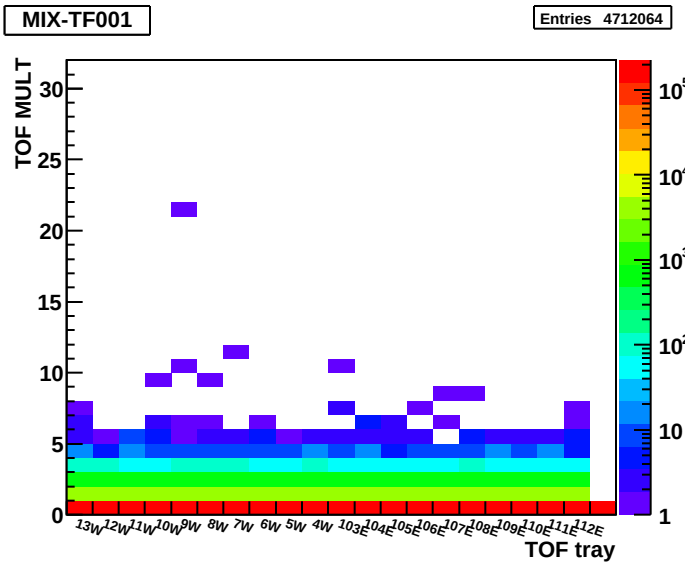

DAQ10k HT bits errors
Entries 0

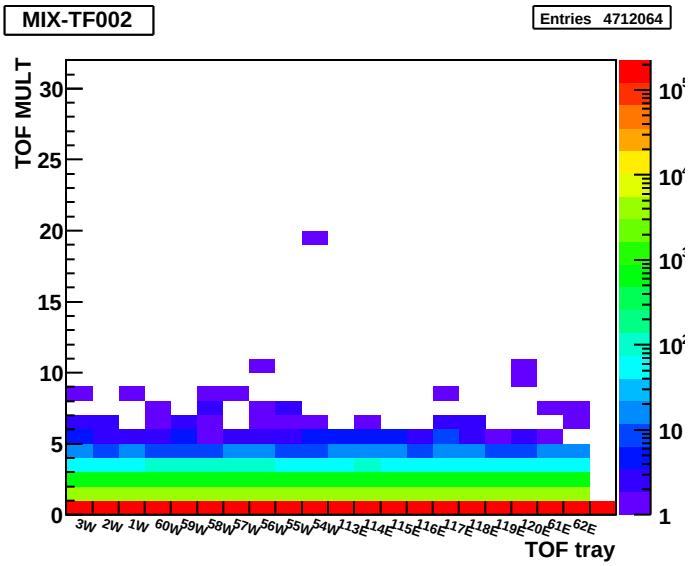

TPC Readout Mask (simulated)
Entries 53

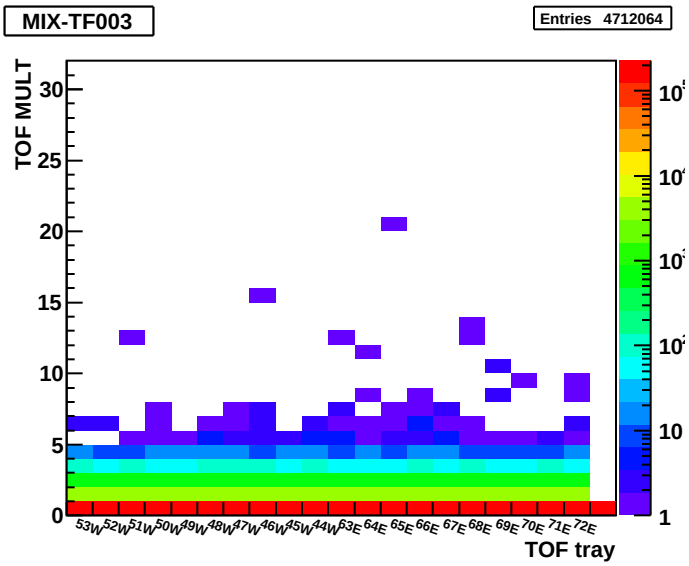

BunchId7Bit (all events)
Entries 112192

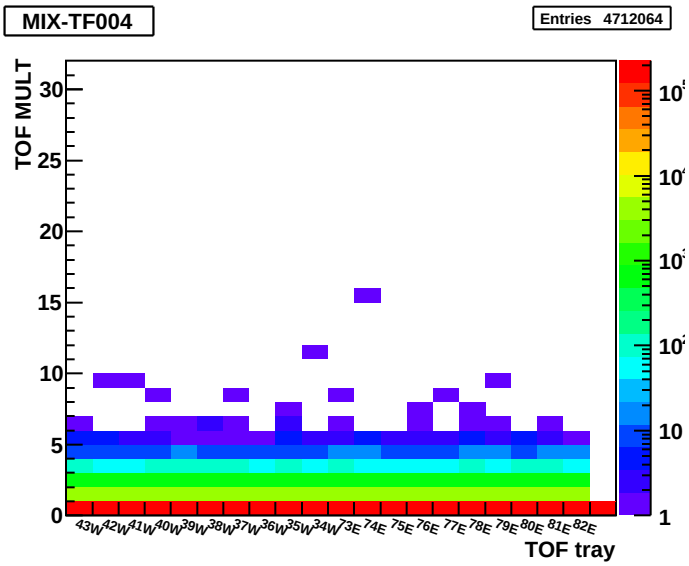

Abort-gaps
Entries 80

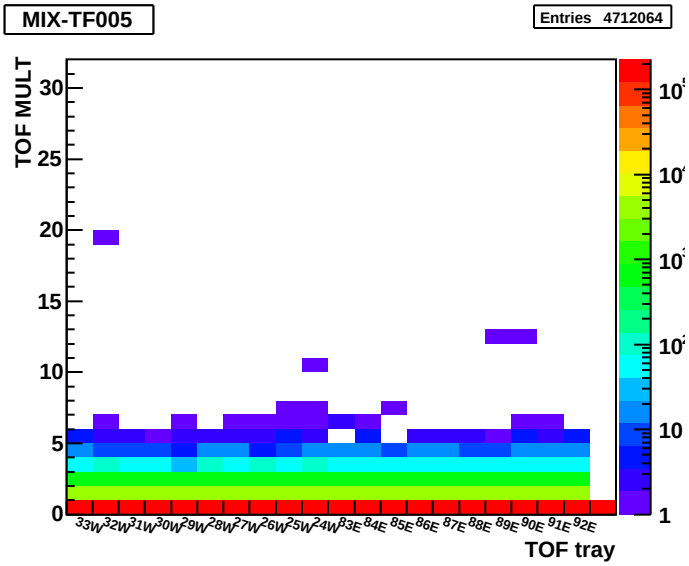

!Yellow-Fill
Entries 3613

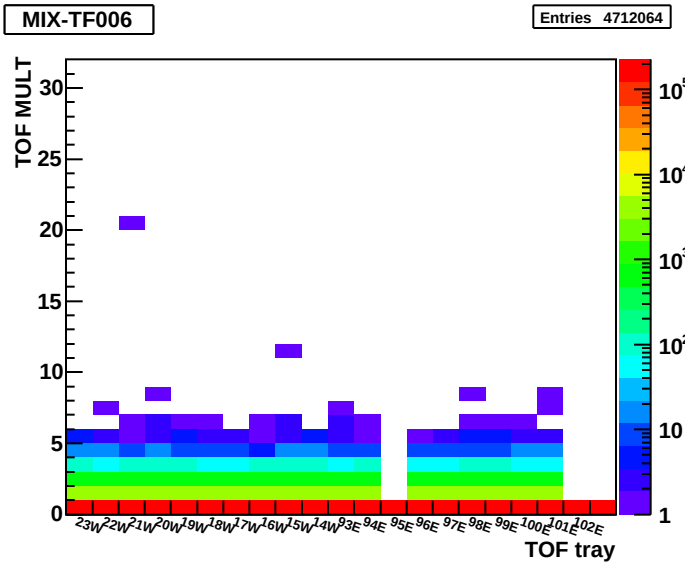

MIX-TF001
Entries 4712064
TOF MULT
TOF tray

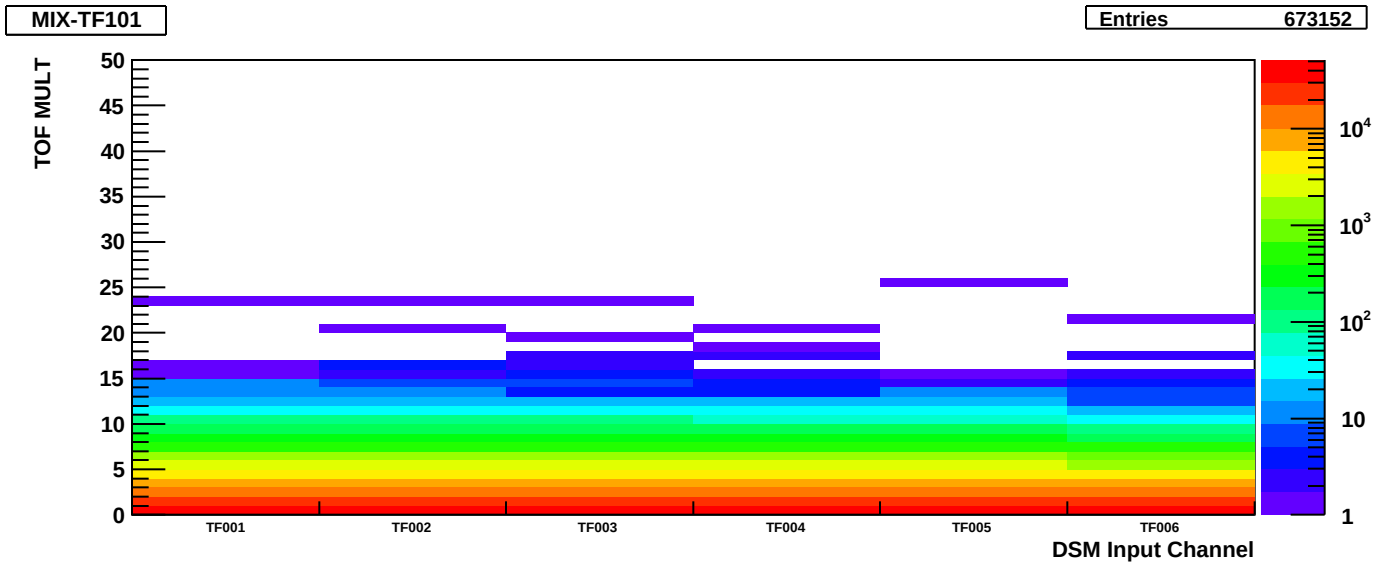

MIX-TF002
Entries 4712064
TOF MULT
TOF tray

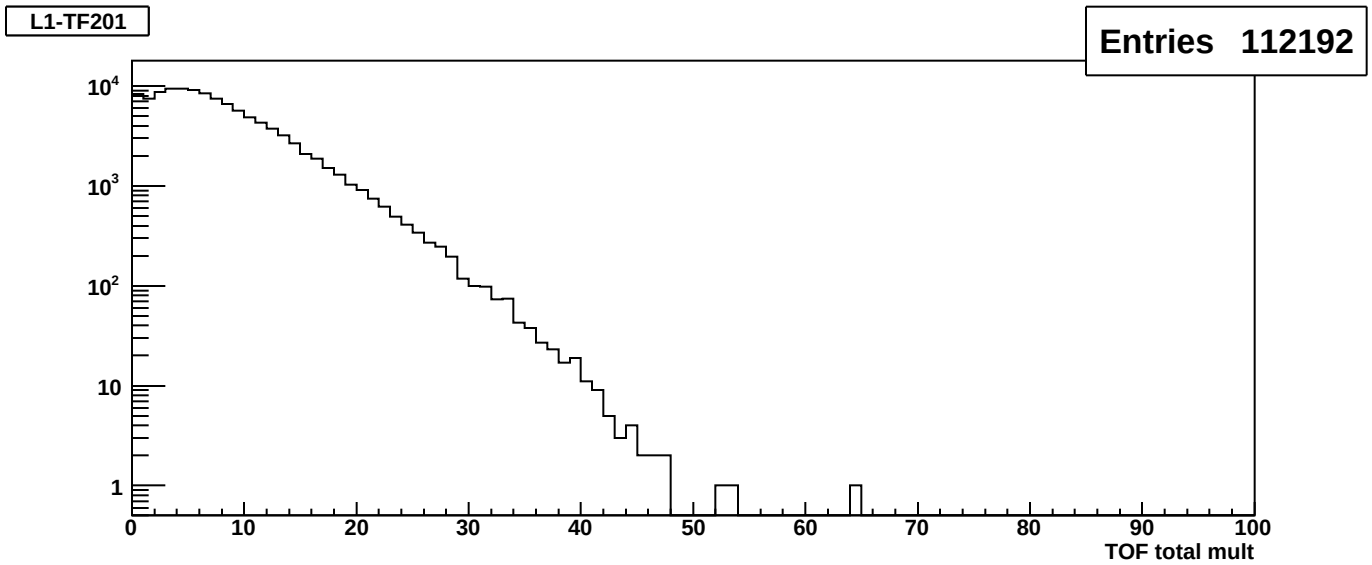

MIX-TF003
Entries 4712064
TOF MULT
TOF tray

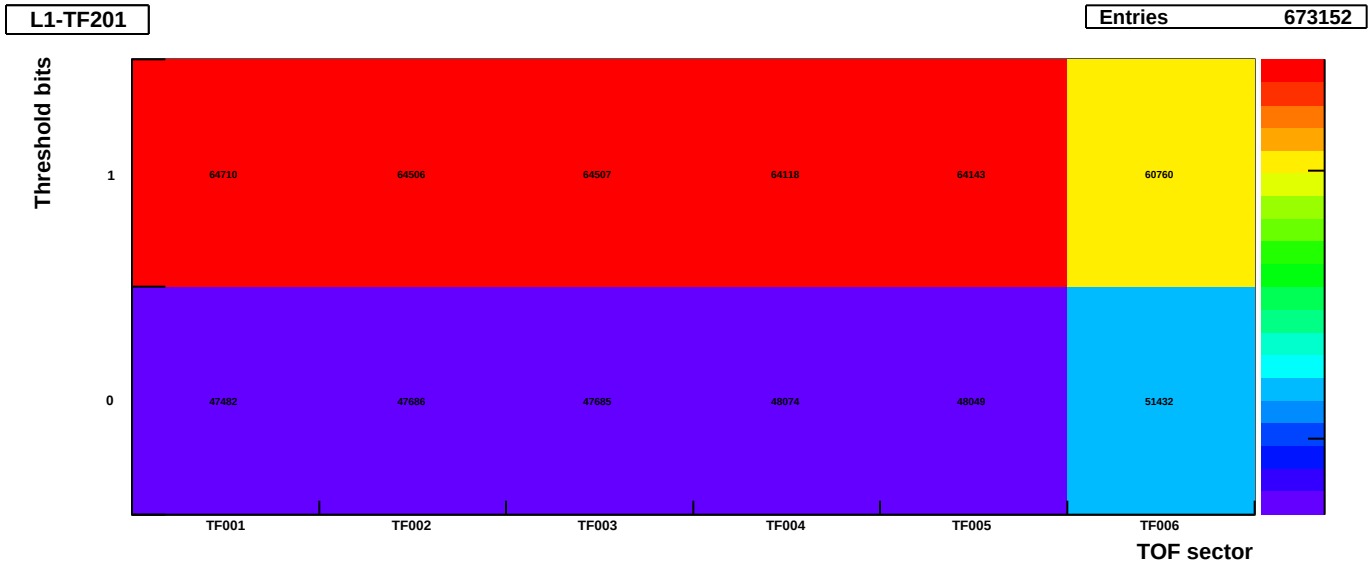

MIX-TF004
Entries 4712064
TOF MULT
TOF tray

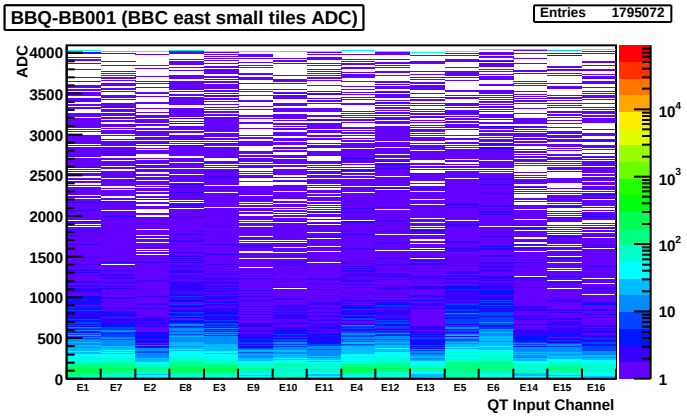

MIX-TF005
Entries 4712064
TOF MULT
TOF tray

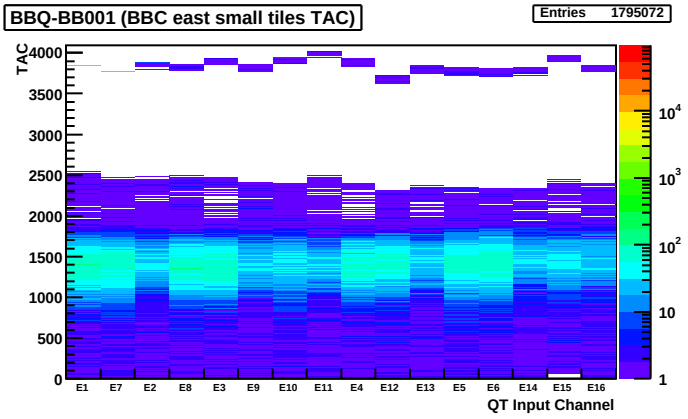

MIX-TF006
Entries 4712064
TOF MULT
TOF tray

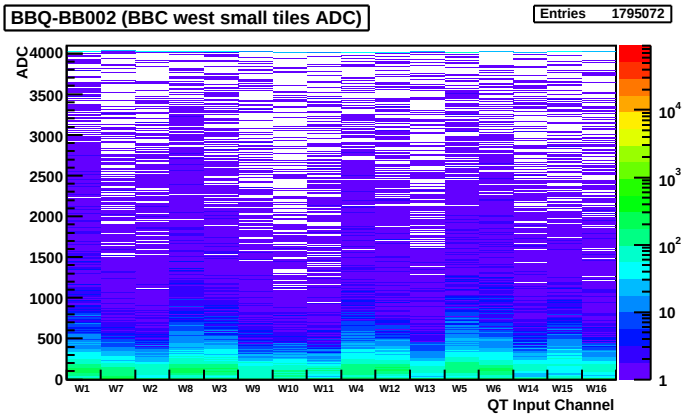

MIX-TF101
Entries 673152
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

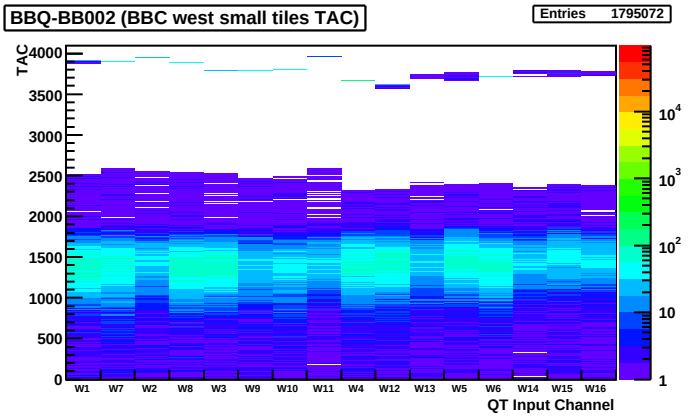

L1-TF201
Entries 112192
TOF total mult

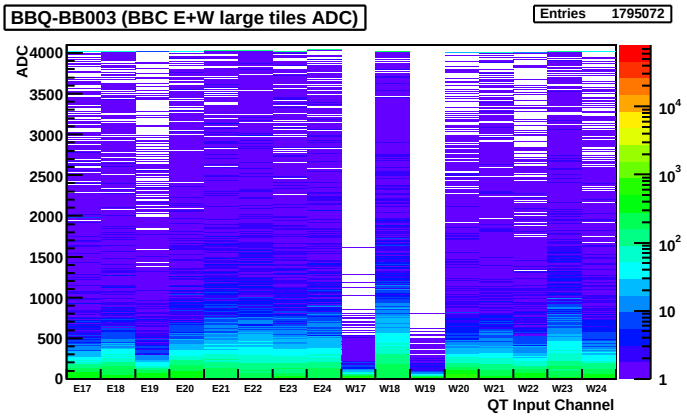

L1-TF201
Entries 673152
Threshold bits
64710
64506
64507
64118
64143
60760
47482
47686
47685
48074
48049
51432
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

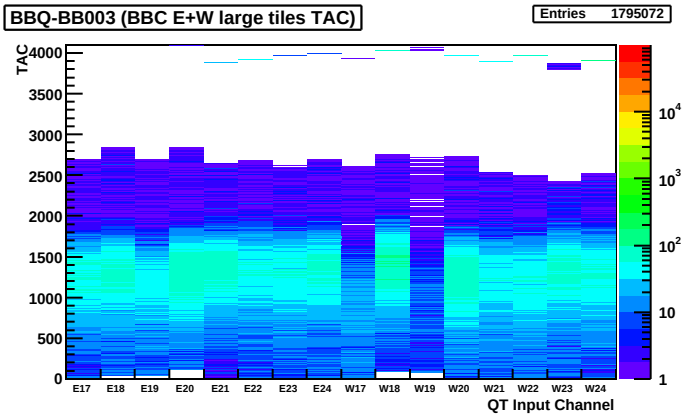

BBQ-BB001 (BBC east small tiles ADC)
Entries 1795072
ADC
QT Input Channel

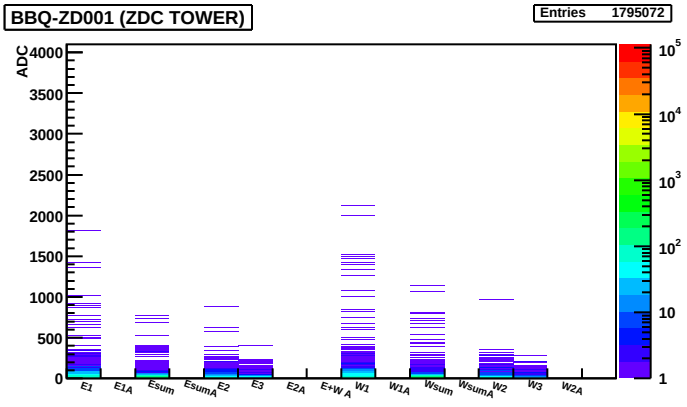

BBQ-BB001 (BBC east small tiles TAC)
Entries 1795072
TAC
QT Input Channel

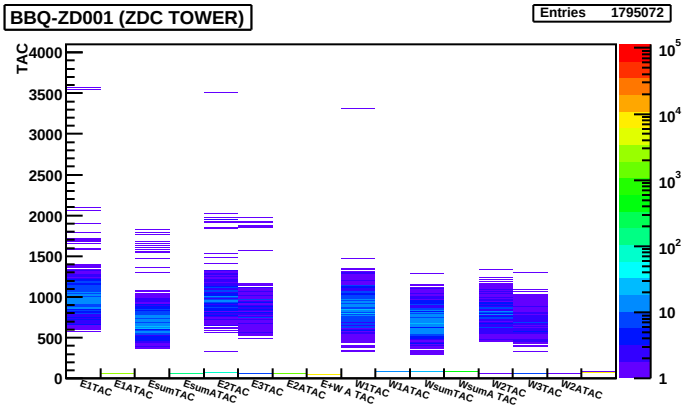

BBQ-BB002 (BBC west small tiles ADC)
Entries 1795072
ADC
QT Input Channel

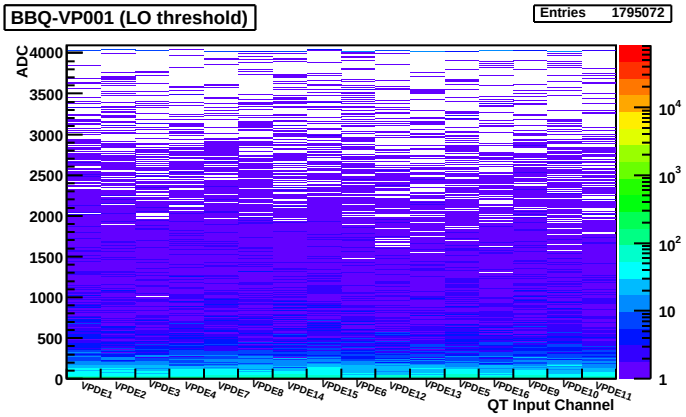

BBQ-BB002 (BBC west small tiles TAC)
Entries 1795072
TAC
QT Input Channel

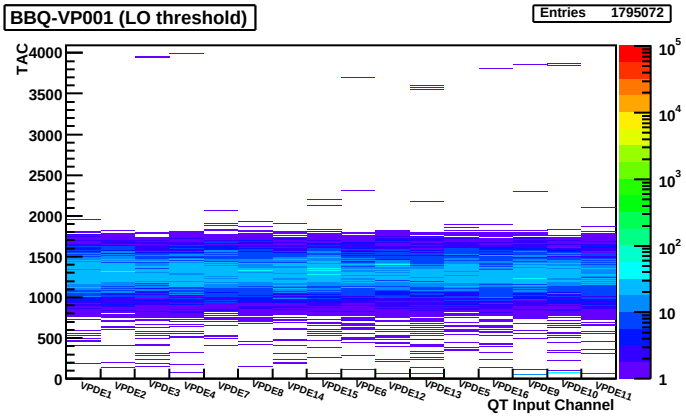

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1795072
ADC
QT Input Channel

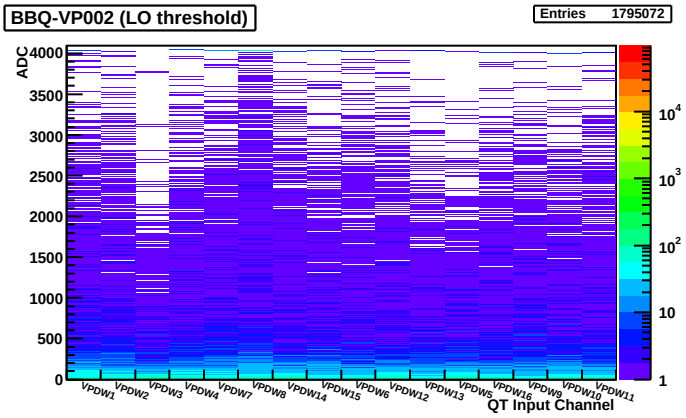

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1795072
TAC
QT Input Channel

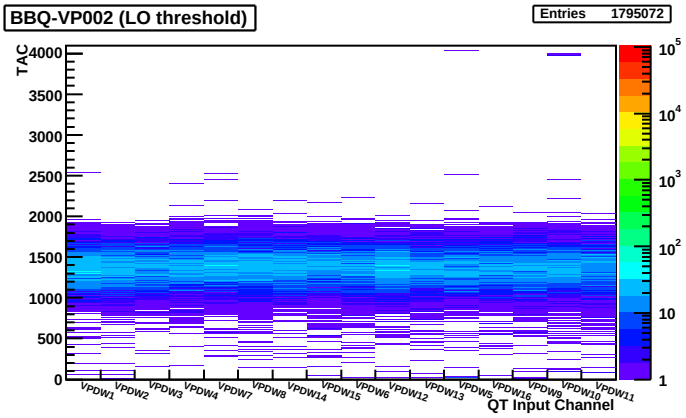

BBQ-ZD001 (ZDC TOWER)
Entries 1795072
ADC

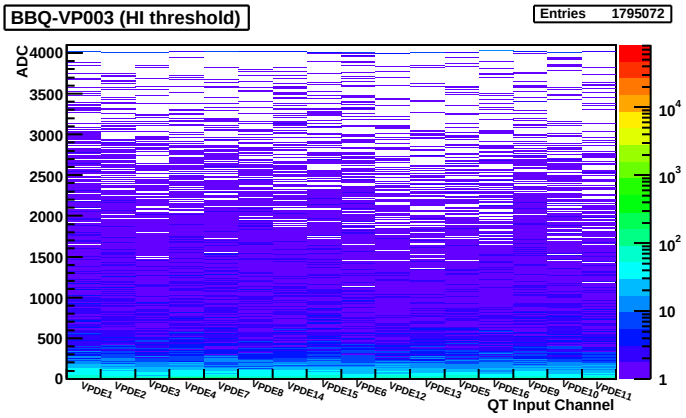

BBQ-ZD001 (ZDC TOWER)
Entries 1795072
TAC

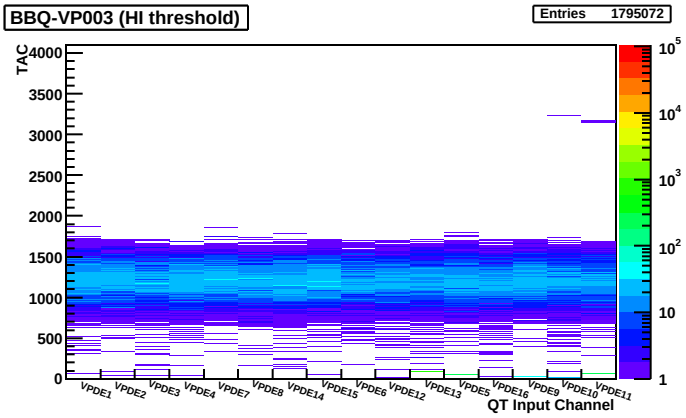

BBQ-VP001 (LO threshold)
Entries 1795072
ADC
QT Input Channel

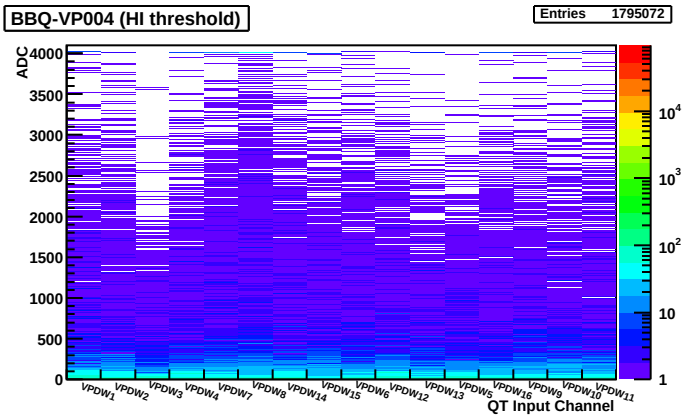

BBQ-VP001 (LO threshold)
Entries 1795072
TAC
QT Input Channel

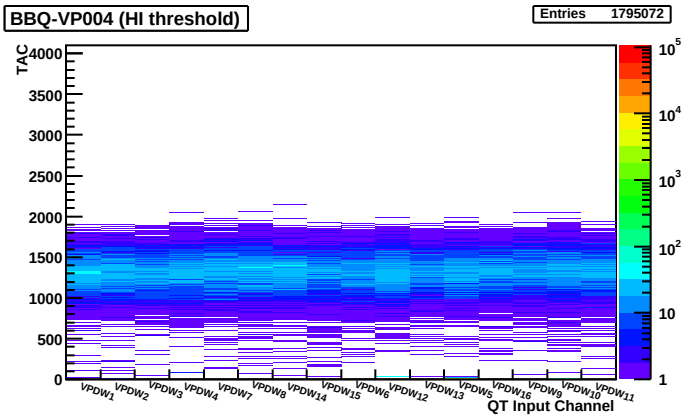

BBQ-VP002 (LO threshold)
Entries 1795072
ADC
QT Input Channel

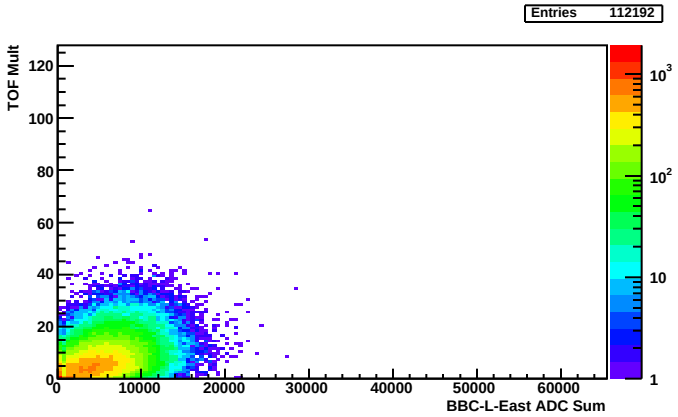

BBQ-VP002 (LO threshold)
Entries 1795072
TAC
QT Input Channel

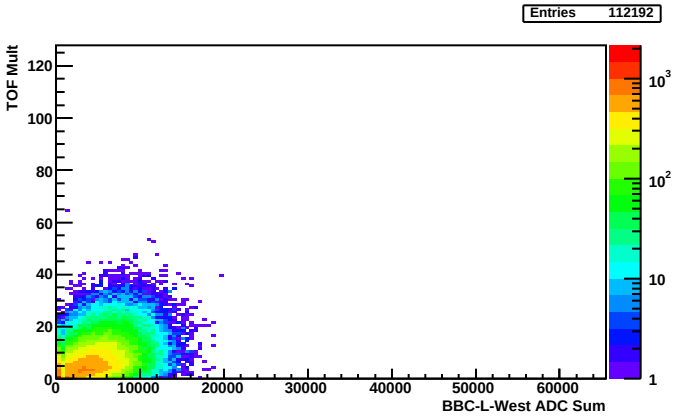

BBQ-VP003 (HI threshold)
Entries 1795072
ADC
QT Input Channel

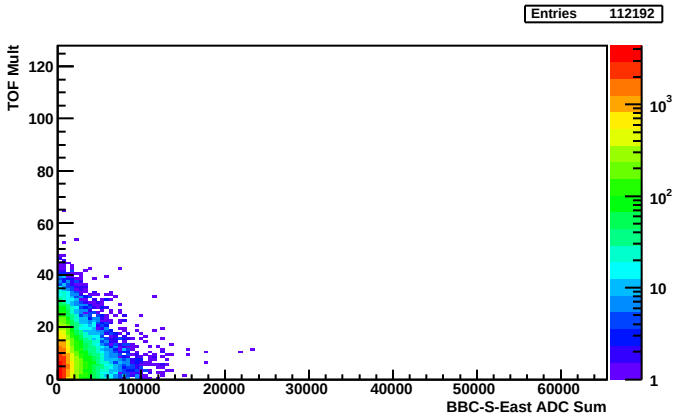

BBQ-VP003 (HI threshold)
Entries 1795072
TAC
QT Input Channel

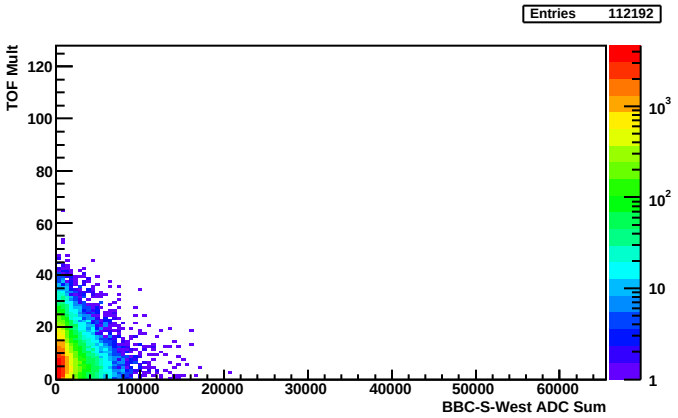

BBQ-VP004 (HI threshold)
Entries 1795072
ADC
QT Input Channel

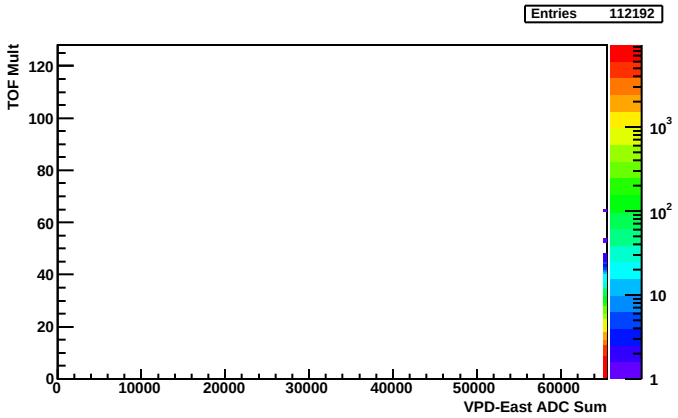

BBQ-VP004 (HI threshold)
Entries 1795072
TAC
QT Input Channel

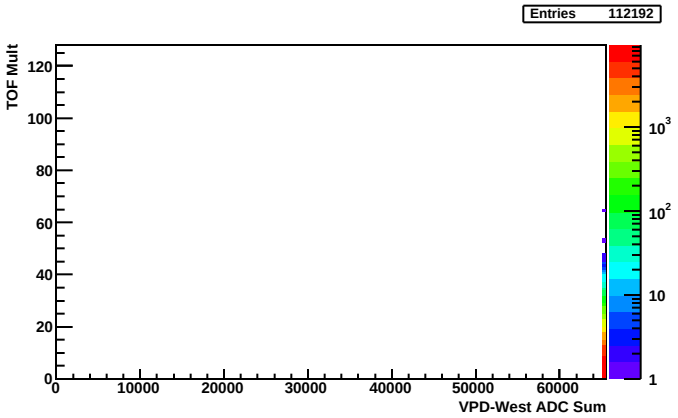

Entries 112192
TOF Mult
BBC-L-East ADC Sum

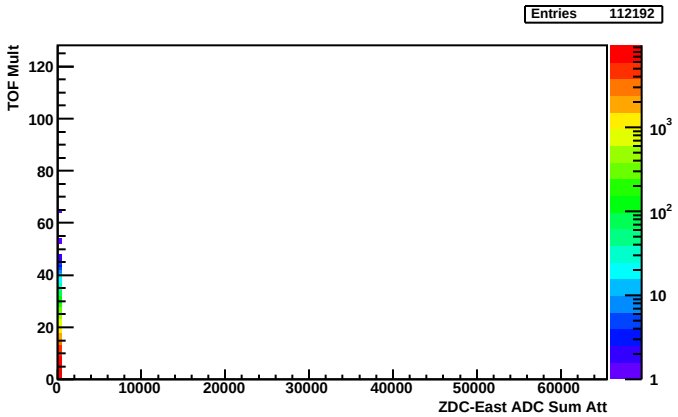

Entries 112192
TOF Mult
BBC-L-West ADC Sum

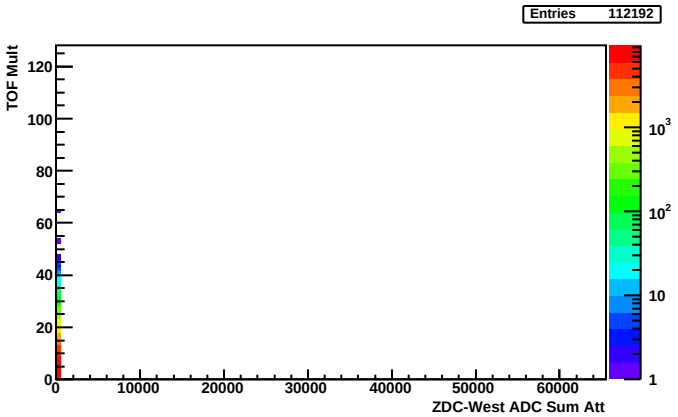

Entries 112192
TOF Mult
BBC-S-East ADC Sum

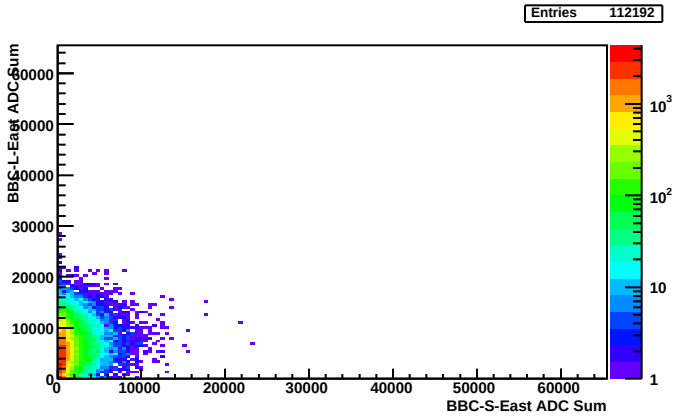

Entries 112192
TOF Mult
BBC-S-West ADC Sum

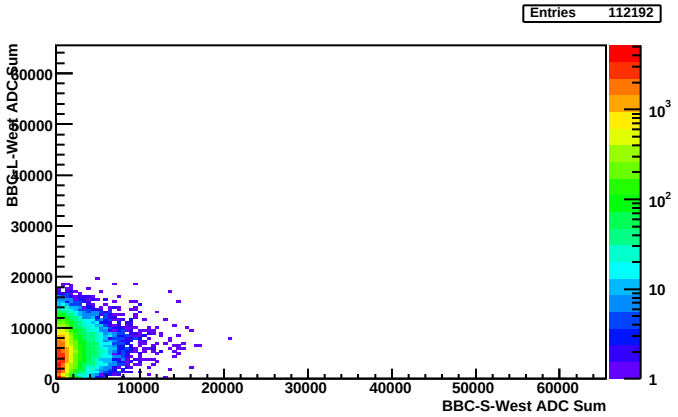

Entries 112192
TOF Mult
VPD-East ADC Sum

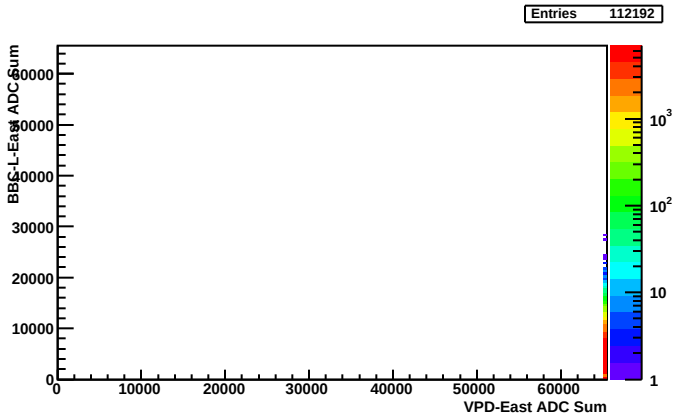

Entries 112192
TOF Mult
VPD-West ADC Sum

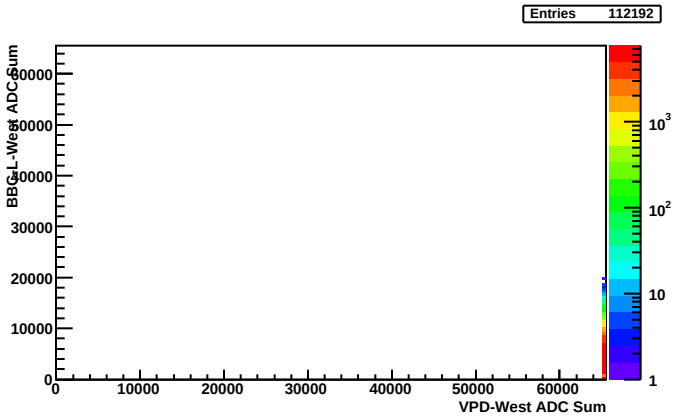

Entries 112192
TOF Mult
ZDC-East ADC Sum Att

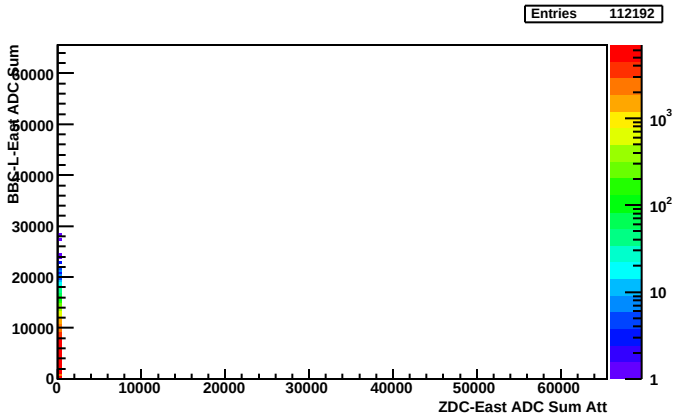

Entries 112192
TOF Mult
ZDC-West ADC Sum Att

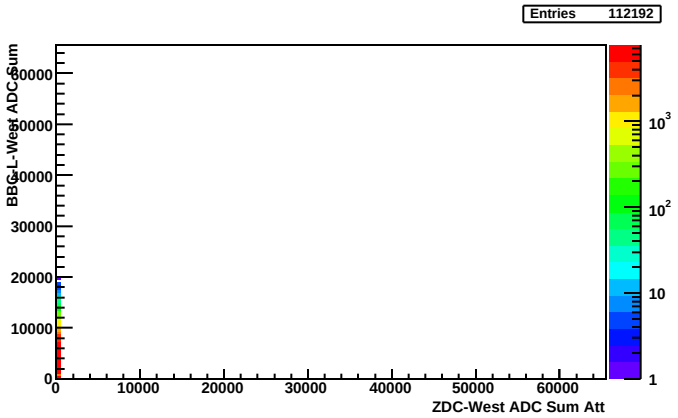

Entries 112192
BBC-L-East ADC Sum
BBC-S-East ADC Sum

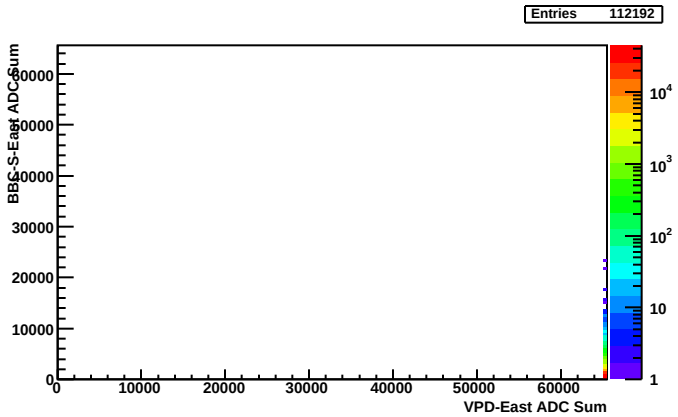

Entries 112192
BBC-L-West ADC Sum
BBC-S-West ADC Sum

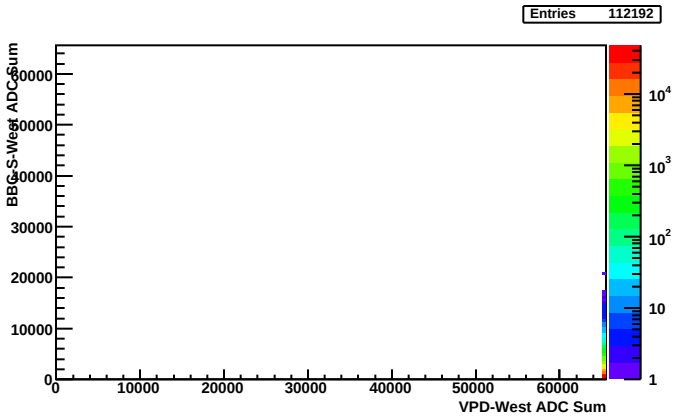

Entries 112192
BBC-L-East ADC Sum
VPD-East ADC Sum

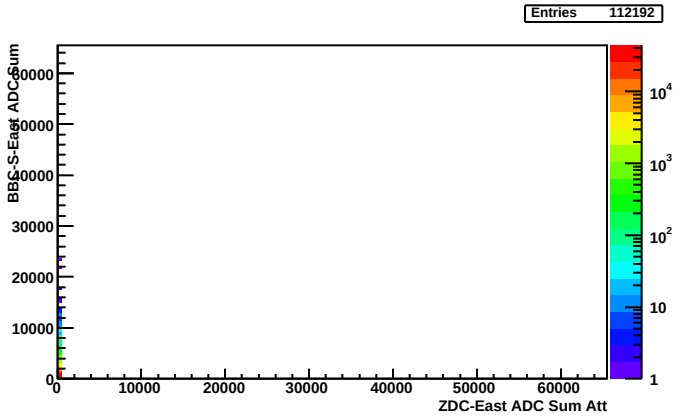

Entries 112192
BBC-L-West ADC Sum
VPD-West ADC Sum

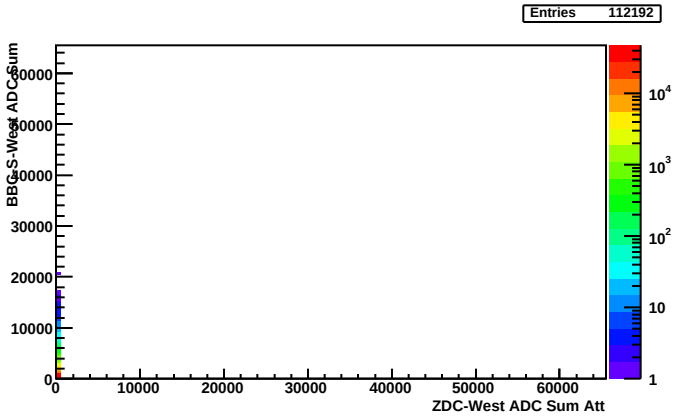

Entries 112192
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

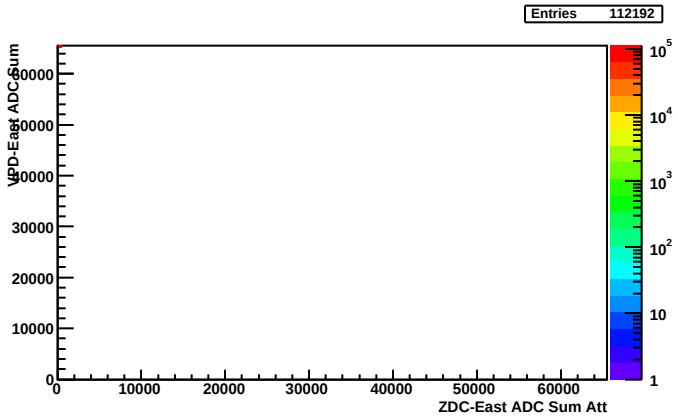

Entries 112192
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

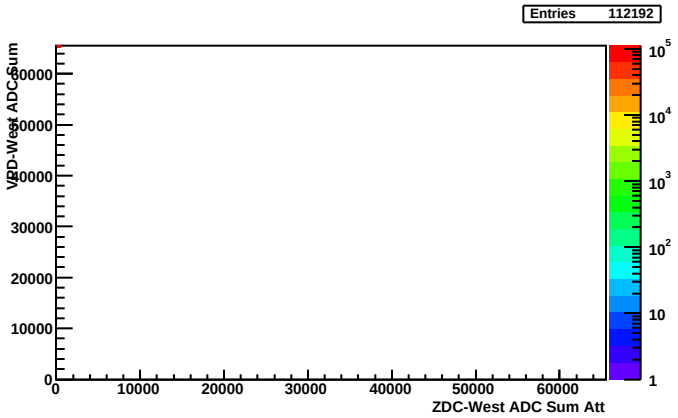

Entries 112192
BBC-S-East ADC Sum
VPD-East ADC Sum

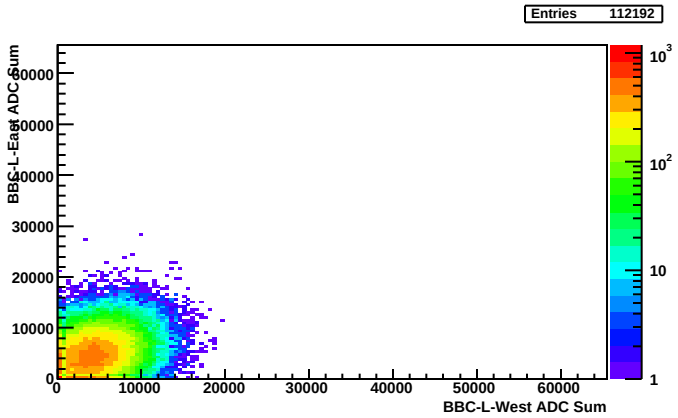

Entries 112192
BBC-S-West ADC Sum
VPD-West ADC Sum

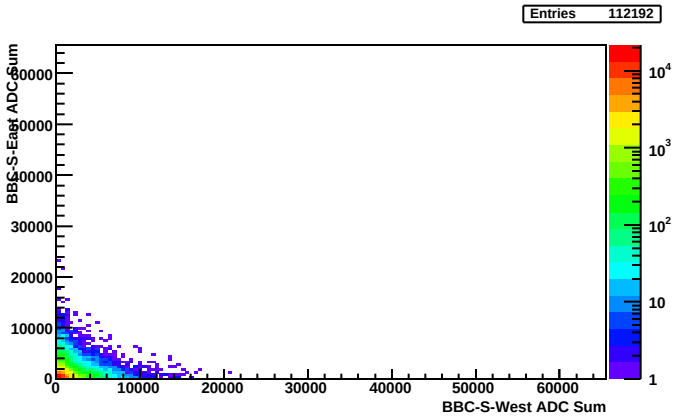

Entries 112192
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 112192
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 112192
VPD-East ADC Sum
ZDC-East ADC Sum Att


Entries 112192
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 112192
BBC-L-East ADC Sum
BBC-L-West ADC Sum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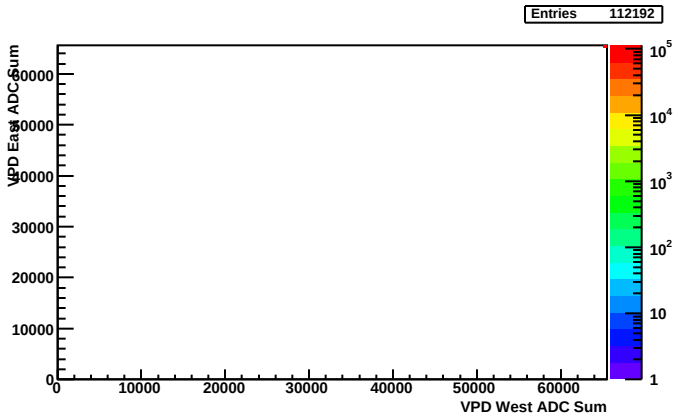
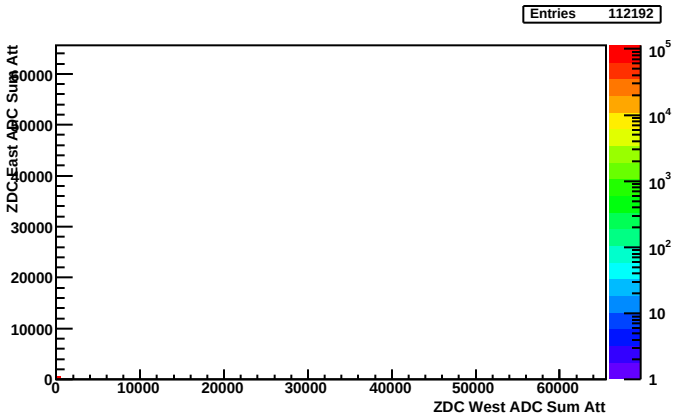
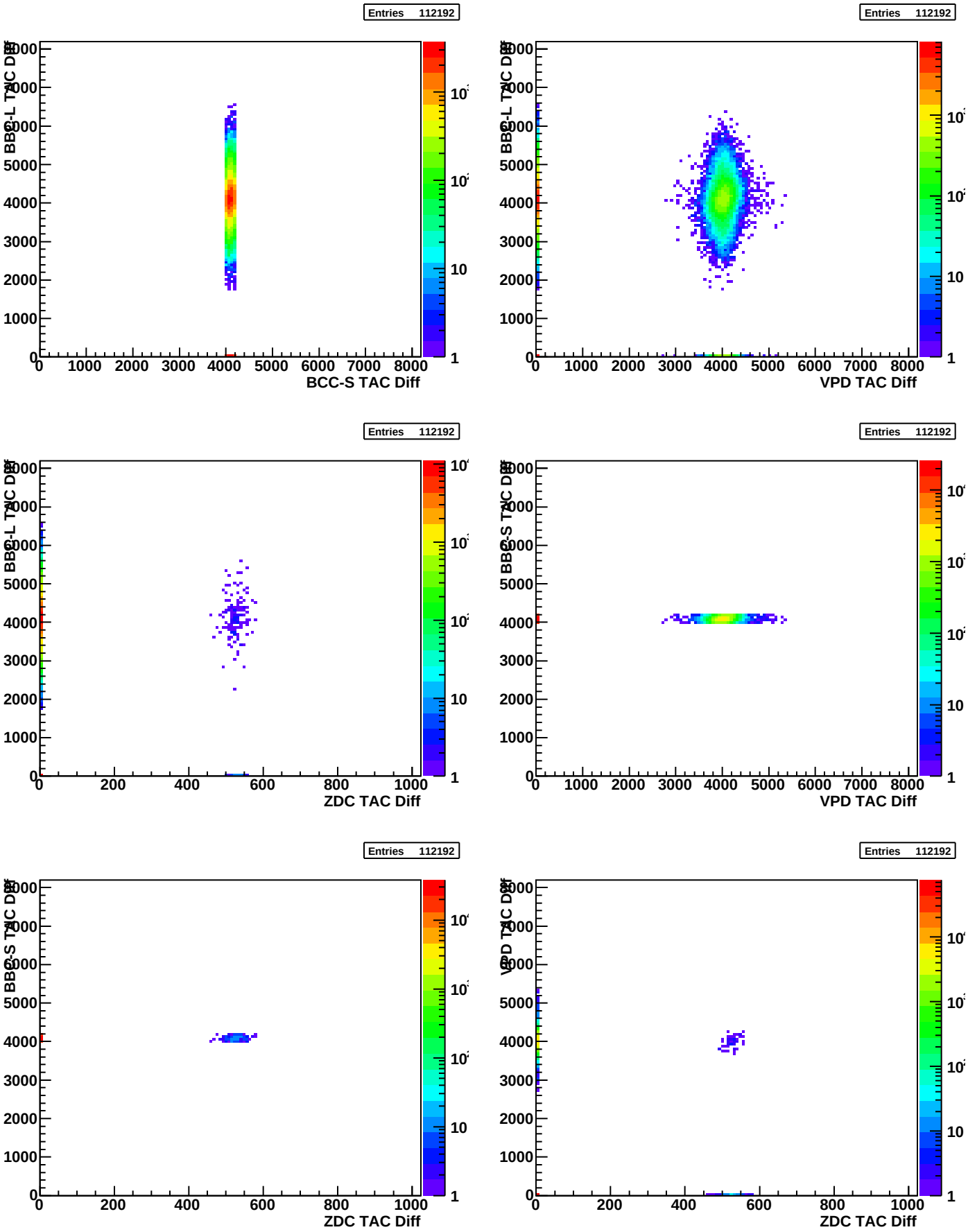
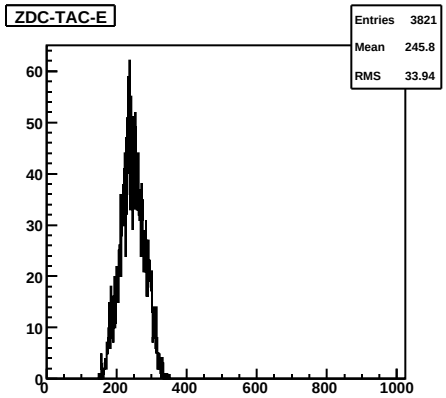
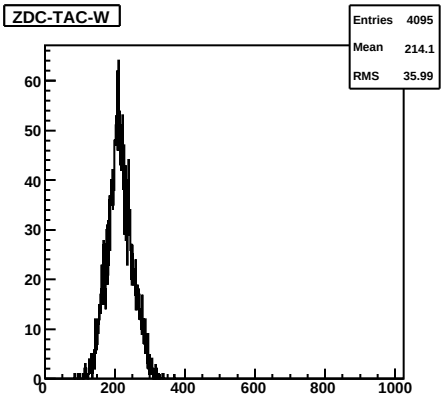
BBC-S-East ADC Sum
BBC-S-West ADC Sum

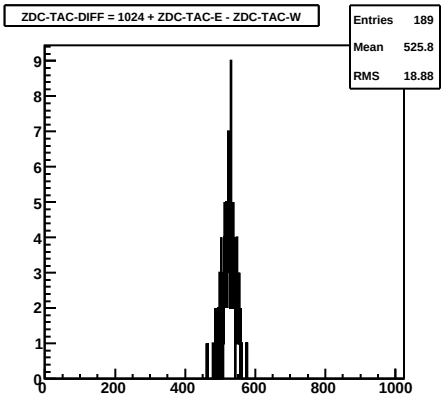

Entries 112192
VPD East ADC Sum
VPD West ADC Sum

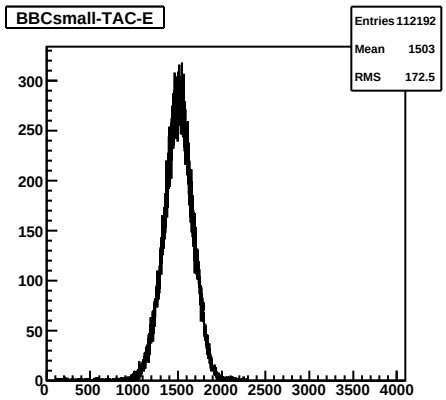

Entries 112192
ZDC East ADC Sum Att
ZDC West ADC Sum Att

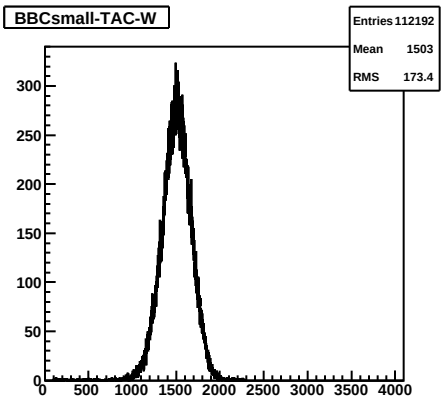

Entries 112192
BBC-L TAC Diff
BCC-S TAC Diff
Entries 112192
BBC-L TAC Diff
VPD TAC Diff
Entries 112192
BBC-L TAC Diff
ZDC TAC Diff
Entries 112192
BBC-S TAC Diff
VPD TAC Diff
Entries 112192
BBC-S TAC Diff
ZDC TAC Diff
Entries 112192
VPD TAC Diff
ZDC TAC Diff

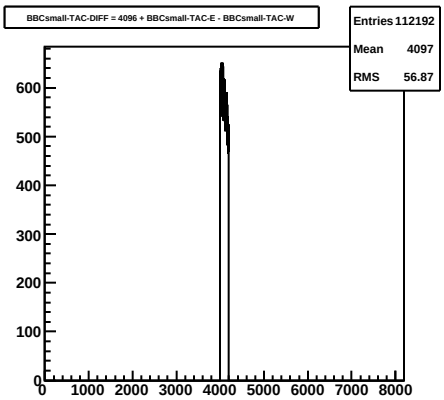

ZDC-TAC-E
Entries 3821
Mean 245.8
RMS 33.94

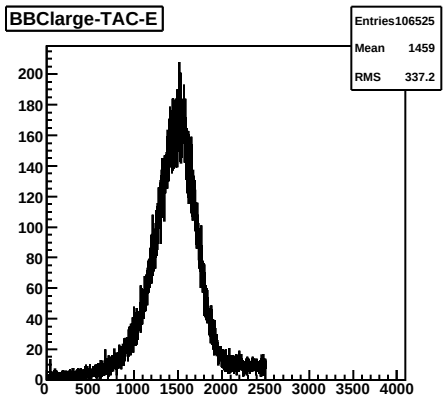

ZDC-TAC-W
Entries 4095
Mean 214.1
RMS 35.99

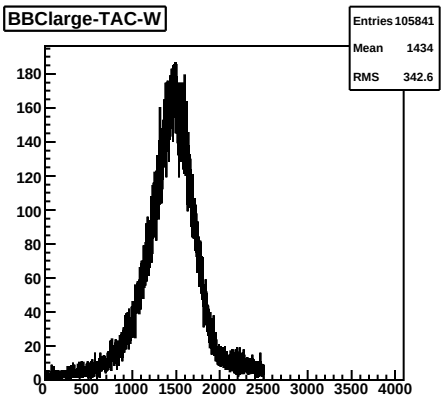

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 189
Mean 525.8
RMS 18.88

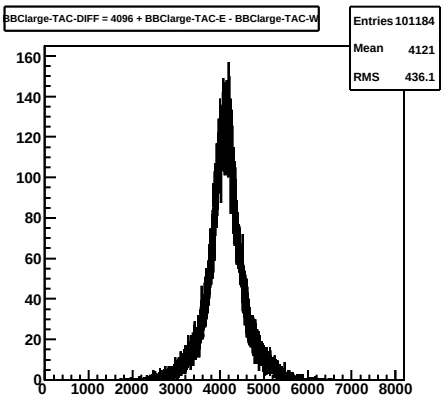

BBCsmall-TAC-E
Entries 112192
Mean 1503
RMS 172.5

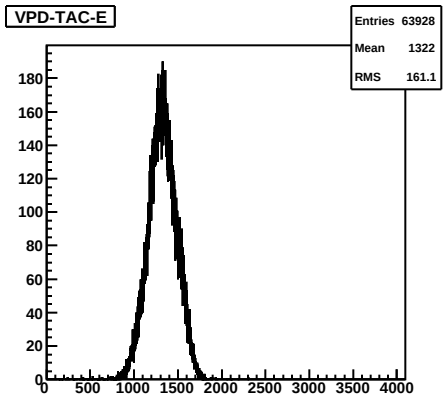

BBCsmall-TAC-W
Entries 112192
Mean 1503
RMS 173.4

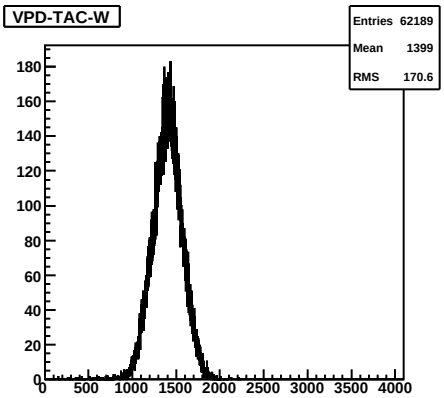

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 112192
Mean 4097
RMS 56.87

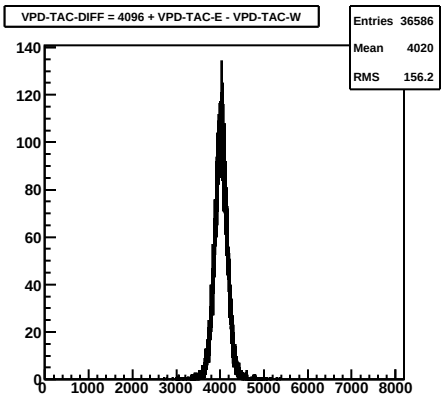

BBClarge-TAC-E
Entries 106525
Mean 1459
RMS 337.2

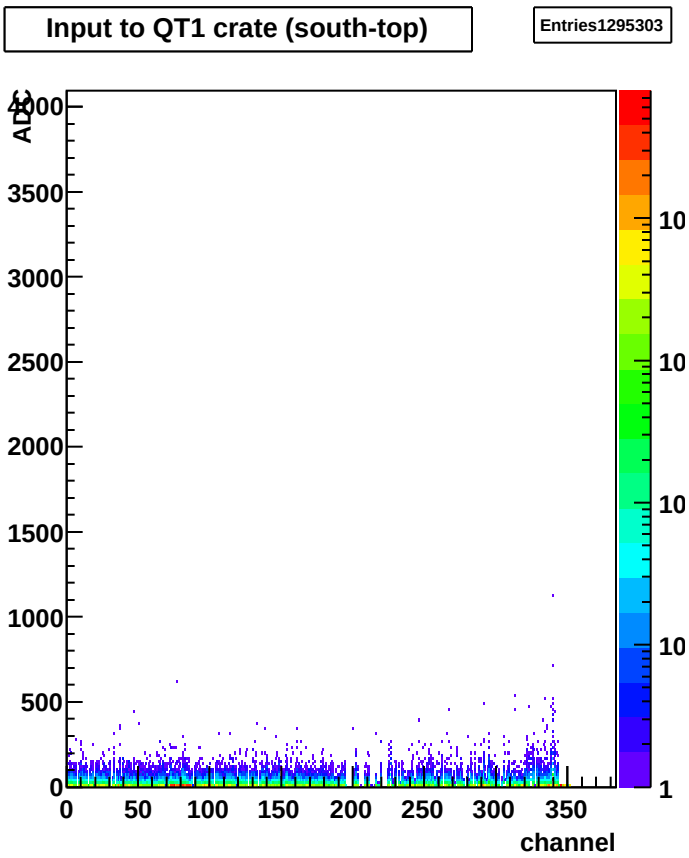

BBClarge-TAC-W
Entries 105841
Mean 1434
RMS 342.6

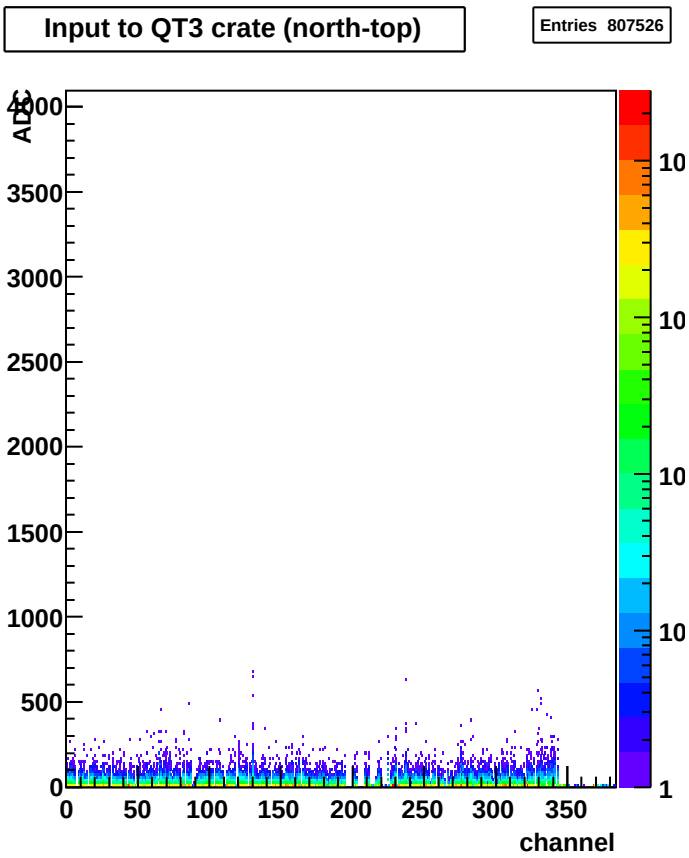

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 101184
Mean 4121
RMS 436.1

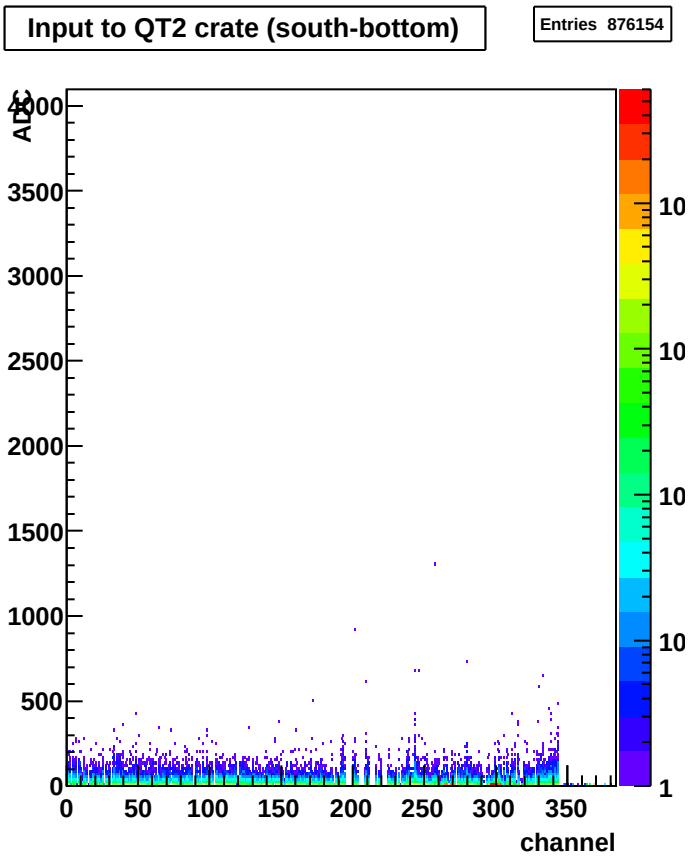

VPD-TAC-E
Entries 63928
Mean 1322
RMS 161.1

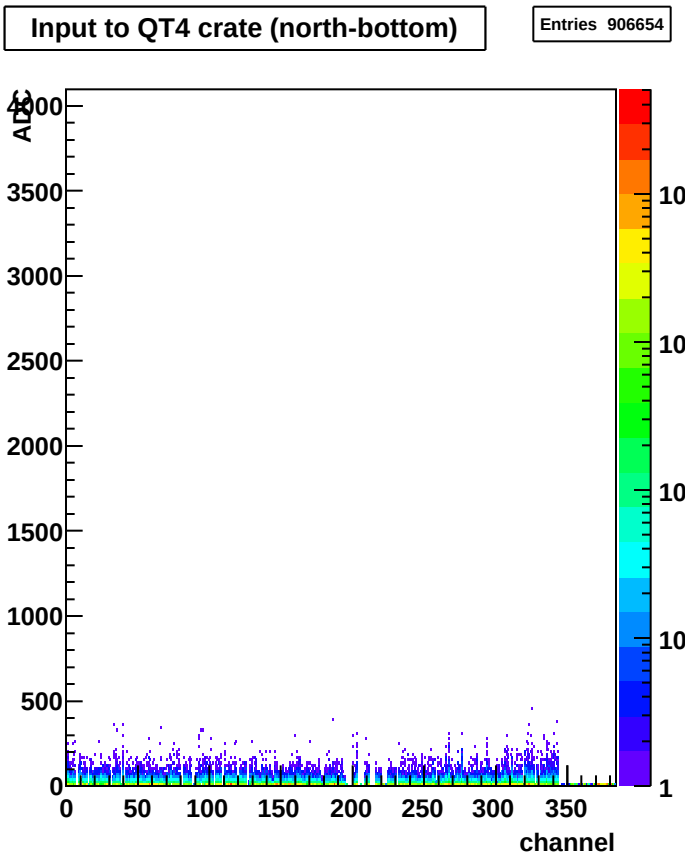

VPD-TAC-W
Entries 62189
Mean 1399
RMS 170.6

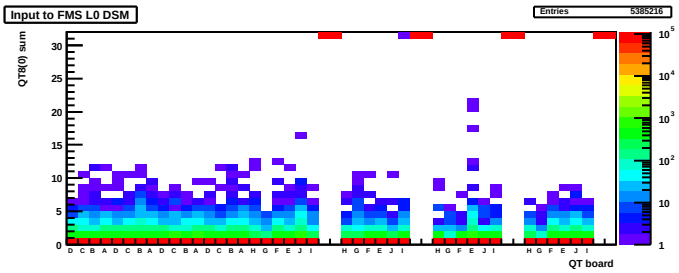

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 36586
Mean 4020
RMS 156.2

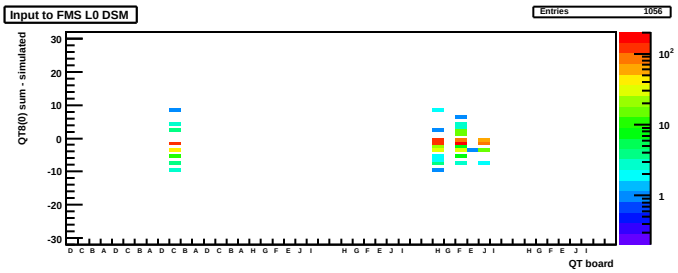

Input to QT1 crate (south-top)
Entries1295303
ADC
channel

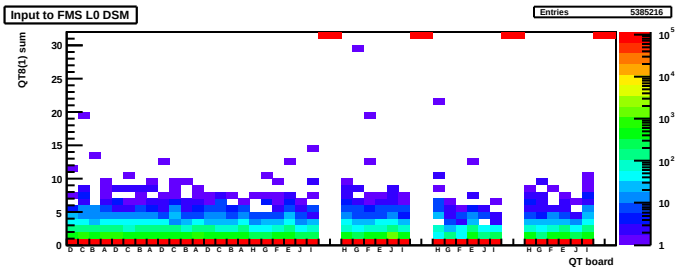

Input to QT3 crate (north-top)
Entries 807526
ADC
channel

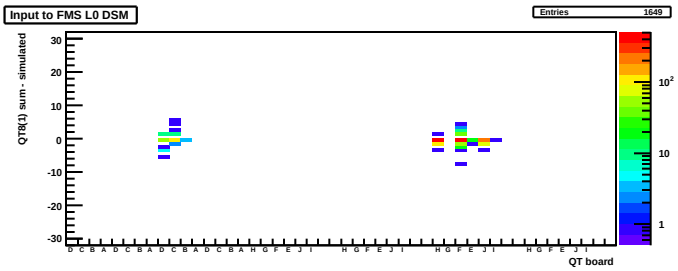

Input to QT2 crate (south-bottom)
Entries 876154
ADC
channel

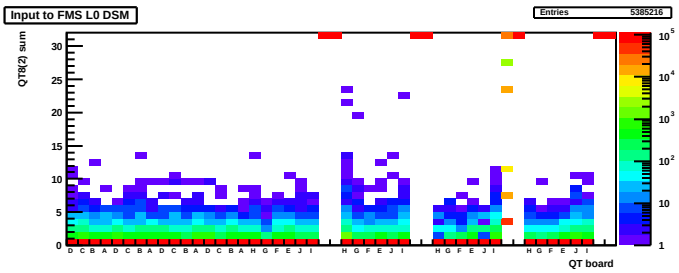

Input to QT4 crate (north-bottom)
Entries 906654
ADC
channel

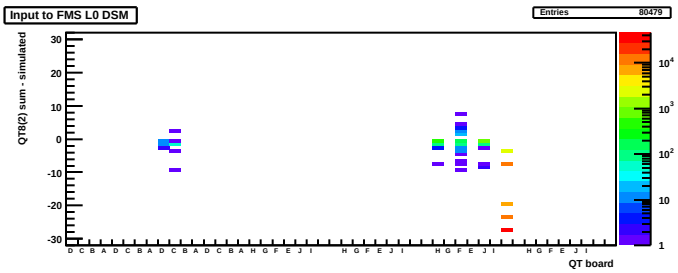

Input to FMS L0 DSM
Entries 5385216
QT8(0) sum
QT board

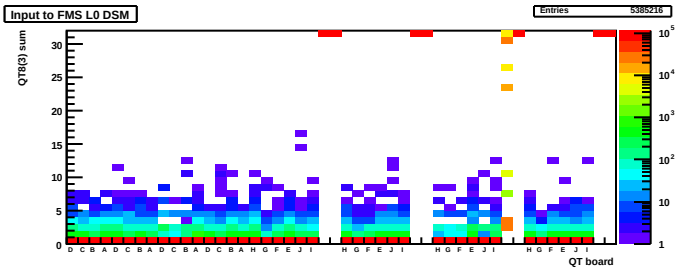

Input to FMS L0 DSM
Entries 1056
QT8(0) sum - simulated
QT board

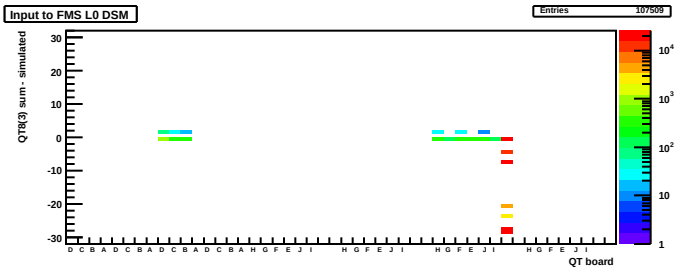

Input to FMS L0 DSM
Entries 5385216
QT8(1) sum
QT board

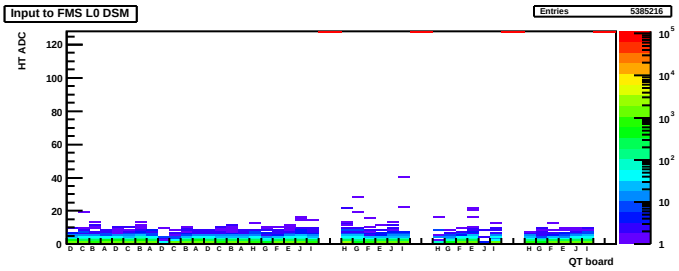

Input to FMS L0 DSM
Entries 1649
QT8(1) sum - simulated
QT board

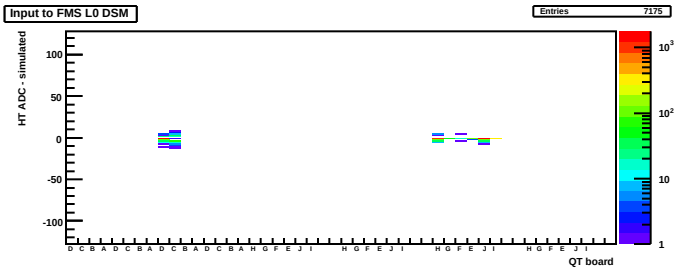

Input to FMS L0 DSM
Entries 5385216
QT8(2) sum
QT board

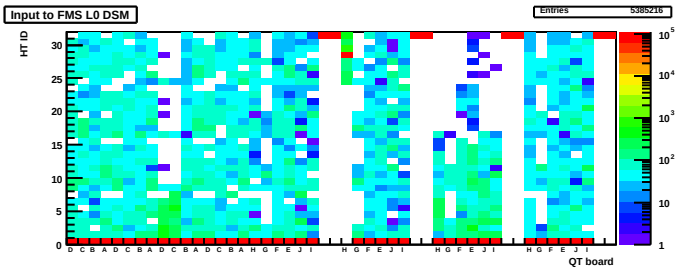

Input to FMS L0 DSM
Entries 80479
QT8(2) sum - simulated
QT board

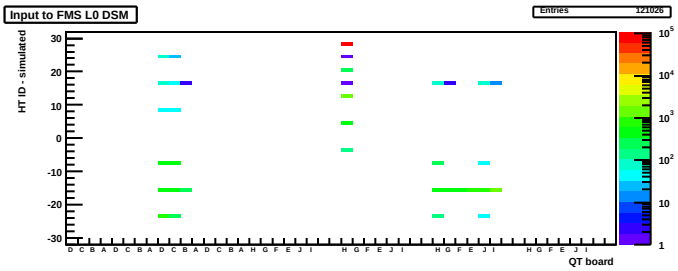

Input to FMS L0 DSM
Entries 5385216
QT8(3) sum
QT board

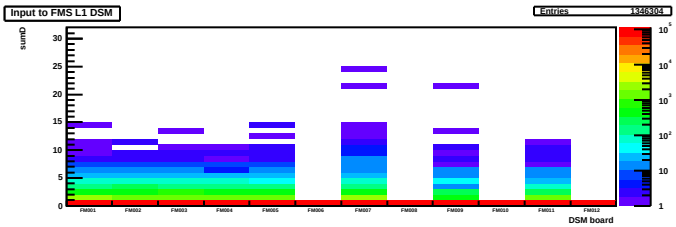

Input to FMS L0 DSM
Entries 107509
QT8(3) sum - simulated
QT board

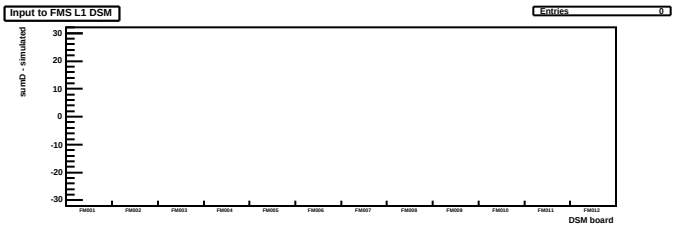

Input to FMS L0 DSM
Entries 5385216
HT ADC
QT board

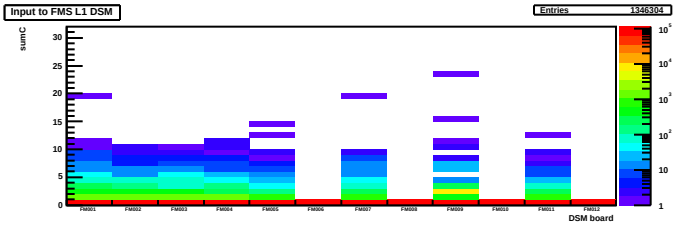

Input to FMS L0 DSM
Entries 7175
HT ADC - simulated
QT board

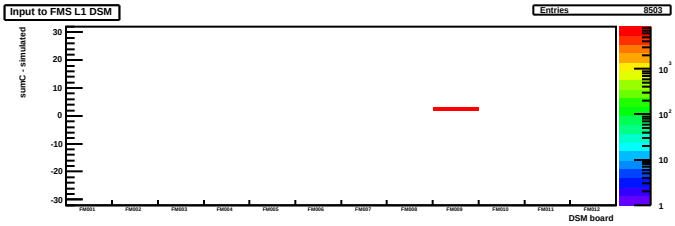

Input to FMS L0 DSM
Entries 5385216
HT ID
QT board

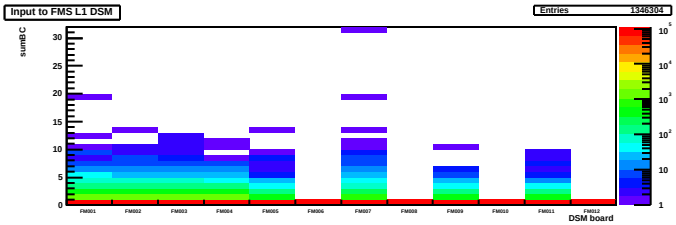

Input to FMS L0 DSM
Entries 121026
HT ID - simulated
QT board

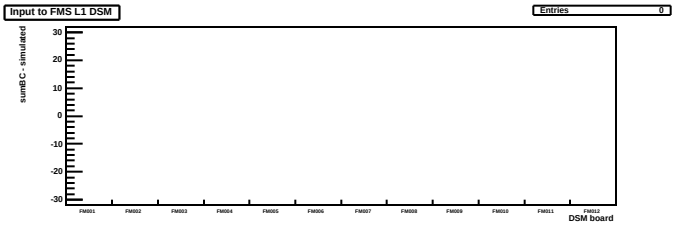

Input to FMS L1 DSM
Entries 1346304
sumD
DSM board

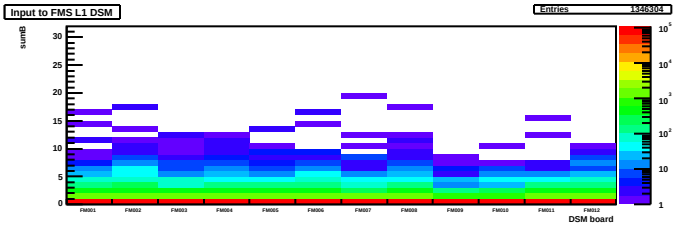

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

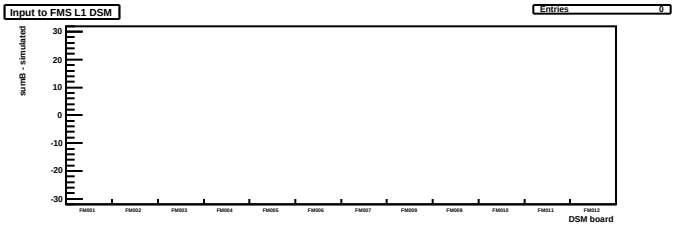

Input to FMS L1 DSM
Entries 1346304
sumC
DSM board

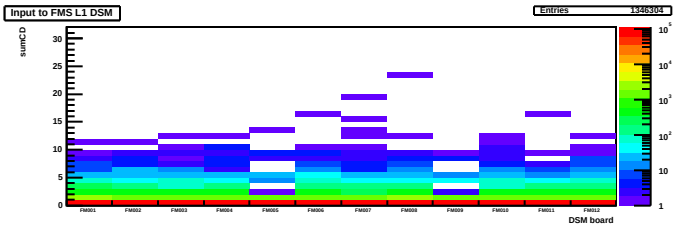

Input to FMS L1 DSM
Entries 8503
sumC - simulated
DSM board

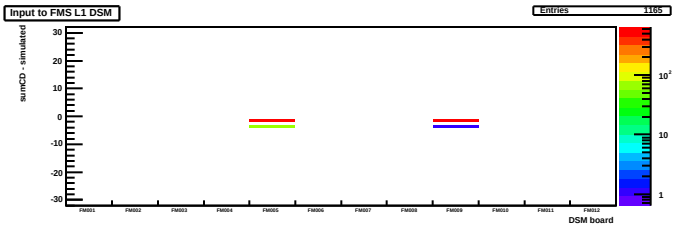

Input to FMS L1 DSM
Entries 1346304
sumBC
DSM board

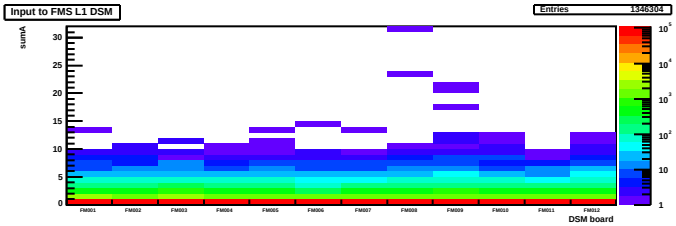

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

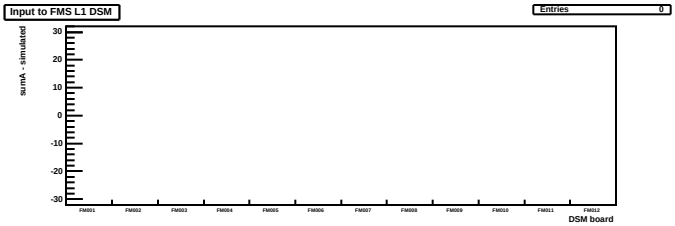

Input to FMS L1 DSM
Entries 1346304
sumB
DSM board

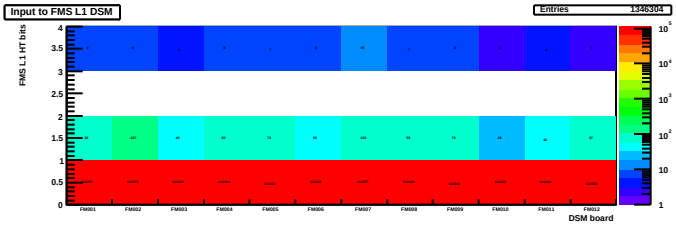

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

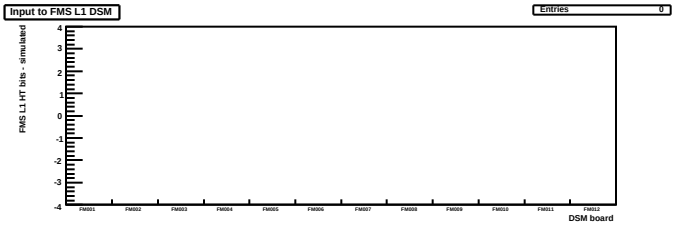

Input to FMS L1 DSM
Entries 1346304
sumCD
DSM board

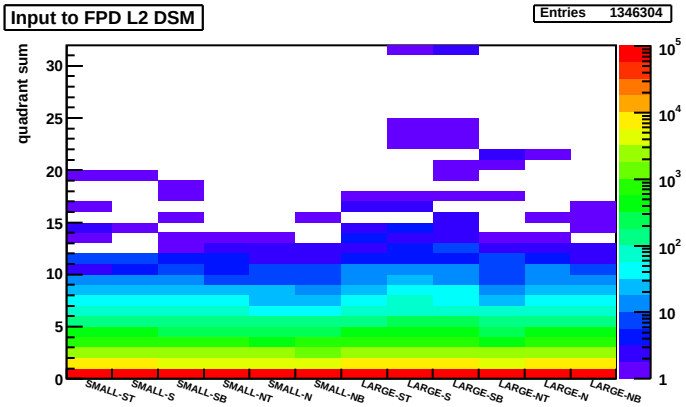

Input to FMS L1 DSM
Entries 1165
sumCD - simulated
DSM board

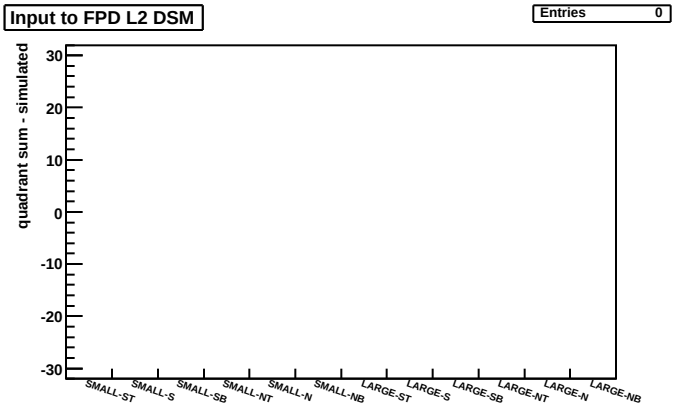

Input to FMS L1 DSM
Entries 1346304
sumA
DSM board

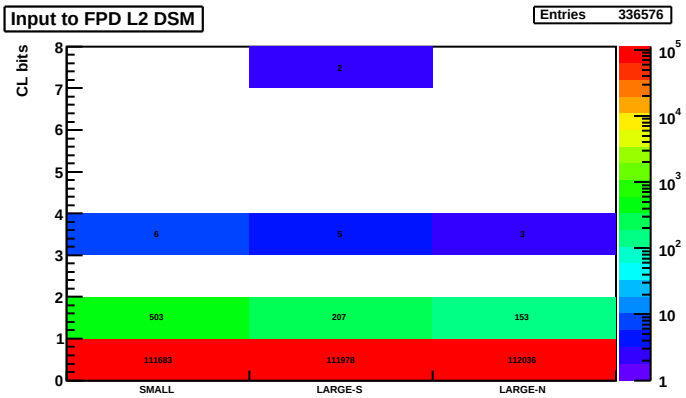

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

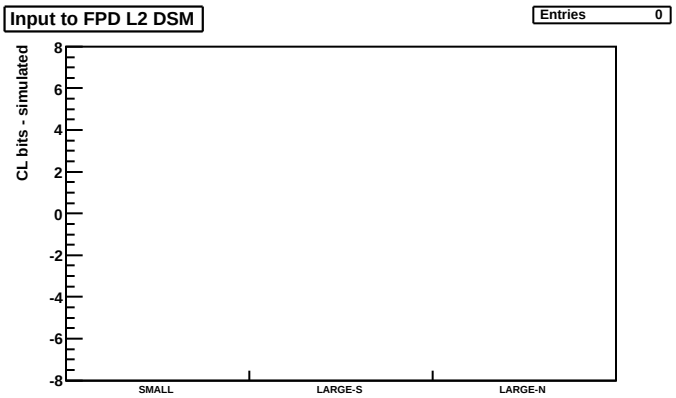

Input to FMS L1 DSM
Entries 1346304
FMS L1 HT bits
DSM board

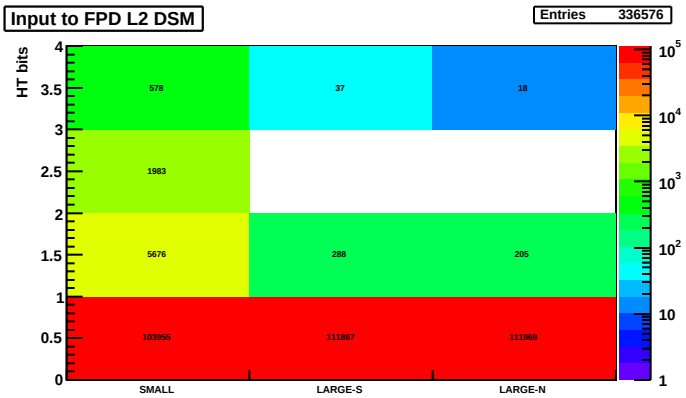

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

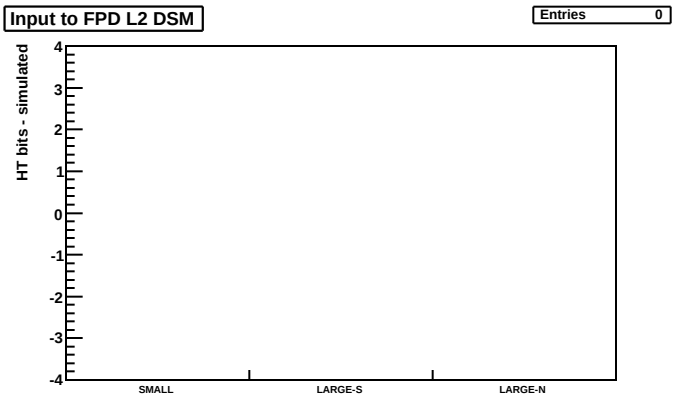

Input to FPD L2 DSM
Entries 1346304
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

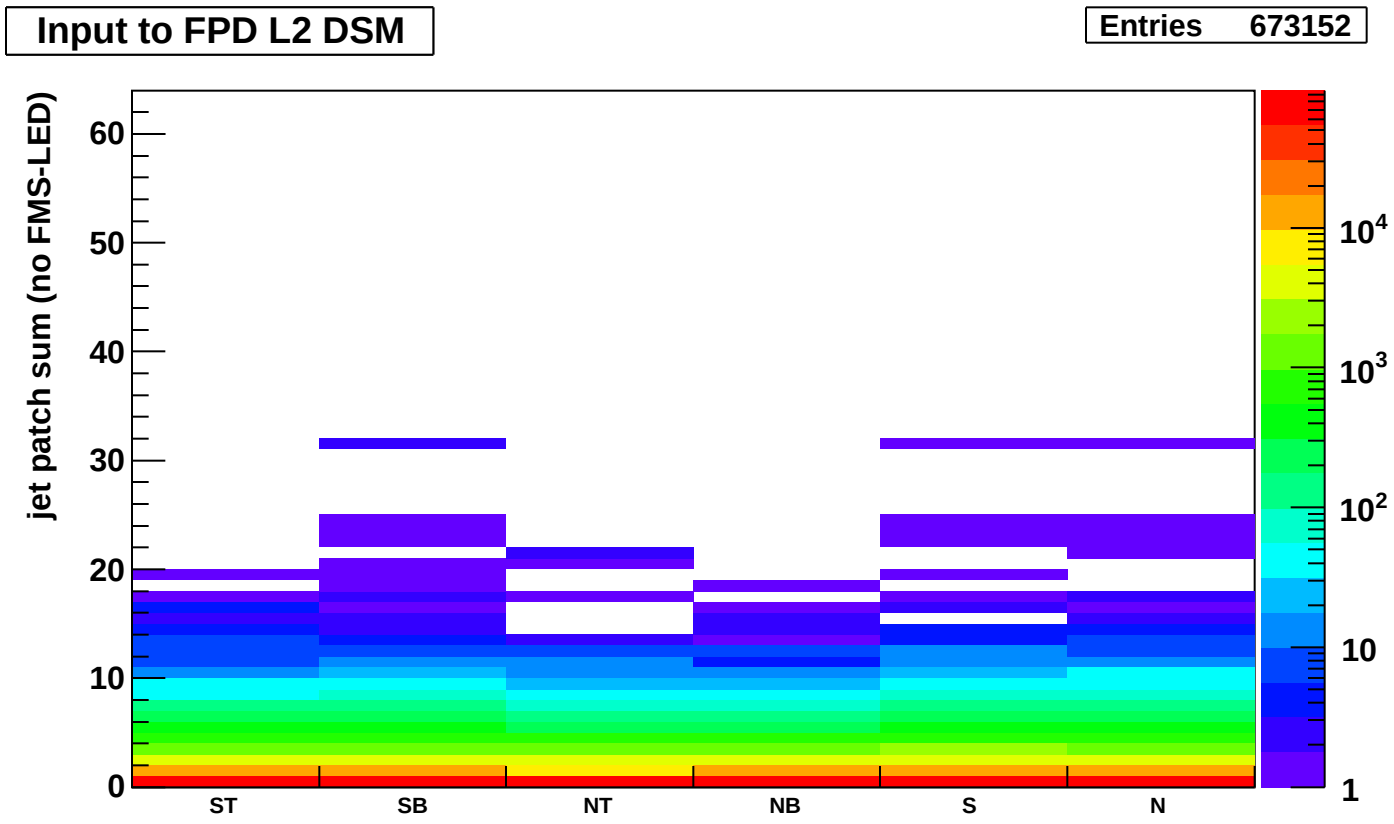

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

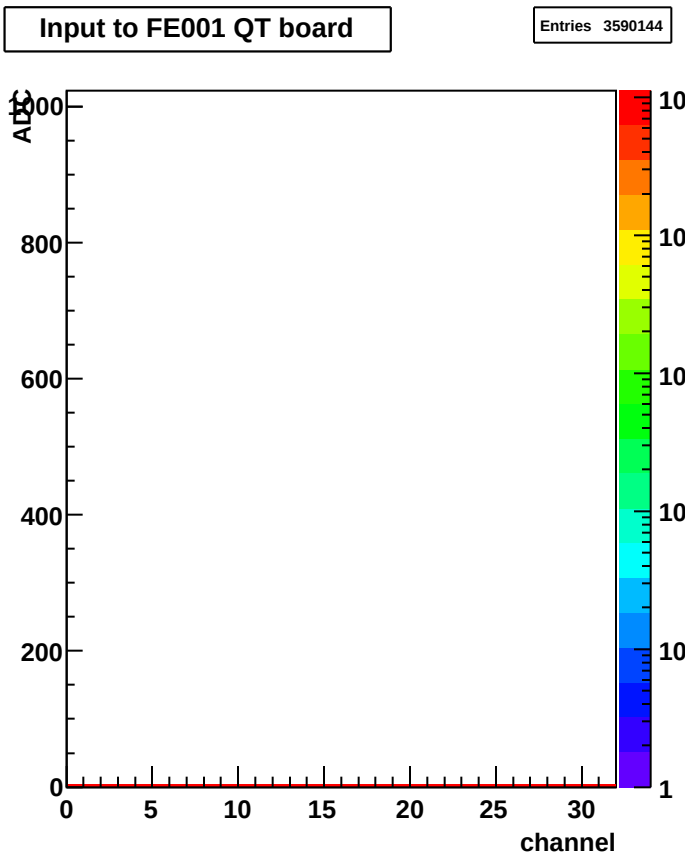

Input to FPD L2 DSM
Entries 336576
CL bits
2
6
5
3
503
207
153
111683
111978
112036
SMALL
LARGE-S
LARGE-N

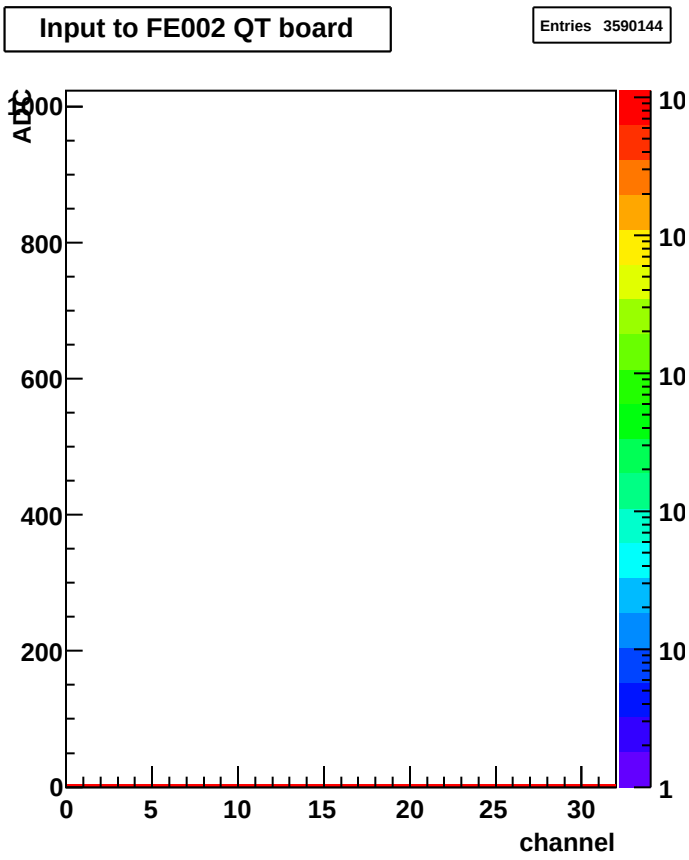

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

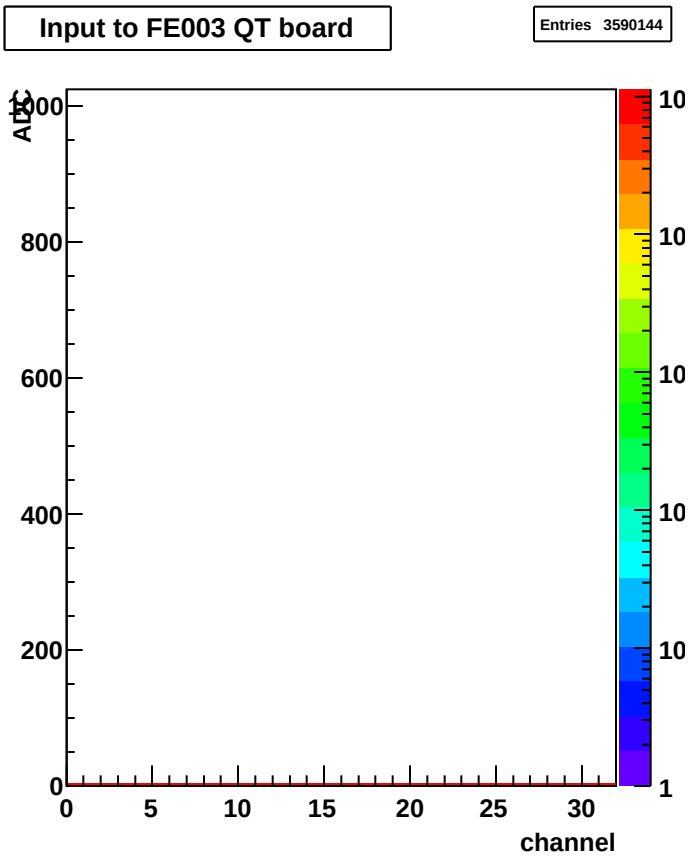

Input to FPD L2 DSM
Entries 336576
HT bits
578
37
18
1983
5676
288
205
103955
111867
111969
SMALL
LARGE-S
LARGE-N

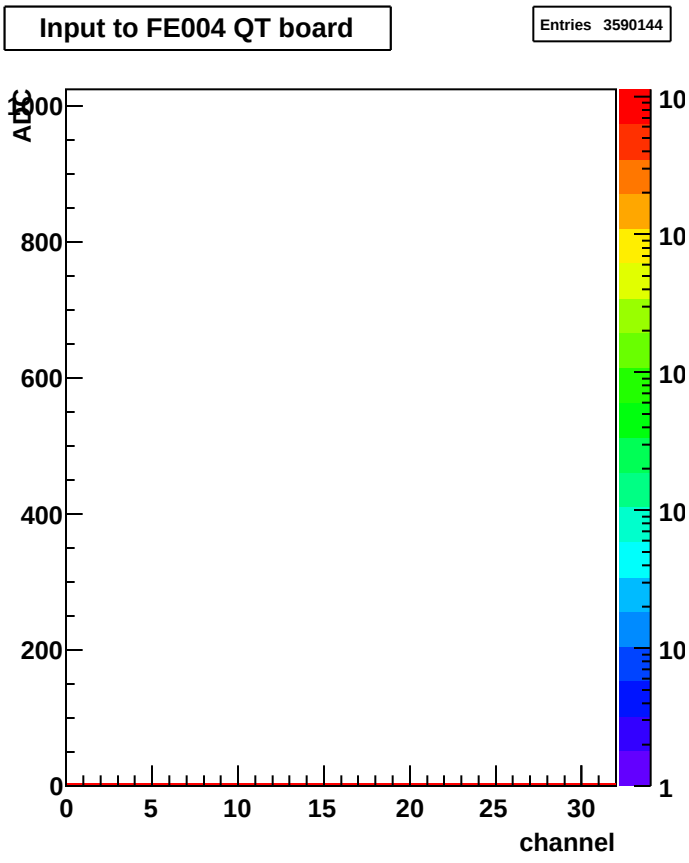

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

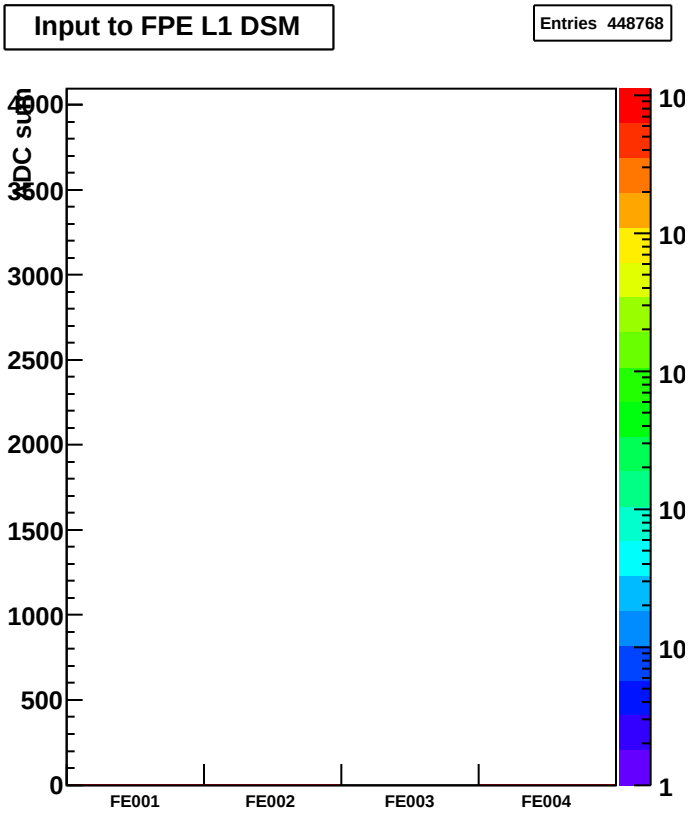

Input to FPD L2 DSM
Entries 673152
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

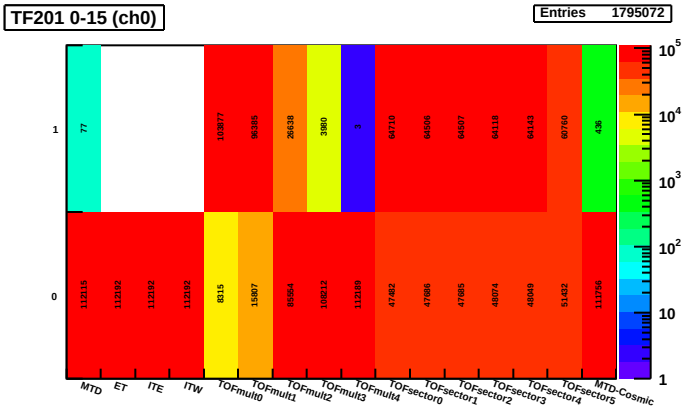

Input to FE001 QT board
Entries 3590144
ADC
channel

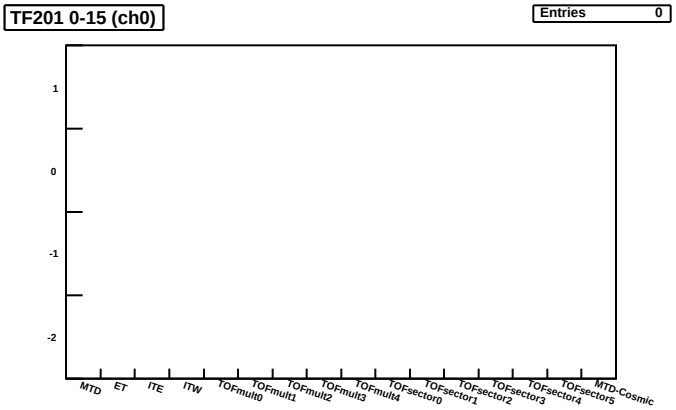

Input to FE002 QT board
Entries 3590144
ADC
channel

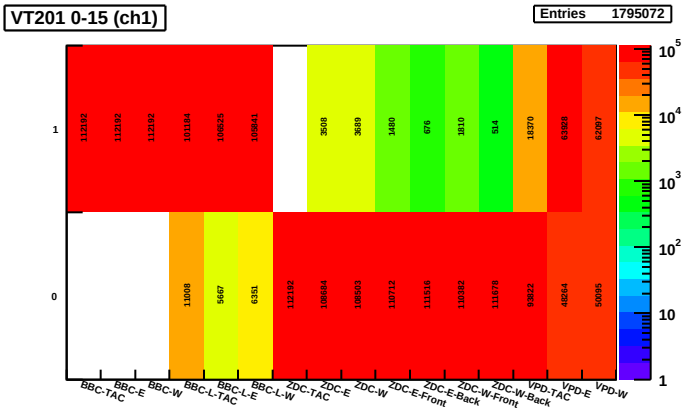

Input to FE003 QT board
Entries 3590144
ADC
channel

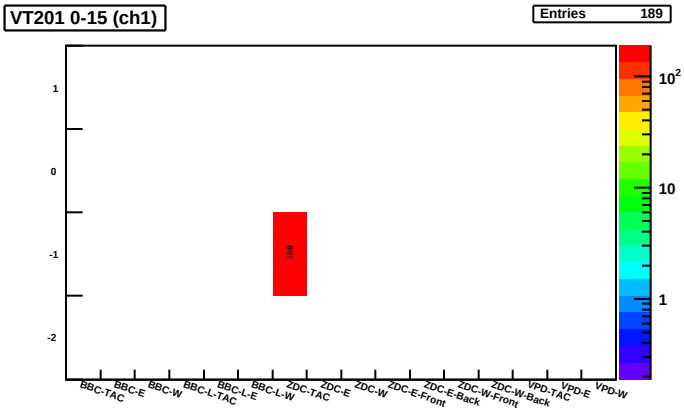

Input to FE004 QT board
Entries 3590144
ADC
channel

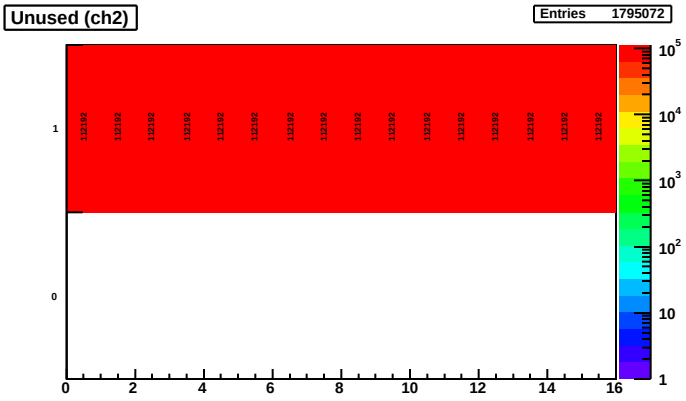

Input to FPE L1 DSM
Entries 448768
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

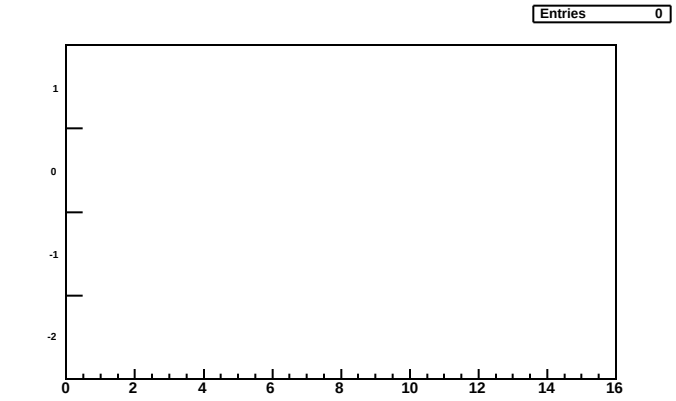

TF201 0-15 (ch0)
Entries 1795072
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

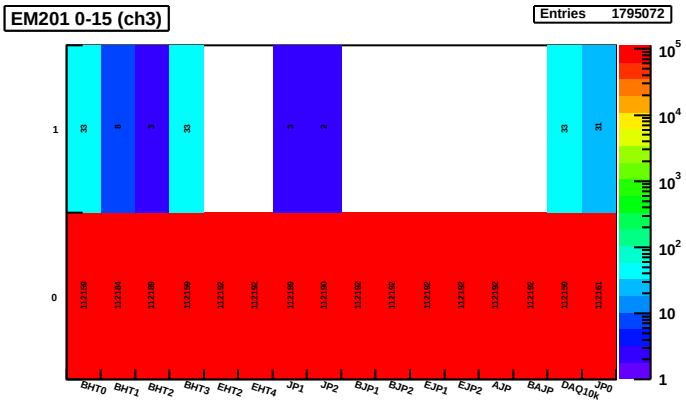

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

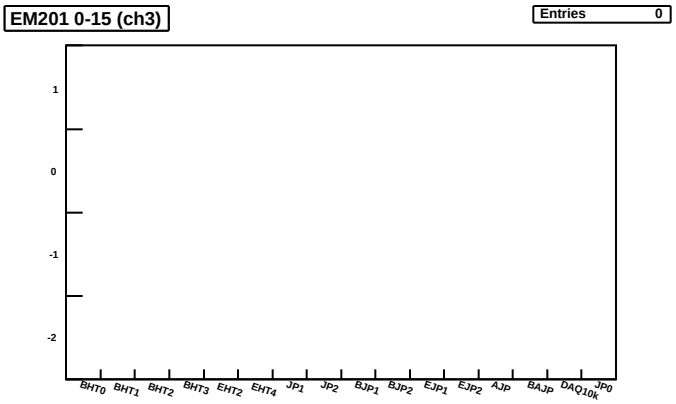

VT201 0-15 (ch1)
Entries 1795072
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 189
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 1795072


Entries 0


EM201 0-15 (ch3)
Entries 1795072
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0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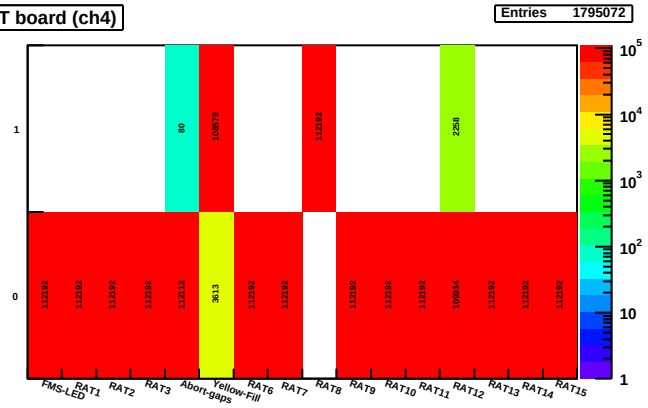

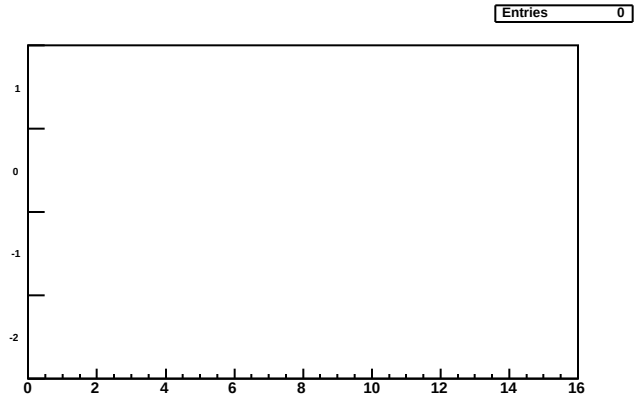

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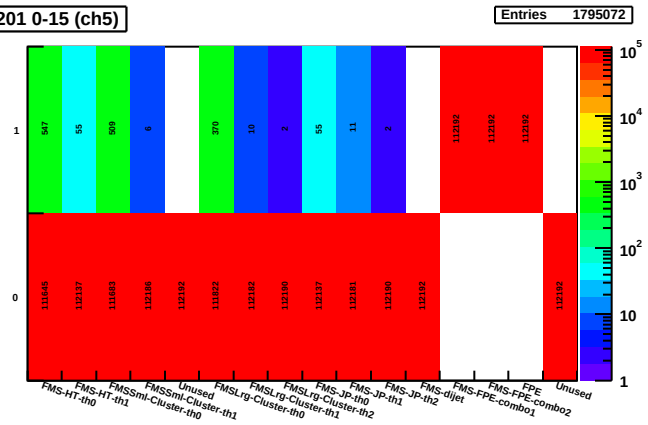

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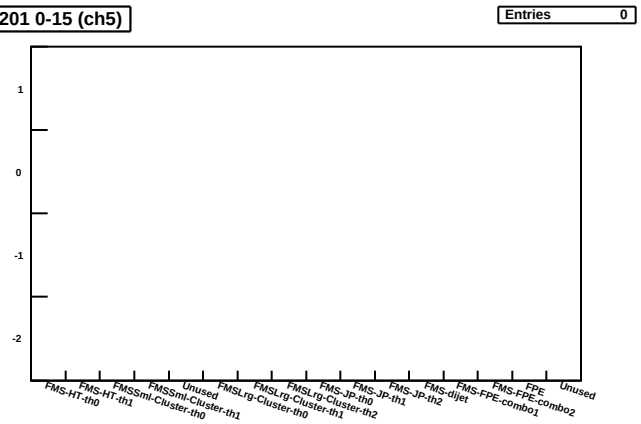

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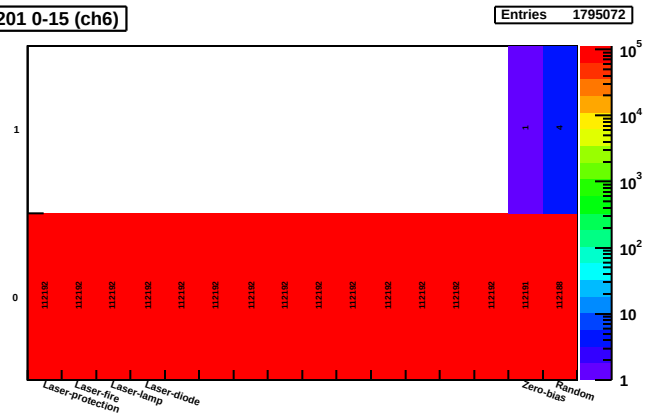

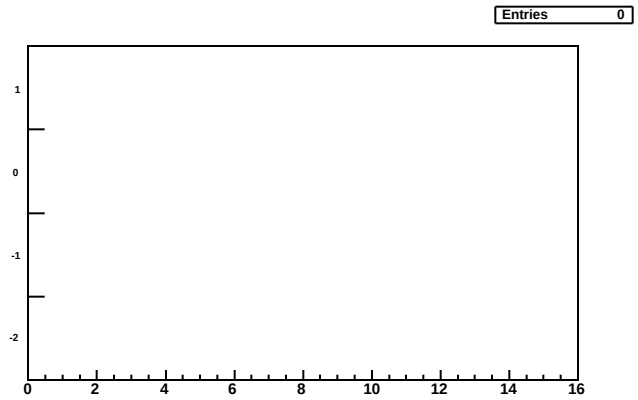
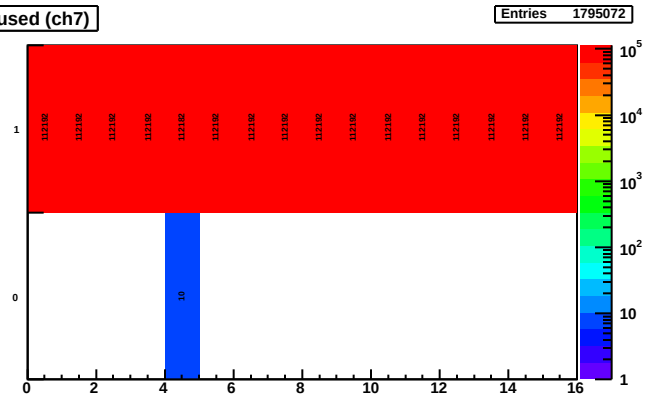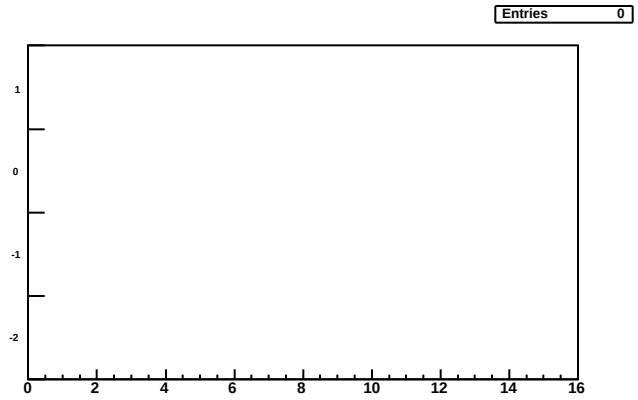
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