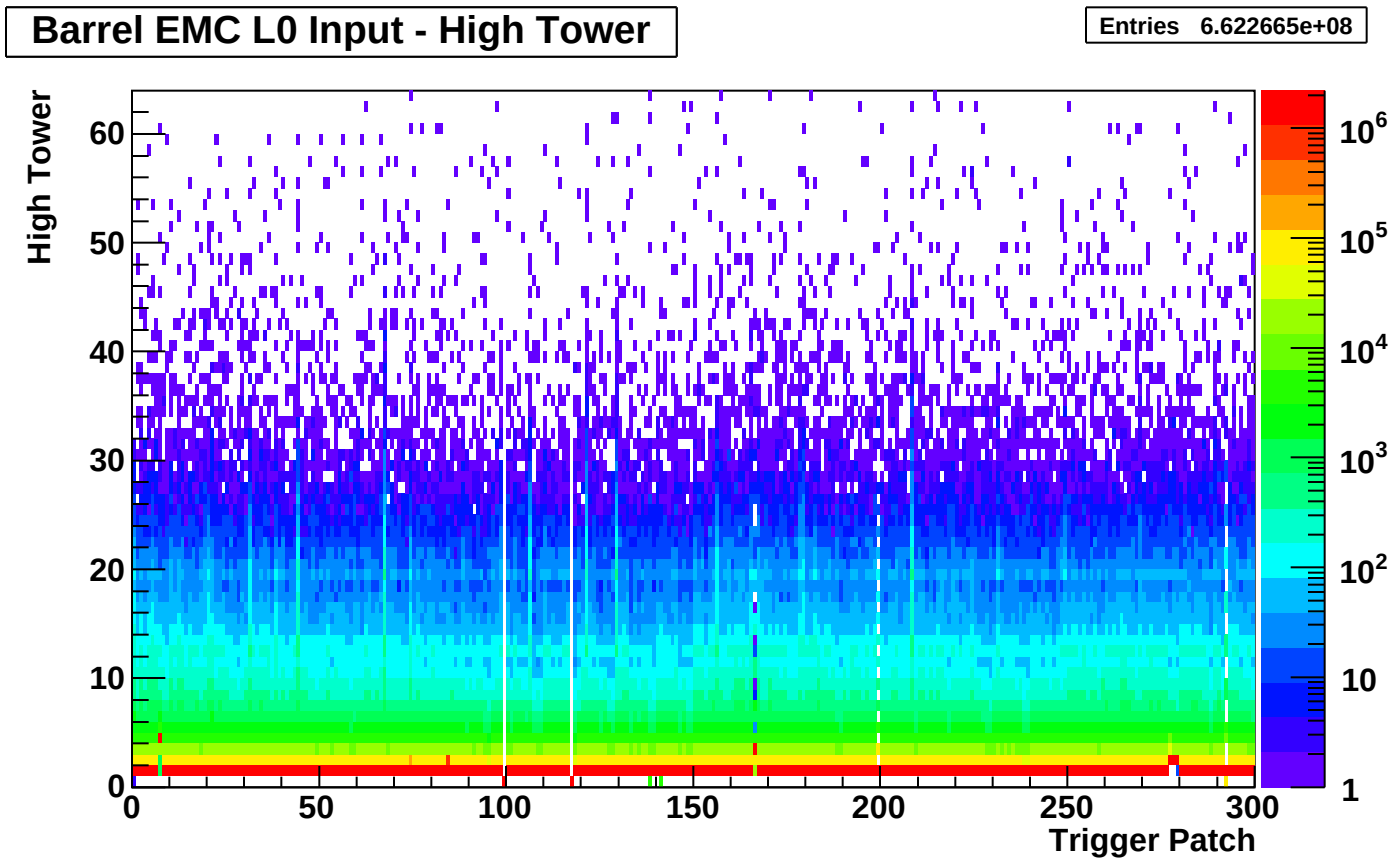
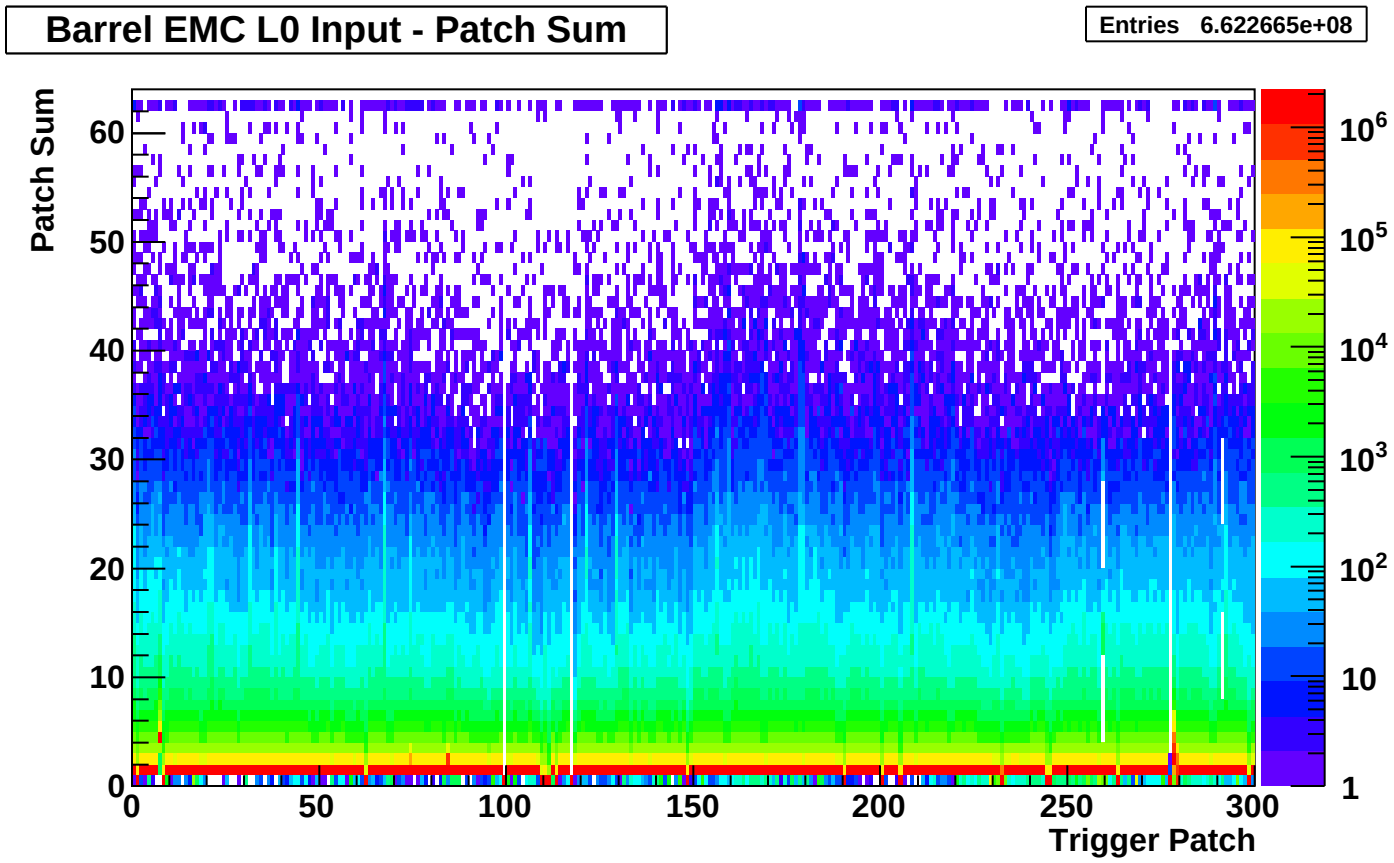
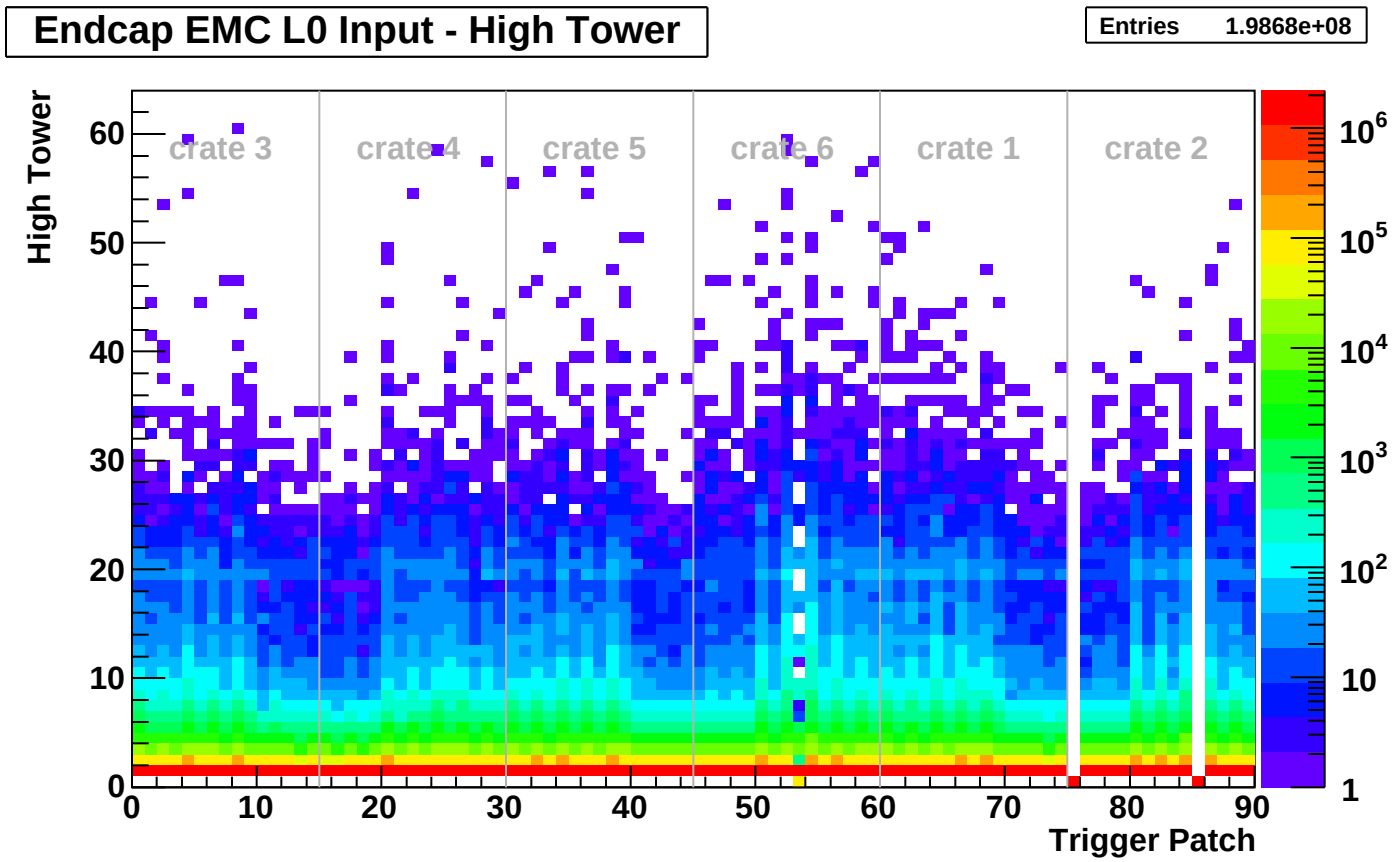
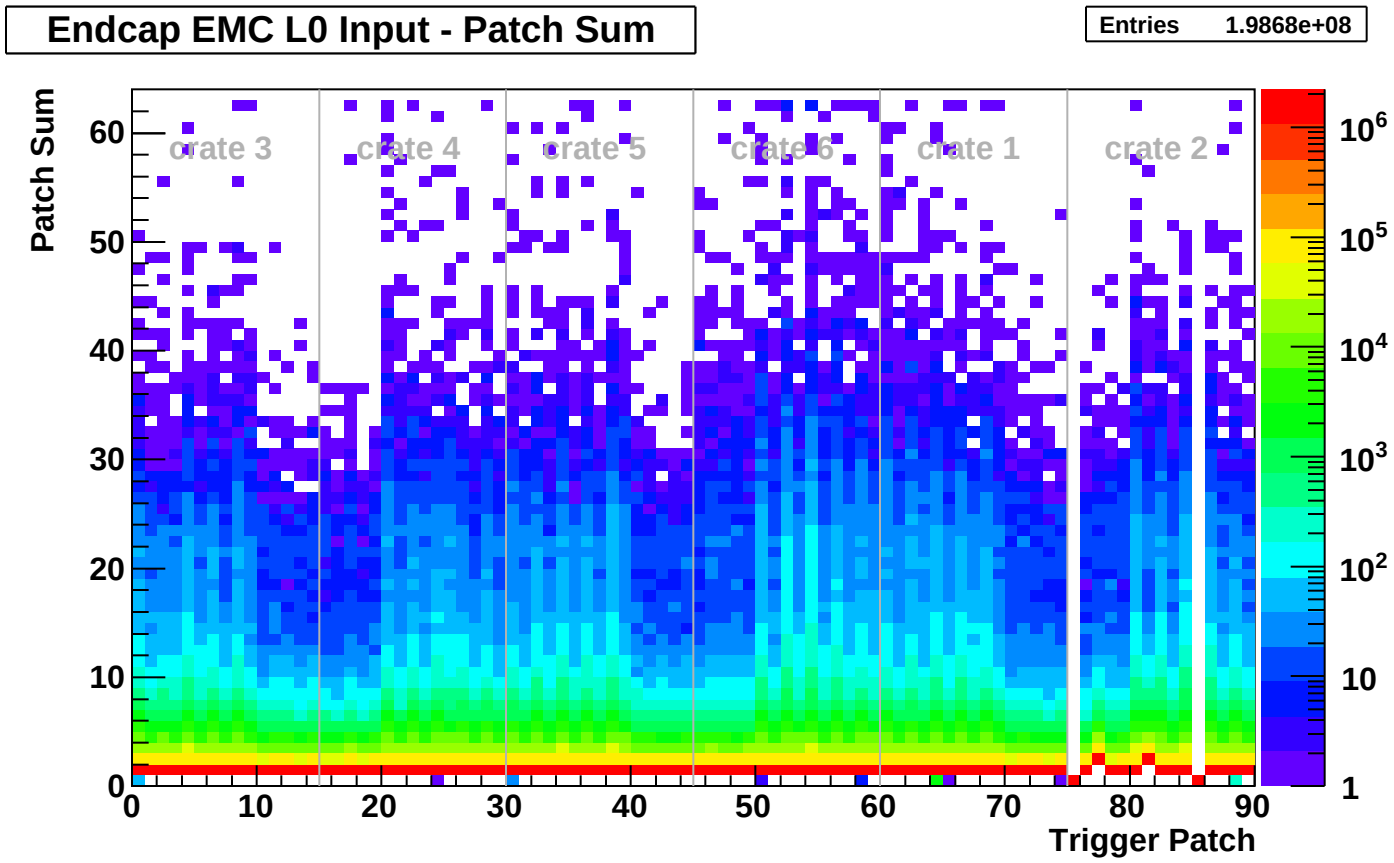
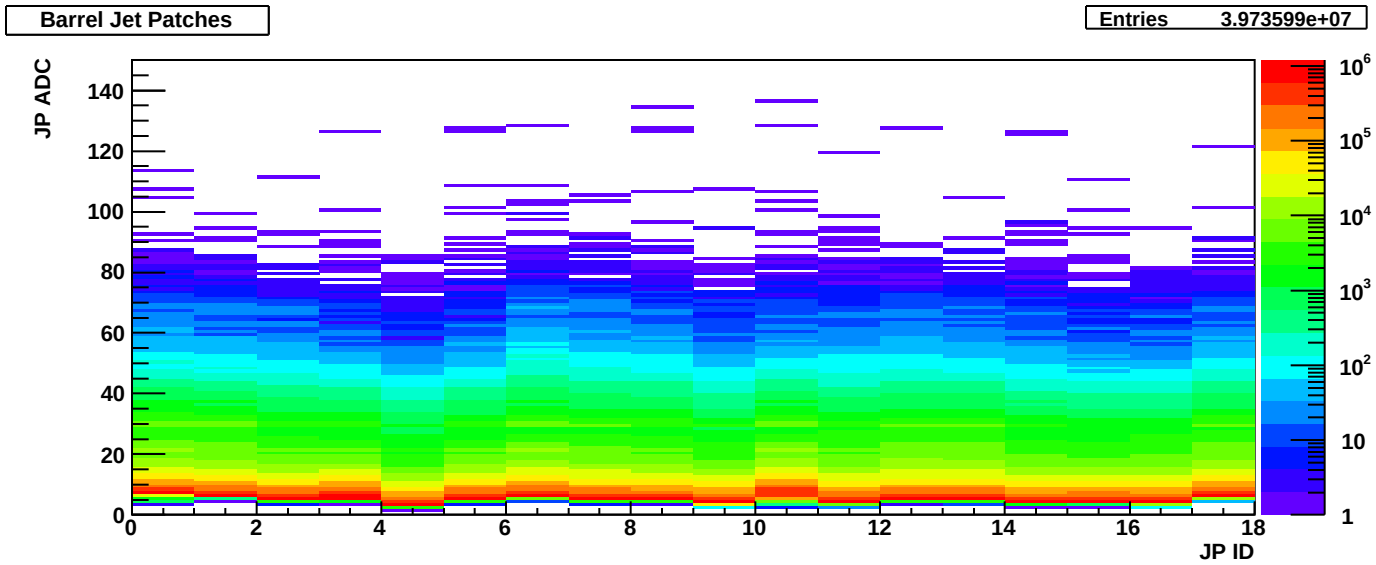
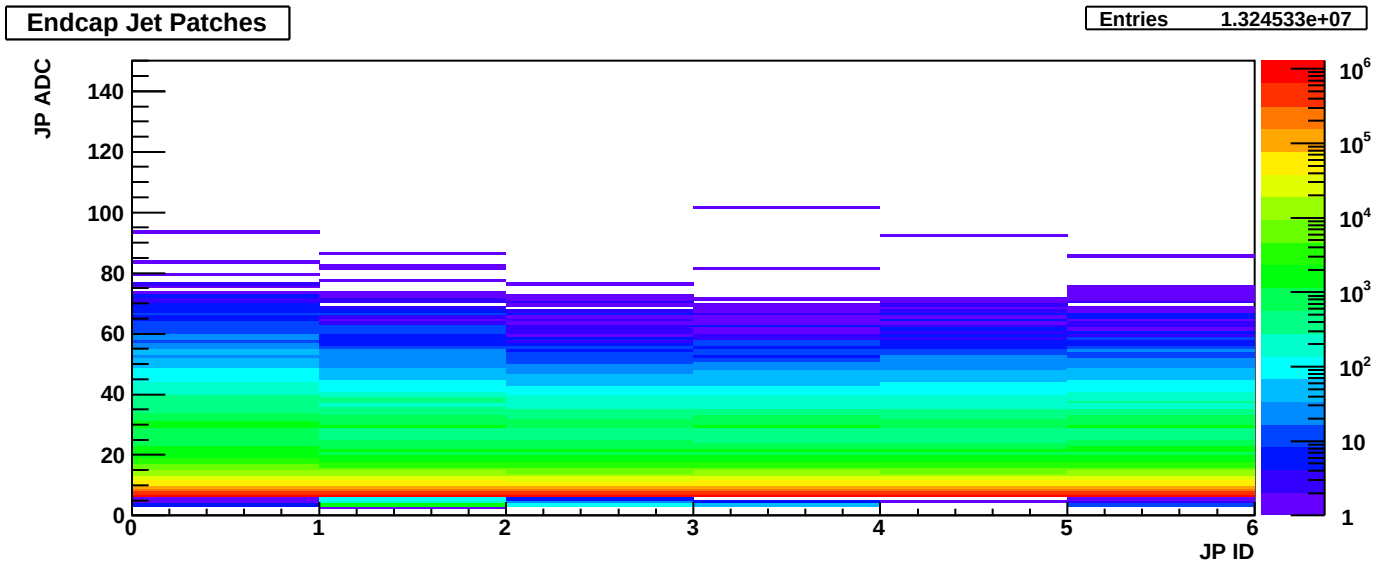

Barrel EMC L0 Input - High Tower
Entries 6.622665e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

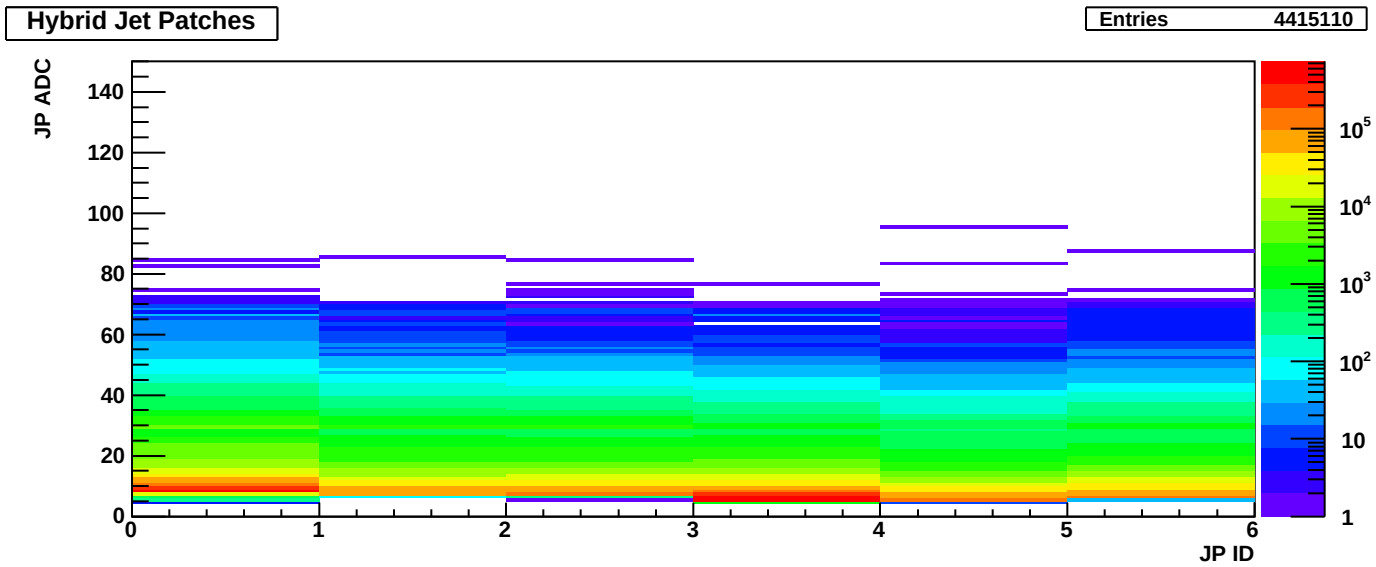

Barrel EMC L0 Input - Patch Sum
Entries 6.622665e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

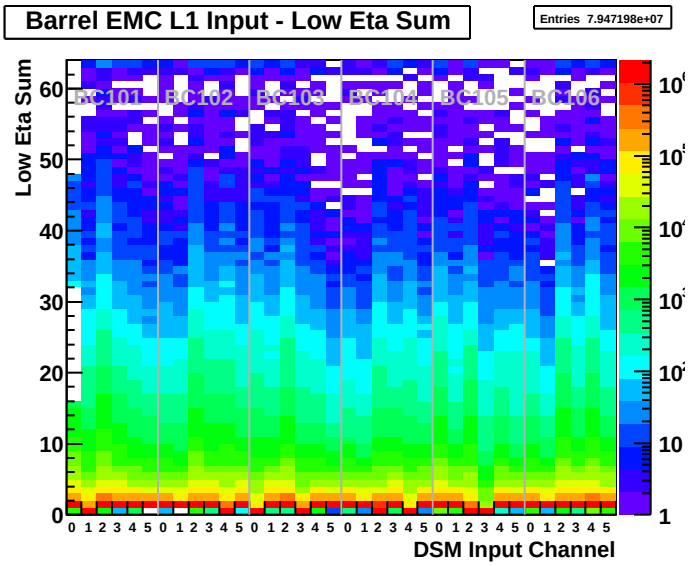

Endcap EMC L0 Input - High Tower
Entries 1.9868e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

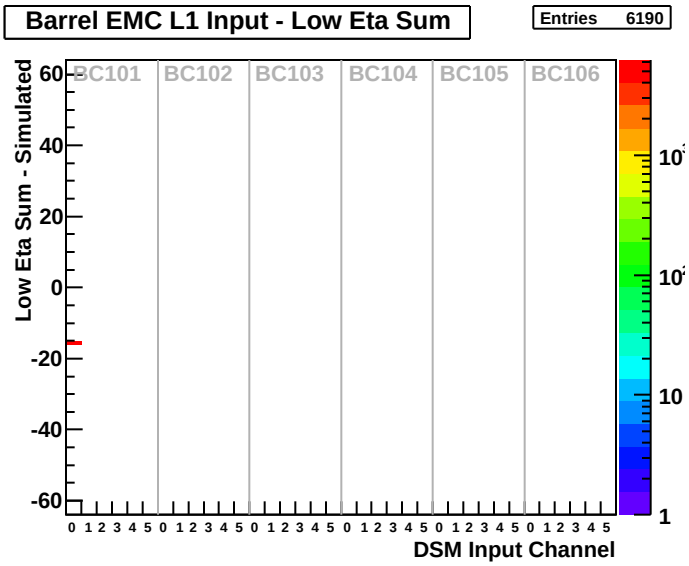

Endcap EMC L0 Input - Patch Sum
Entries 1.9868e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

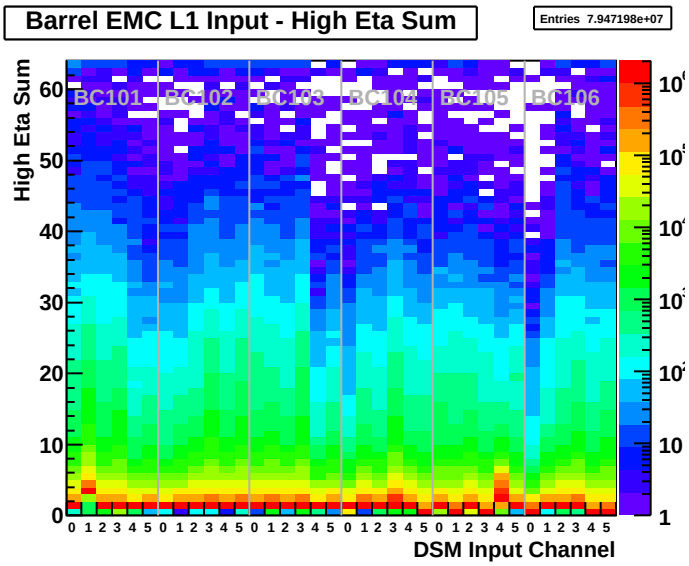

Barrel Jet Patches
Entries 3.973599e+07
JP ADC
JP ID

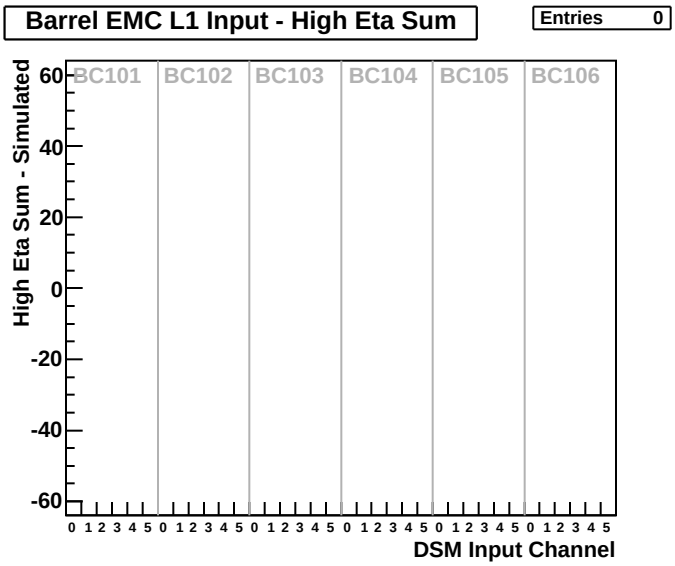

Endcap Jet Patches
Entries 1.324533e+07
JP ADC
JP ID

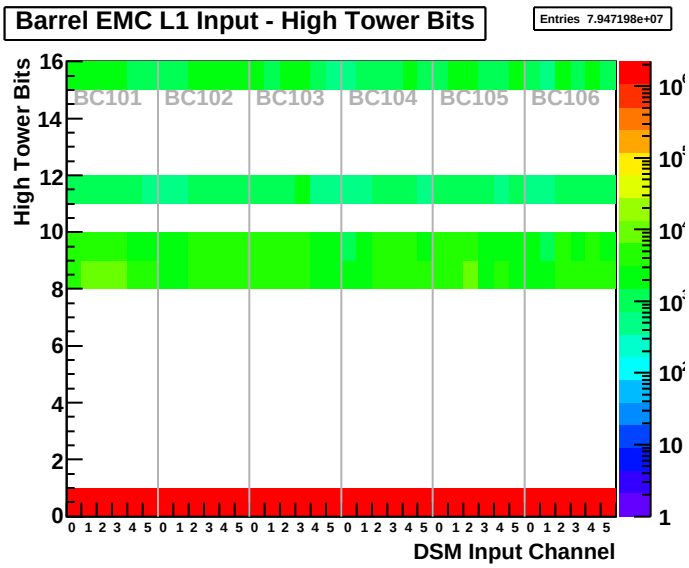

Hybrid Jet Patches
Entries 4415110
JP ADC
JP ID

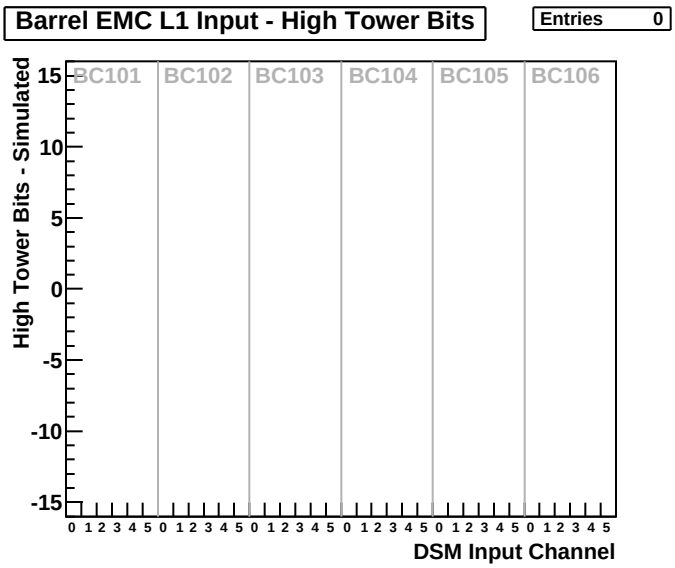

Barrel EMC L1 Input - Low Eta Sum
Entries 7.947198e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

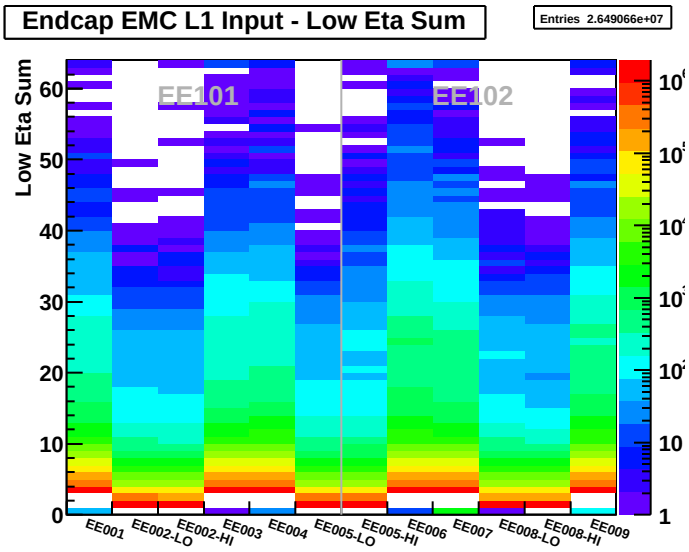

Barrel EMC L1 Input - Low Eta Sum
Entries 6190
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

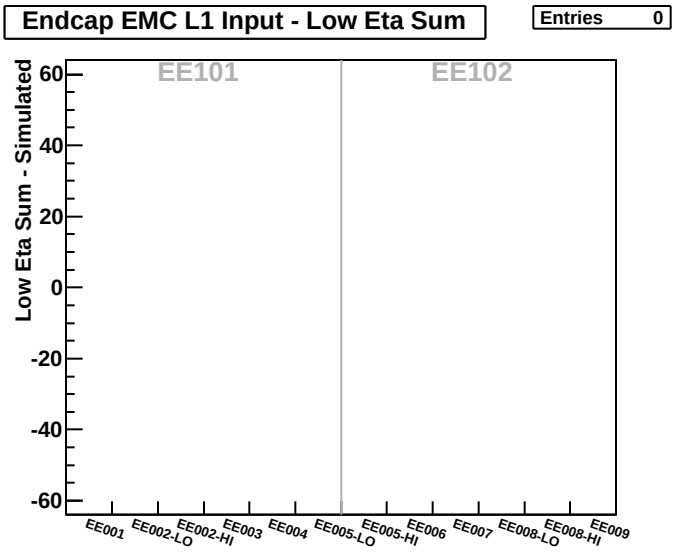

Barrel EMC L1 Input - High Eta Sum
Entries 7.947198e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

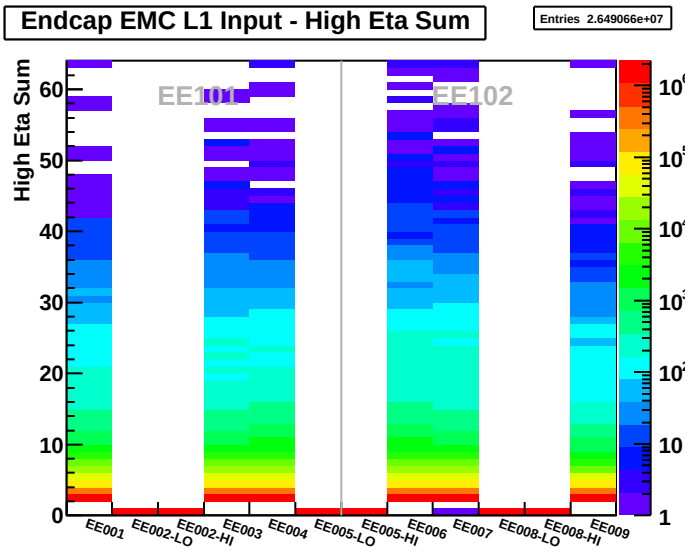

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

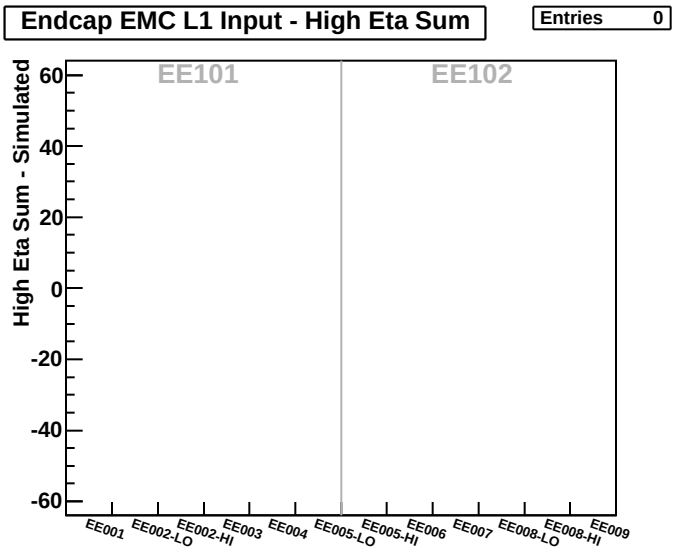

Barrel EMC L1 Input - High Tower Bits
Entries 7.947198e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

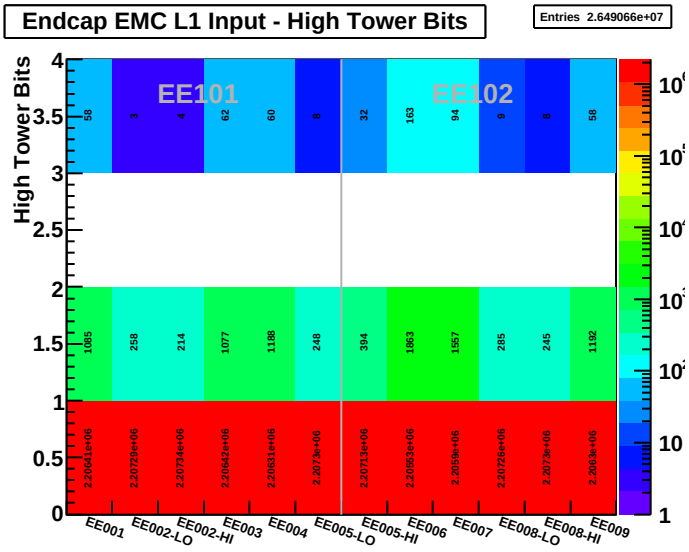

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

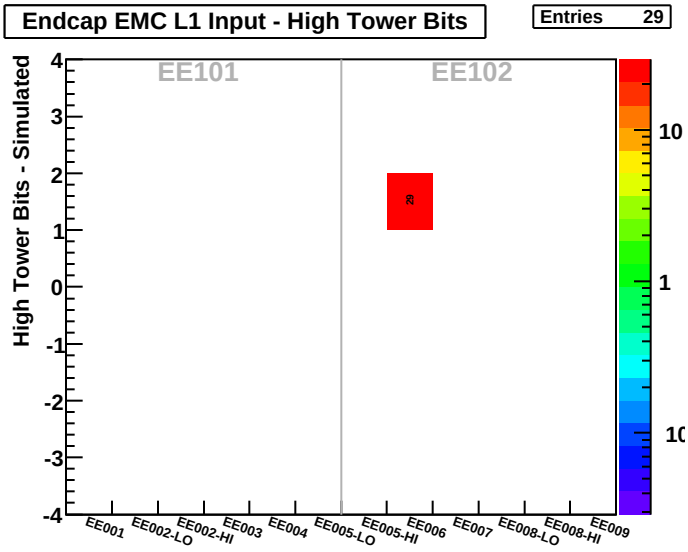

Endcap EMC L1 Input - Low Eta Sum
Entries 2.649066e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

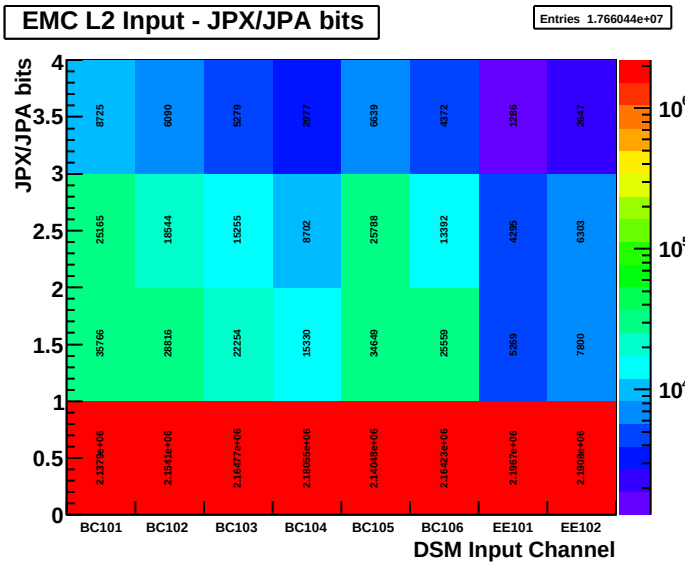

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

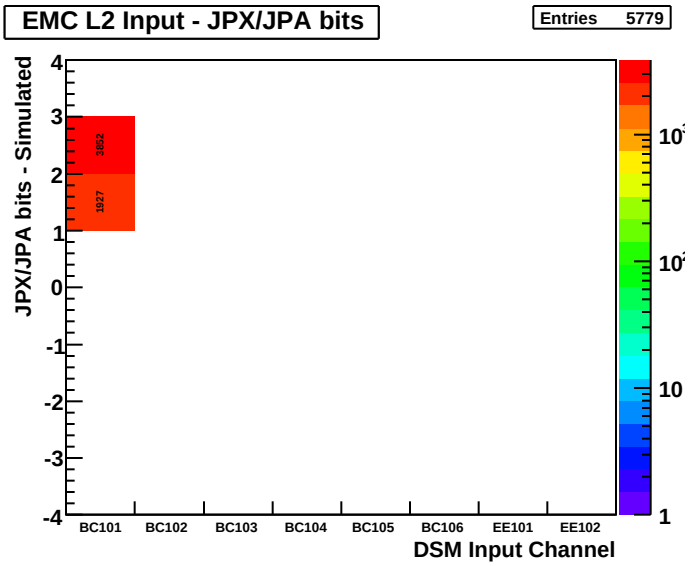

Endcap EMC L1 Input - High Eta Sum
Entries 2.649066e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

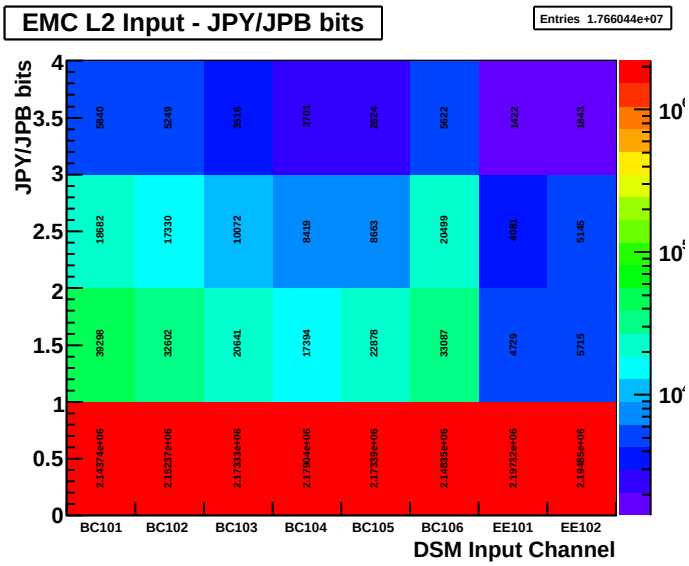

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

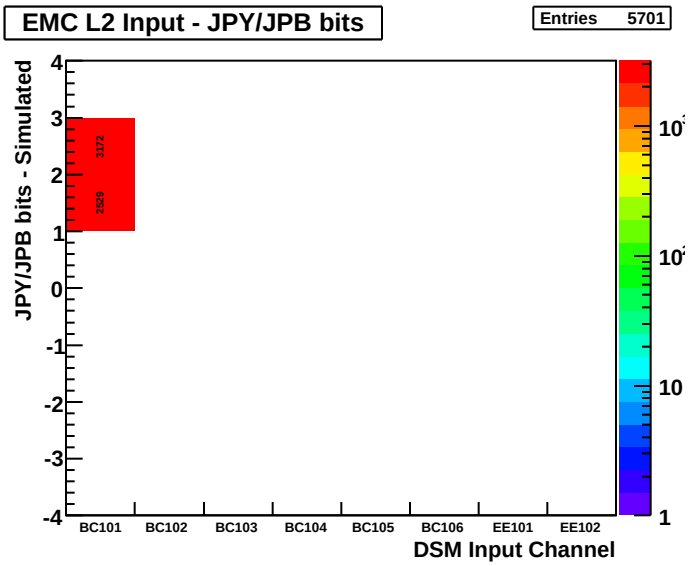

Endcap EMC L1 Input - High Tower Bits
Entries 2.649066e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

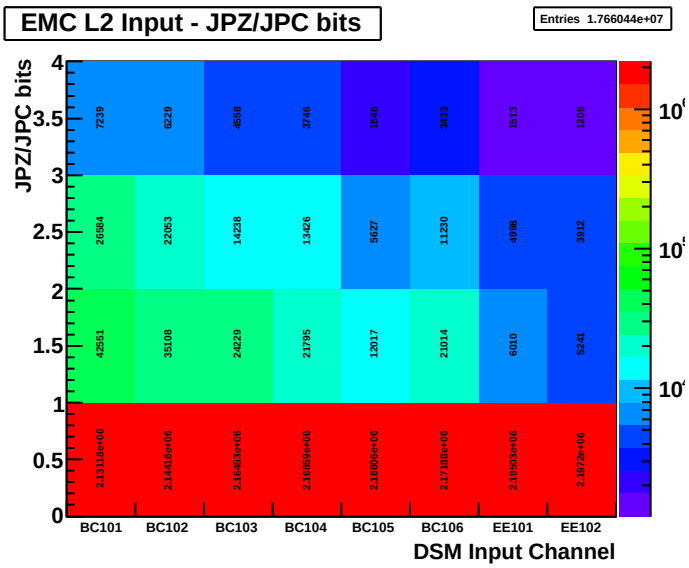

Endcap EMC L1 Input - High Tower Bits
Entries 29
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

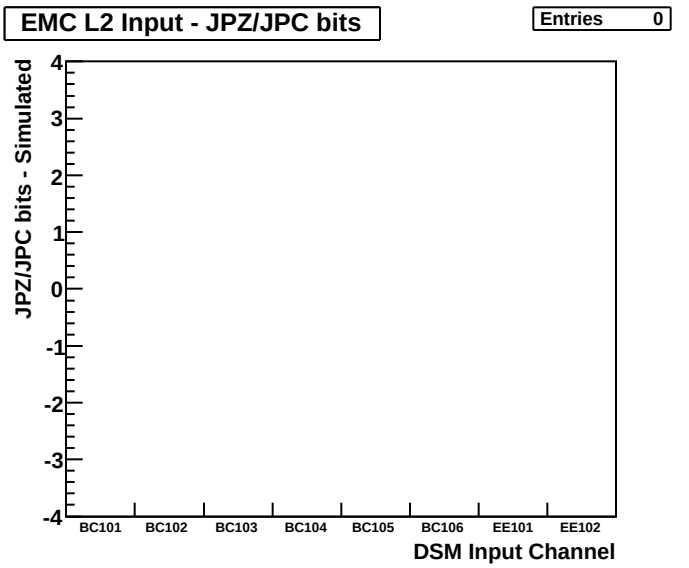

EMC L2 Input - JPX/JPA bits
Entries 1.766044e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
8725 6090 5279 2977 6539 4372 1286 2647
25165 18544 15255 8702 25788 13392 4295 6303
35766 28816 22254 15330 34649 29559 5269 7800
2.1379e+06 2.1541e+06 2.16477e+06 2.18055e+06 2.14049e+06 2.16423e+06 2.1967e+06 2.1908e+06

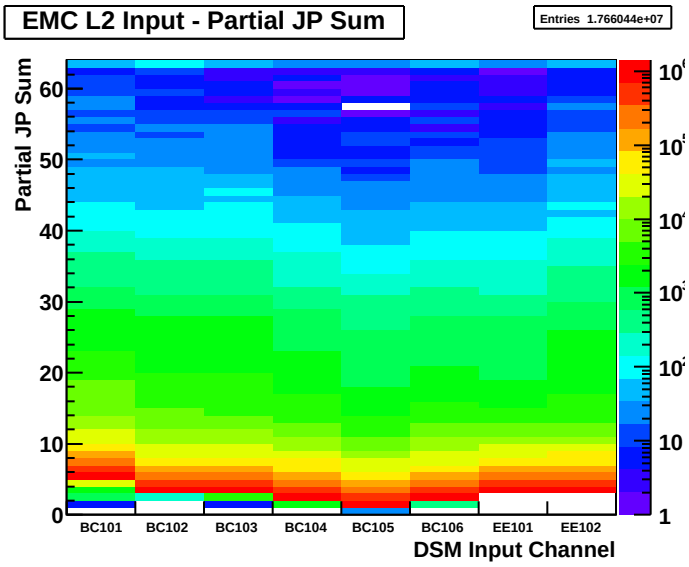

EMC L2 Input - JPX/JPA bits
Entries 5779
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3852
1927

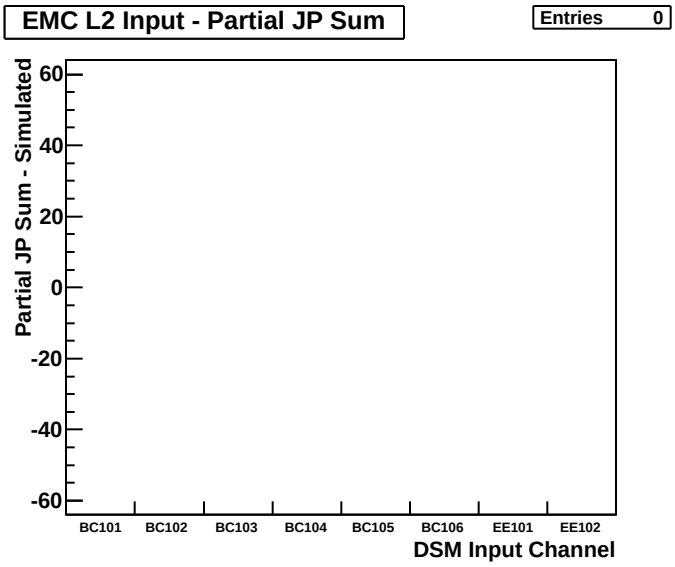

EMC L2 Input - JPY/JPB bits
Entries 1.766044e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5840 5249 3516 2701 2524 5622 1422 1843
18682 17330 10072 8419 8563 20499 4081 5145
39298 32602 20641 17394 22878 33087 4729 5715
2.14374e+06 2.15237e+06 2.17333e+06 2.17904e+06 2.17339e+06 2.14835e+06 2.19732e+06 2.19485e+06

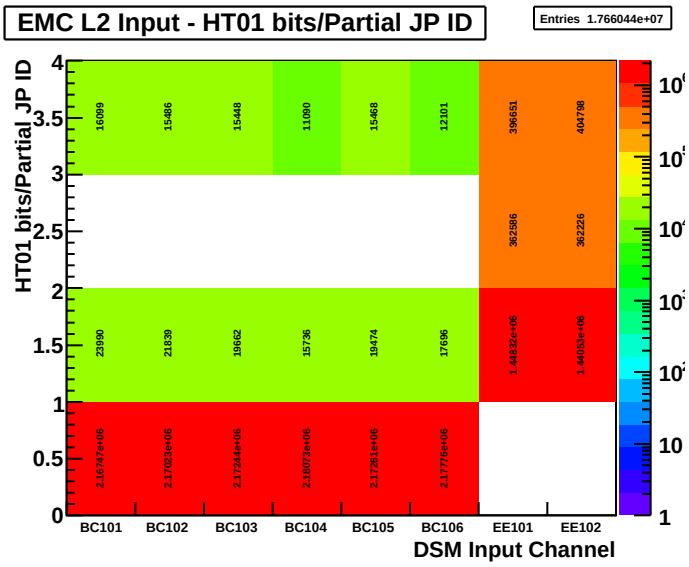

EMC L2 Input - JPY/JPB bits
Entries 5701
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3172
2529

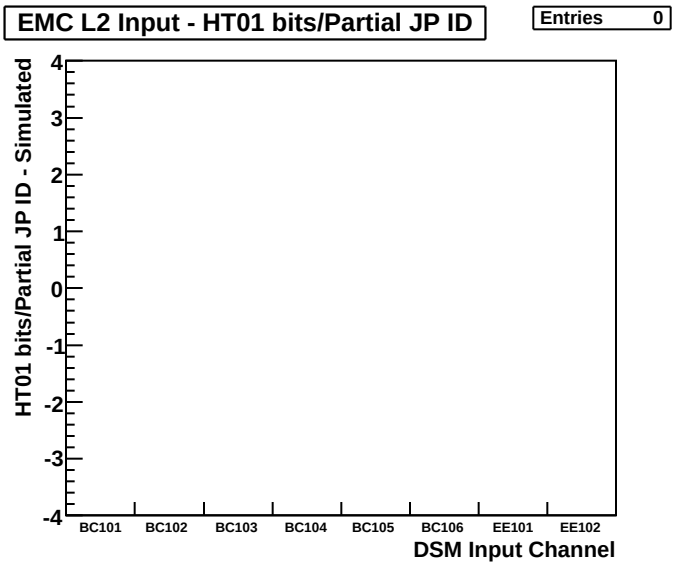

EMC L2 Input - JPZ/JPC bits
Entries 1.766044e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
7239 6229 4558 3746 1846 3433 1513 1205
26584 22053 14238 13426 5627 11230 4998 3912
42551 35108 24229 21795 12017 21014 6010 5241
2.13118e+06 2.14416e+06 2.16453e+06 2.16859e+06 2.18806e+06 2.17188e+06 2.19503e+06 2.1972e+06

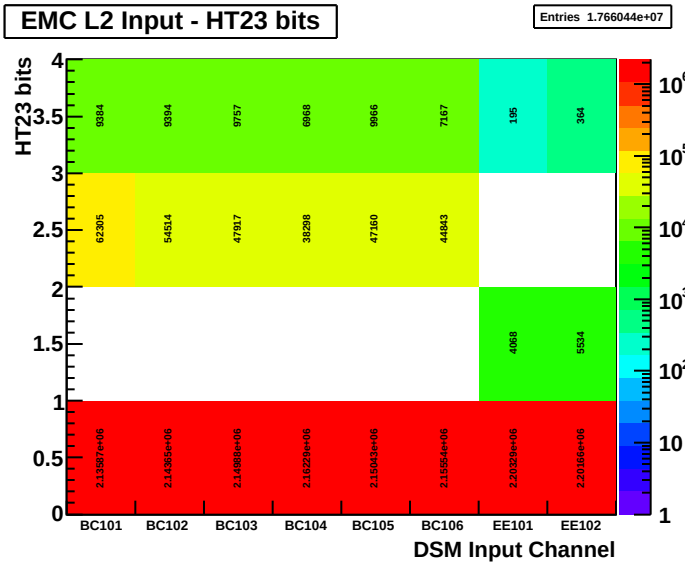

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

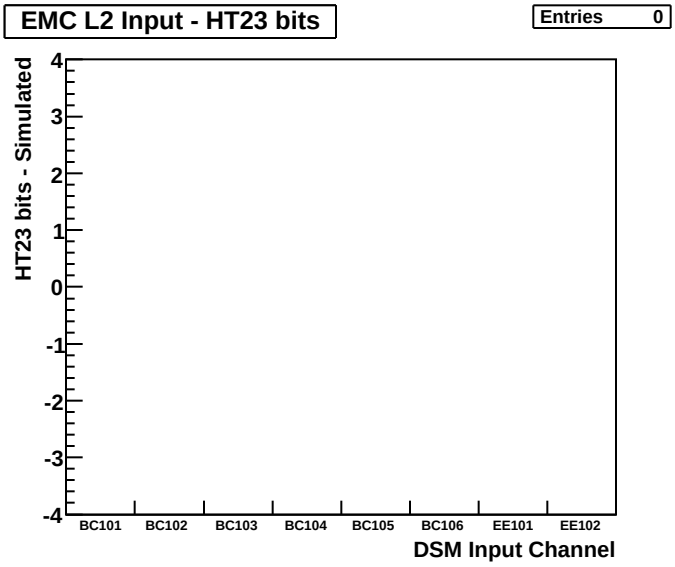

EMC L2 Input - Partial JP Sum
Entries 1.766044e+07
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

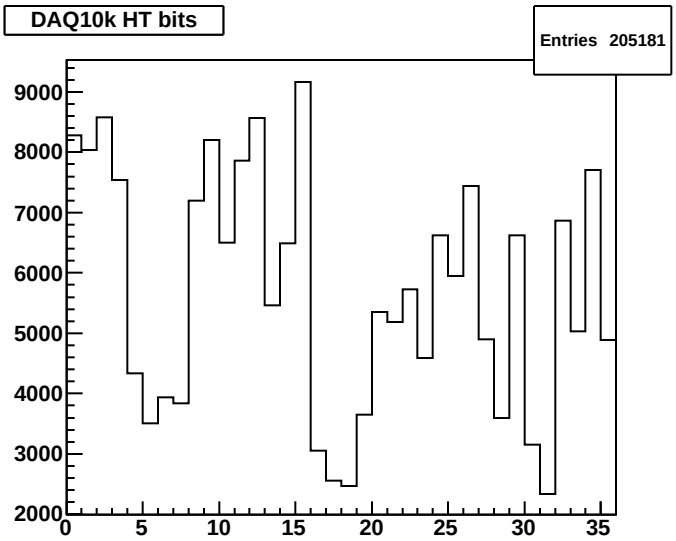

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

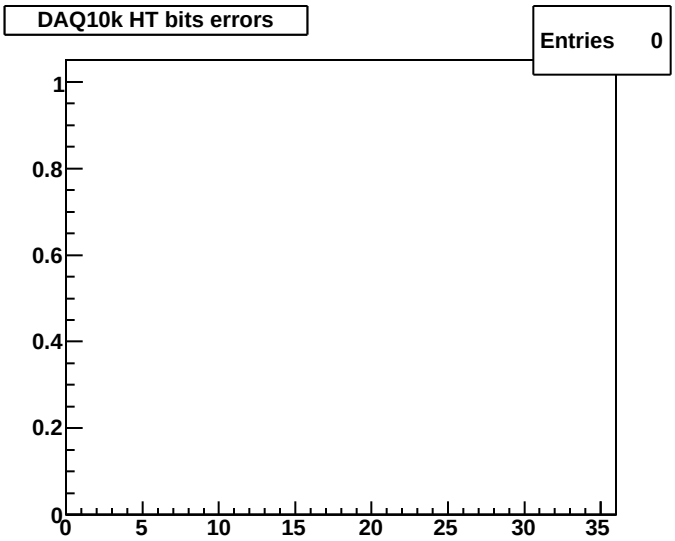

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.766044e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

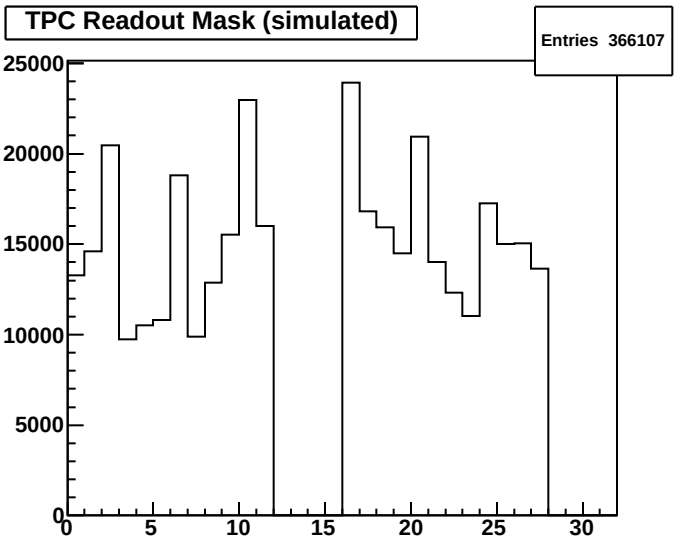

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

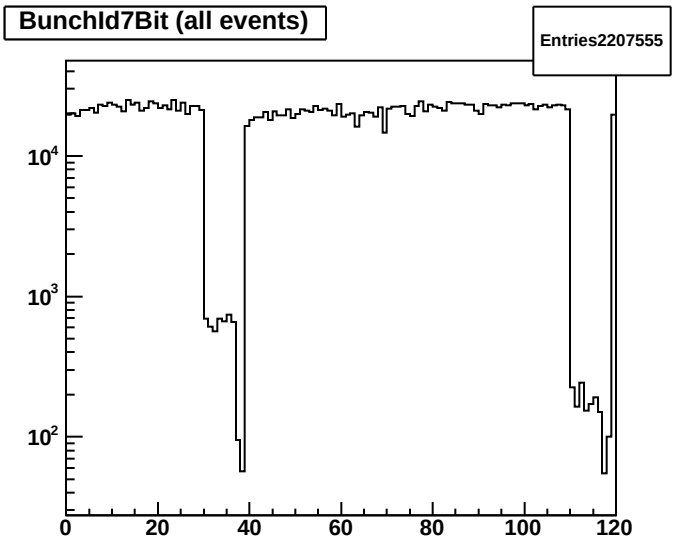

EMC L2 Input - HT23 bits
Entries 1.766044e+07
HT23 bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

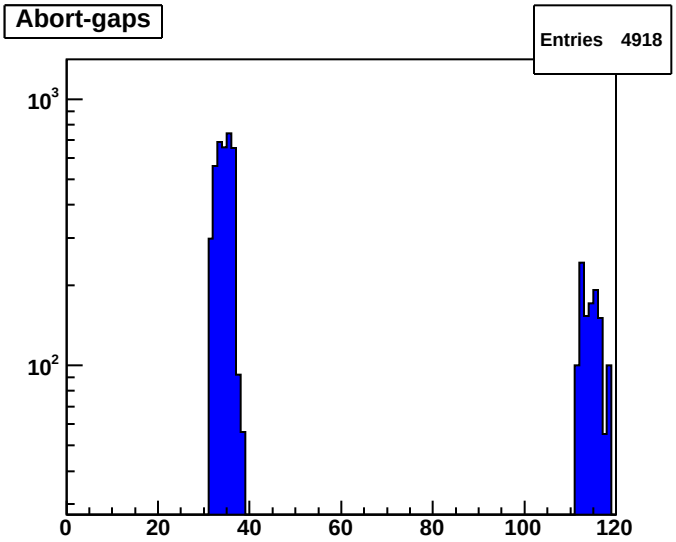

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

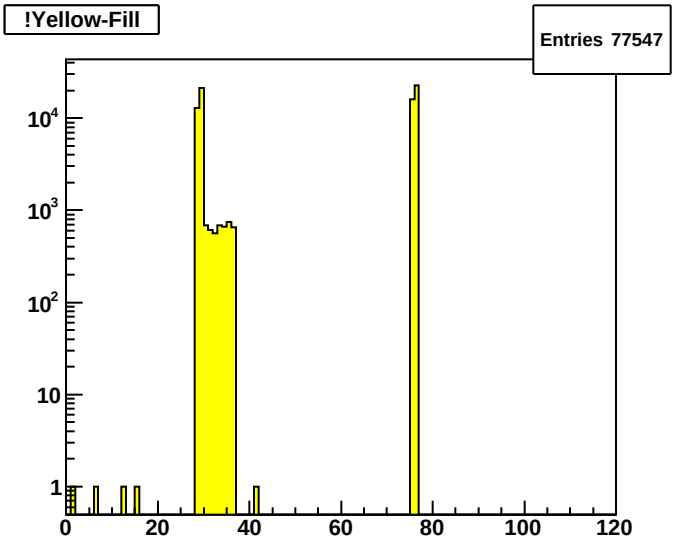

DAQ10k HT bits
Entries 205181

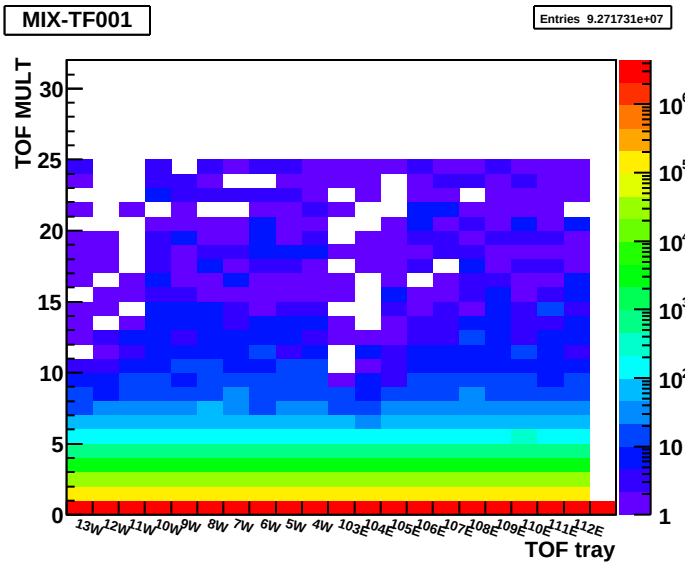

DAQ10k HT bits errors
Entries 0

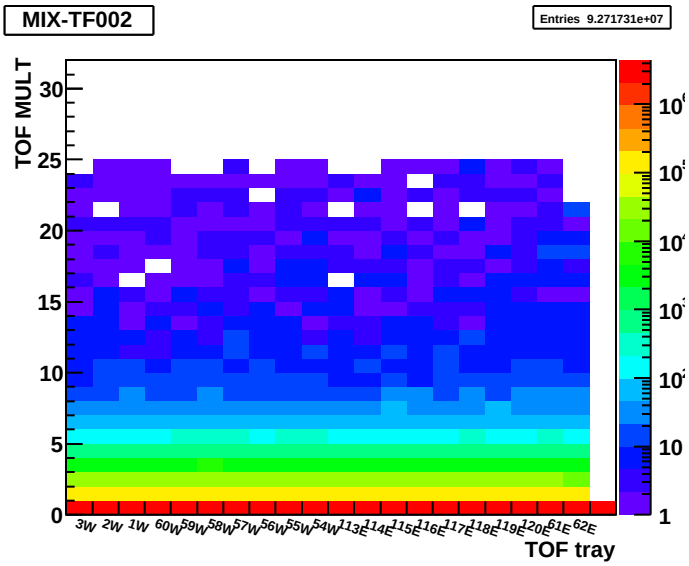

TPC Readout Mask (simulated)
Entries 366107

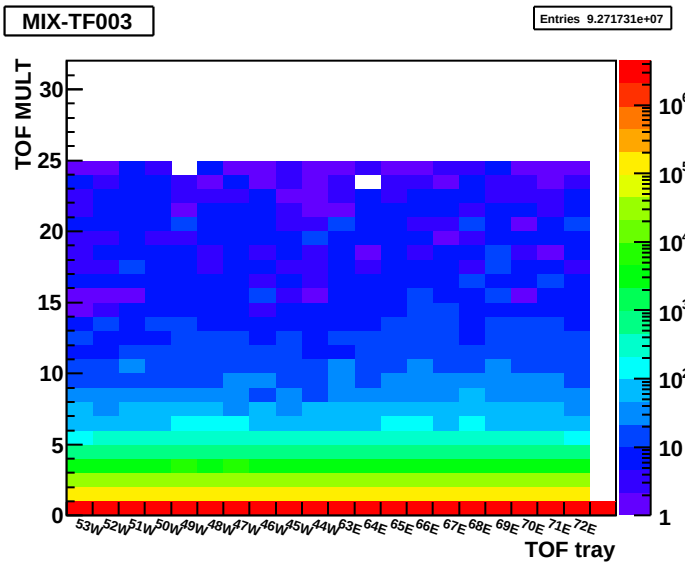

BunchId7Bit (all events)
Entries2207555

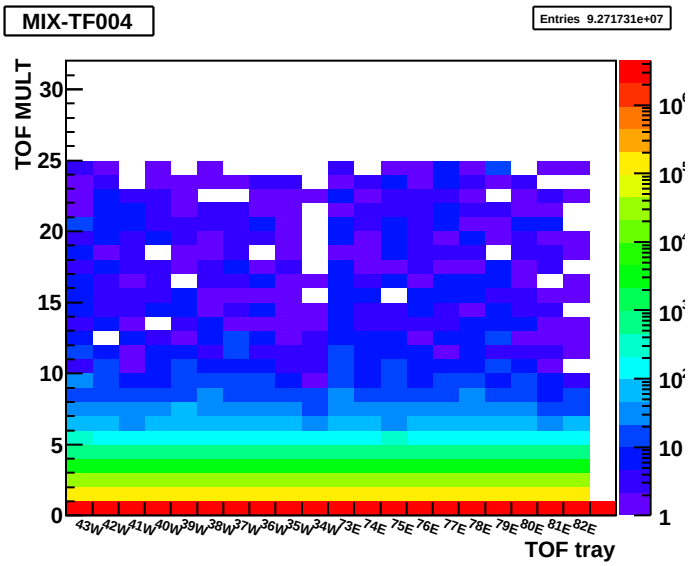

Abort-gaps
Entries 4918

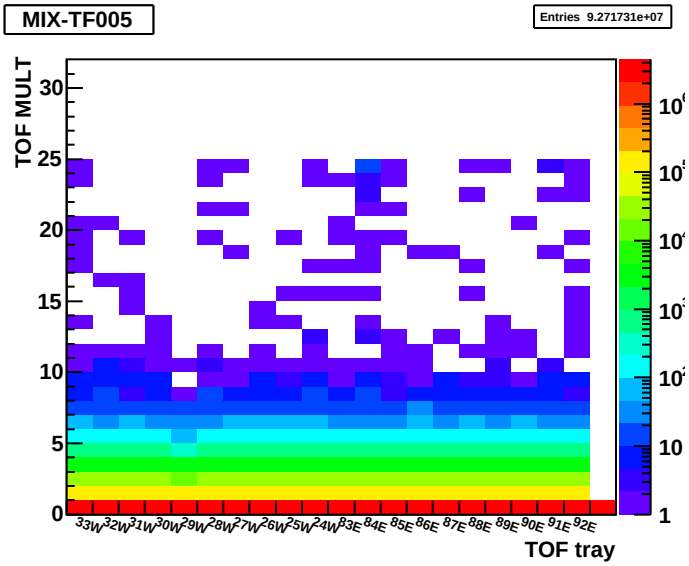

!Yellow-Fill
Entries 77547

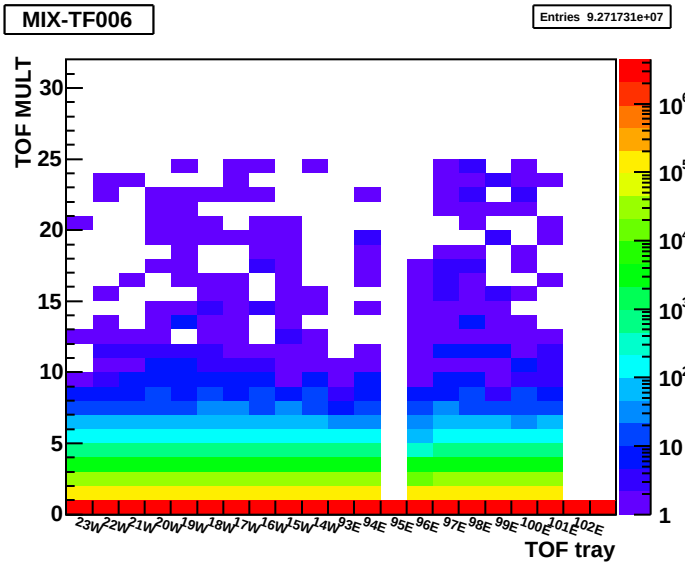

MIX-TF001
Entries 9.271731e+07
TOF MULT
TOF tray

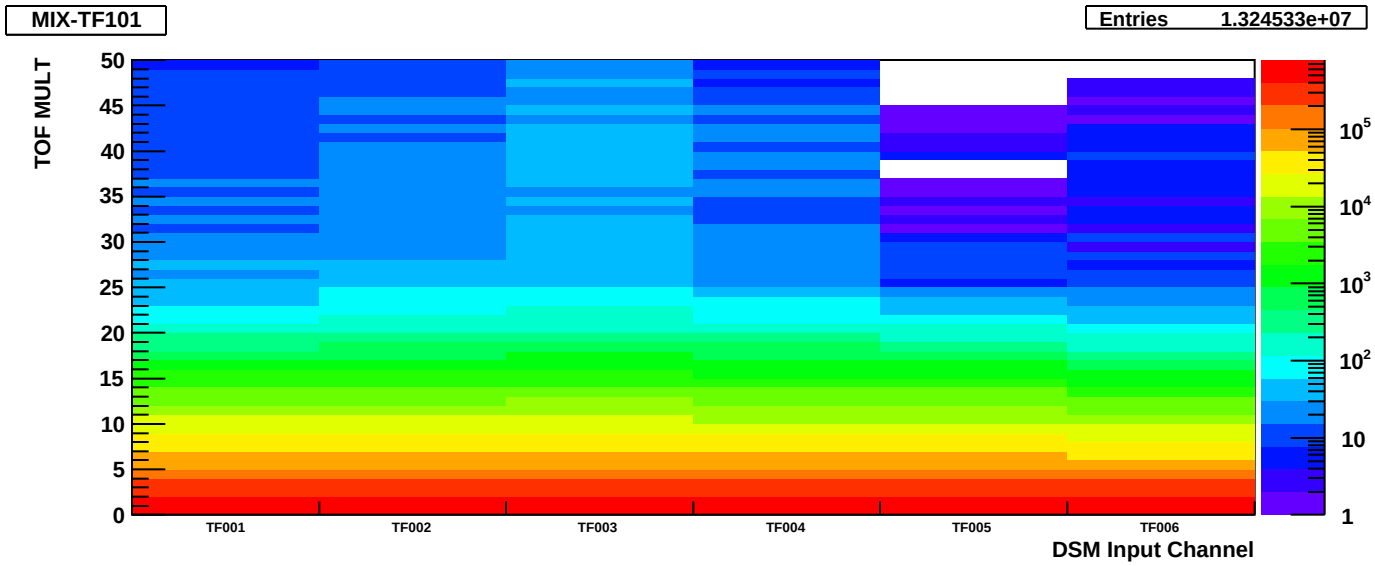

MIX-TF002
Entries 9.271731e+07
TOF MULT
TOF tray

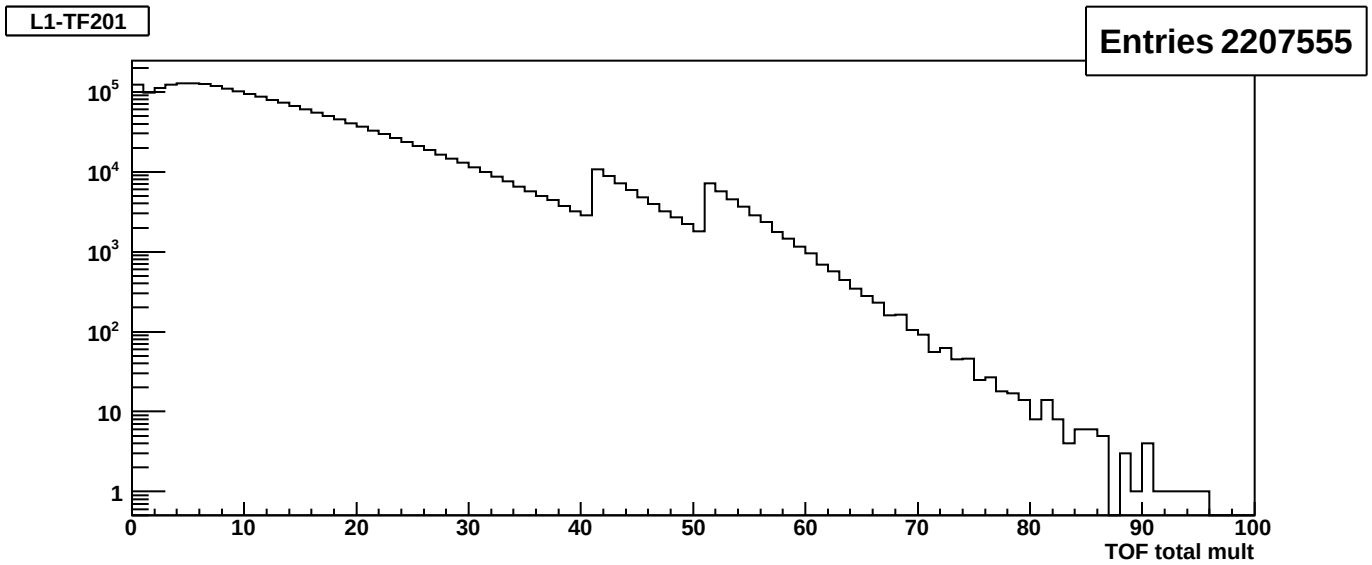

MIX-TF003
Entries 9.271731e+07
TOF MULT
TOF tray

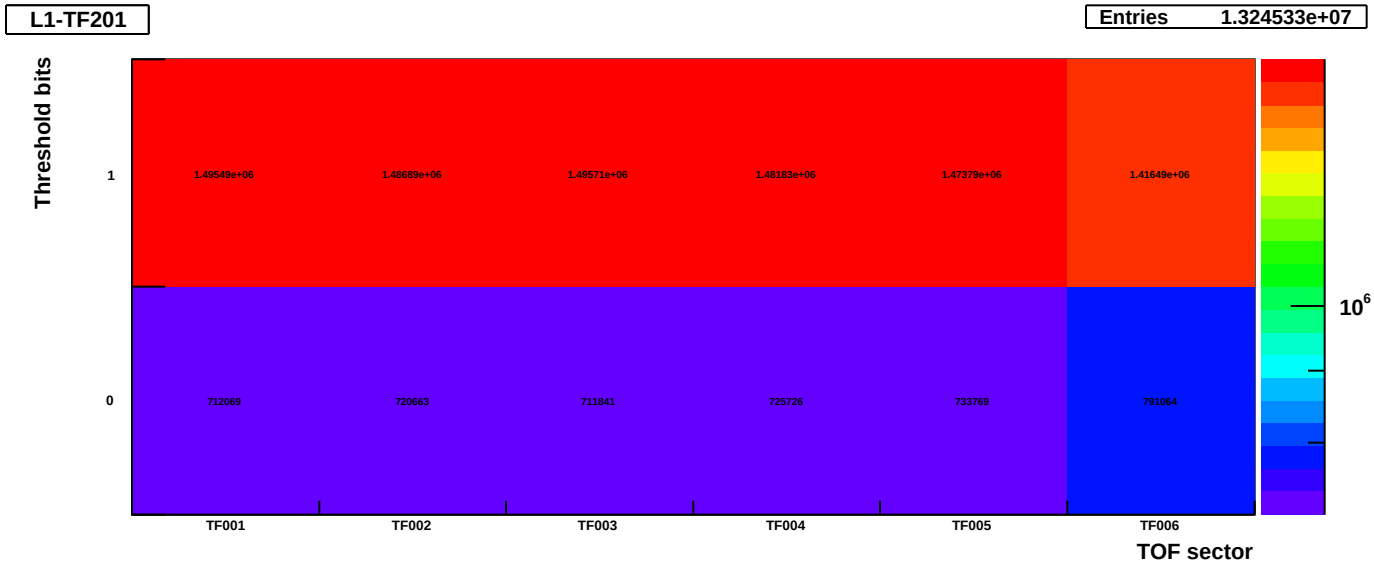

MIX-TF004
Entries 9.271731e+07
TOF MULT
TOF tray

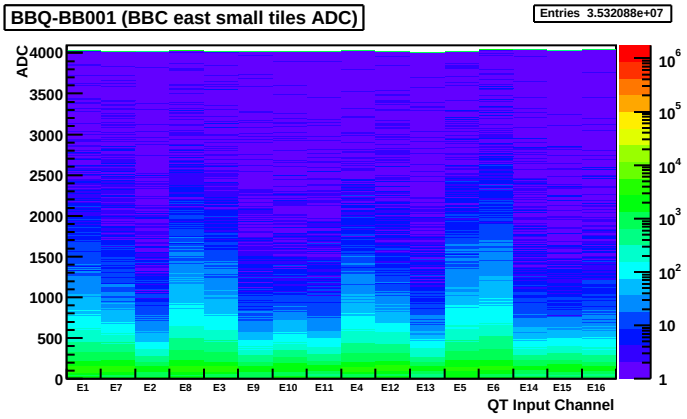

MIX-TF005
Entries 9.271731e+07
TOF MULT
TOF tray

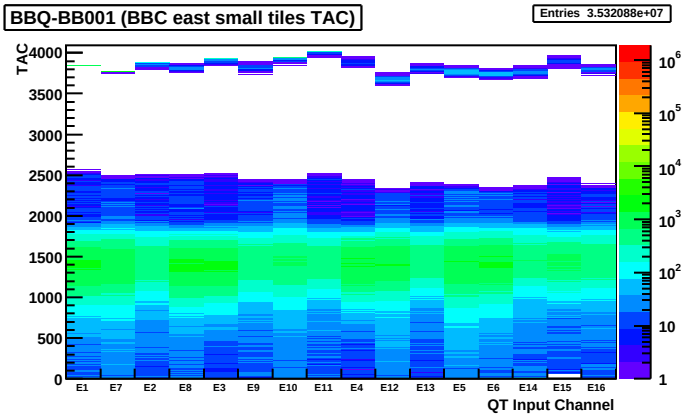

MIX-TF006
Entries 9.271731e+07
TOF MULT
TOF tray

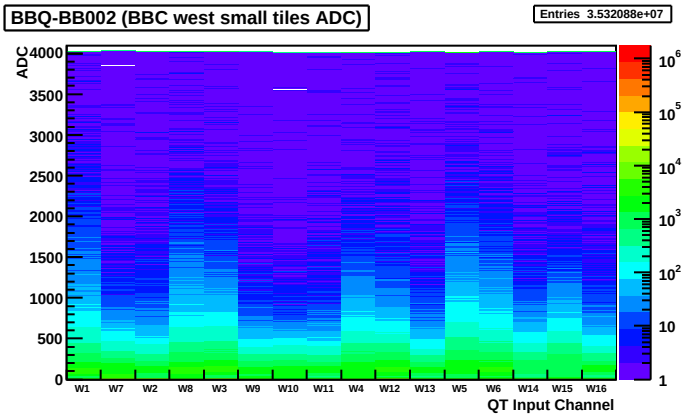

MIX-TF101
Entries 1.324533e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

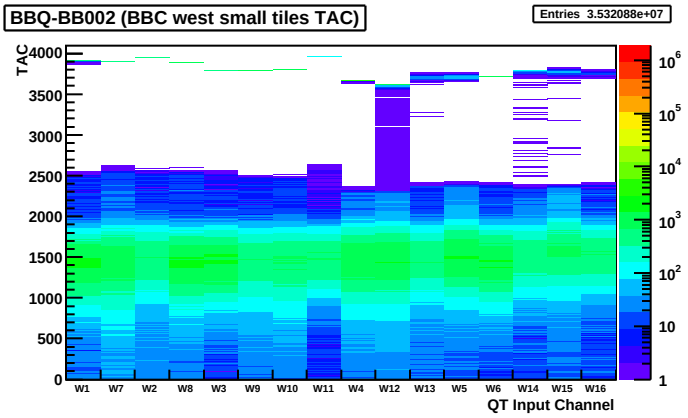

L1-TF201
Entries 2207555
TOF total mult

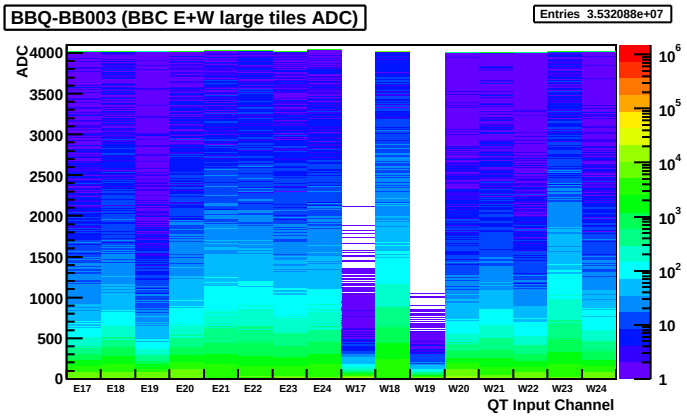

L1-TF201
Entries 1.324533e+07
Threshold bits
1.49549e+06
1.48689e+06
1.49571e+06
1.48183e+06
1.47379e+06
1.41649e+06
712069
720663
711841
725726
733769
791064
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

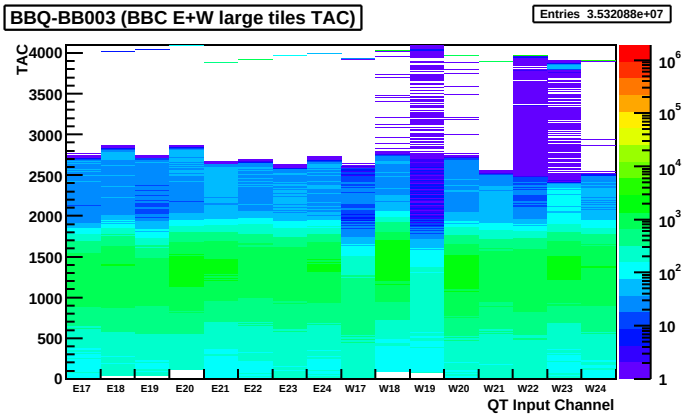

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.532088e+07
ADC
QT Input Channel

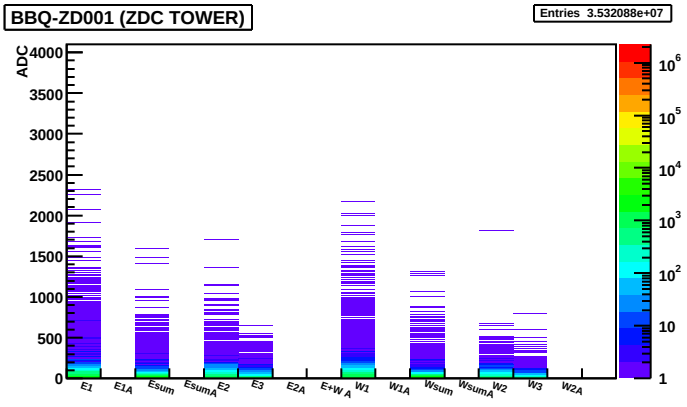

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.532088e+07
TAC
QT Input Channel

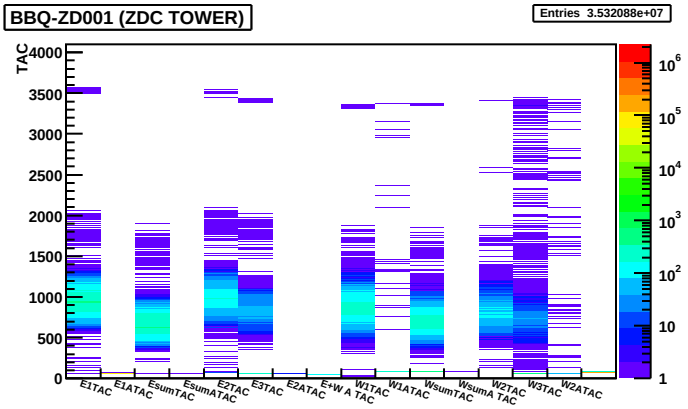

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.532088e+07
ADC
QT Input Channel

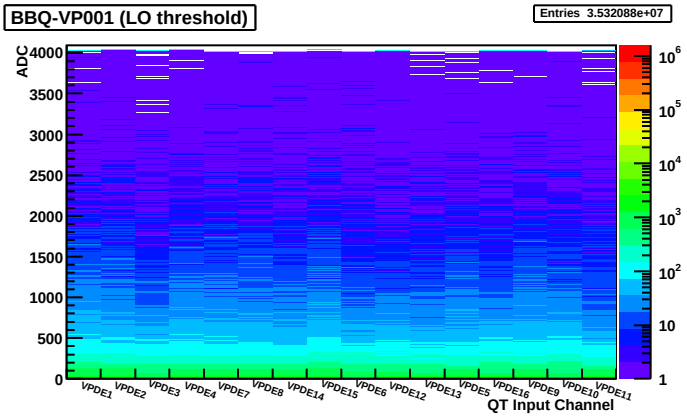

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.532088e+07
TAC
QT Input Channel

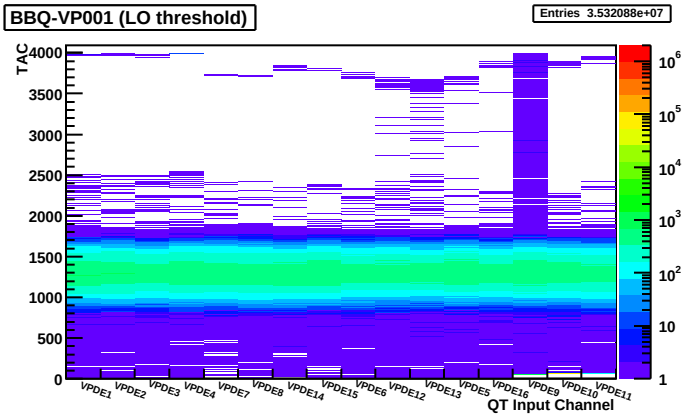

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.532088e+07
ADC
QT Input Channel

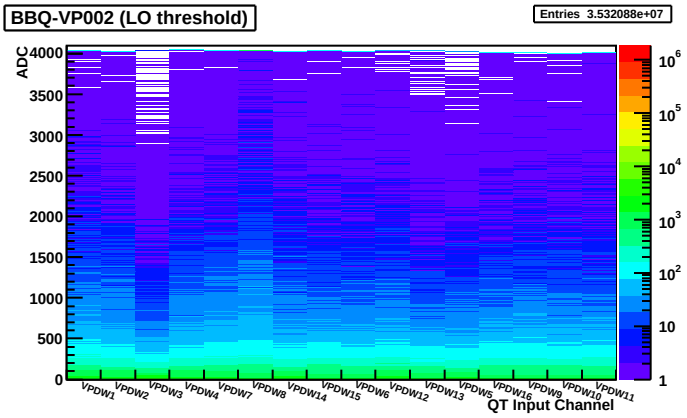

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.532088e+07
TAC
QT Input Channel

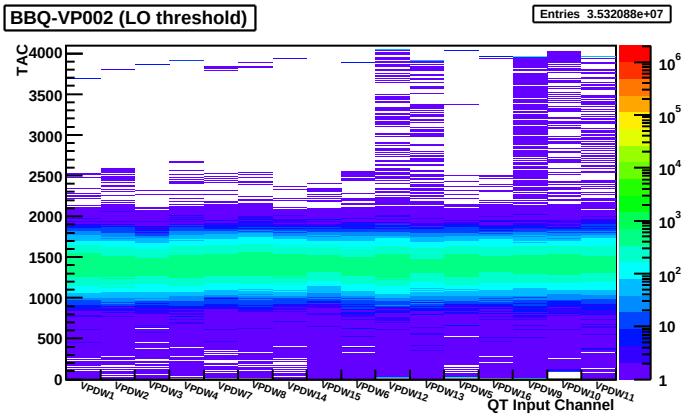

BBQ-ZD001 (ZDC TOWER)
Entries 3.532088e+07
ADC

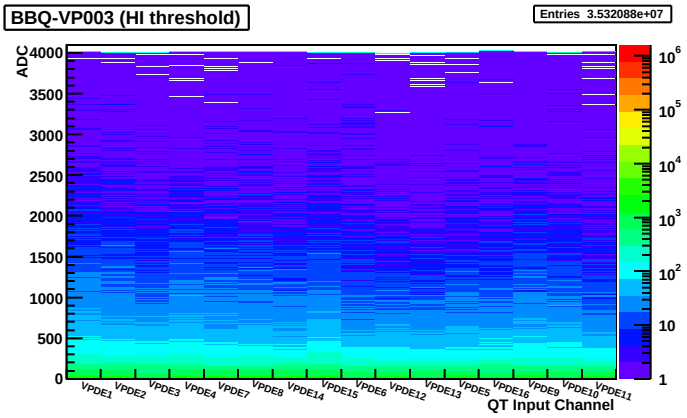

BBQ-ZD001 (ZDC TOWER)
Entries 3.532088e+07
TAC

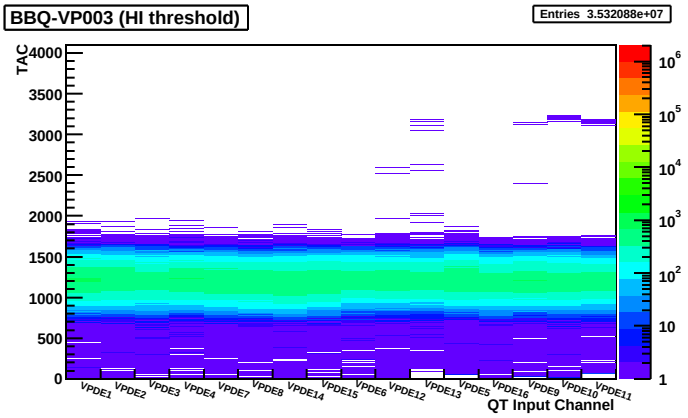

BBQ-VP001 (LO threshold)
Entries 3.532088e+07
ADC
QT Input Channel

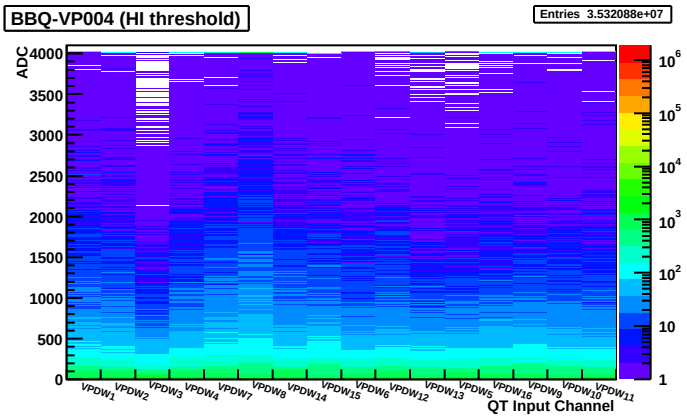

BBQ-VP001 (LO threshold)
Entries 3.532088e+07
TAC
QT Input Channel

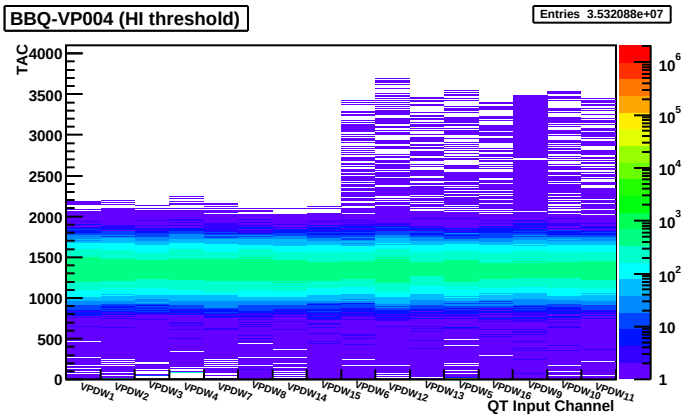

BBQ-VP002 (LO threshold)
Entries 3.532088e+07
ADC
QT Input Channel

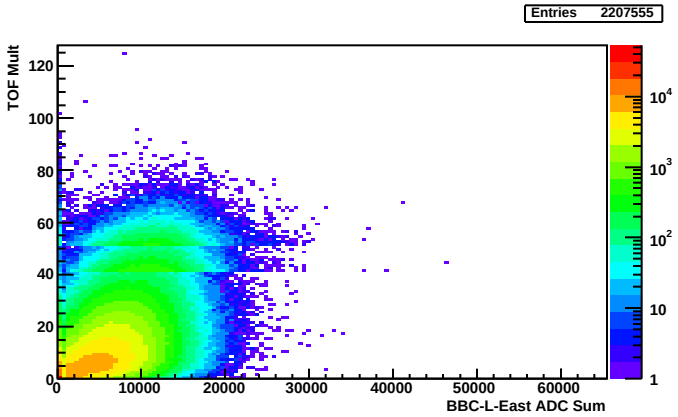

BBQ-VP002 (LO threshold)
Entries 3.532088e+07
TAC
QT Input Channel

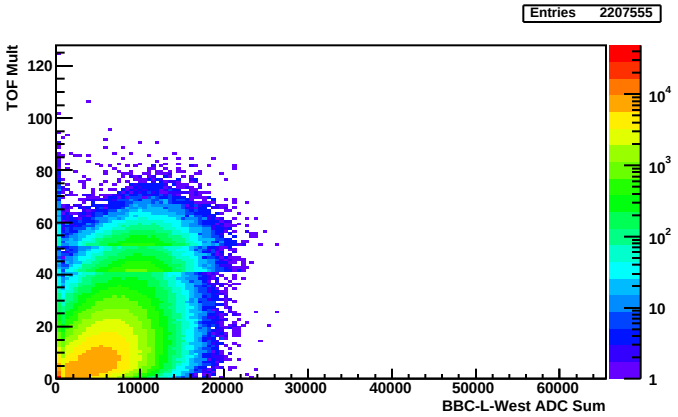

BBQ-VP003 (HI threshold)
Entries 3.532088e+07
ADC
QT Input Channel

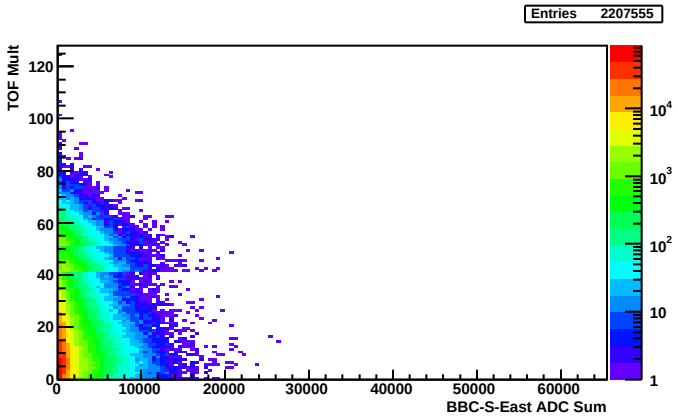

BBQ-VP003 (HI threshold)
Entries 3.532088e+07
TAC
QT Input Channel

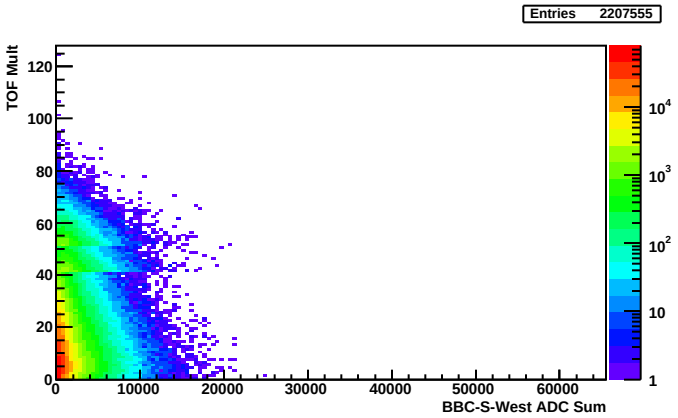

BBQ-VP004 (HI threshold)
Entries 3.532088e+07
ADC
QT Input Channel

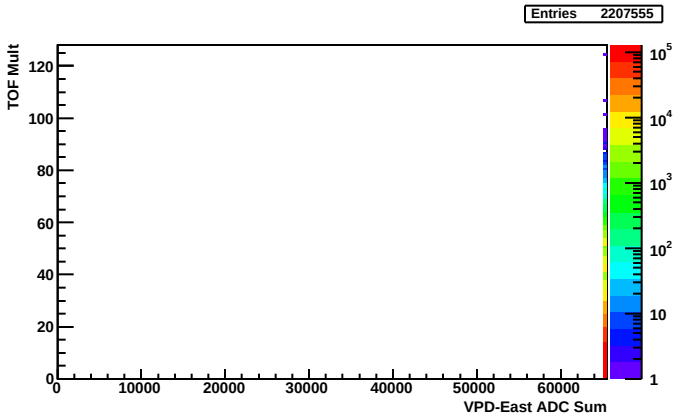

BBQ-VP004 (HI threshold)
Entries 3.532088e+07
TAC
QT Input Channel

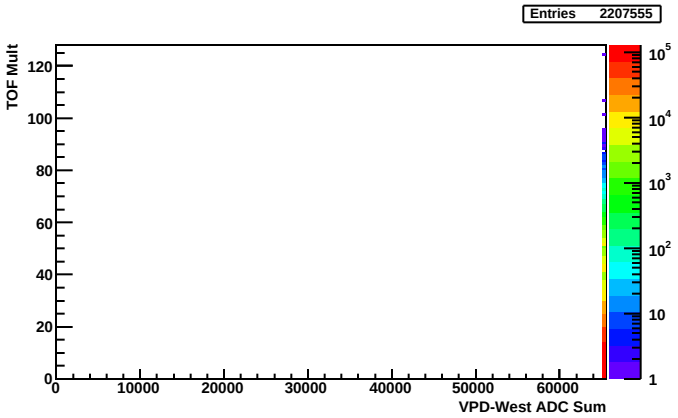

Entries 2207555
TOF Mult
BBC-L-East ADC Sum

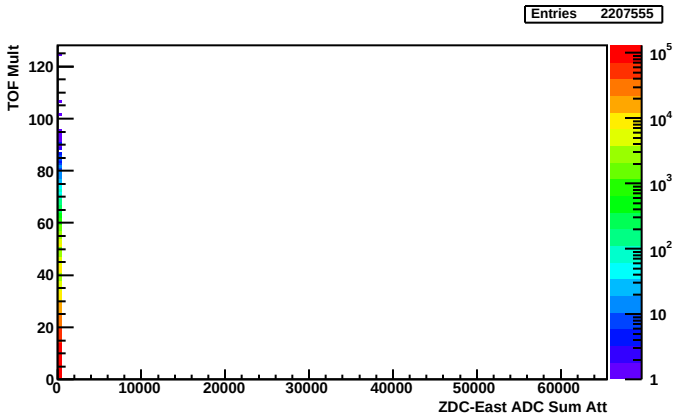

Entries 2207555
TOF Mult
BBC-L-West ADC Sum

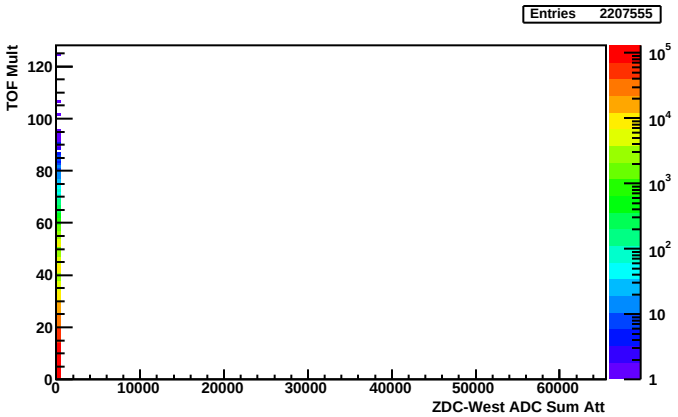

Entries 2207555
TOF Mult
BBC-S-East ADC Sum

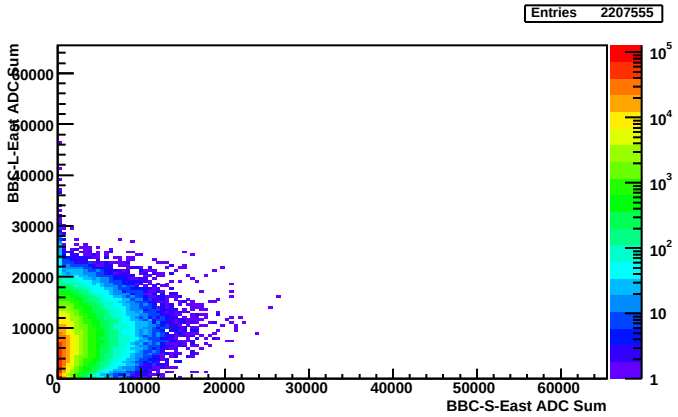

Entries 2207555
TOF Mult
BBC-S-West ADC Sum

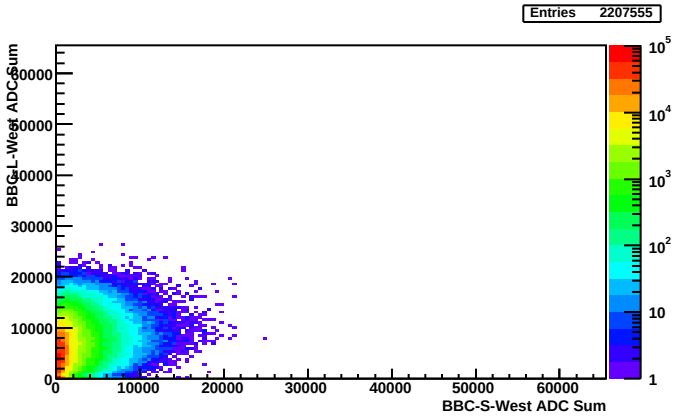

Entries 2207555
TOF Mult
VPD-East ADC Sum

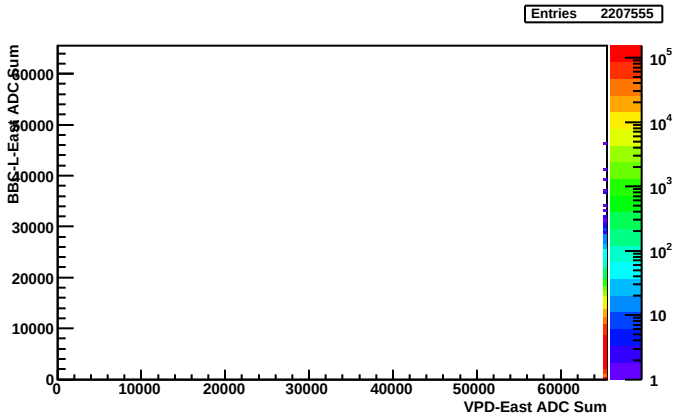

Entries 2207555
TOF Mult
VPD-West ADC Sum

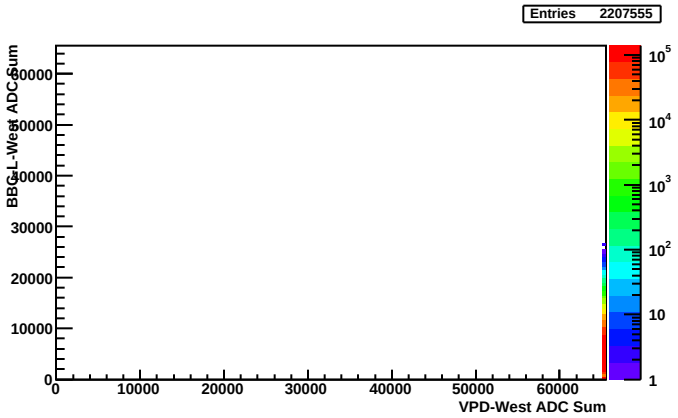

Entries 2207555
TOF Mult
ZDC-East ADC Sum Att

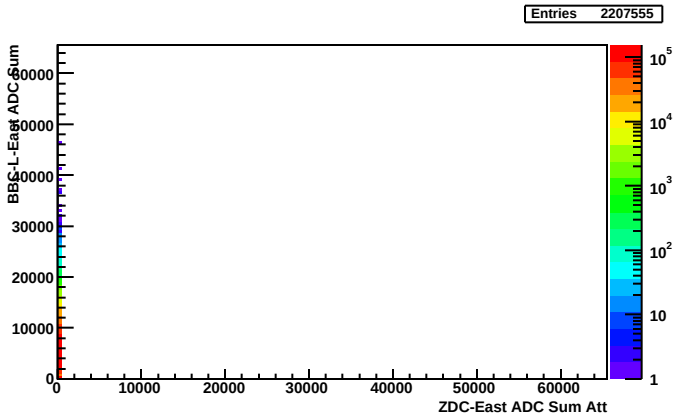

Entries 2207555
TOF Mult
ZDC-West ADC Sum Att

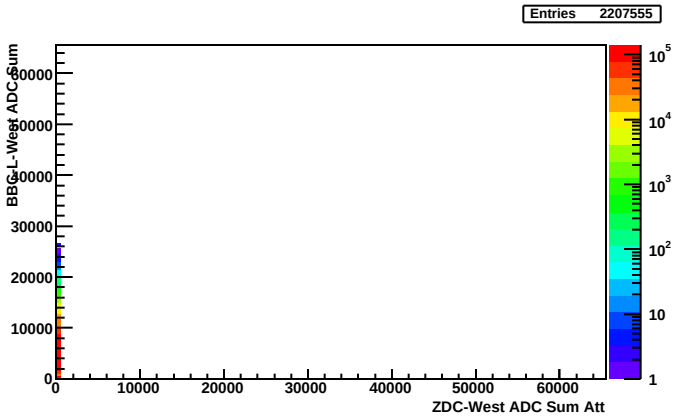

Entries 2207555
BBC-L-East ADC Sum
BBC-S-East ADC Sum

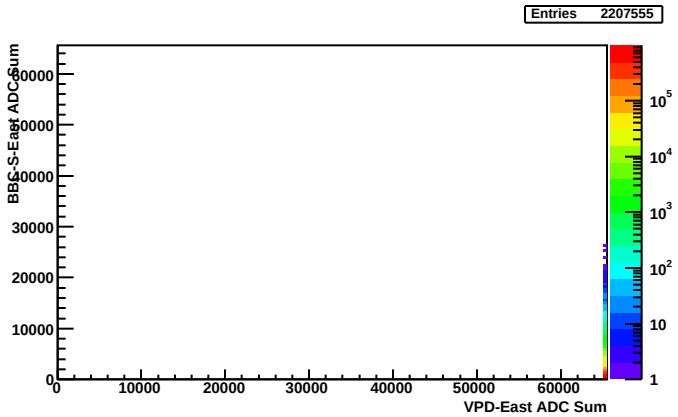

Entries 2207555
BBC-L-West ADC Sum
BBC-S-West ADC Sum

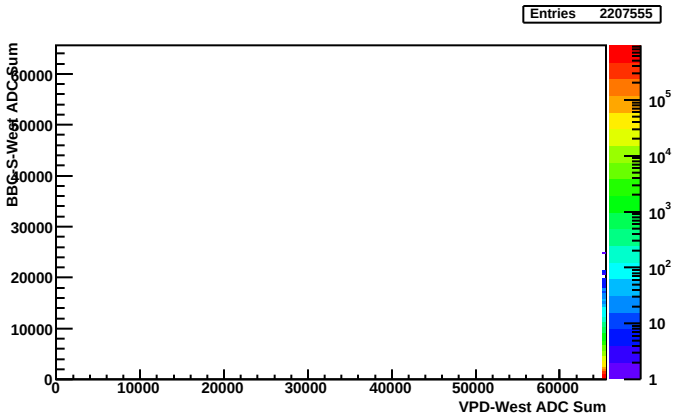

Entries 2207555
BBC-L-East ADC Sum
VPD-East ADC Sum

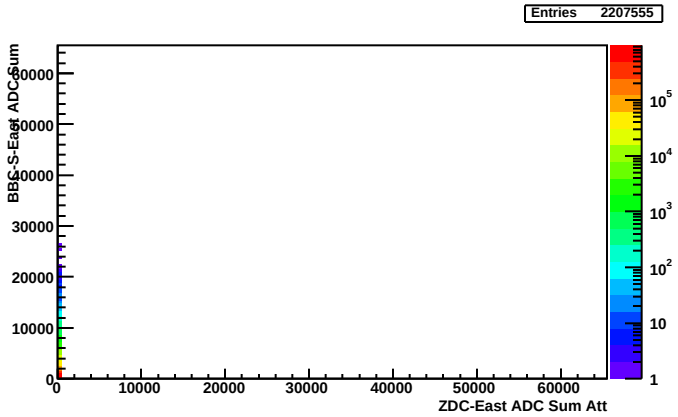

Entries 2207555
BBC-L-West ADC Sum
VPD-West ADC Sum

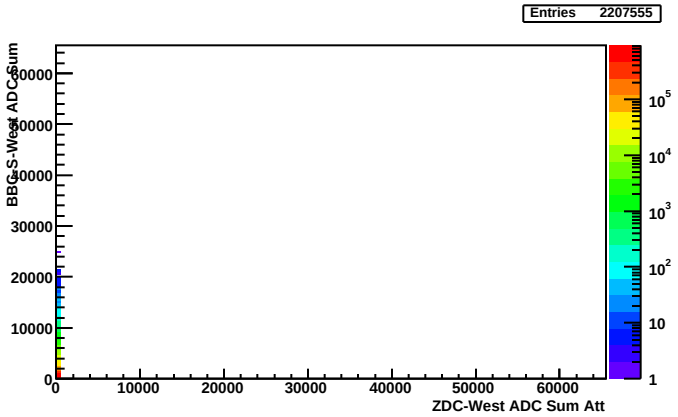

Entries 2207555
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

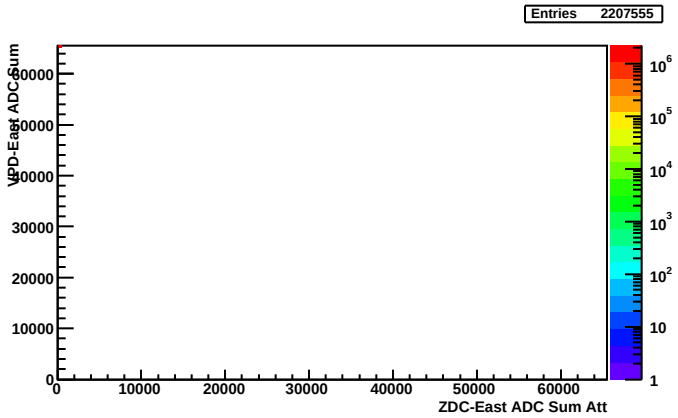

Entries 2207555
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

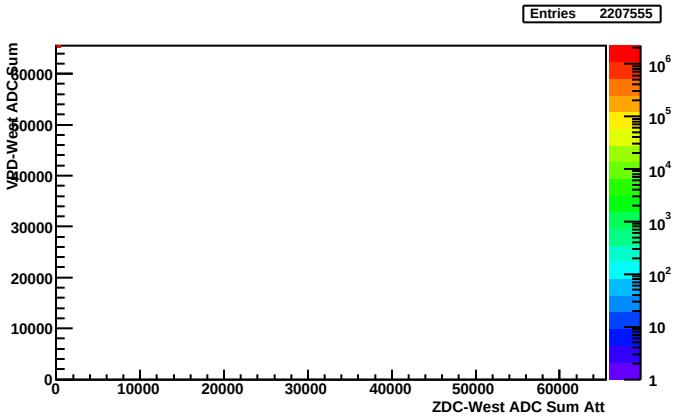

Entries 2207555
BBC-S-East ADC Sum
VPD-East ADC Sum

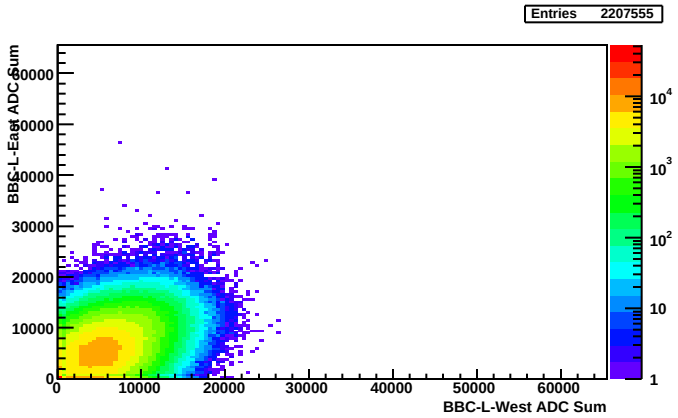

Entries 2207555
BBC-S-West ADC Sum
VPD-West ADC Sum

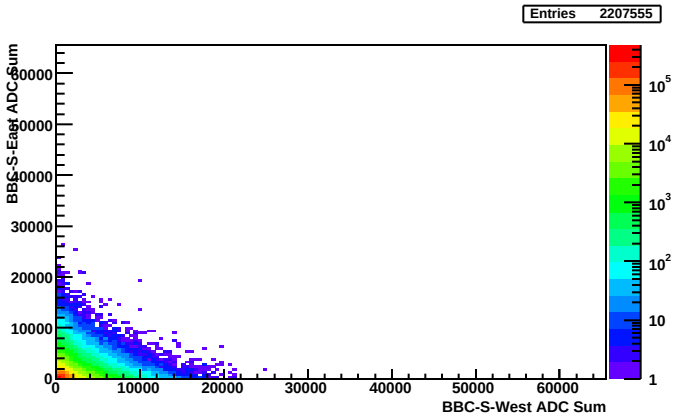

Entries 2207555
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

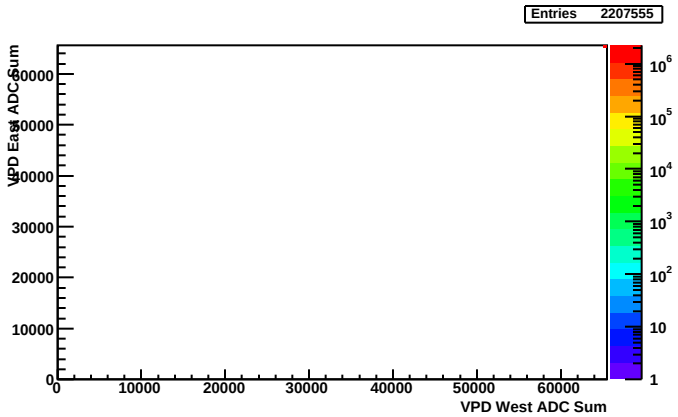

Entries 2207555
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

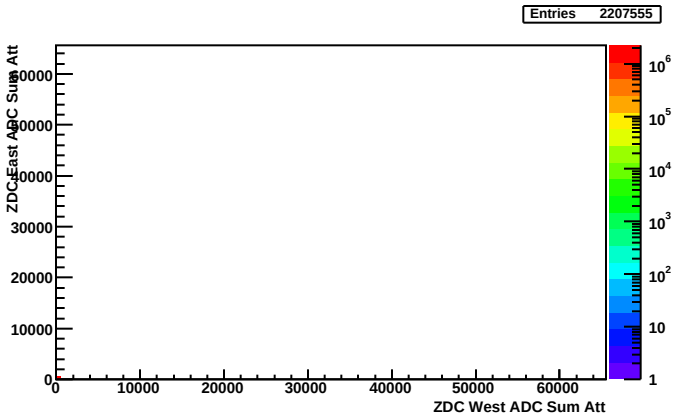

Entries 2207555
VPD-East ADC Sum
ZDC-East ADC Sum Att

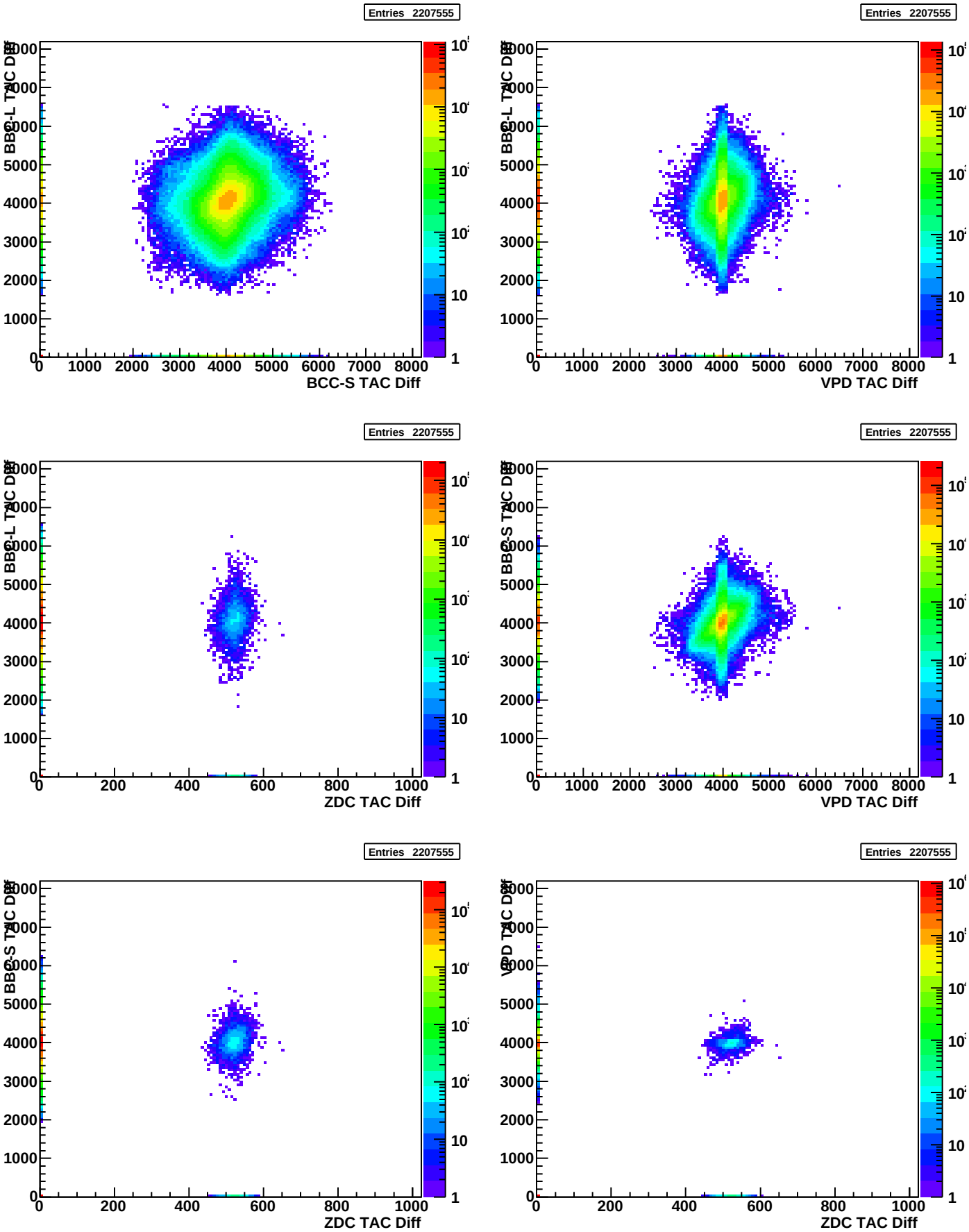

Entries 2207555
VPD-West ADC Sum
ZDC-West ADC Sum Att

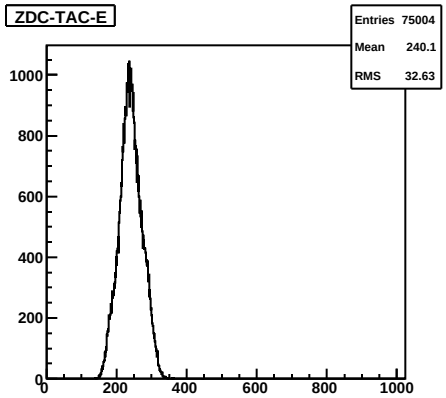

Entries 2207555
BBC-L-East ADC Sum
BBC-L-West ADC Sum

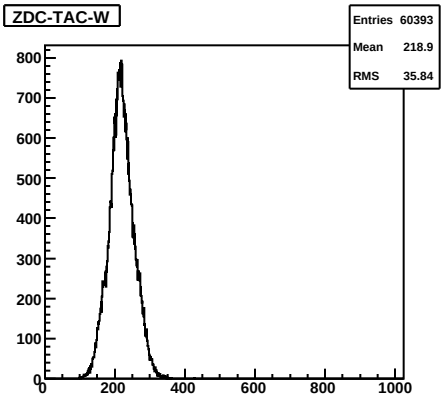

Entries 2207555
BBC-S-East ADC Sum
BBC-S-West ADC Sum

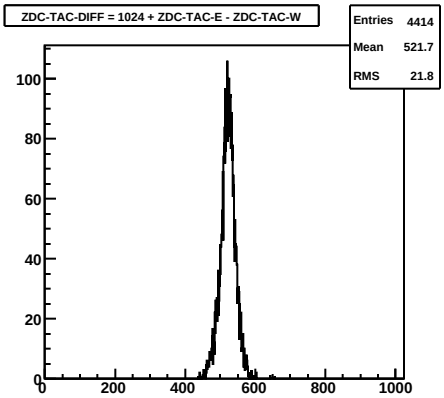

Entries 2207555
VPD East ADC Sum
VPD West ADC Sum

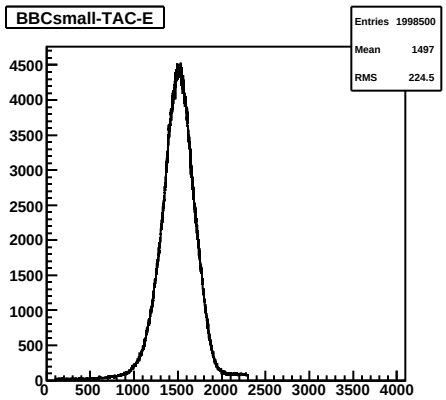

Entries 2207555
ZDC East ADC Sum Att
ZDC West ADC Sum Att

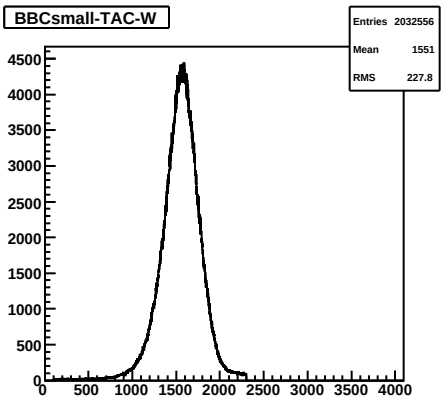

Entries 2207555
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2207555
BBC-L TAC Diff
VPD TAC Diff
Entries 2207555
BBC-L TAC Diff
ZDC TAC Diff
Entries 2207555
BBC-S TAC Diff
VPD TAC Diff
Entries 2207555
BBC-S TAC Diff
ZDC TAC Diff
Entries 2207555
VPD TAC Diff
ZDC TAC Diff

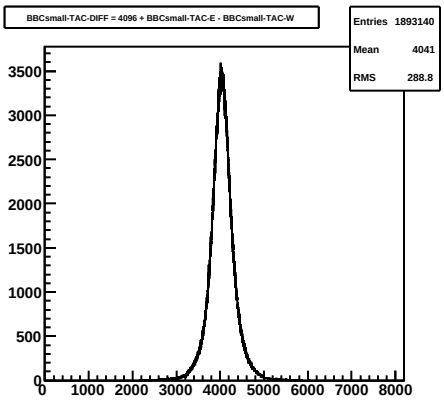

ZDC-TAC-E
Entries 75004
Mean 240.1
RMS 32.63

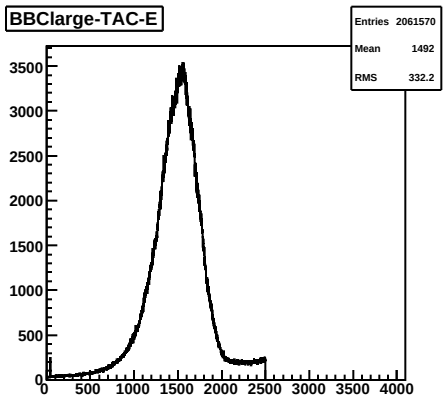

ZDC-TAC-W
Entries 60393
Mean 218.9
RMS 35.84

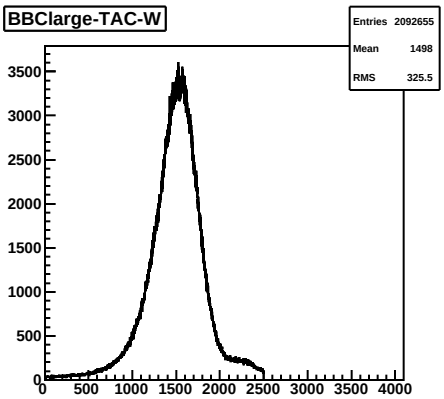

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 4414
Mean 521.7
RMS 21.8

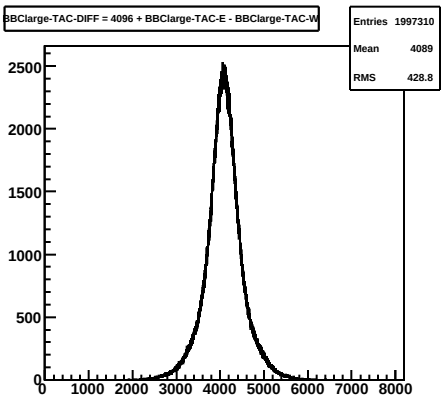

BBCsmall-TAC-E
Entries 1998500
Mean 1497
RMS 224.5

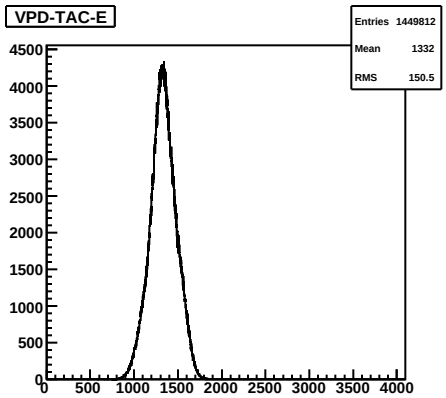

BBCsmall-TAC-W
Entries 2032556
Mean 1551
RMS 227.8

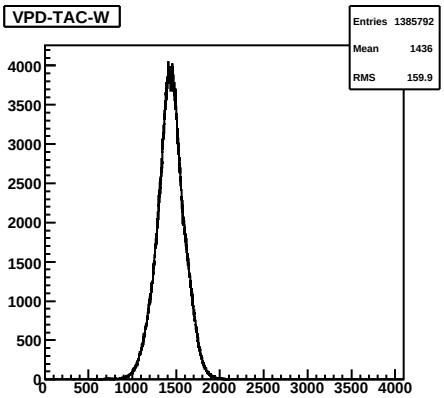

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1893140
Mean 4041
RMS 288.8

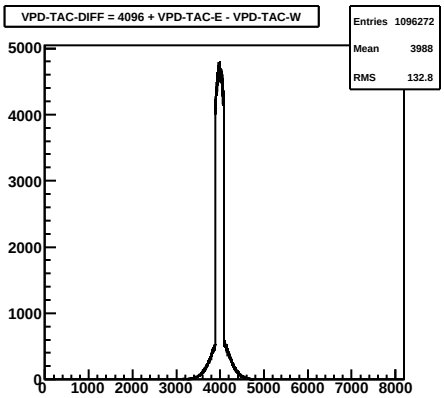

BBClarge-TAC-E
Entries 2061570
Mean 1492
RMS 332.2

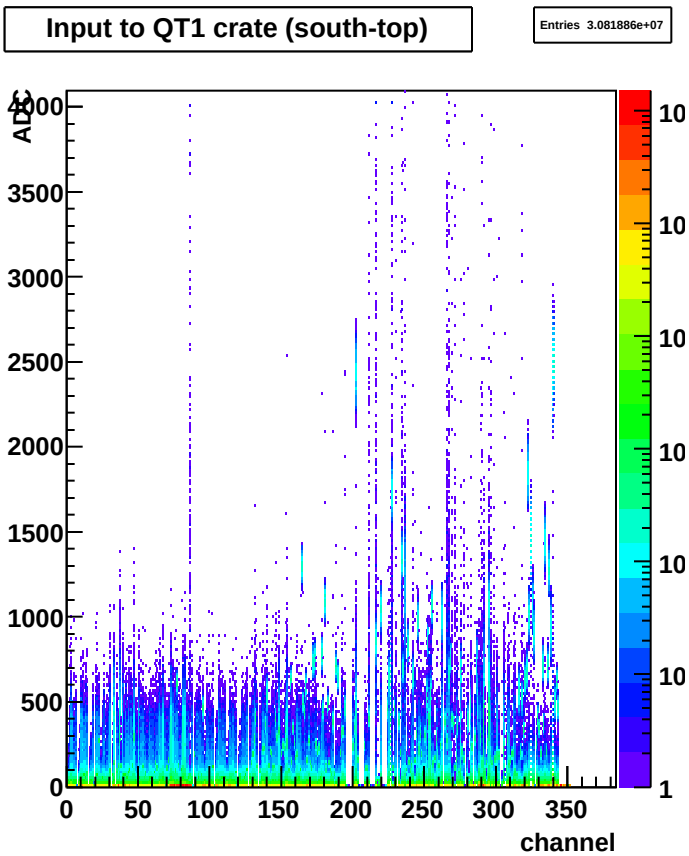

BBClarge-TAC-W
Entries 2092655
Mean 1498
RMS 325.5

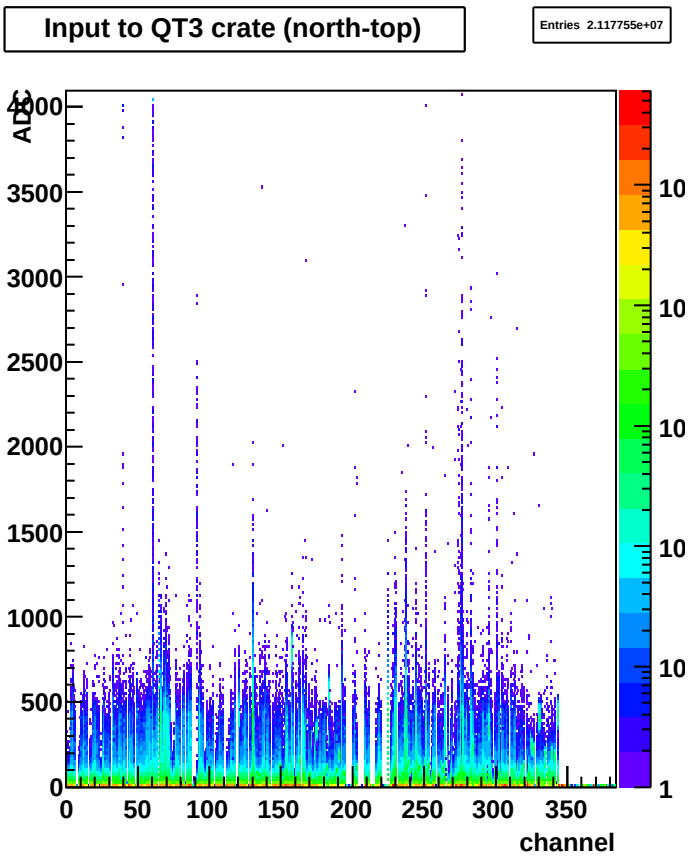

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1997310
Mean 4089
RMS 428.8

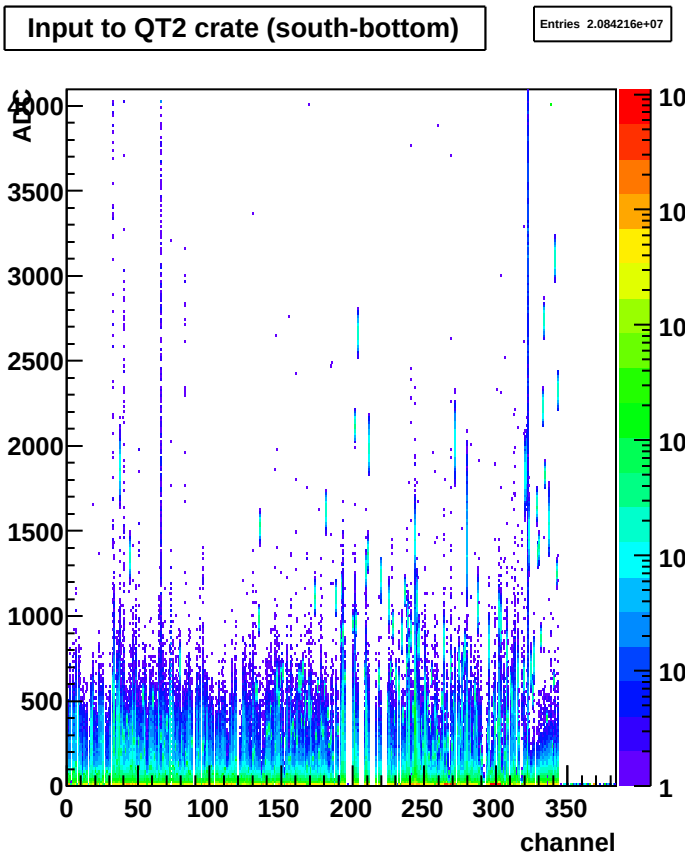

VPD-TAC-E
Entries 1449812
Mean 1332
RMS 150.5

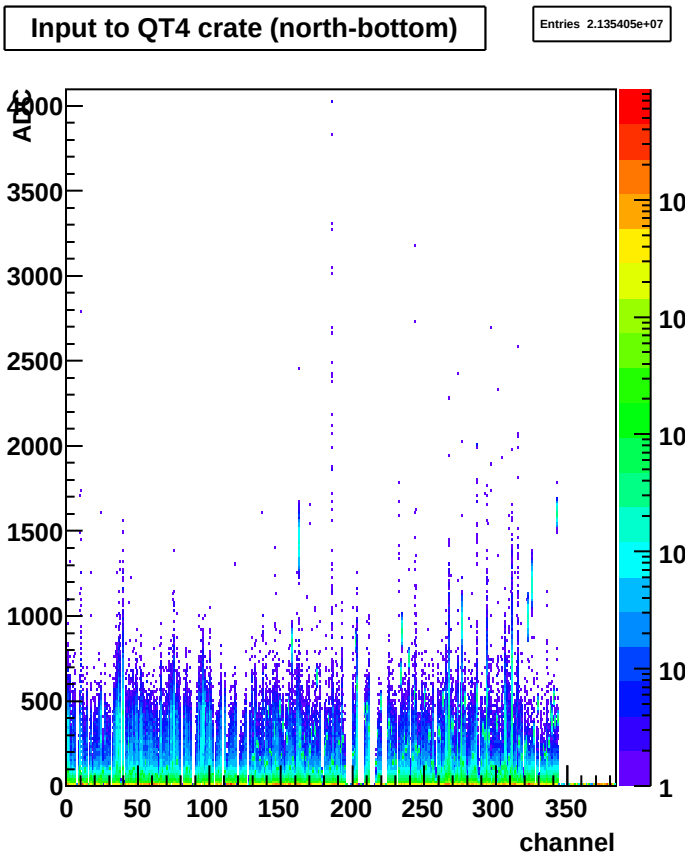

VPD-TAC-W
Entries 1385792
Mean 1436
RMS 159.9

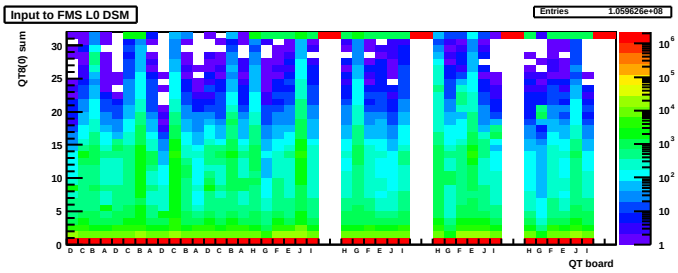

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1096272
Mean 3988
RMS 132.8

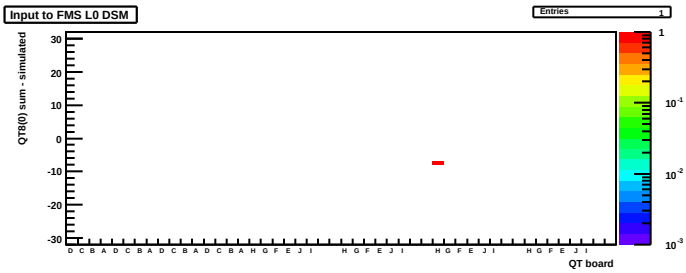

Input to QT1 crate (south-top)
Entries 3.081886e+07
ADC
channel

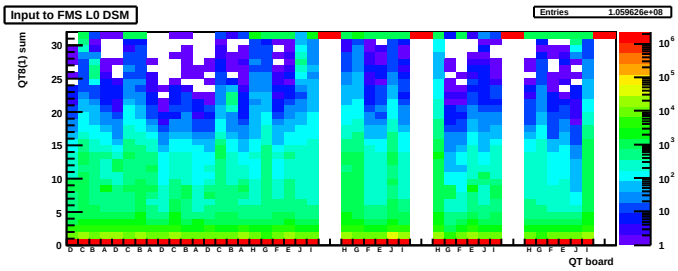

Input to QT3 crate (north-top)
Entries 2.117755e+07
ADC
channel

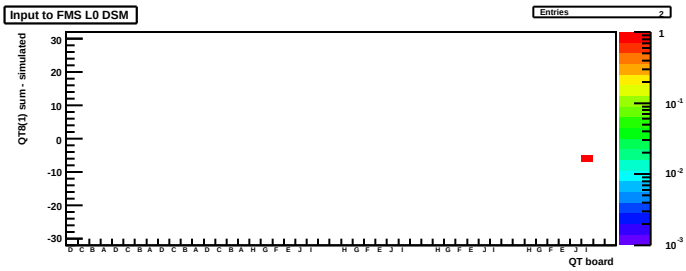

Input to QT2 crate (south-bottom)
Entries 2.084216e+07
ADC
channel

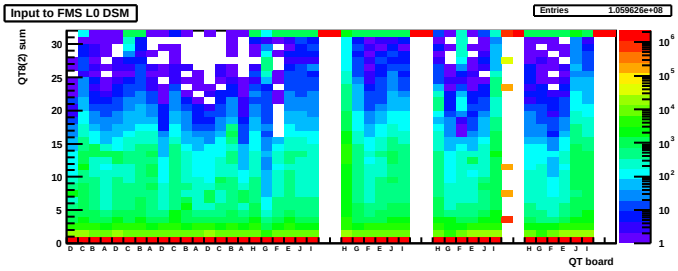

Input to QT4 crate (north-bottom)
Entries 2.135405e+07
ADC
channel

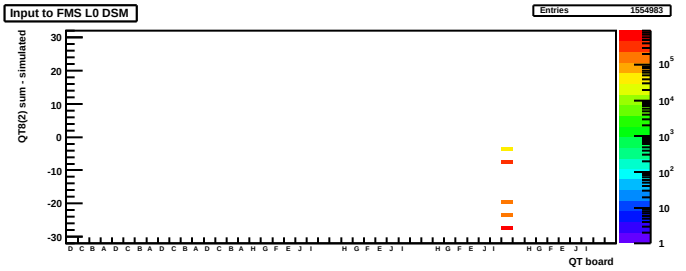

Input to FMS L0 DSM
Entries 1.059626e+08
QT8(0) sum
QT board

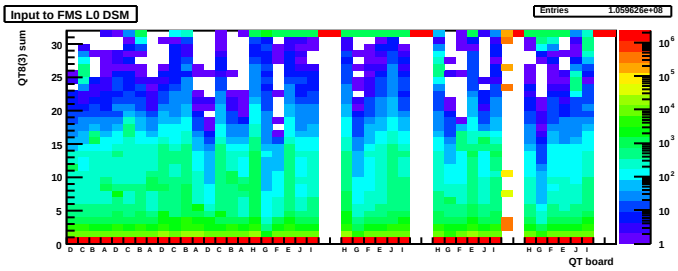

Input to FMS L0 DSM
Entries 1
QT8(0) sum - simulated
QT board

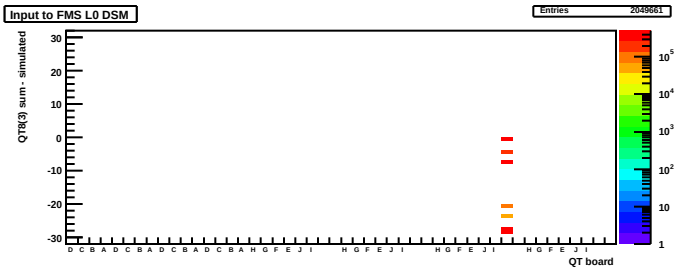

Input to FMS L0 DSM
Entries 1.059626e+08
QT8(1) sum
QT board

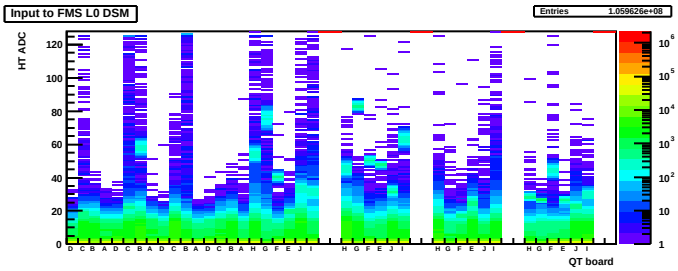

Input to FMS L0 DSM
Entries 2
QT8(1) sum - simulated
QT board

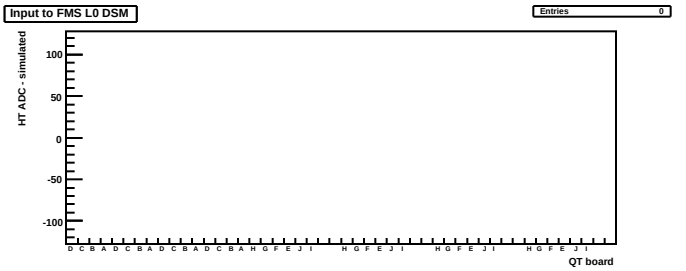

Input to FMS L0 DSM
Entries 1.059626e+08
QT8(2) sum
QT board

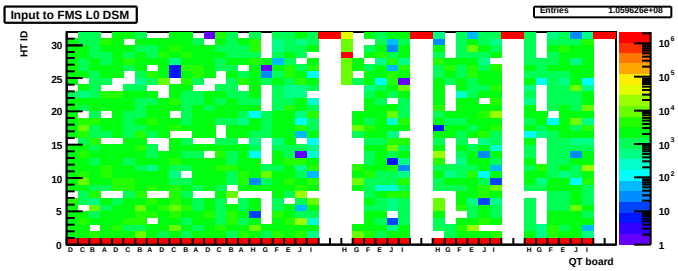

Input to FMS L0 DSM
Entries 1554983
QT8(2) sum - simulated
QT board

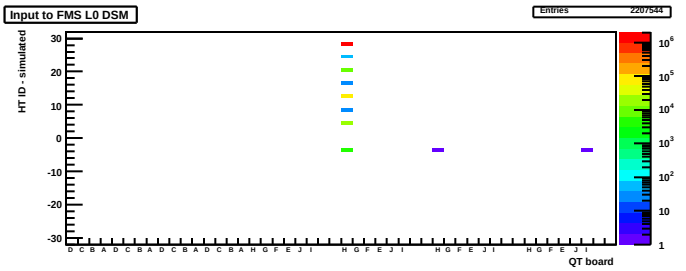

Input to FMS L0 DSM
Entries 1.059626e+08
QT8(3) sum
QT board

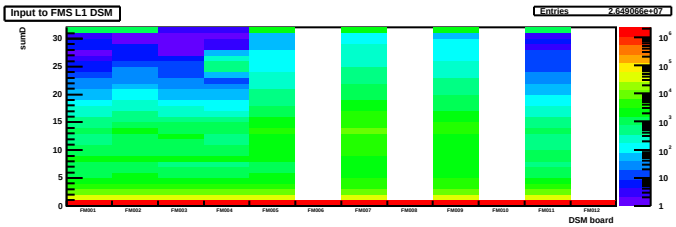

Input to FMS L0 DSM
Entries 2049661
QT8(3) sum - simulated
QT board

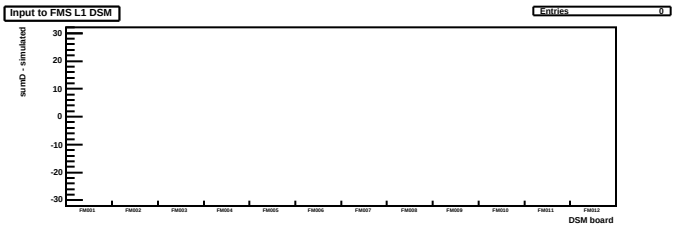

Input to FMS L0 DSM
Entries 1.059626e+08
HT ADC
QT board

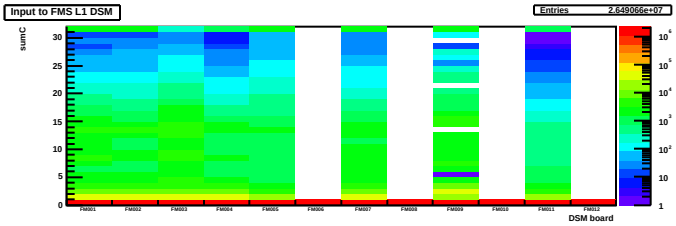

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

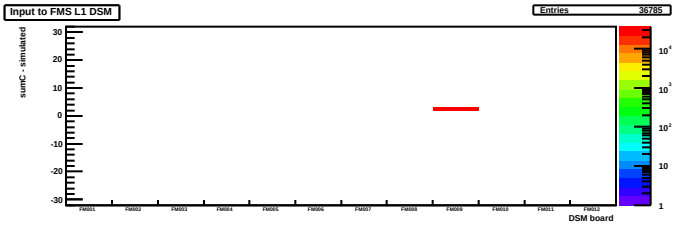

Input to FMS L0 DSM
Entries 1.059626e+08
HT ID
QT board

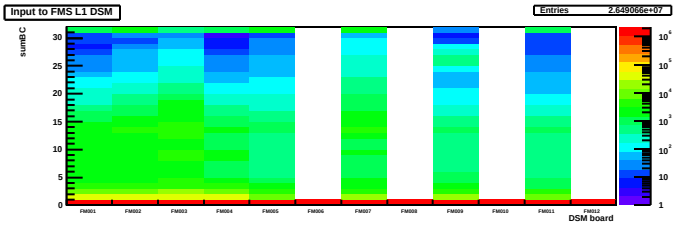

Input to FMS L0 DSM
Entries 2207544
HT ID - simulated
QT board

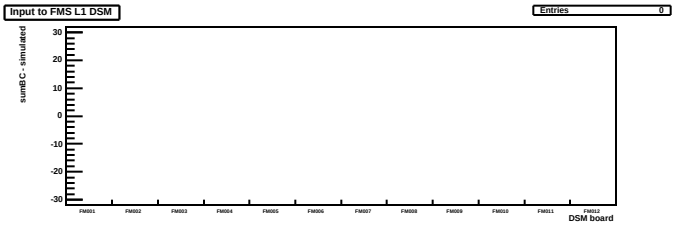

Input to FMS L1 DSM
Entries 2.649066e+07
sumD
DSM board

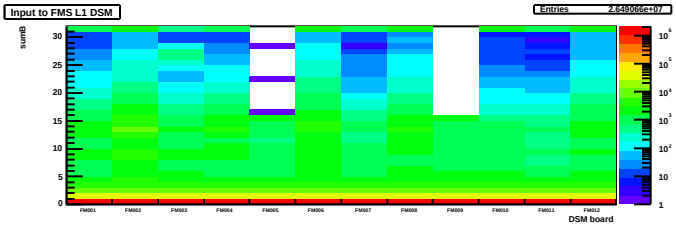

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

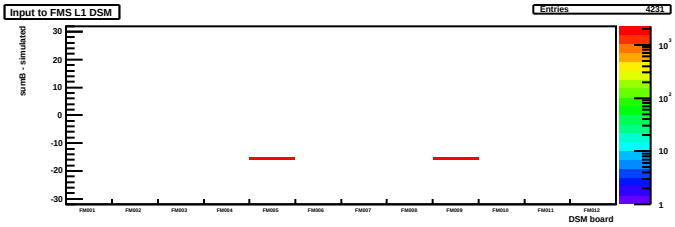

Input to FMS L1 DSM
Entries 2.649066e+07
sumC
DSM board

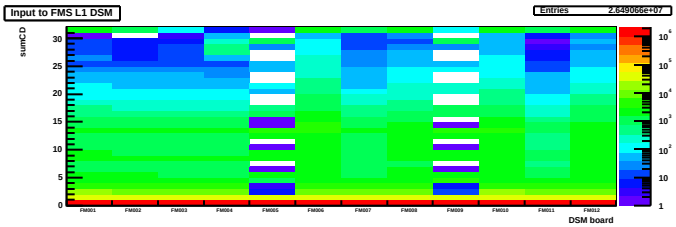

Input to FMS L1 DSM
Entries 36785
sumC - simulated
DSM board

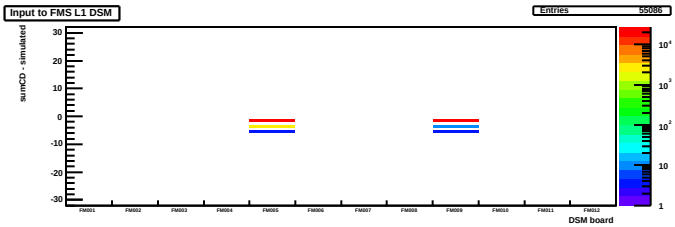

Input to FMS L1 DSM
Entries 2.649066e+07
sumBC
DSM board

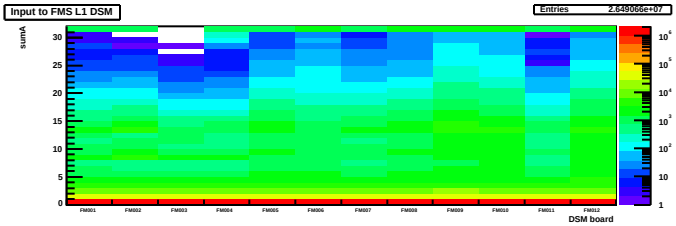

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

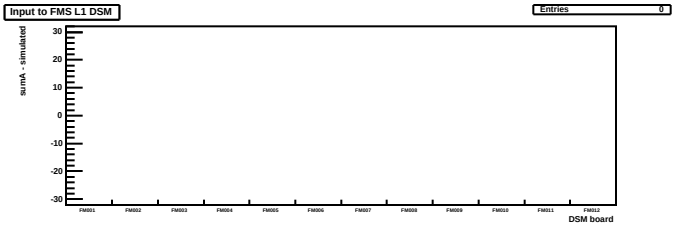

Input to FMS L1 DSM
Entries 2.649066e+07
sumB
DSM board

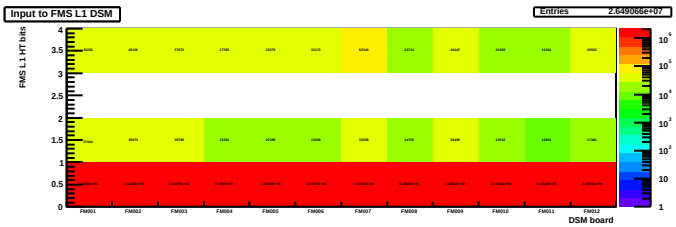

Input to FMS L1 DSM
Entries 4231
sumB - simulated
DSM board

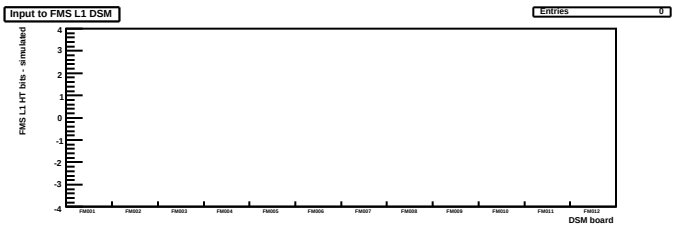

Input to FMS L1 DSM
Entries 2.649066e+07
sumCD
DSM board

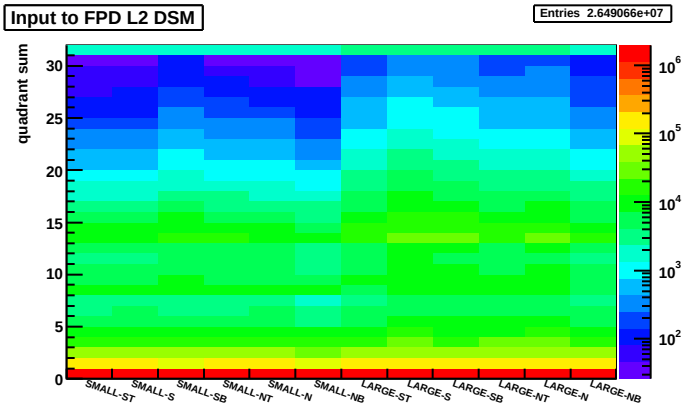

Input to FMS L1 DSM
Entries 55086
sumCD - simulated
DSM board

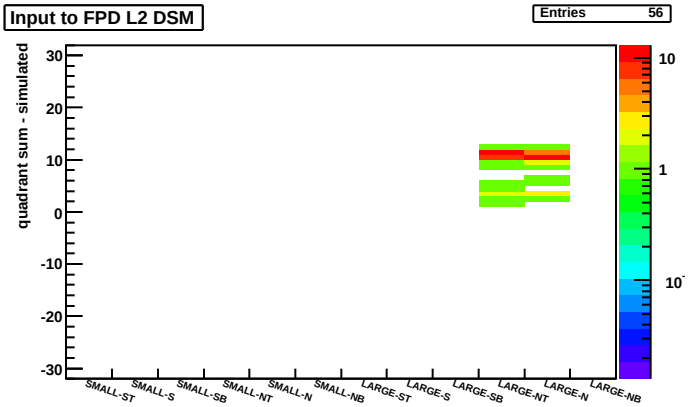

Input to FMS L1 DSM
Entries 2.649066e+07
sumA
DSM board

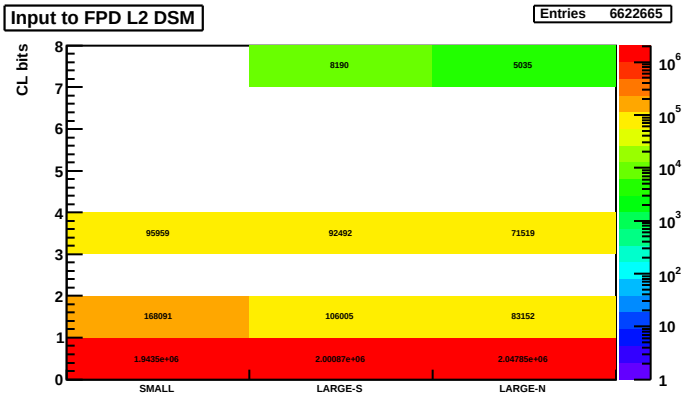

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

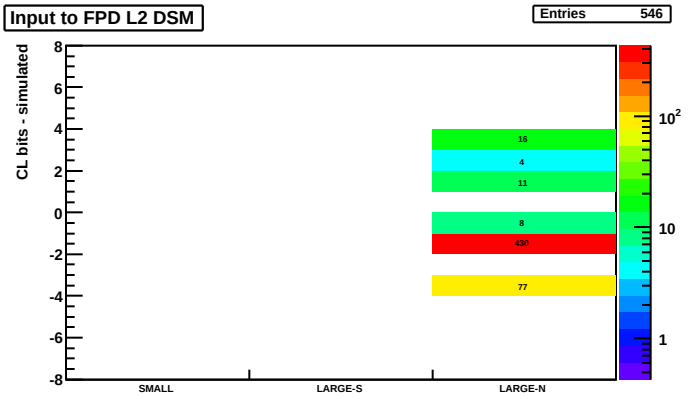

Input to FMS L1 DSM
Entries 2.649066e+07
FMS L1 HT bits
DSM board

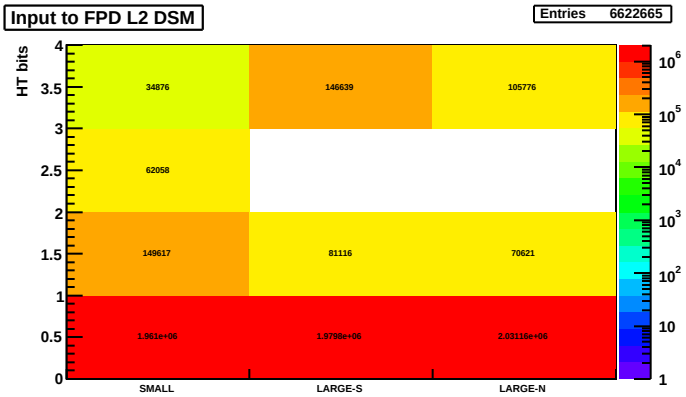

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

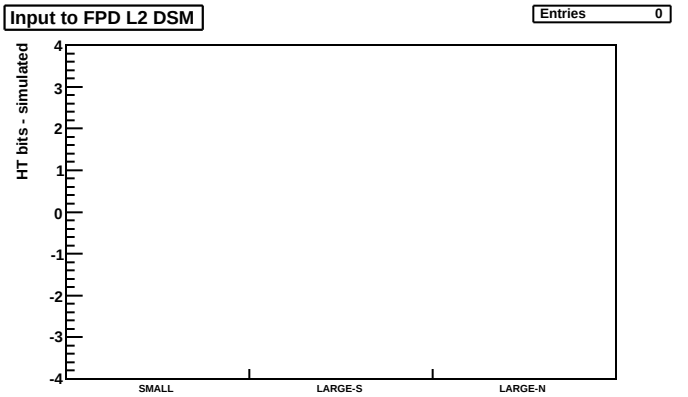

Input to FPD L2 DSM
Entries 2.649066e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

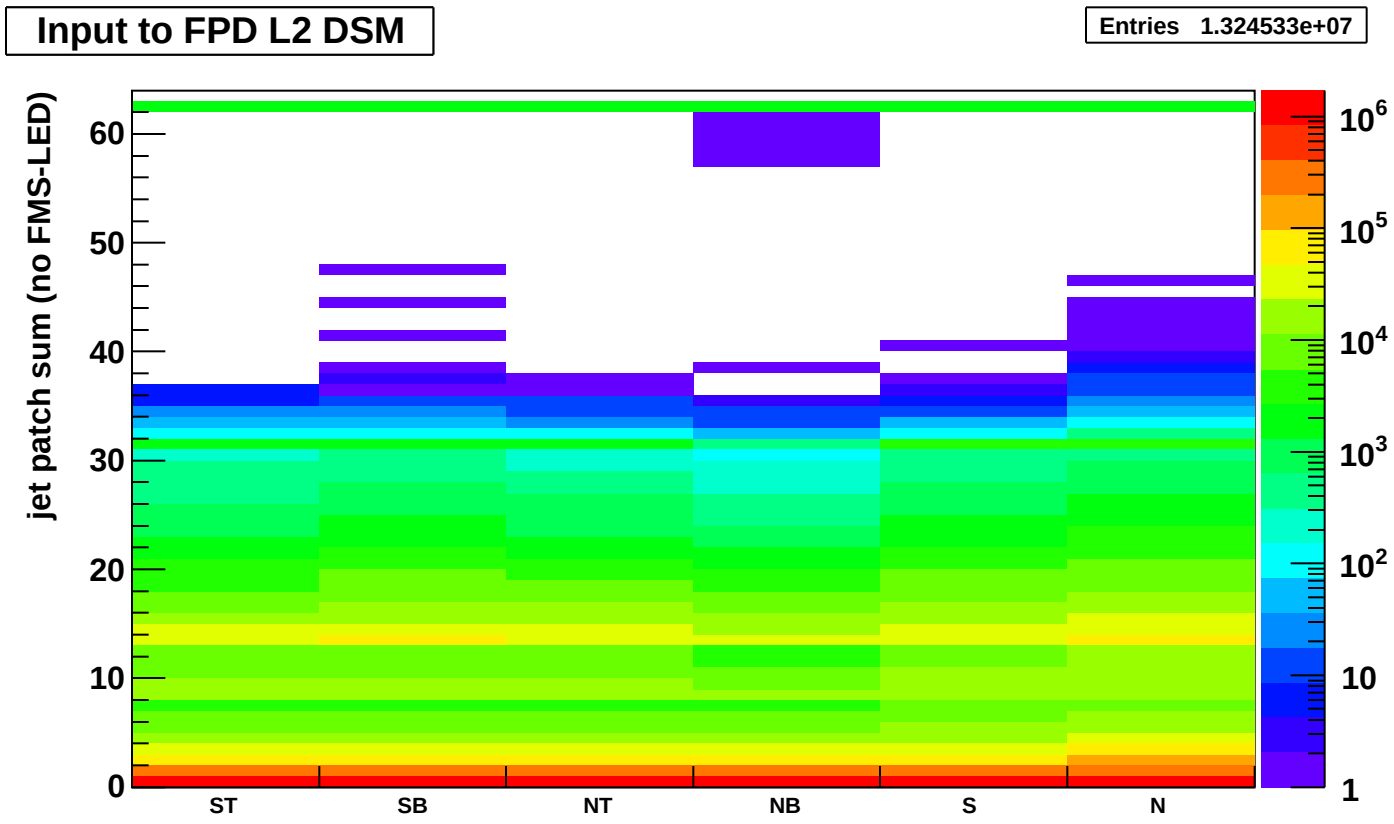

Input to FPD L2 DSM
Entries 56
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

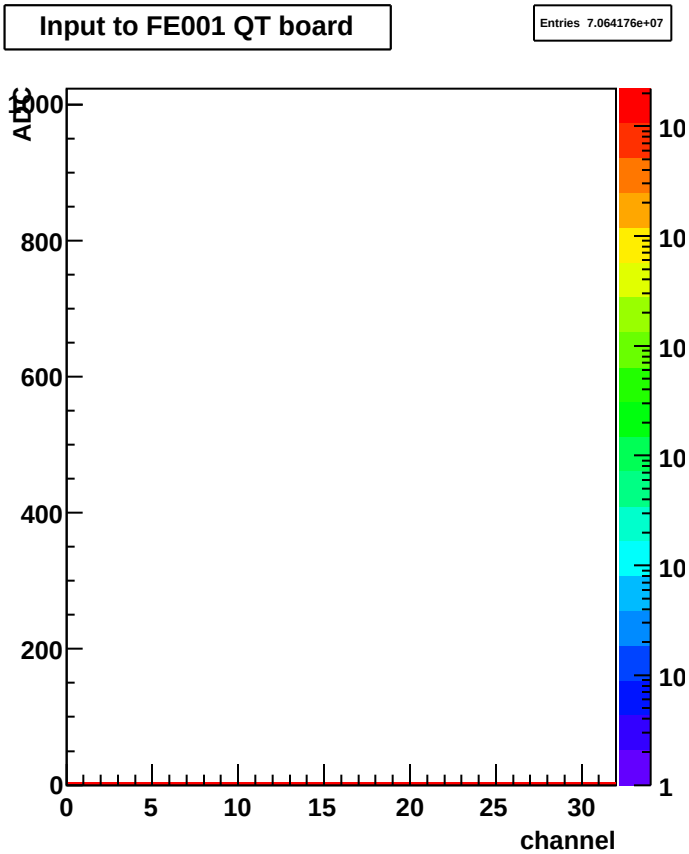

Input to FPD L2 DSM
Entries 6622665
CL bits
8190
5035
95959
92492
71519
168091
106005
83152
1.9435e+06
2.00087e+06
2.04785e+06
SMALL
LARGE-S
LARGE-N

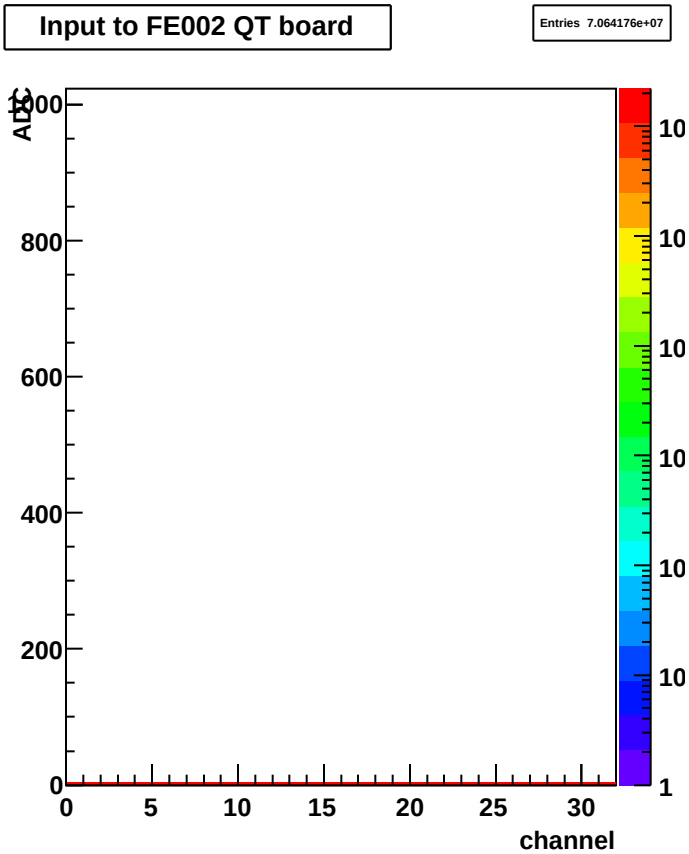

Input to FPD L2 DSM
Entries 546
CL bits - simulated
16
4
11
8
430
77
SMALL
LARGE-S
LARGE-N

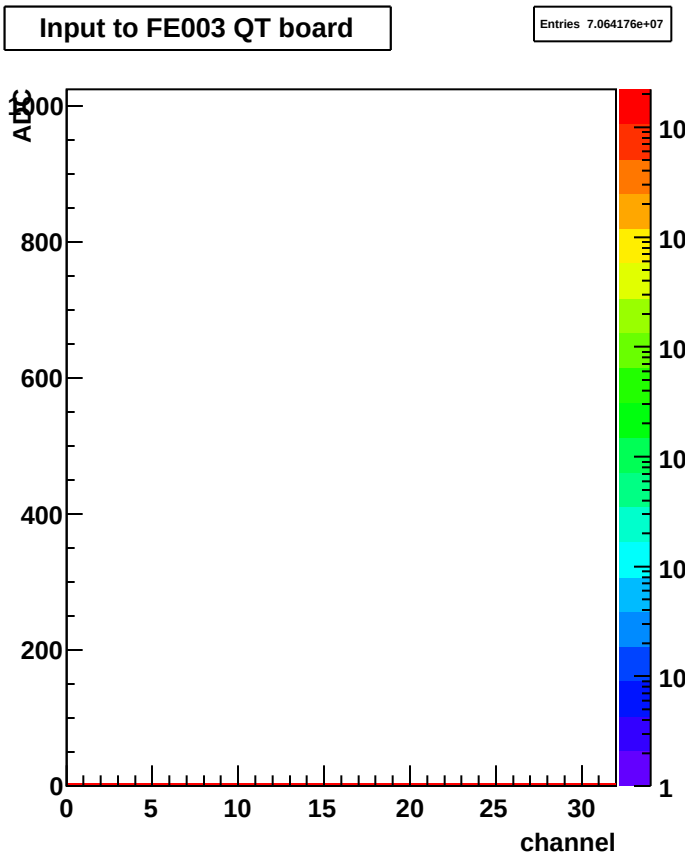

Input to FPD L2 DSM
Entries 6622665
HT bits
34876
146639
105776
62058
149617
81116
70621
1.961e+06
1.9798e+06
2.03116e+06
SMALL
LARGE-S
LARGE-N

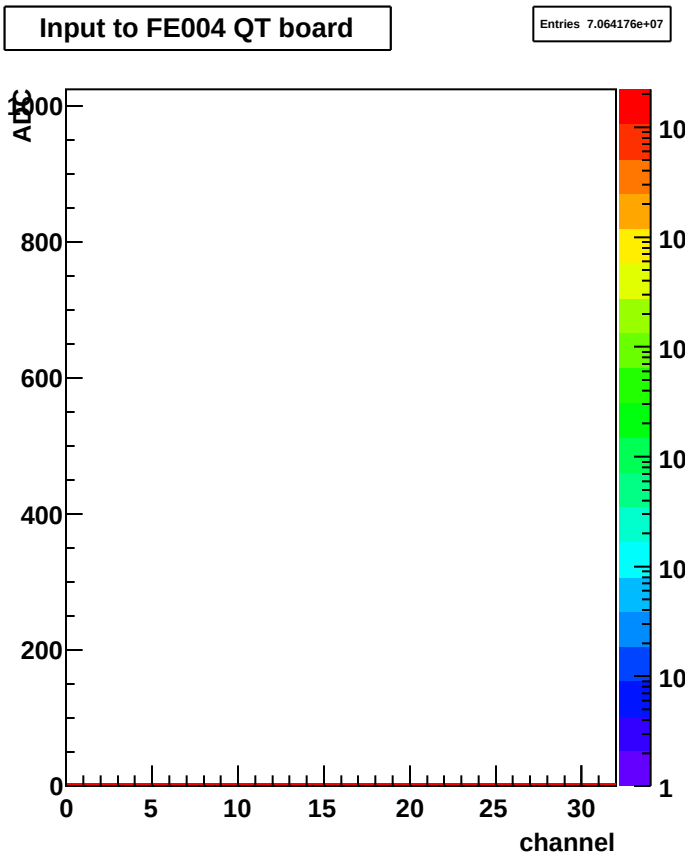

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

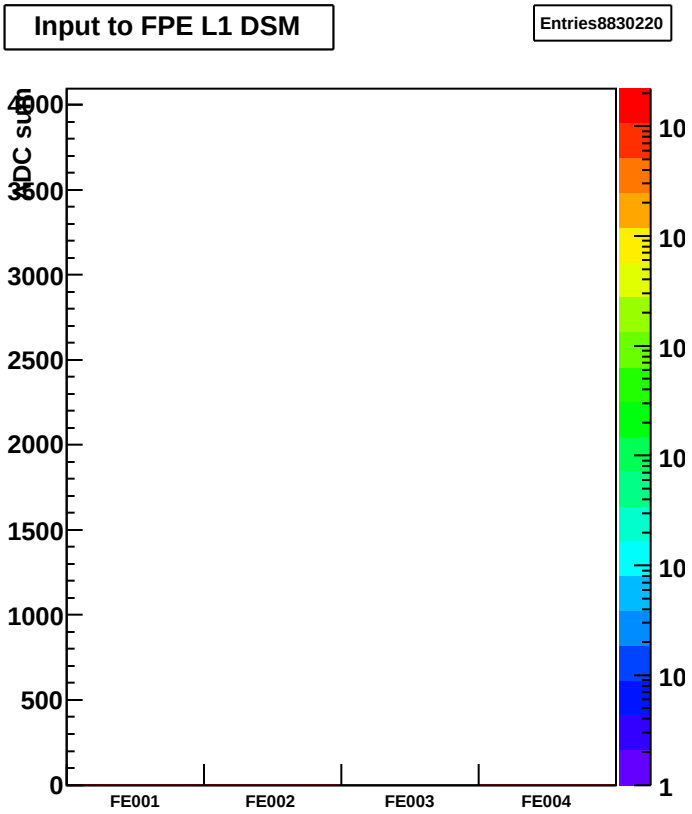

Input to FPD L2 DSM
Entries 1.324533e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 7.064176e+07
ADC
channel

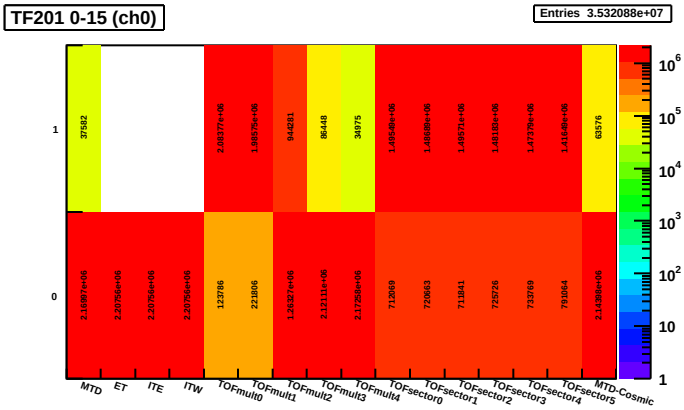

Input to FE002 QT board
Entries 7.064176e+07
ADC
channel


Input to FE003 QT board
Entries 7.064176e+07
ADC
channel

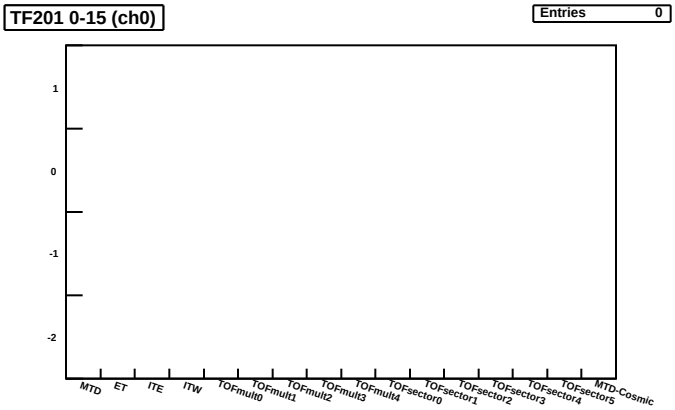

Input to FE004 QT board
Entries 7.064176e+07
ADC
channel


Input to FPE L1 DSM
Entries8830220
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

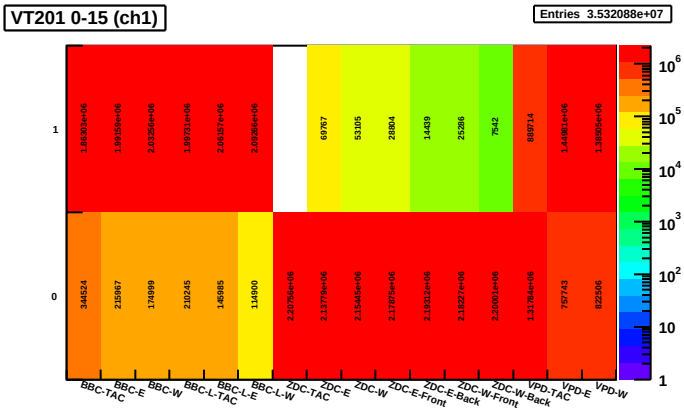
TF201 0-15 (ch0)



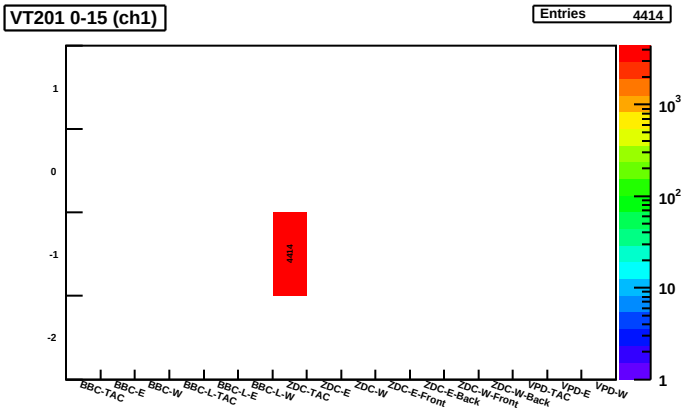
TF201 0-15 (ch0)



VT201 0-15 (ch1)



VT201 0-15 (ch1)



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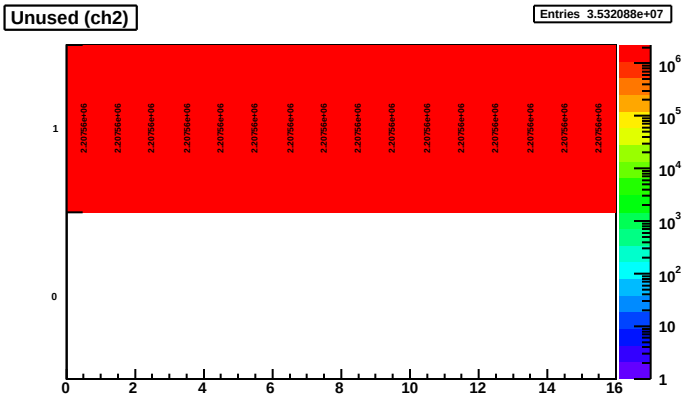

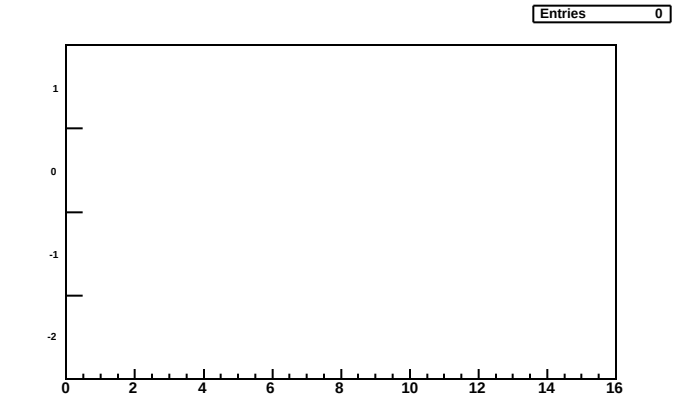

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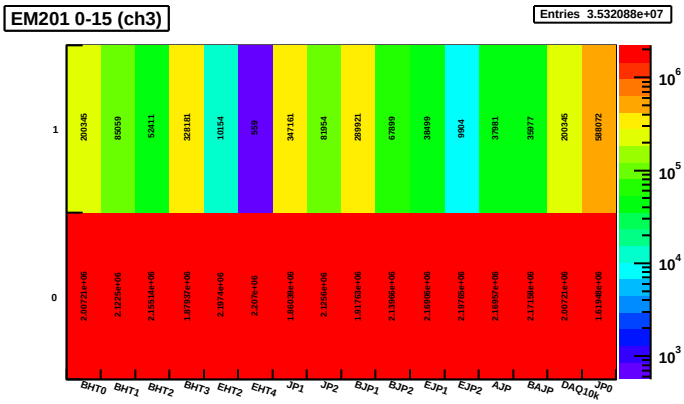

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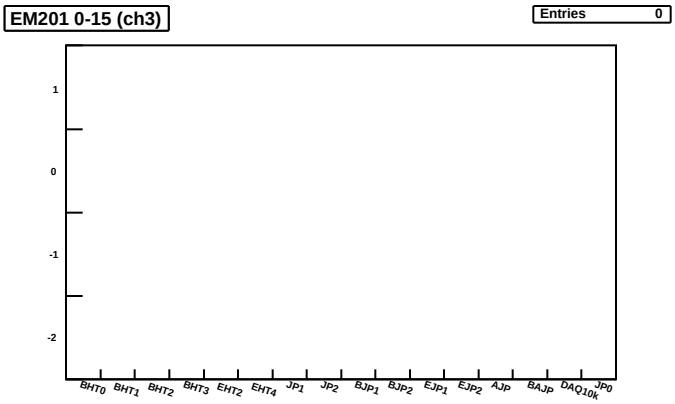
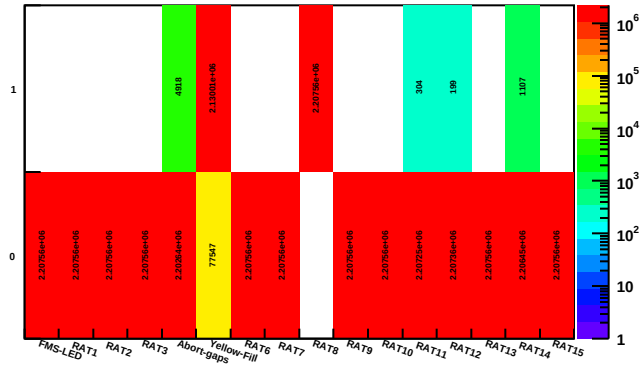
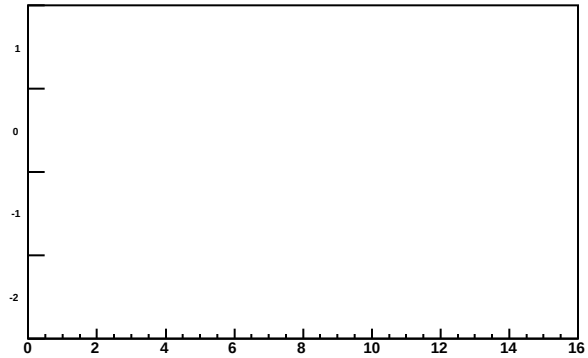
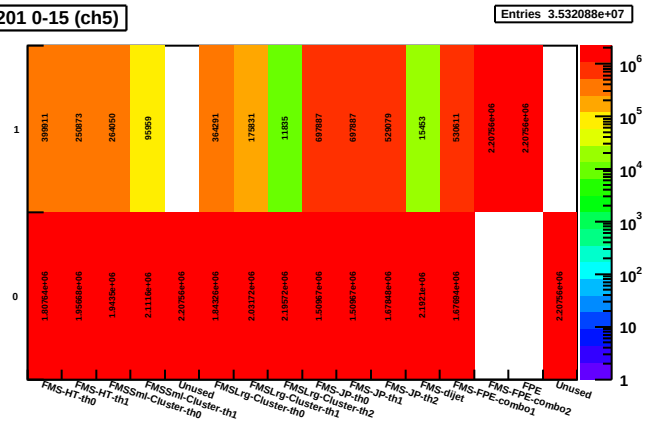


RAT board (ch4)

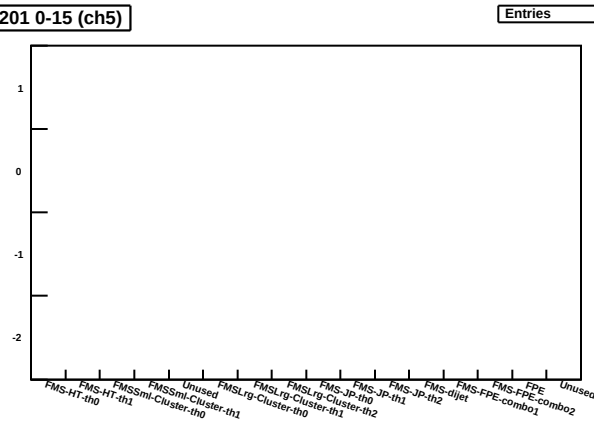




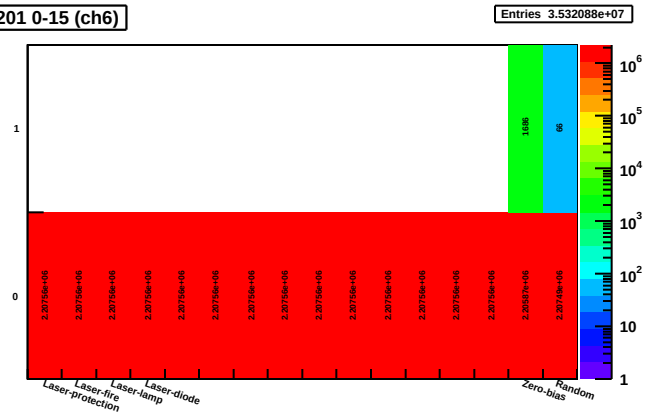
FP201 0-15 (ch5)

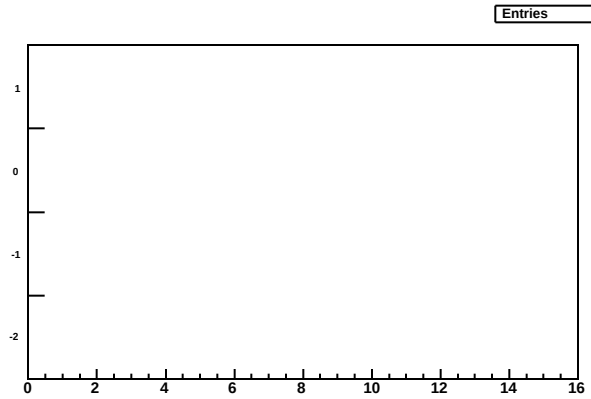
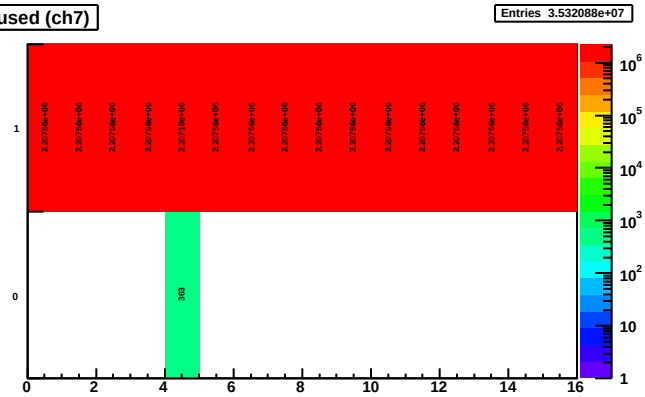
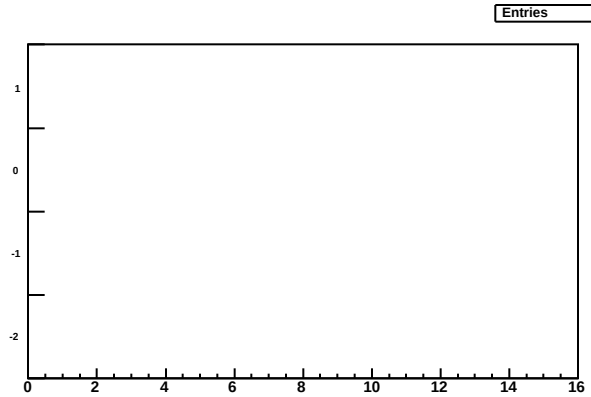


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)