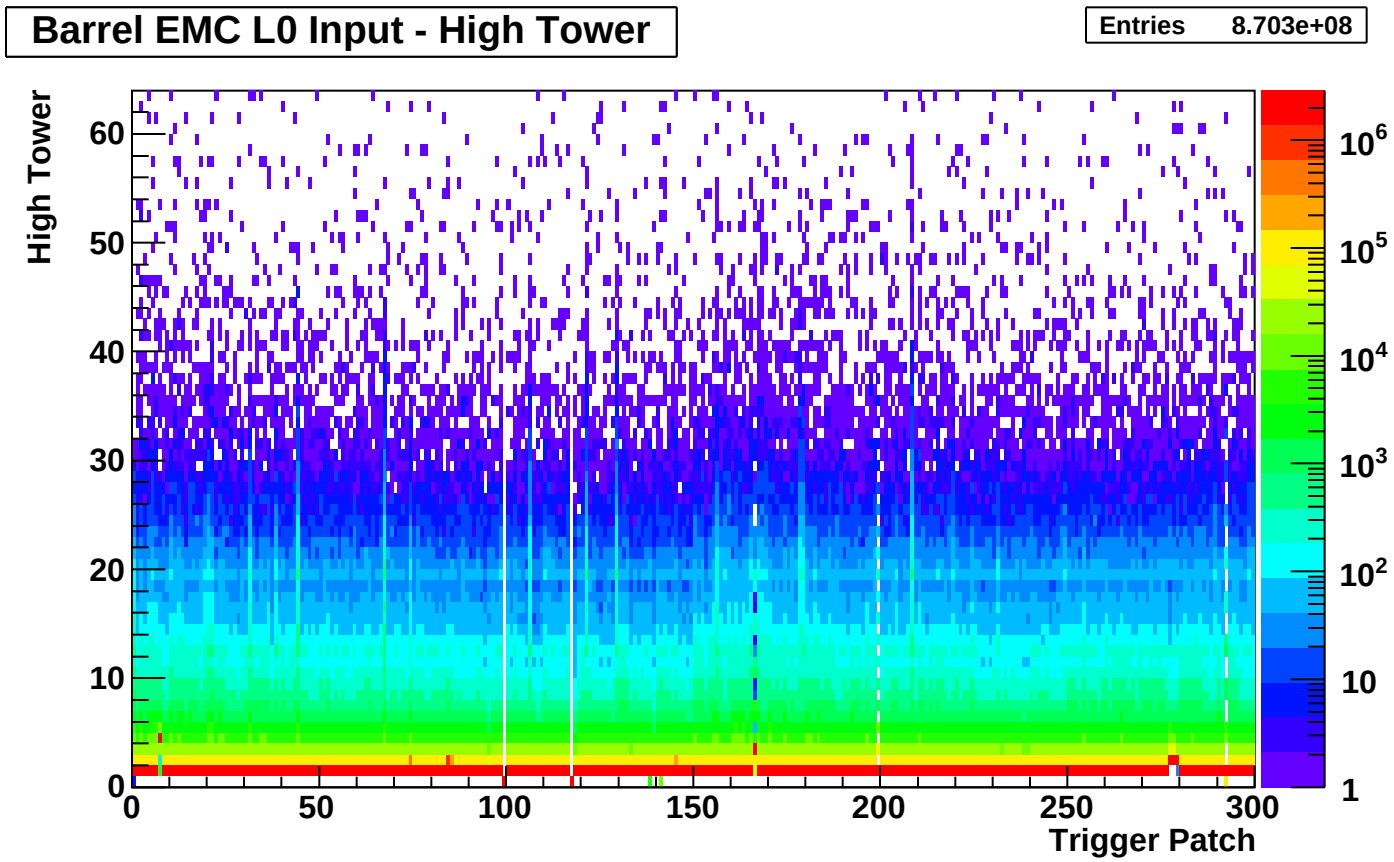
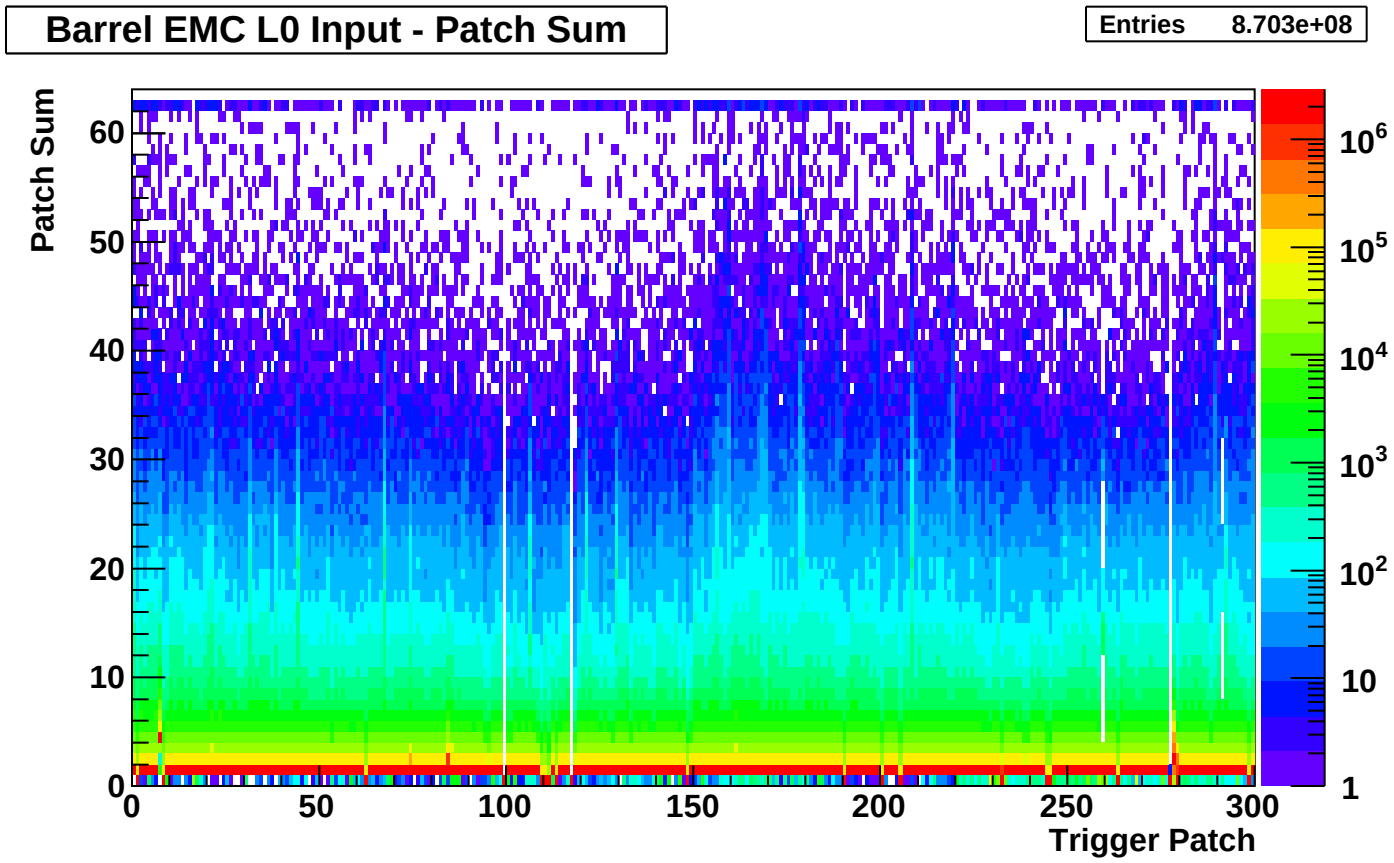
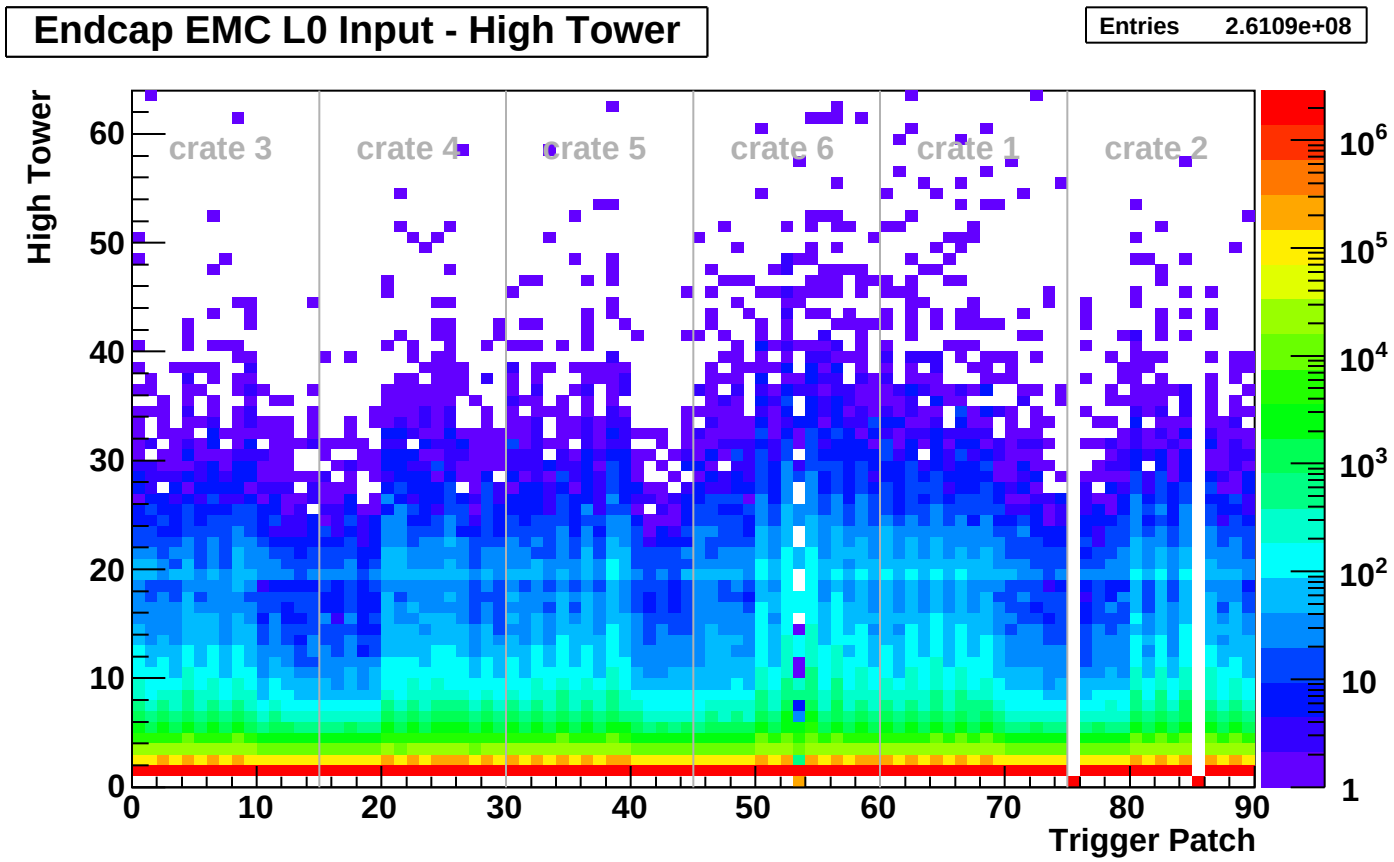
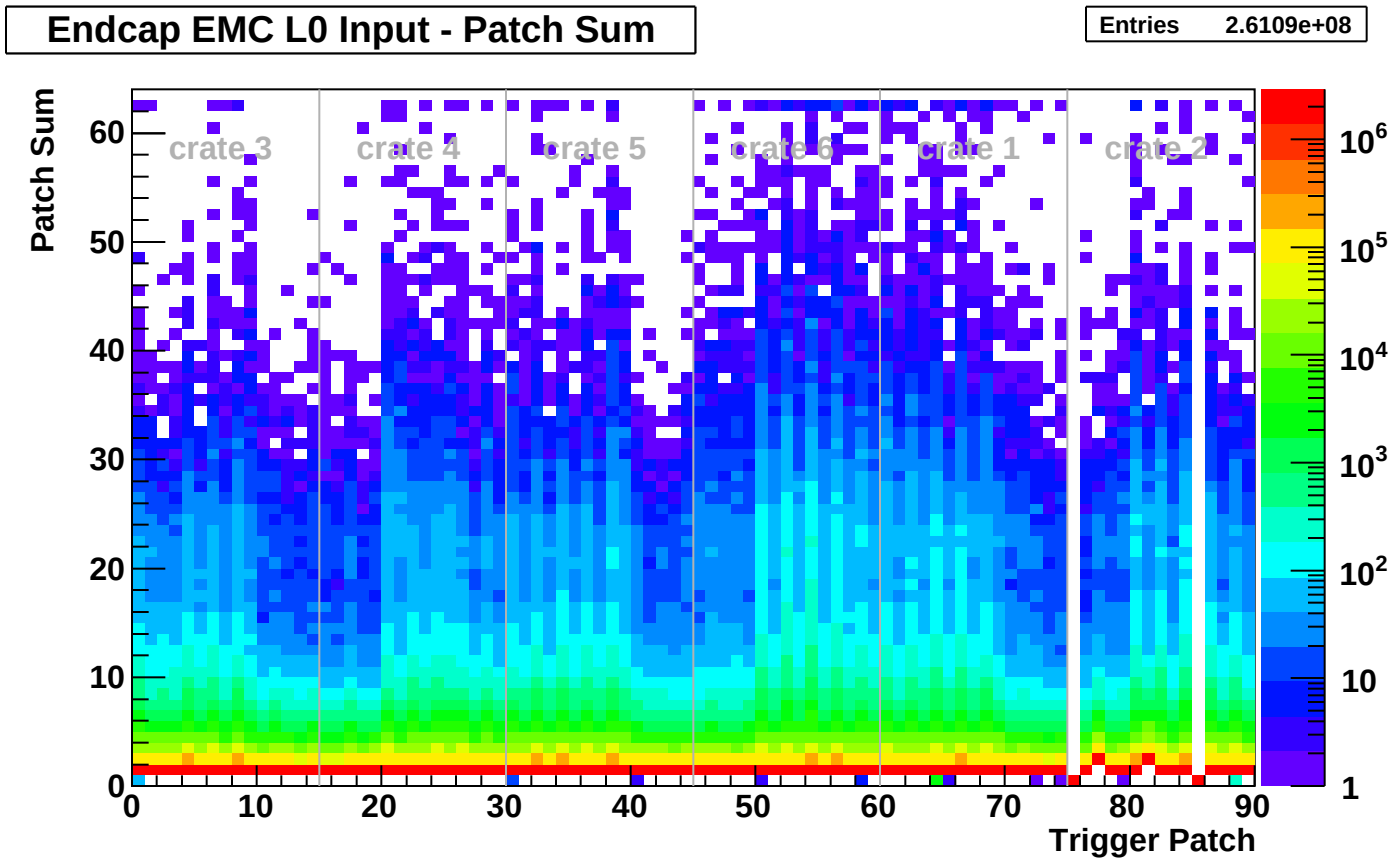
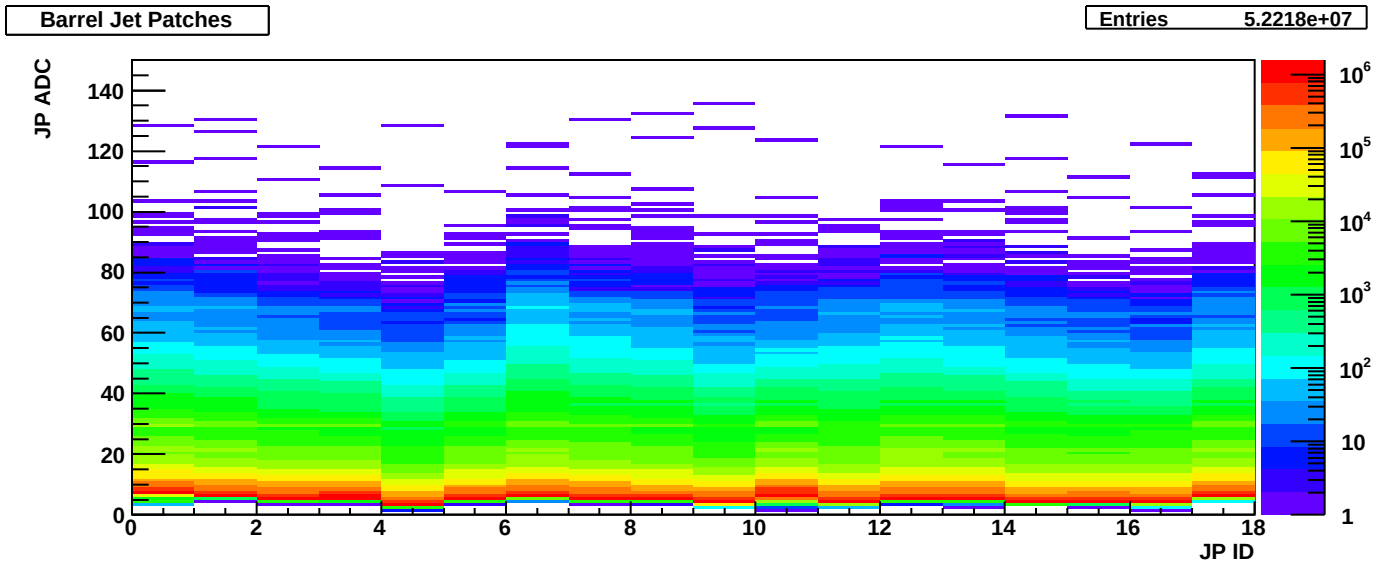
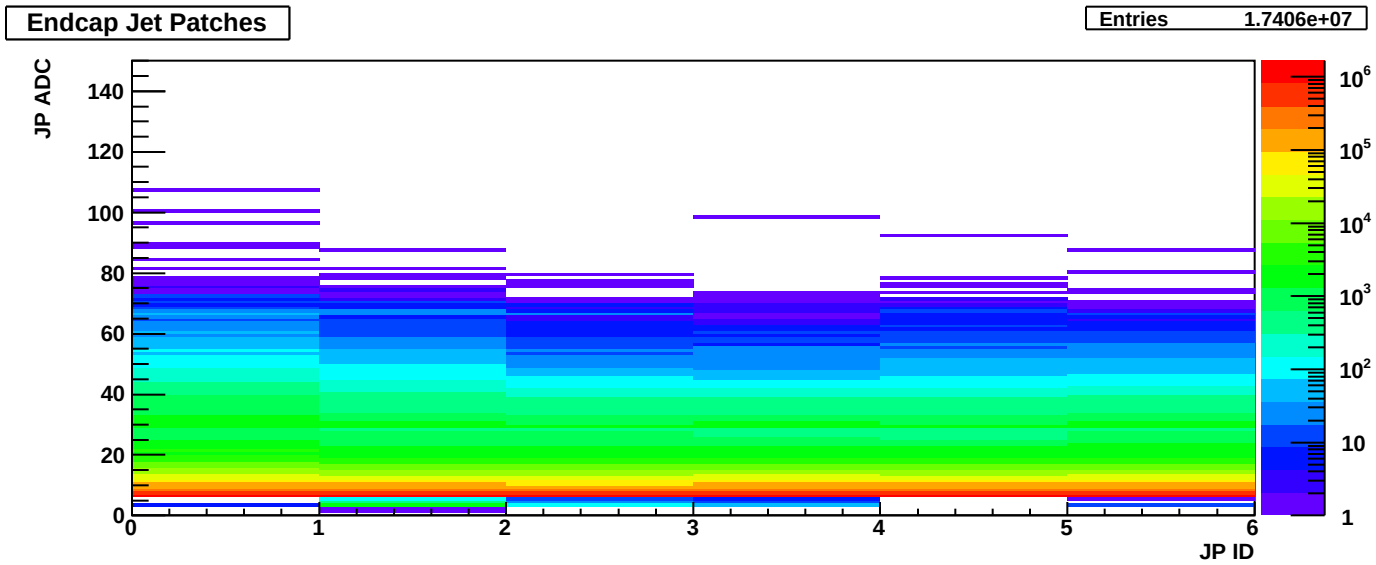

Barrel EMC L0 Input - High Tower
Entries 8.703e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

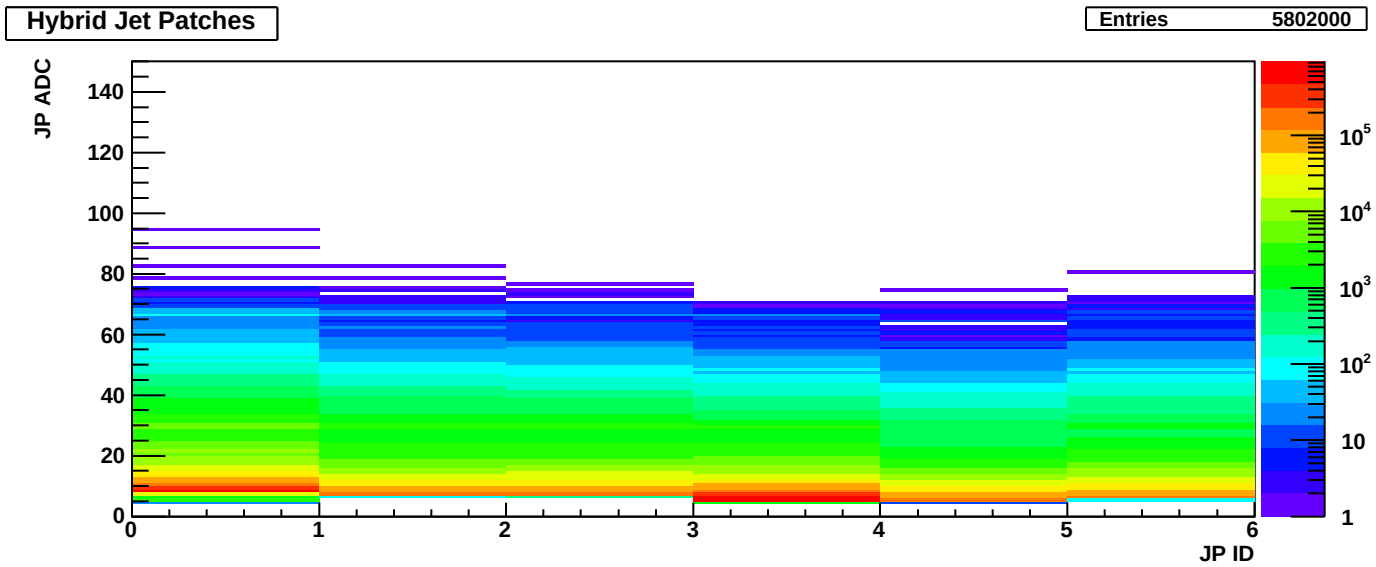

Barrel EMC L0 Input - Patch Sum
Entries 8.703e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

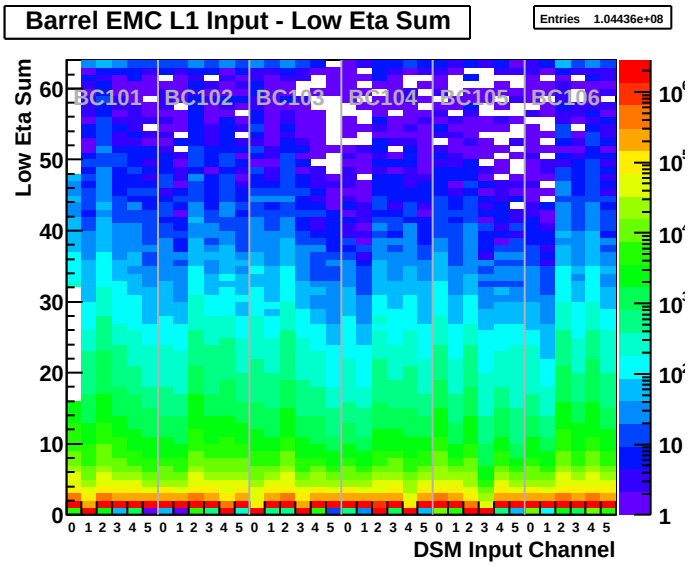

Endcap EMC L0 Input - High Tower
Entries 2.6109e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

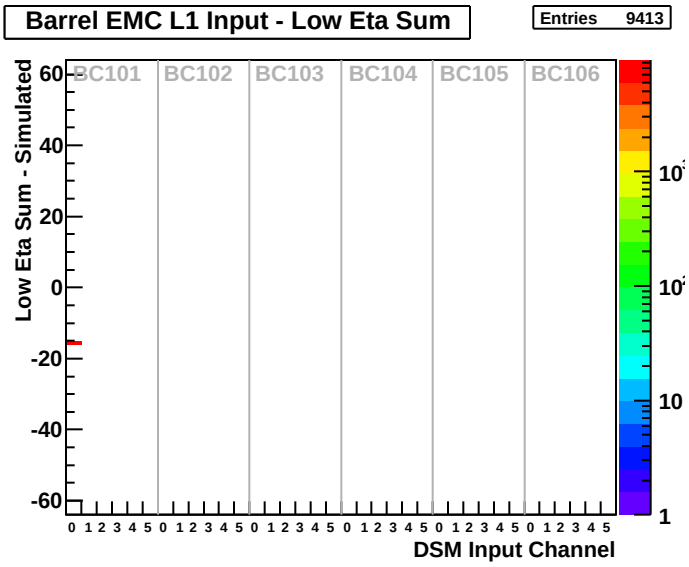

Endcap EMC L0 Input - Patch Sum
Entries 2.6109e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

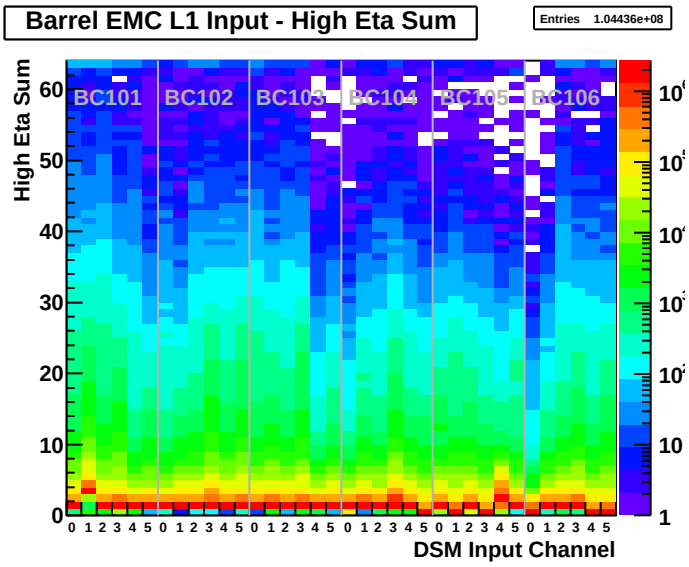

Barrel Jet Patches
Entries 5.2218e+07
JP ADC
JP ID

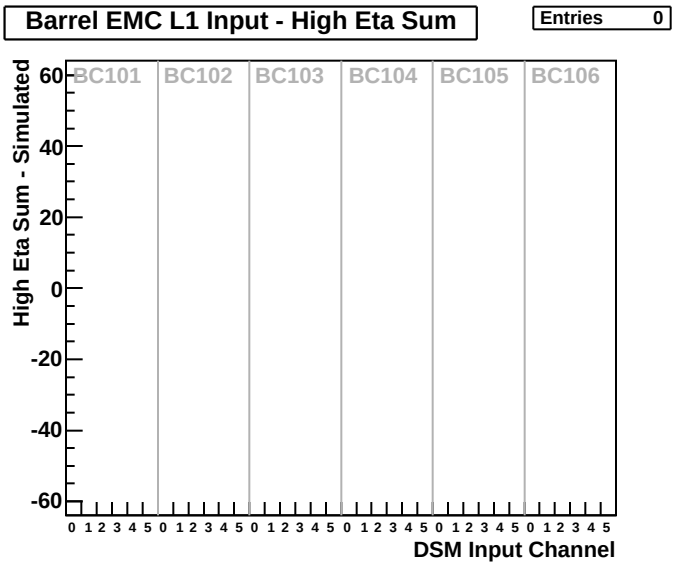

Endcap Jet Patches
Entries 1.7406e+07
JP ADC
JP ID

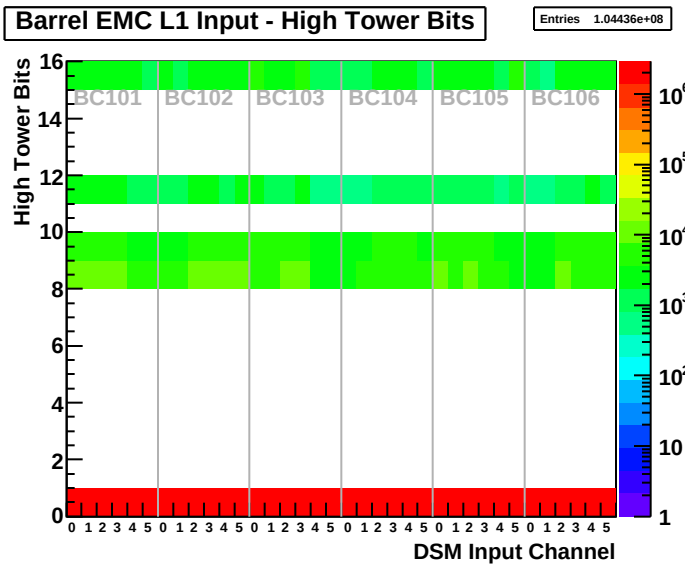

Hybrid Jet Patches
Entries 5802000
JP ADC
JP ID

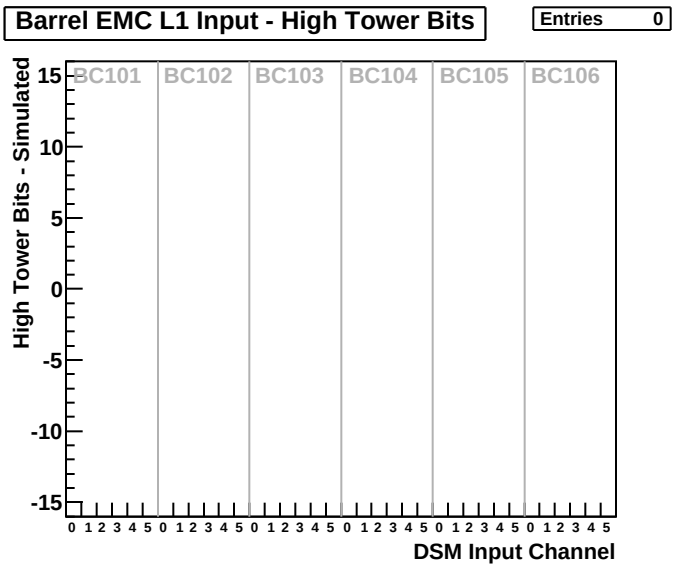

Barrel EMC L1 Input - Low Eta Sum
Entries 1.04436e+08
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

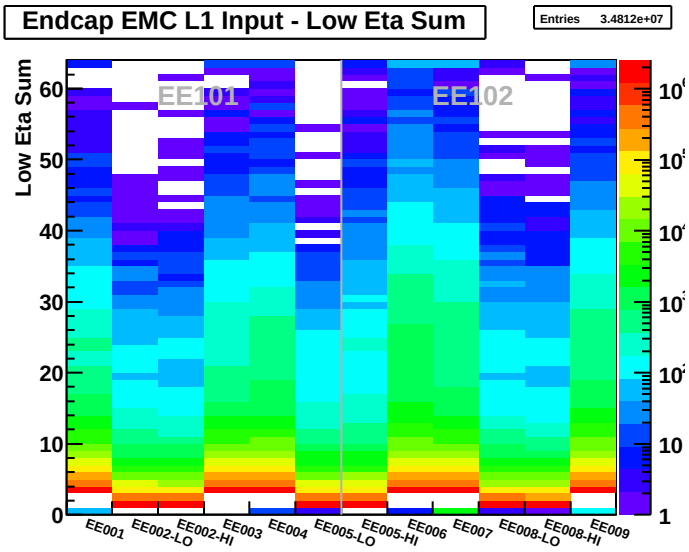

Barrel EMC L1 Input - Low Eta Sum
Entries 9413
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

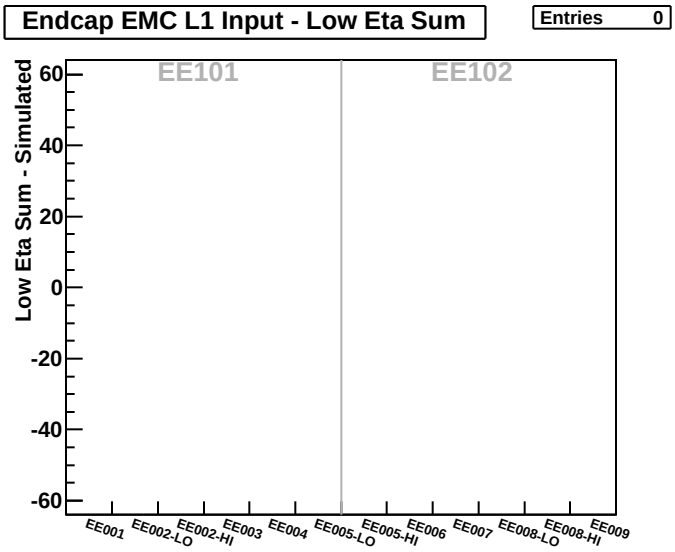

Barrel EMC L1 Input - High Eta Sum
Entries 1.04436e+08
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

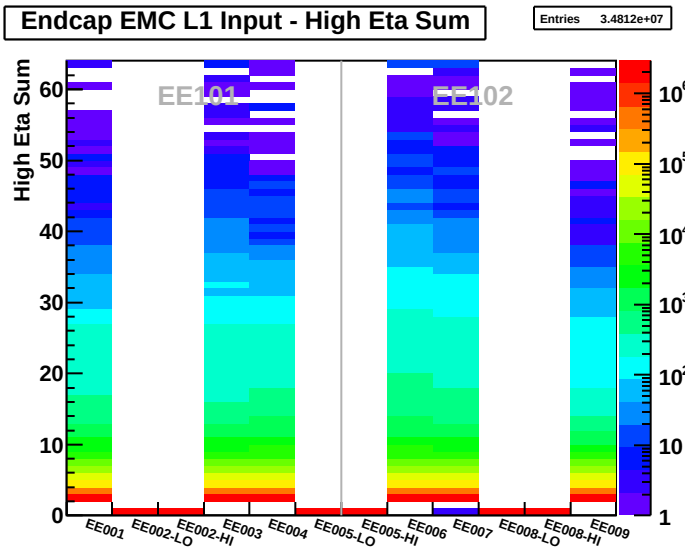

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

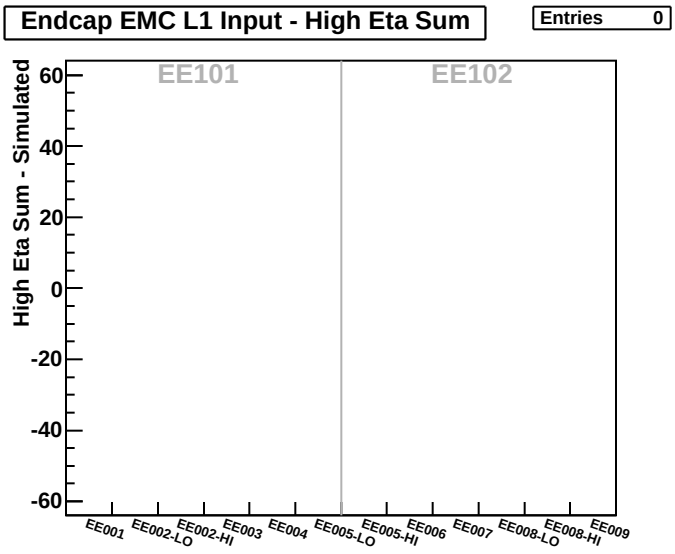

Barrel EMC L1 Input - High Tower Bits
Entries 1.04436e+08
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

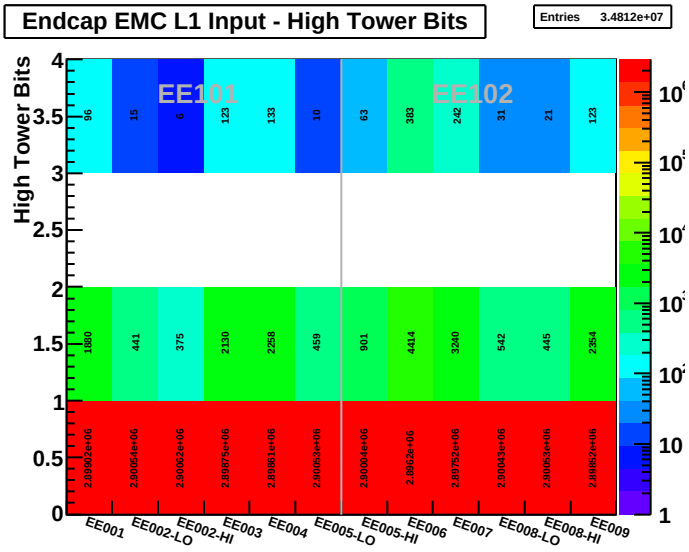

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

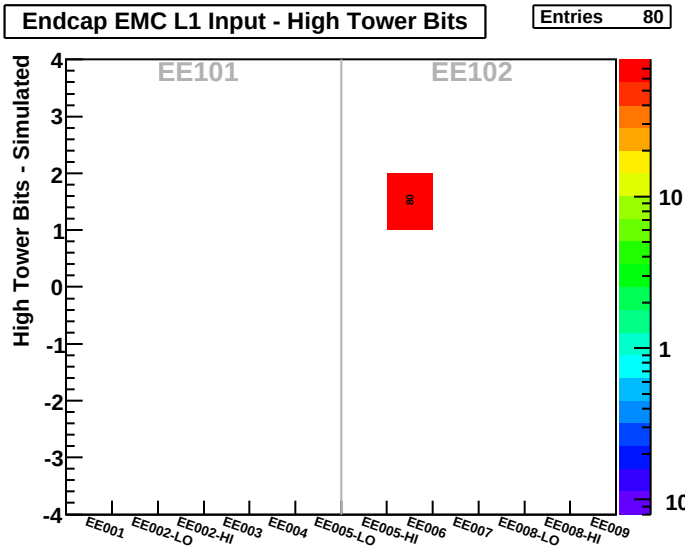

Endcap EMC L1 Input - Low Eta Sum
Entries 3.4812e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

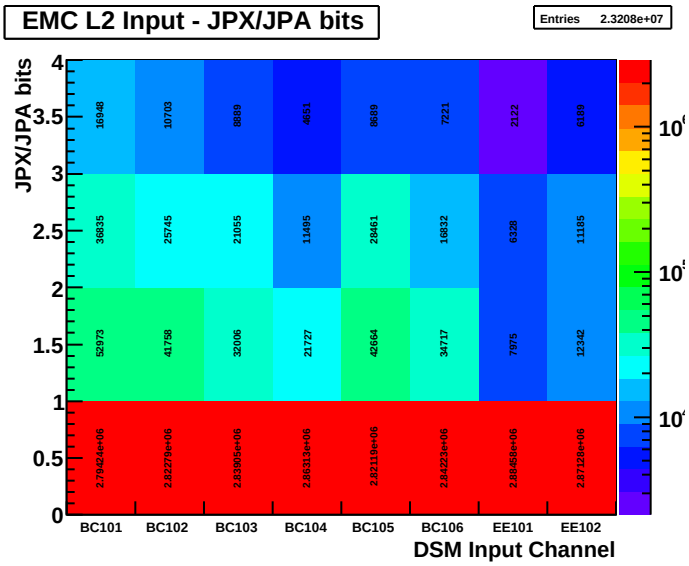

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

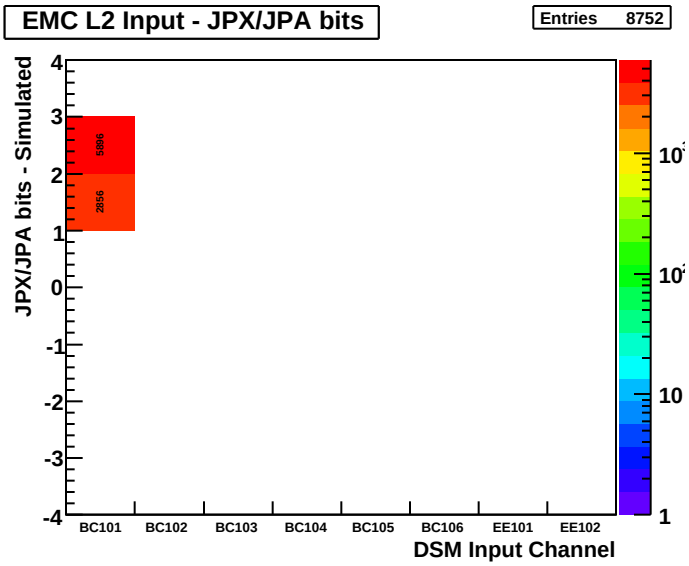

Endcap EMC L1 Input - High Eta Sum
Entries 3.4812e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

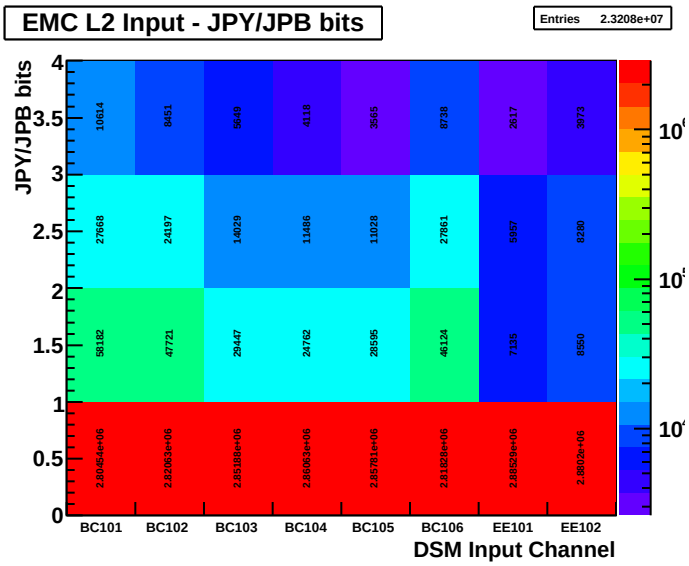

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

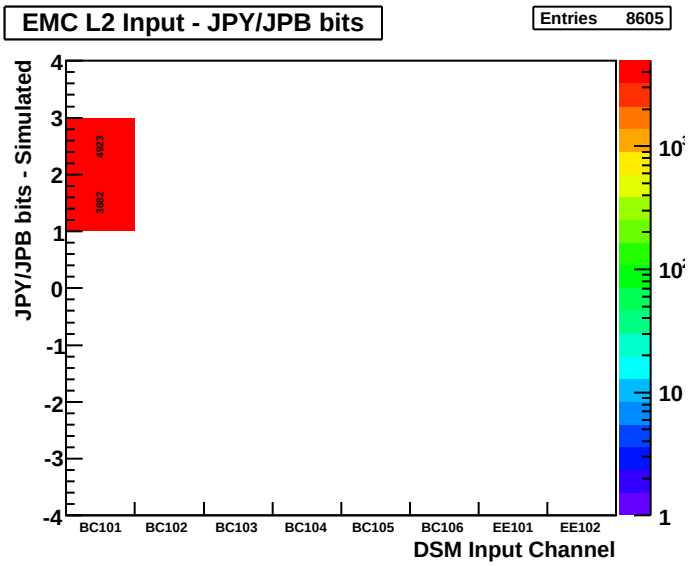

Endcap EMC L1 Input - High Tower Bits
Entries 3.4812e+07
High Tower Bits
EE101
EE102
96 15 6 123 133 10 63 383 242 31 21 123
1880 441 375 2130 2258 459 901 4414 3240 542 445 2354
2.89902e+06 2.90054e+06 2.90062e+06 2.89875e+06 2.89861e+06 2.90053e+06 2.90004e+06 2.8962e+06 2.89752e+06 2.90043e+06 2.90053e+06 2.89852e+06
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

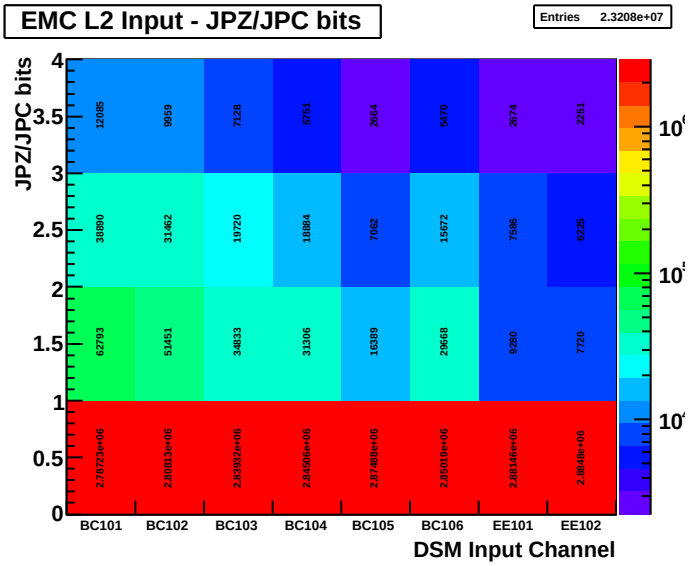

Endcap EMC L1 Input - High Tower Bits
Entries 80
High Tower Bits - Simulated
EE101
EE102
80
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

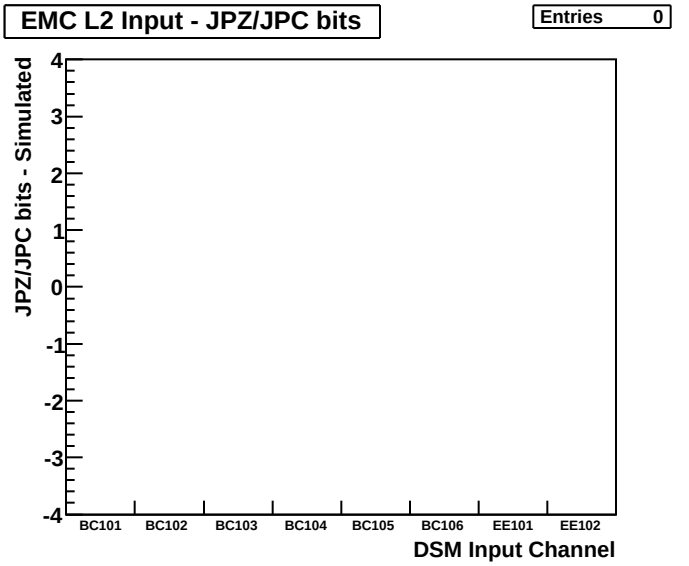

EMC L2 Input - JPX/JPA bits
Entries 2.3208e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
16948 10703 8889 4651 8589 7221 2122 6189
36835 25745 21055 11495 28461 16832 6328 11185
52973 41758 32006 21727 42664 34717 7975 12342
2.79424e+06 2.82279e+06 2.83905e+06 2.86313e+06 2.82119e+06 2.84223e+06 2.88458e+06 2.87128e+06

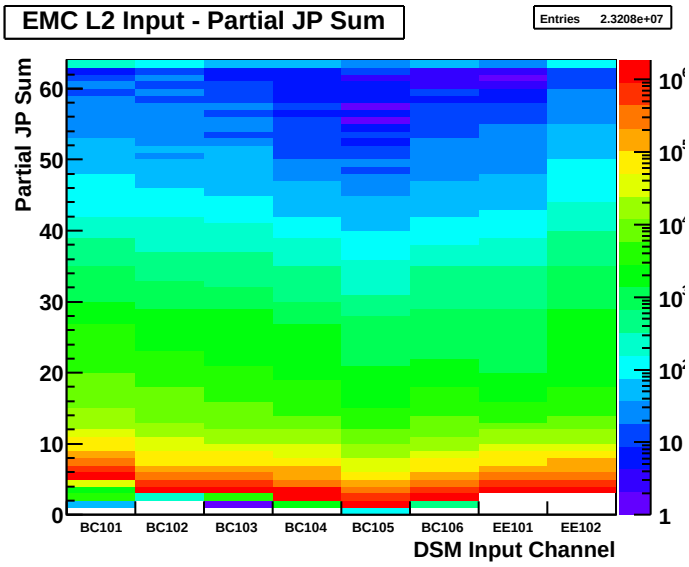

EMC L2 Input - JPX/JPA bits
Entries 8752
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5896
2856

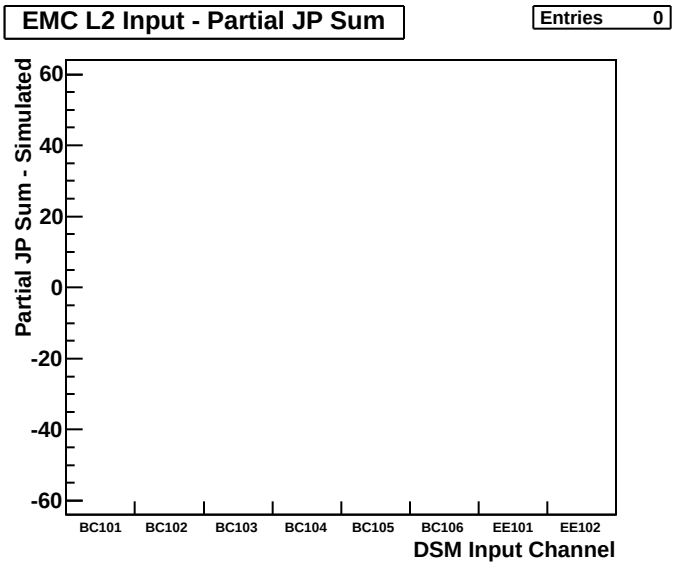

EMC L2 Input - JPY/JPB bits
Entries 2.3208e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
10614 8451 5649 4118 3565 8738 2617 3973
27668 24197 14029 11486 11028 27861 5957 8280
58182 47721 29447 24762 28595 46124 7135 8550
2.80454e+06 2.82063e+06 2.85188e+06 2.86063e+06 2.85781e+06 2.81828e+06 2.88529e+06 2.8802e+06

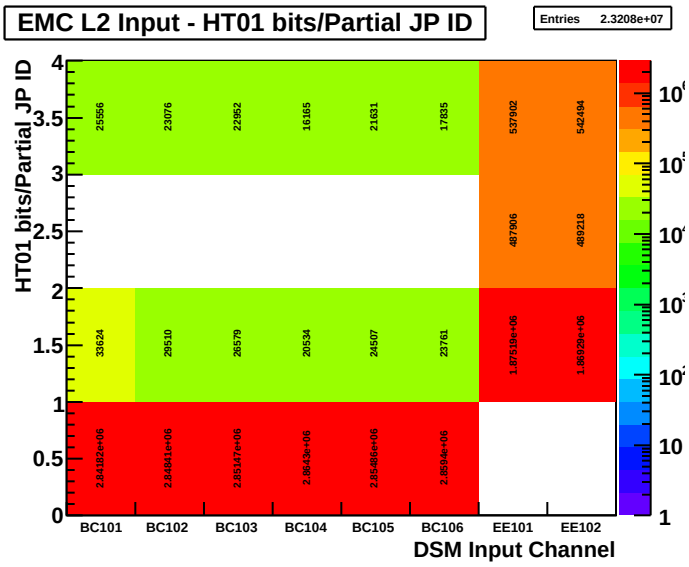

EMC L2 Input - JPY/JPB bits
Entries 8605
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4923
3682

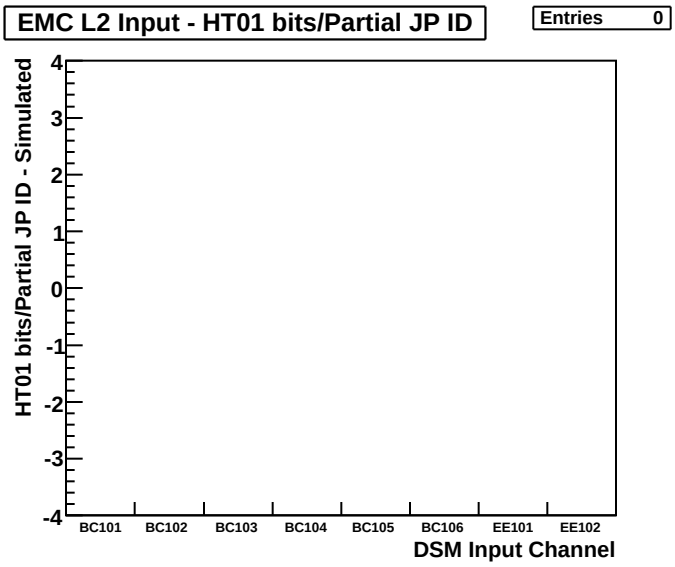

EMC L2 Input - JPZ/JPC bits
Entries 2.3208e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
12085 9959 7128 5751 2664 5470 2674 2251
38890 31462 19720 18884 7062 15672 7588 6225
62793 51451 34833 31306 16389 29668 9280 7720
2.78723e+06 2.80813e+06 2.83932e+06 2.84506e+06 2.87489e+06 2.85019e+06 2.88146e+06 2.8848e+06

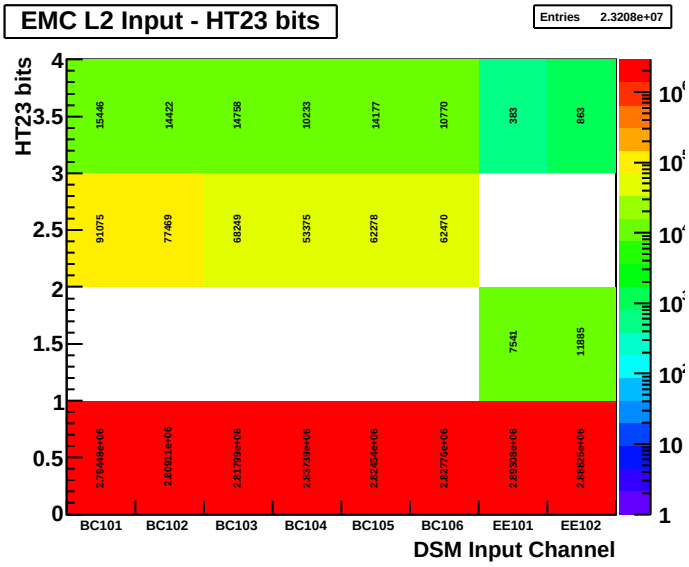

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

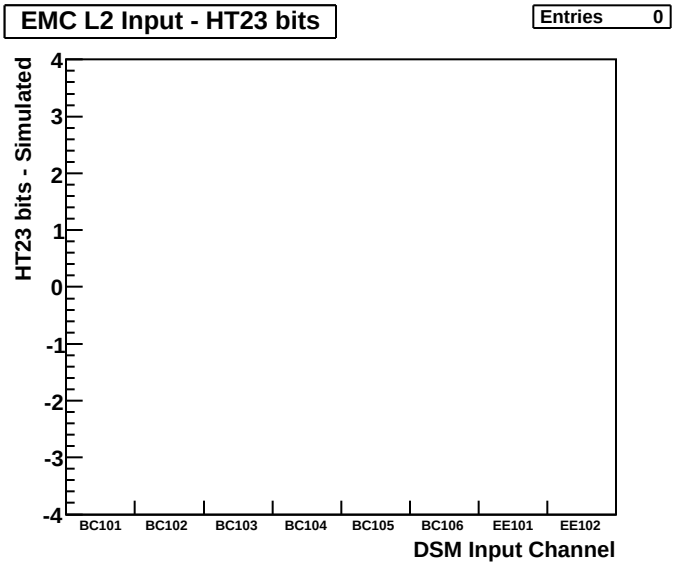

EMC L2 Input - Partial JP Sum
Entries 2.3208e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

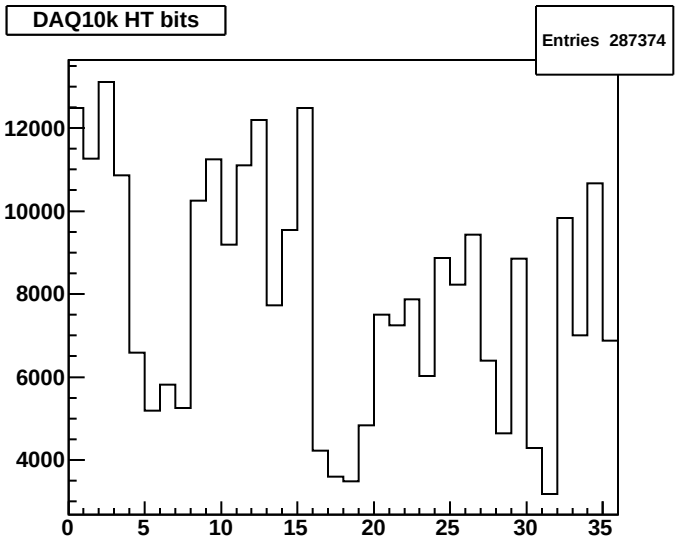

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

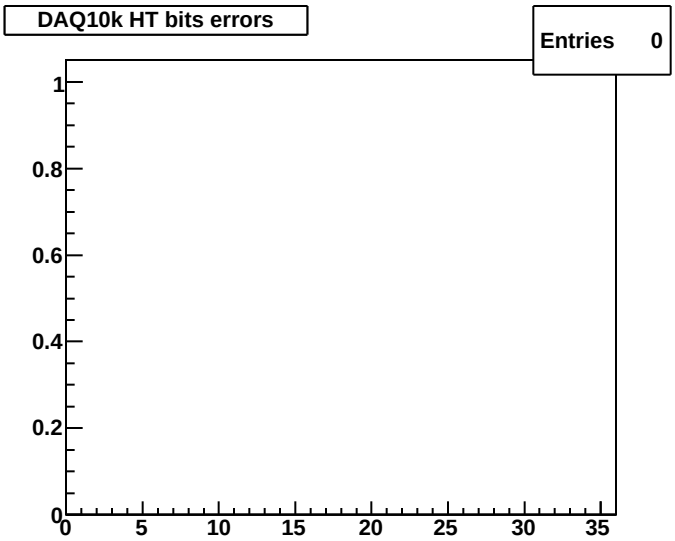

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2.3208e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

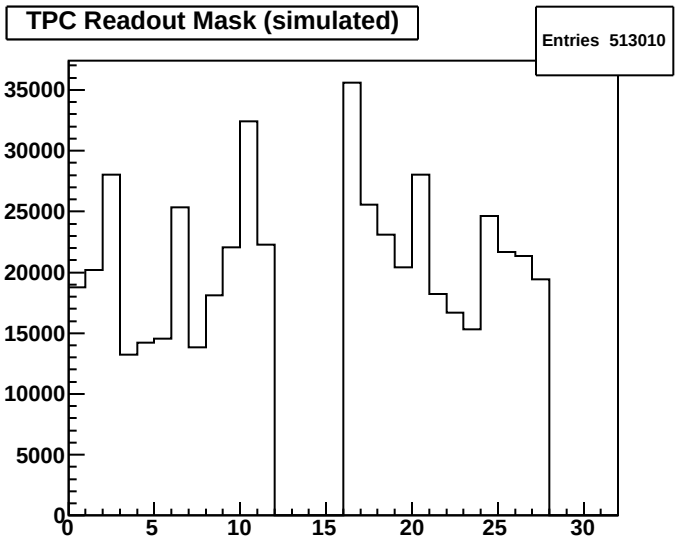

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

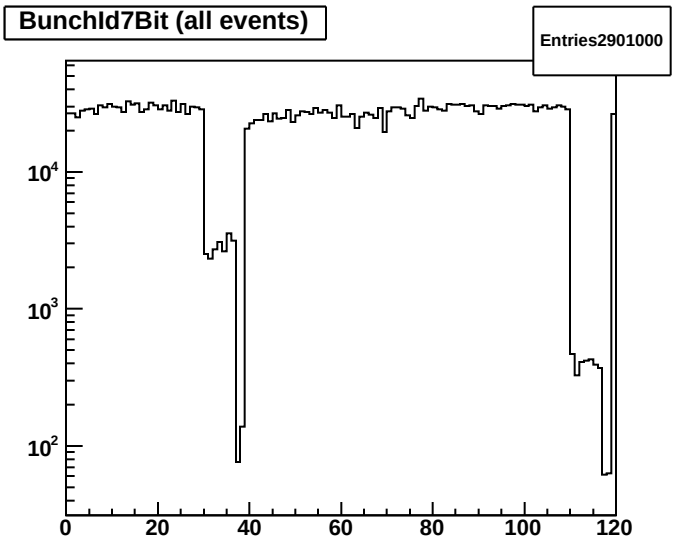

EMC L2 Input - HT23 bits
Entries 2.3208e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

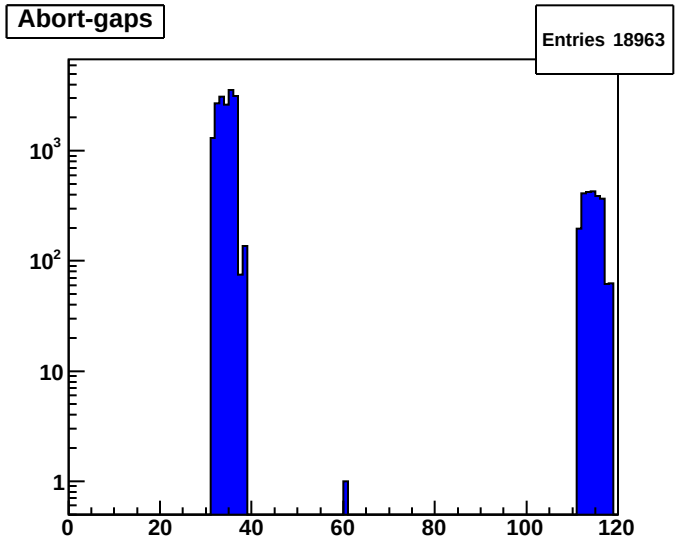

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

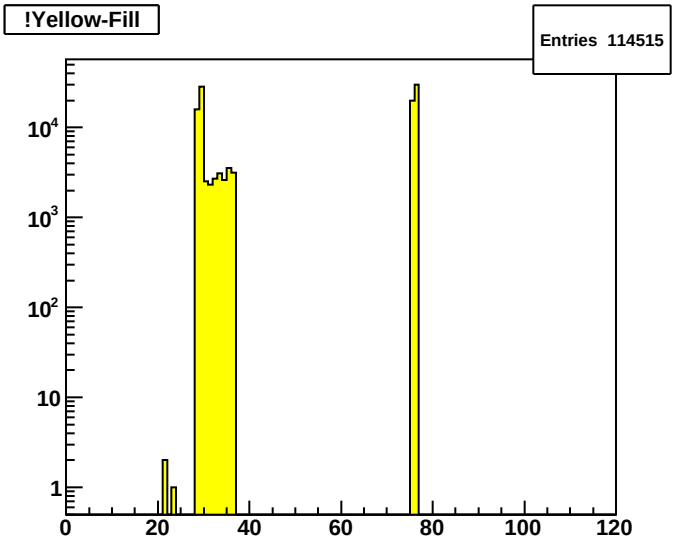

DAQ10k HT bits
Entries 287374

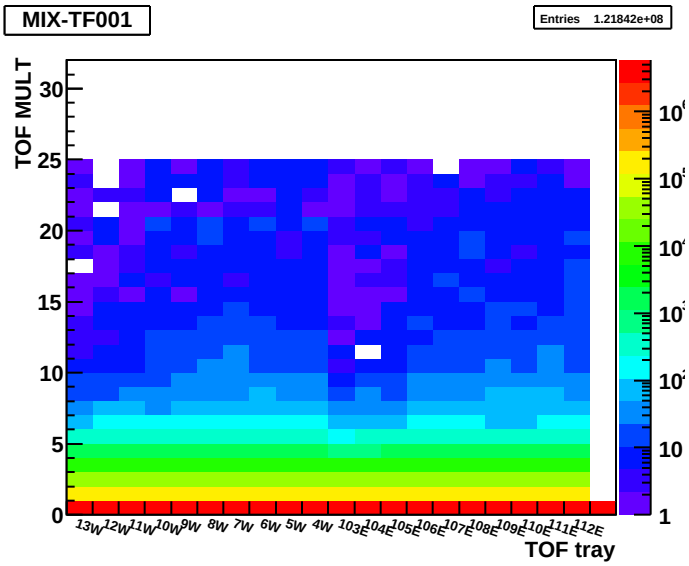

DAQ10k HT bits errors
Entries 0

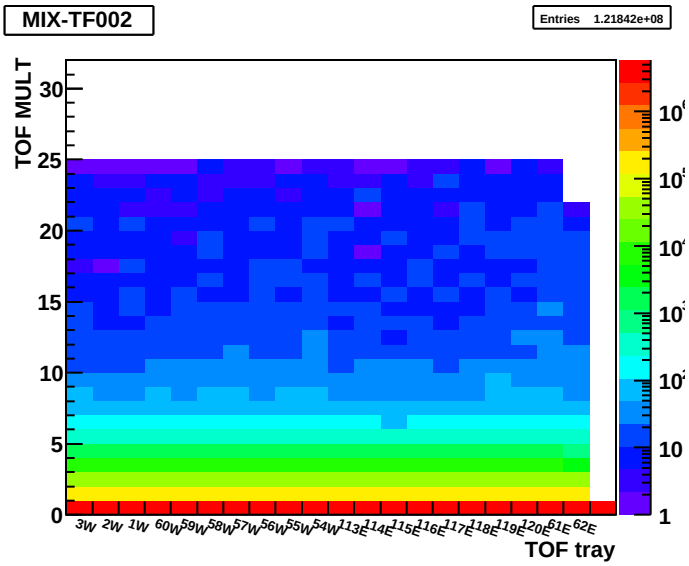

TPC Readout Mask (simulated)
Entries 513010

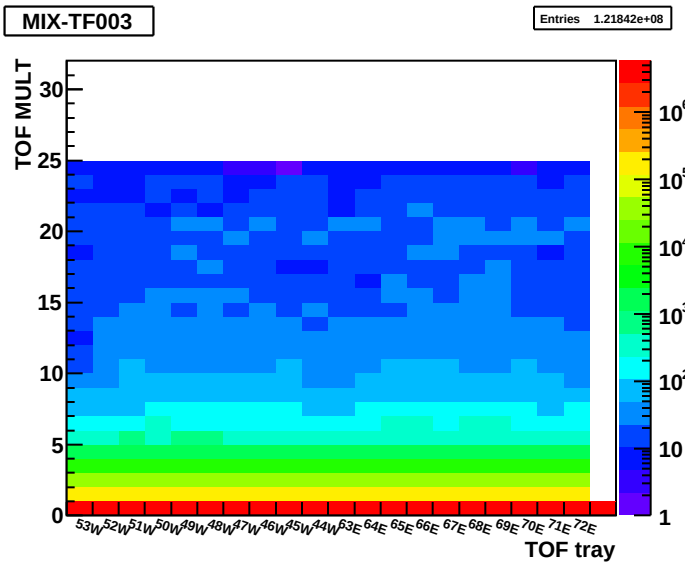

BunchId7Bit (all events)
Entries2901000

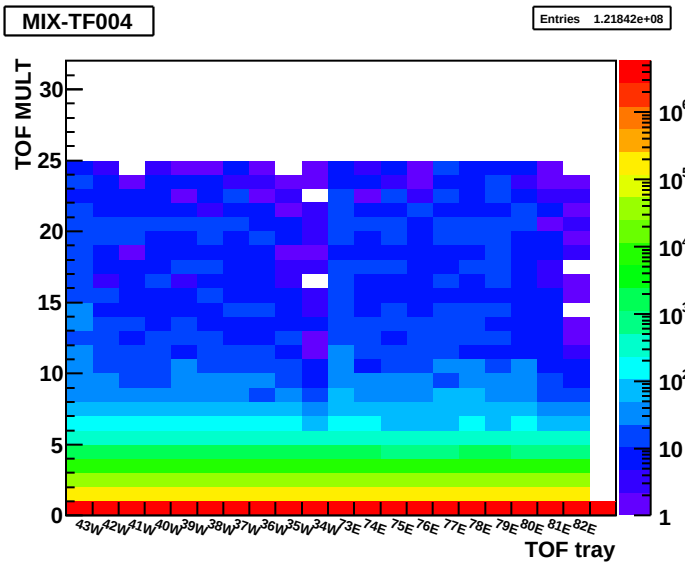

Abort-gaps
Entries 18963

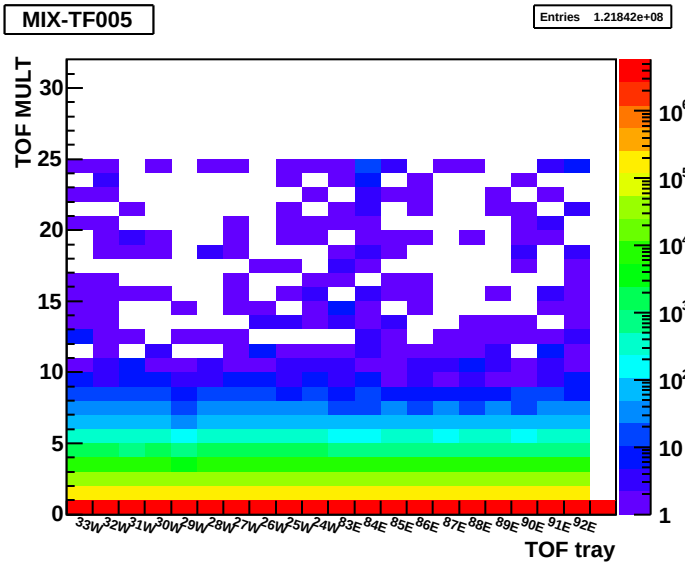

!Yellow-Fill
Entries 114515

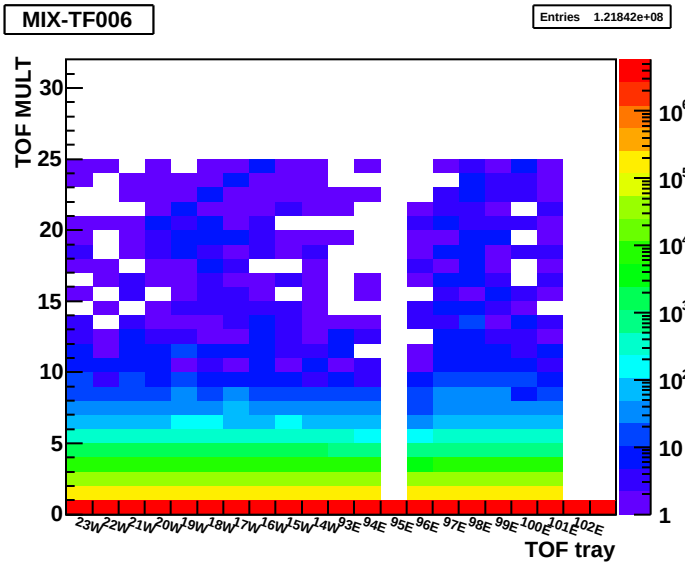

MIX-TF001
Entries 1.21842e+08
TOF MULT
TOF tray

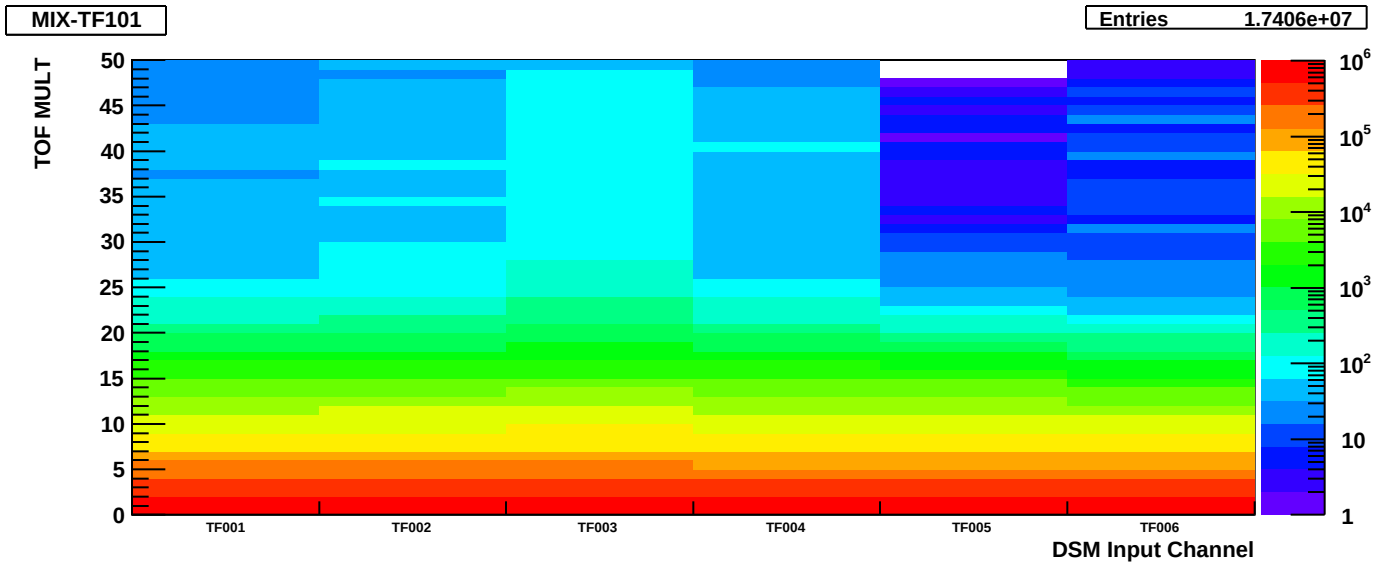

MIX-TF002
Entries 1.21842e+08
TOF MULT
TOF tray

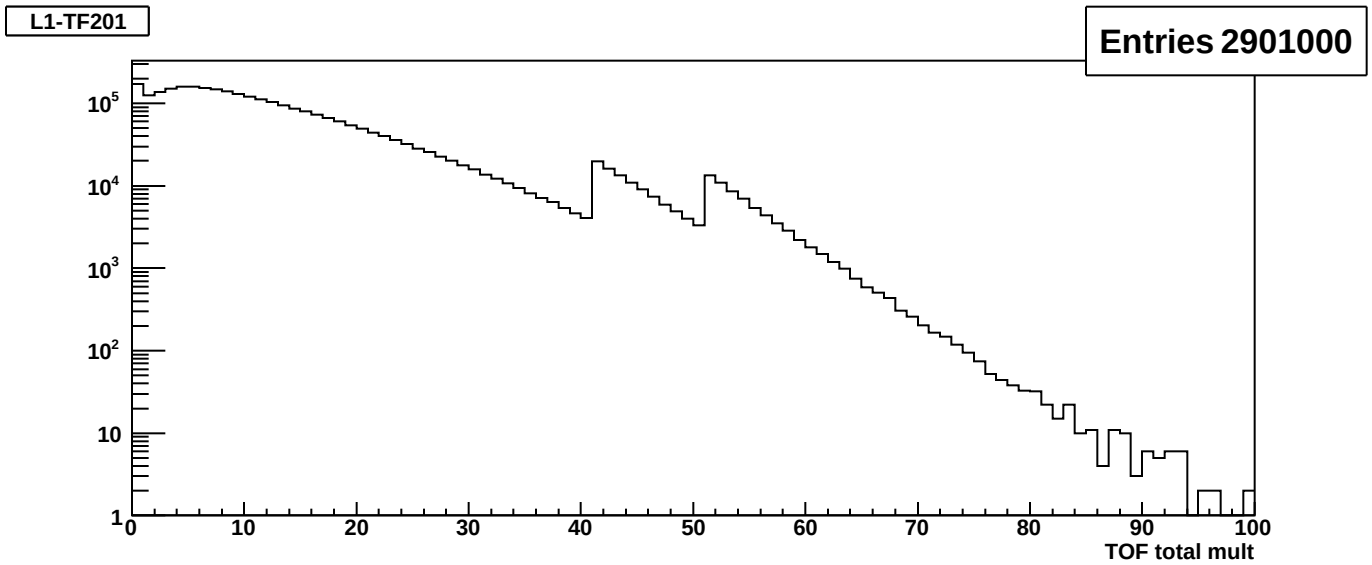

MIX-TF003
Entries 1.21842e+08
TOF MULT
TOF tray

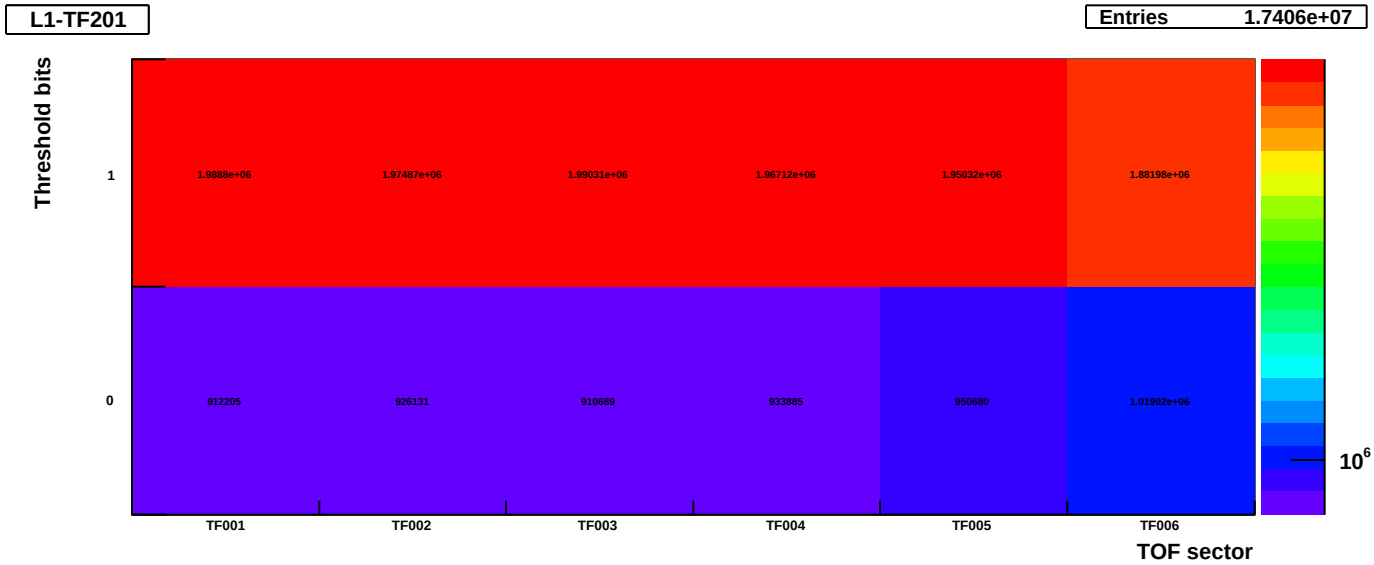

MIX-TF004
Entries 1.21842e+08
TOF MULT
TOF tray

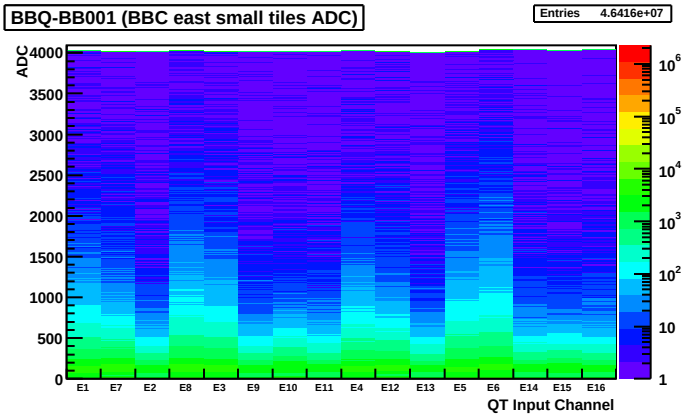

MIX-TF005
Entries 1.21842e+08
TOF MULT
TOF tray

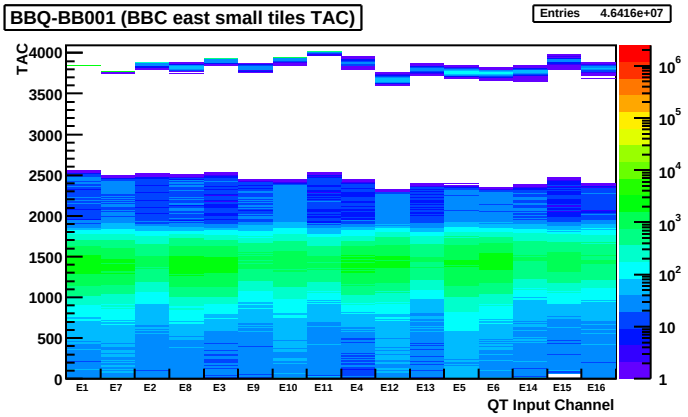

MIX-TF006
Entries 1.21842e+08
TOF MULT
TOF tray

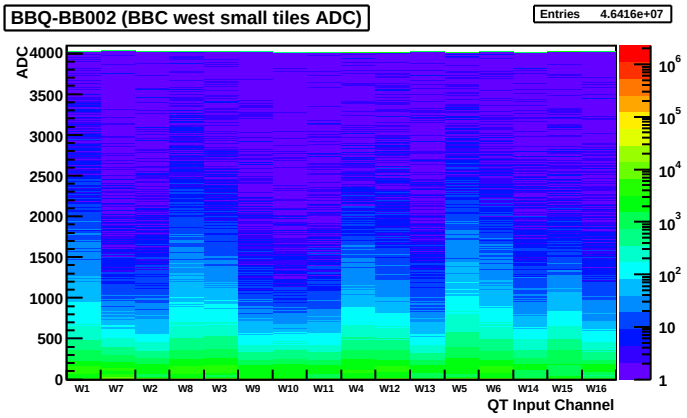

MIX-TF101
Entries 1.7406e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

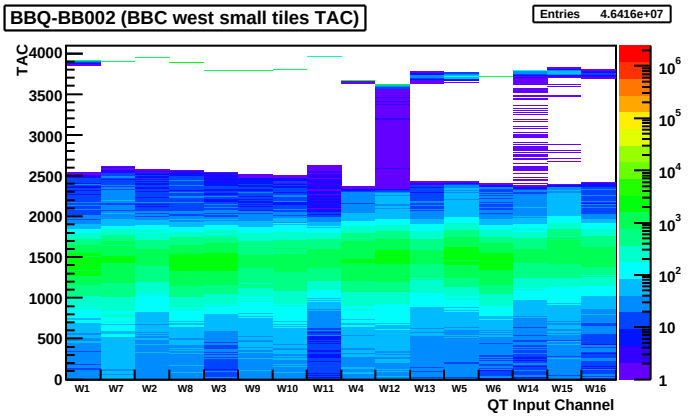

L1-TF201
Entries 2901000
TOF total mult

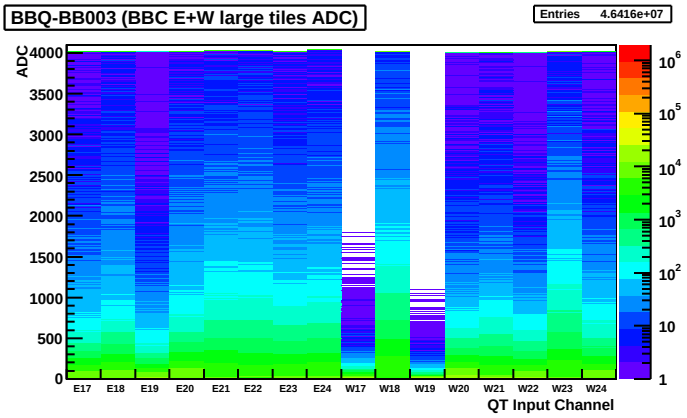

L1-TF201
Entries 1.7406e+07
Threshold bits
1.9888e+06
1.97487e+06
1.99031e+06
1.96712e+06
1.95032e+06
1.88198e+06
912205
926131
910689
933885
950680
1.01902e+06
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

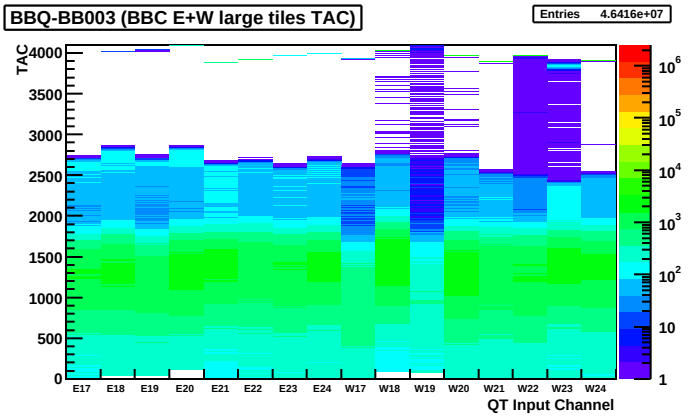

BBQ-BB001 (BBC east small tiles ADC)
Entries 4.6416e+07
ADC
QT Input Channel

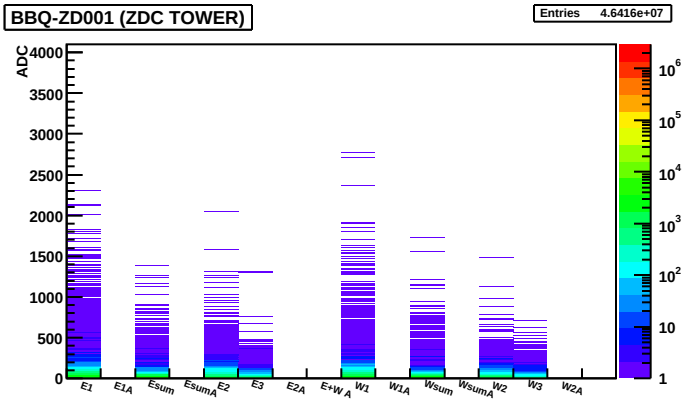

BBQ-BB001 (BBC east small tiles TAC)
Entries 4.6416e+07
TAC
QT Input Channel

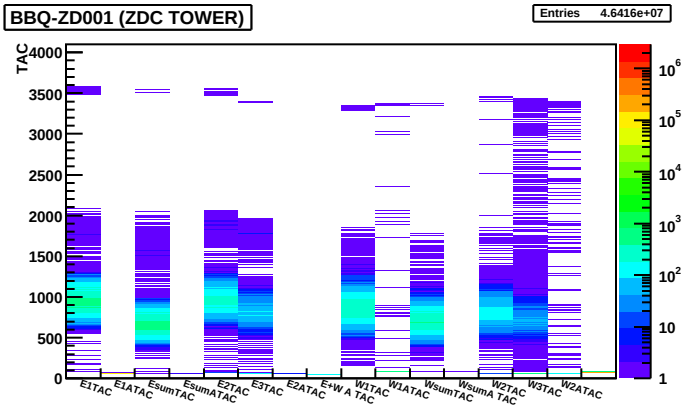

BBQ-BB002 (BBC west small tiles ADC)
Entries 4.6416e+07
ADC
QT Input Channel

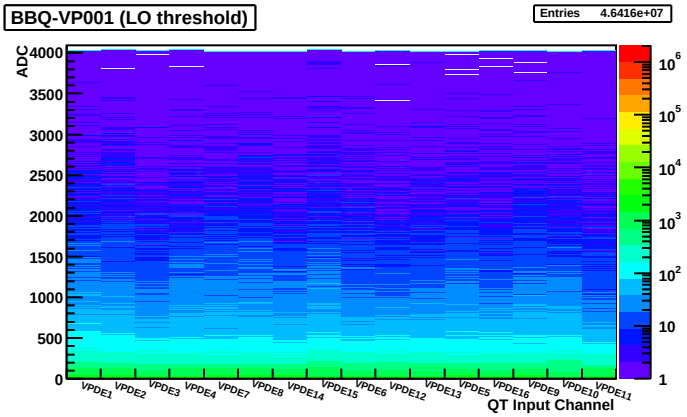

BBQ-BB002 (BBC west small tiles TAC)
Entries 4.6416e+07
TAC
QT Input Channel

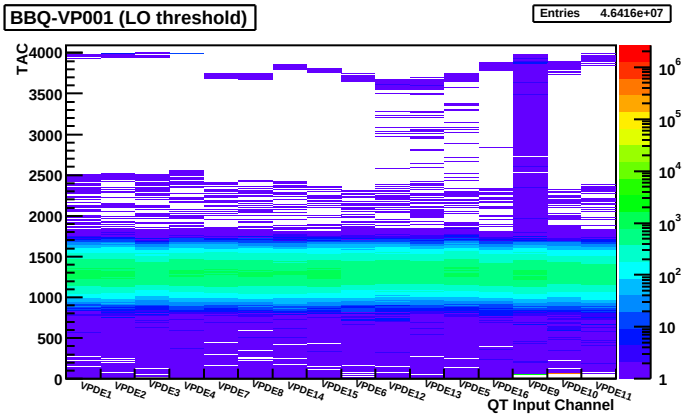

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4.6416e+07
ADC
QT Input Channel

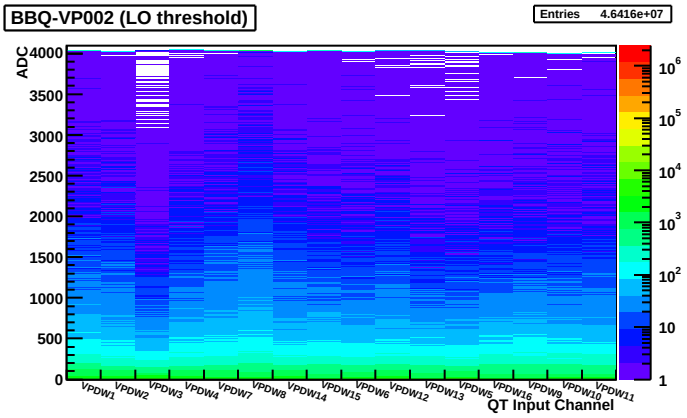

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4.6416e+07
TAC
QT Input Channel

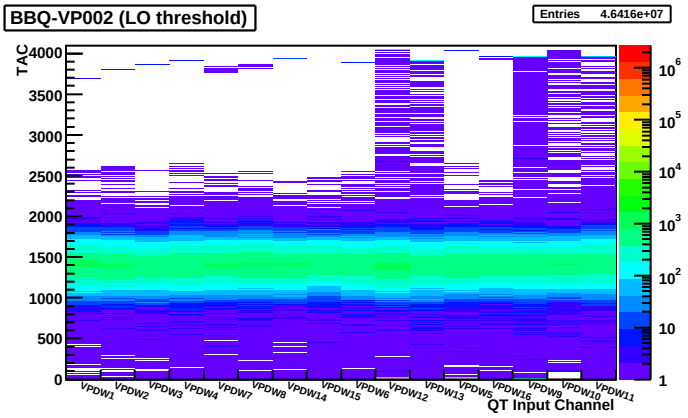

BBQ-ZD001 (ZDC TOWER)
Entries 4.6416e+07
ADC

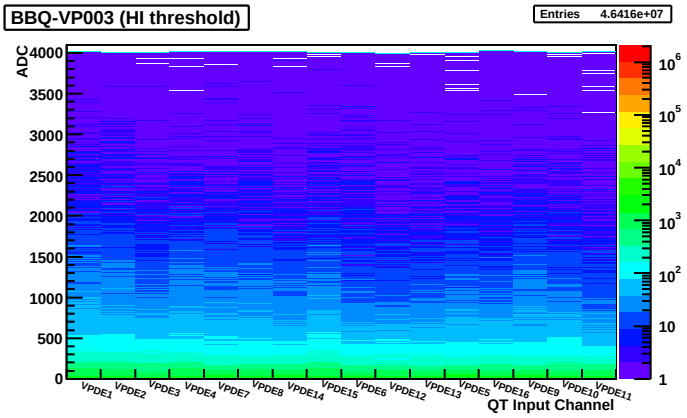

BBQ-ZD001 (ZDC TOWER)
Entries 4.6416e+07
TAC

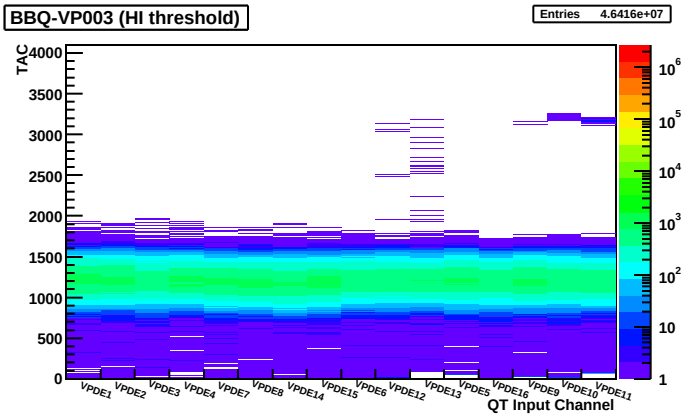

BBQ-VP001 (LO threshold)
Entries 4.6416e+07
ADC
QT Input Channel

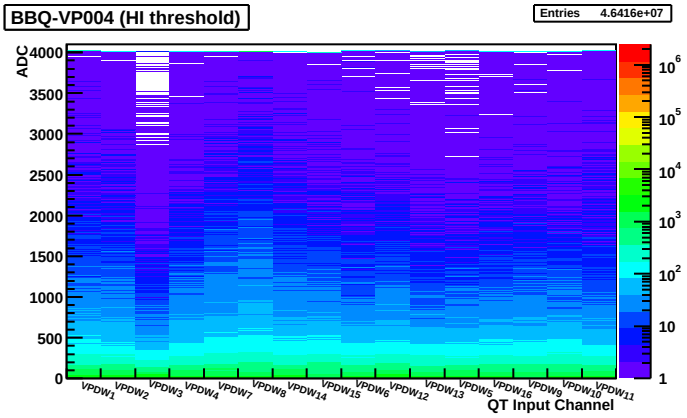

BBQ-VP001 (LO threshold)
Entries 4.6416e+07
TAC
QT Input Channel

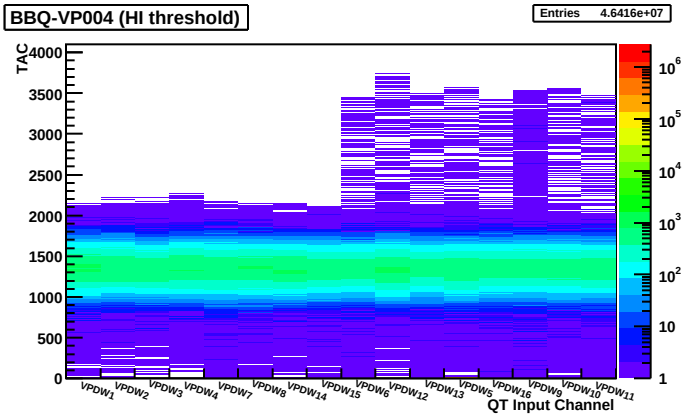

BBQ-VP002 (LO threshold)
Entries 4.6416e+07
ADC
QT Input Channel

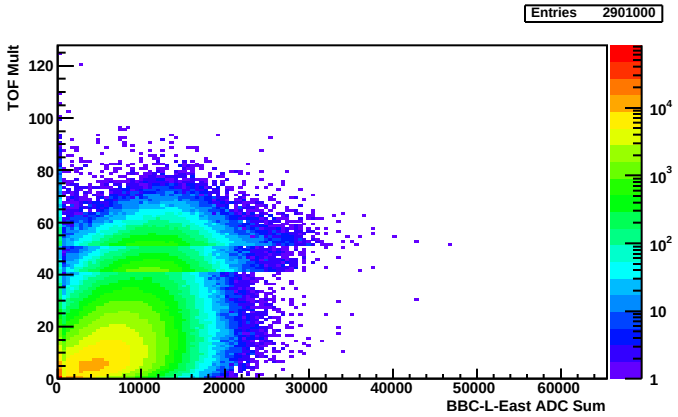

BBQ-VP002 (LO threshold)
Entries 4.6416e+07
TAC
QT Input Channel

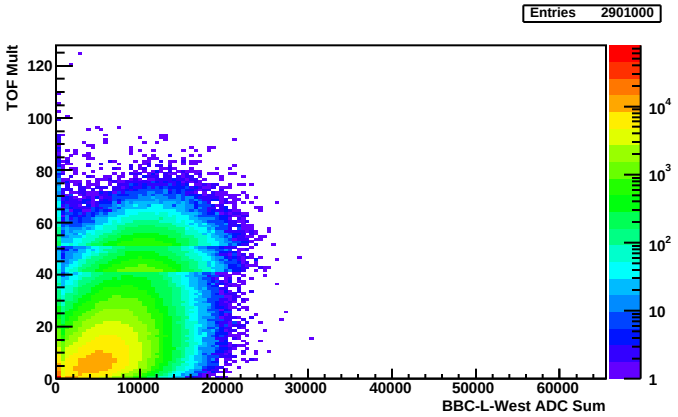

BBQ-VP003 (HI threshold)
Entries 4.6416e+07
ADC
QT Input Channel

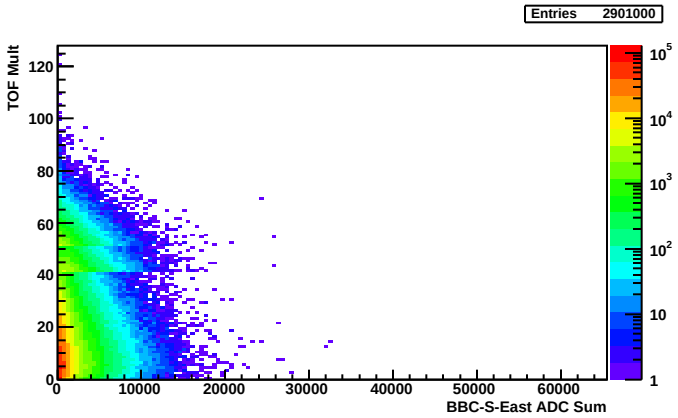

BBQ-VP003 (HI threshold)
Entries 4.6416e+07
TAC
QT Input Channel

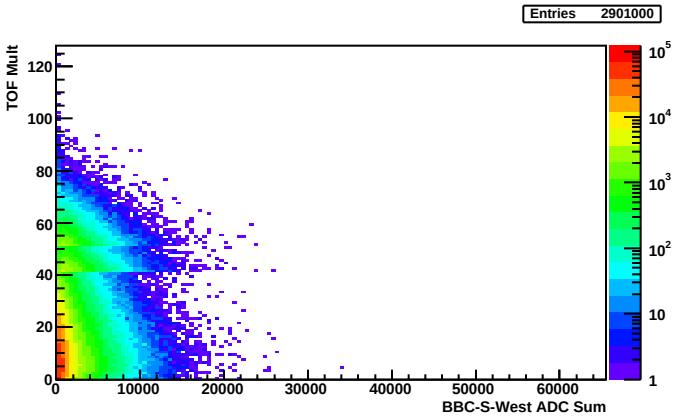

BBQ-VP004 (HI threshold)
Entries 4.6416e+07
ADC
QT Input Channel

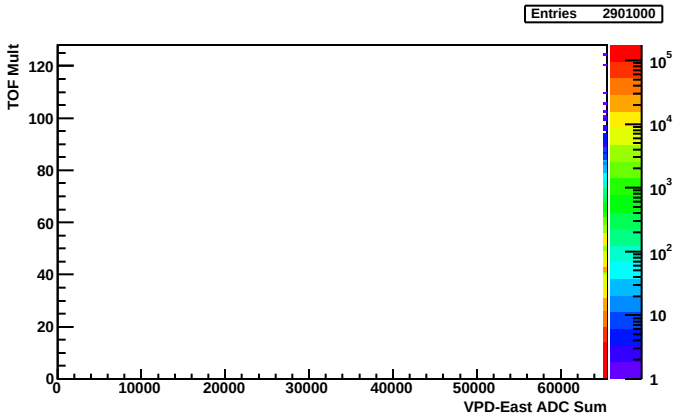

BBQ-VP004 (HI threshold)
Entries 4.6416e+07
TAC
QT Input Channel

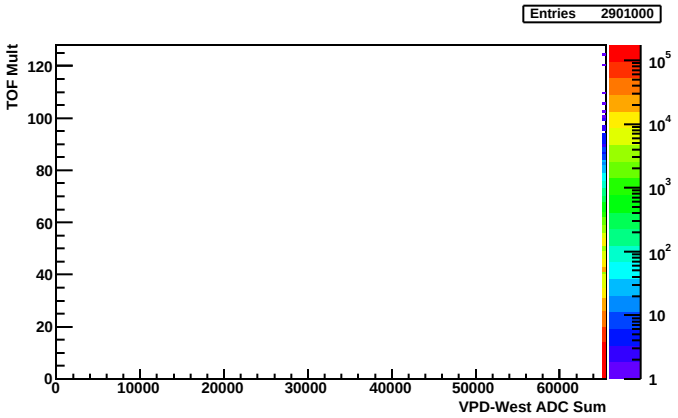

Entries 2901000
TOF Mult
BBC-L-East ADC Sum

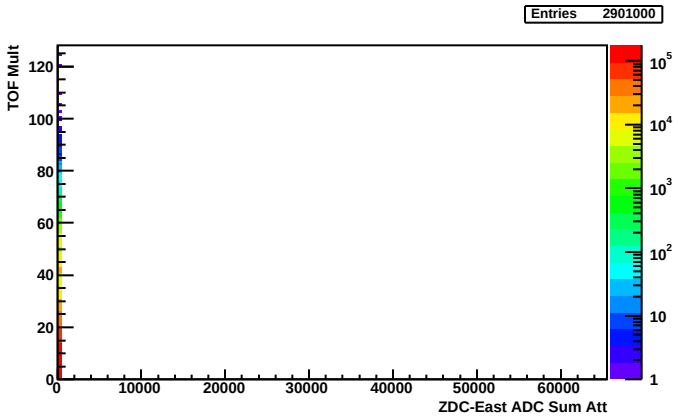

Entries 2901000
TOF Mult
BBC-L-West ADC Sum

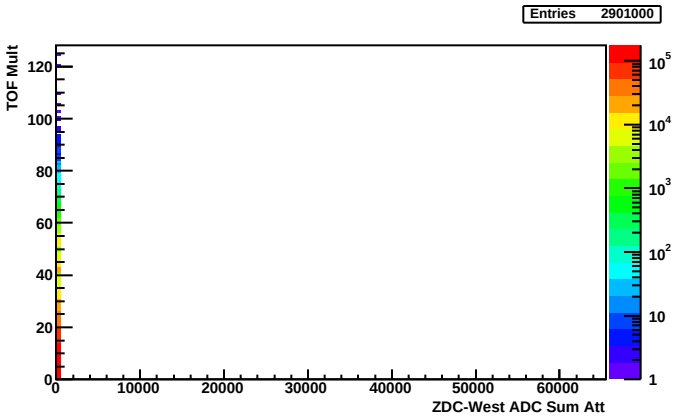

Entries 2901000
TOF Mult
BBC-S-East ADC Sum

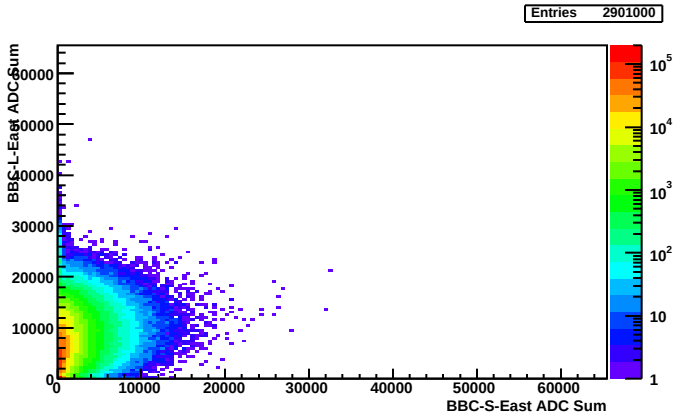

Entries 2901000
TOF Mult
BBC-S-West ADC Sum

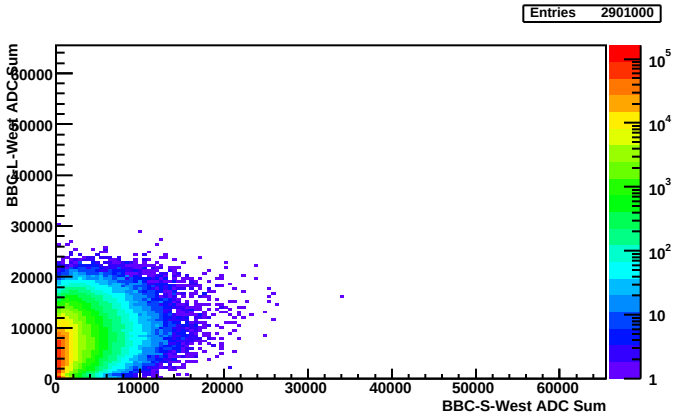

Entries 2901000
TOF Mult
VPD-East ADC Sum

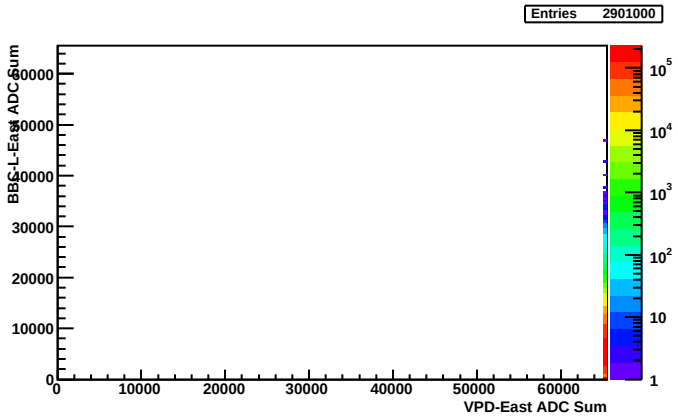

Entries 2901000
TOF Mult
VPD-West ADC Sum

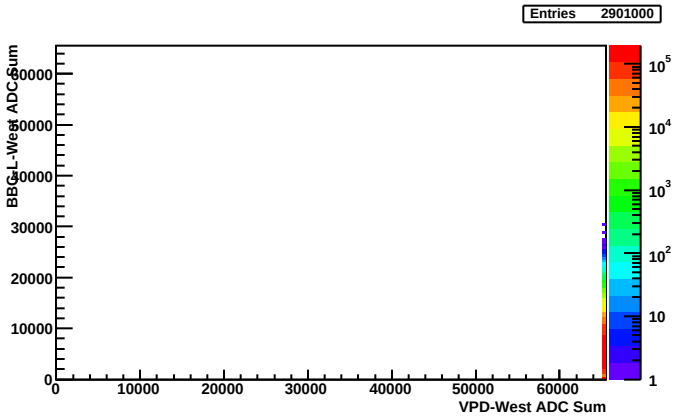

Entries 2901000
TOF Mult
ZDC-East ADC Sum Att

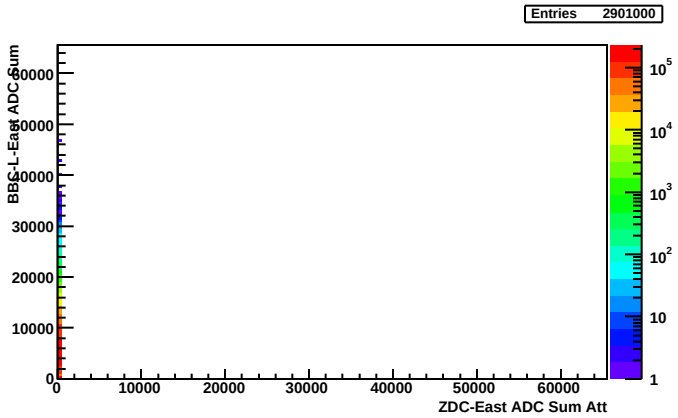

Entries 2901000
TOF Mult
ZDC-West ADC Sum Att

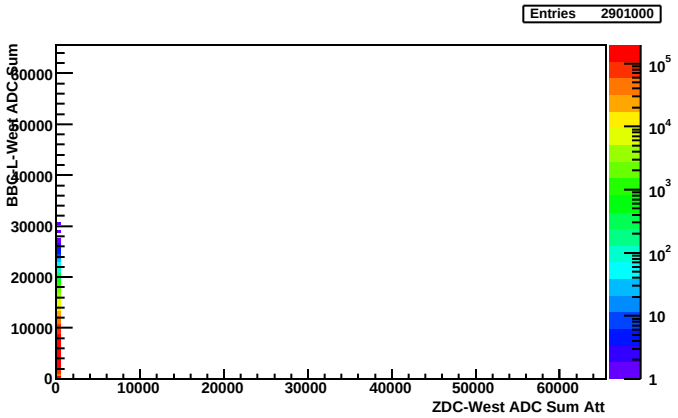

Entries 2901000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

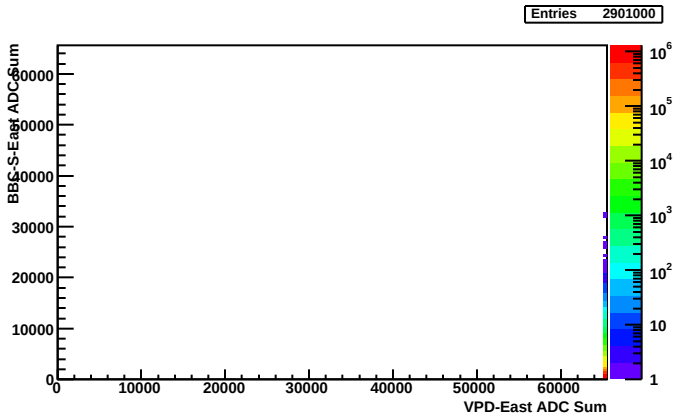

Entries 2901000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

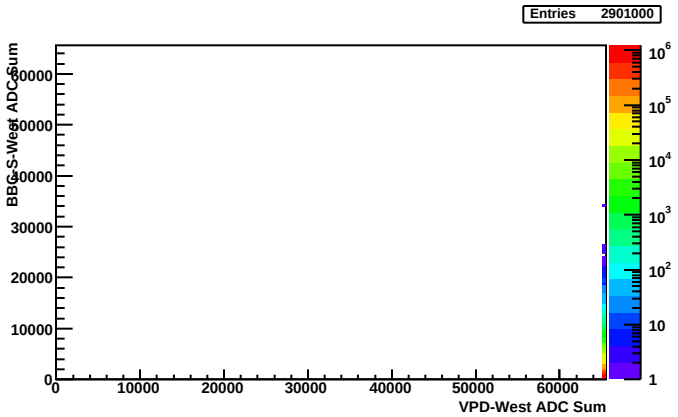

Entries 2901000
BBC-L-East ADC Sum
VPD-East ADC Sum

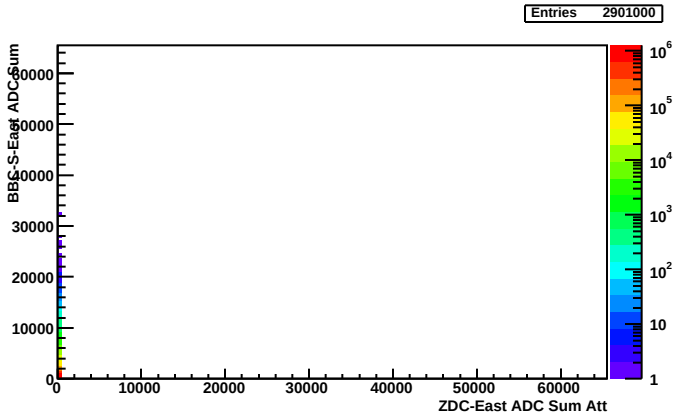

Entries 2901000
BBC-L-West ADC Sum
VPD-West ADC Sum

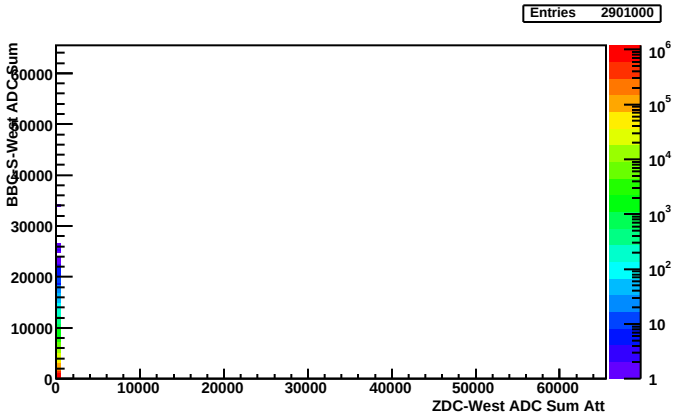

Entries 2901000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

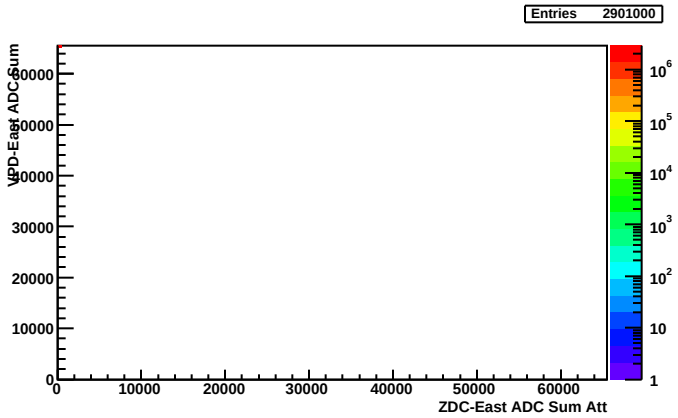

Entries 2901000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

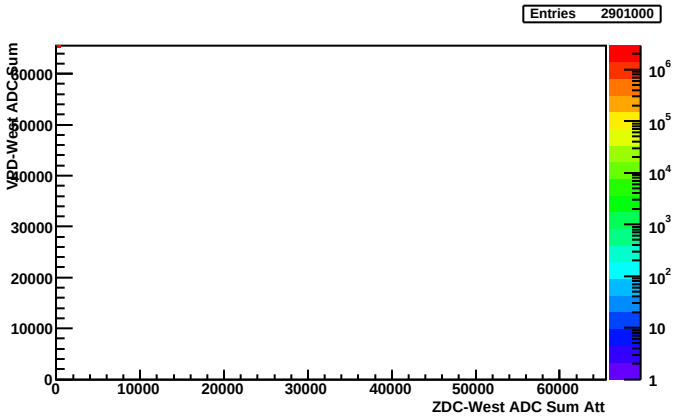

Entries 2901000
BBC-S-East ADC Sum
VPD-East ADC Sum

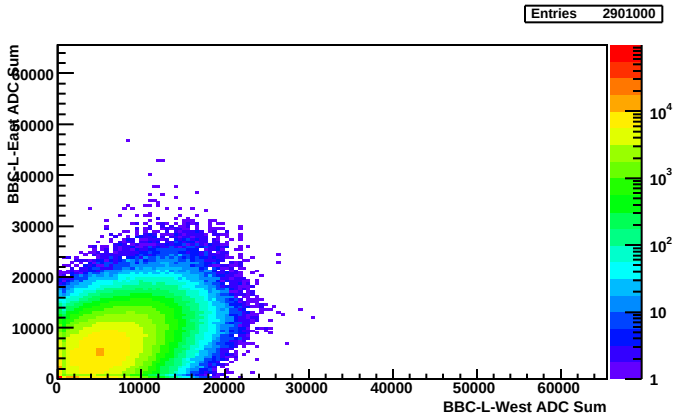

Entries 2901000
BBC-S-West ADC Sum
VPD-West ADC Sum

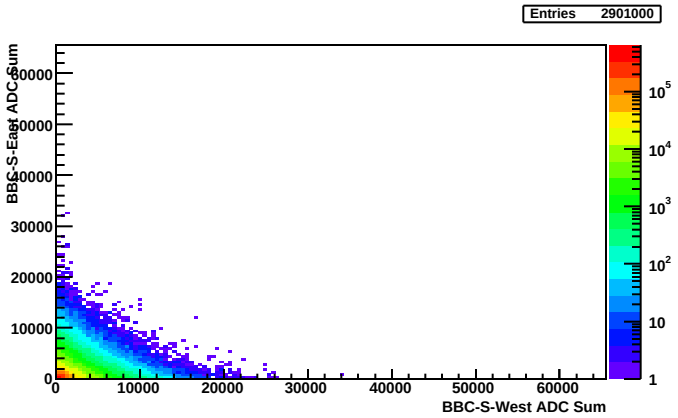

Entries 2901000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

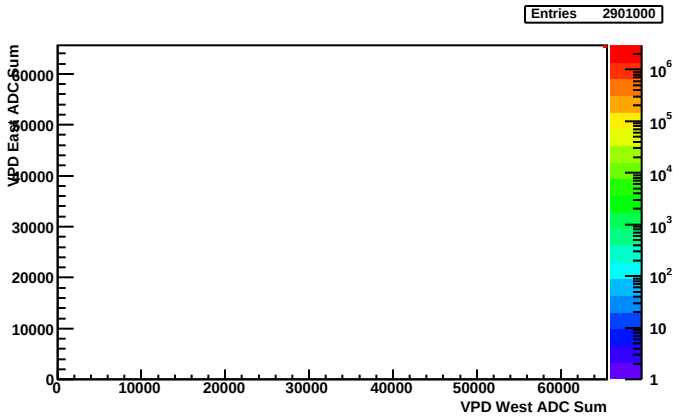

Entries 2901000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

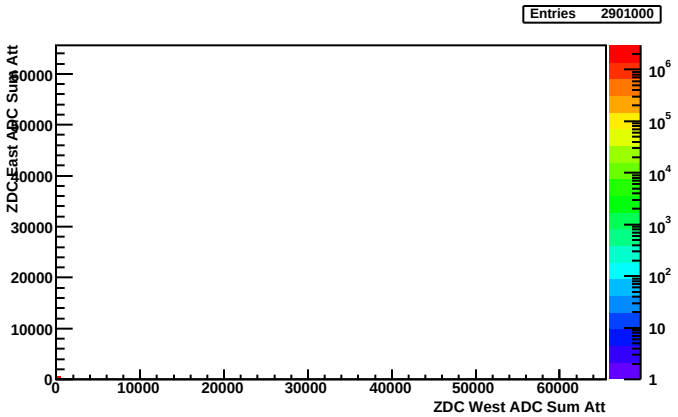

Entries 2901000
VPD-East ADC Sum
ZDC-East ADC Sum Att

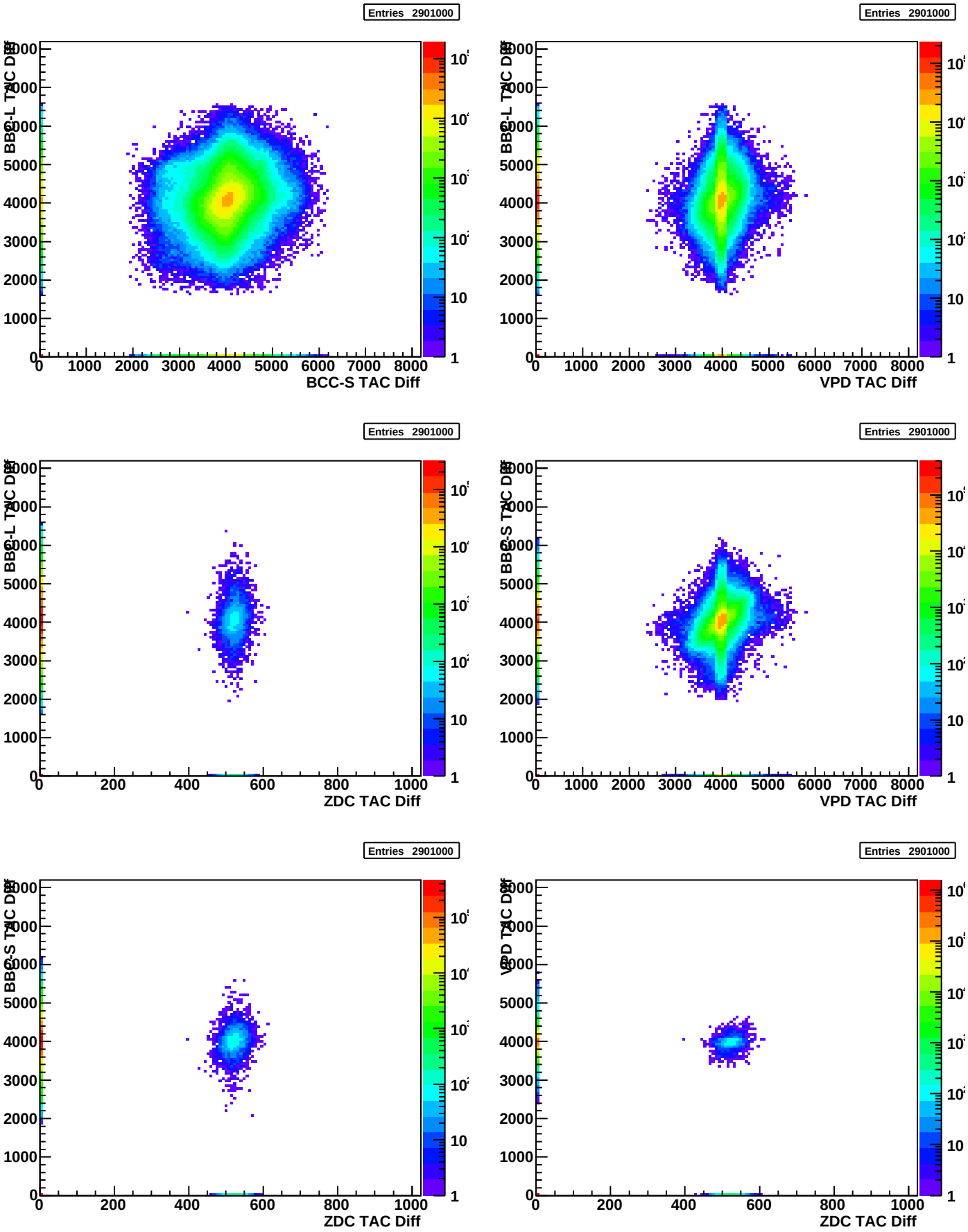

Entries 2901000
VPD-West ADC Sum
ZDC-West ADC Sum Att

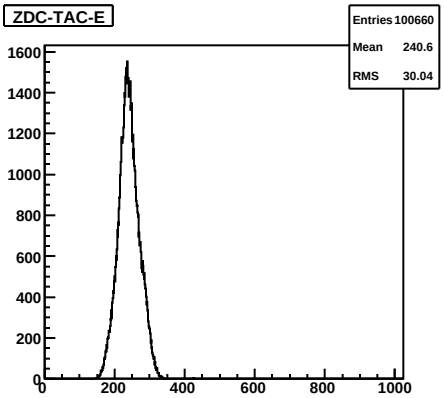

Entries 2901000
BBC-L-East ADC Sum
BBC-L-West ADC Sum

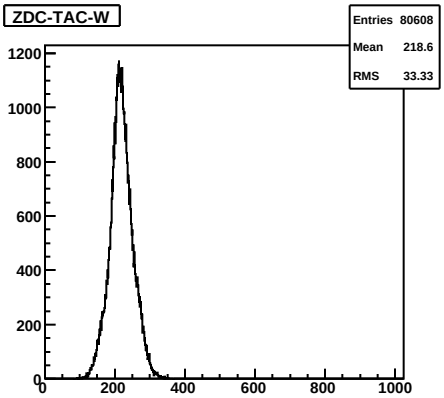

Entries 2901000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

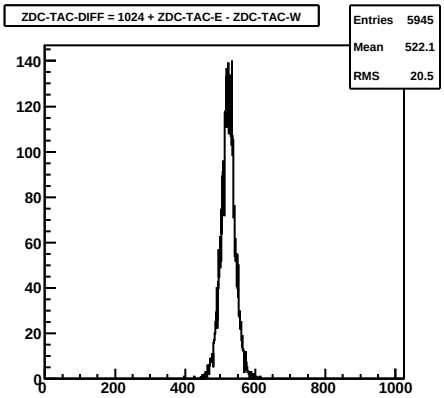

Entries 2901000
VPD East ADC Sum
VPD West ADC Sum

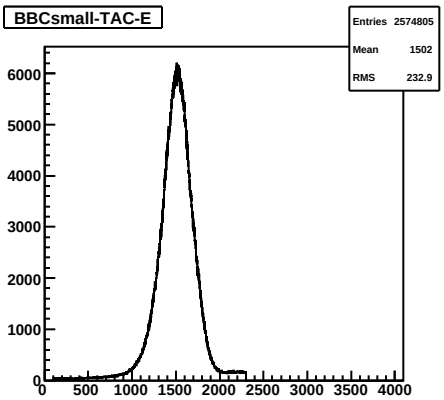

Entries 2901000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

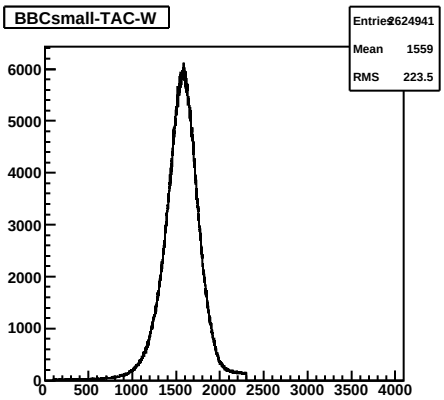

Entries 2901000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2901000
BBC-L TAC Diff
VPD TAC Diff
Entries 2901000
BBC-L TAC Diff
ZDC TAC Diff
Entries 2901000
BBC-S TAC Diff
VPD TAC Diff
Entries 2901000
BBC-S TAC Diff
ZDC TAC Diff
Entries 2901000
VPD TAC Diff
ZDC TAC Diff

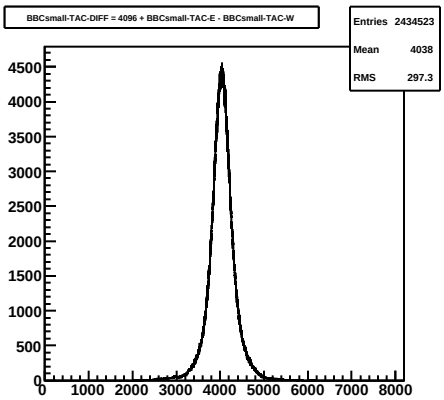

ZDC-TAC-E
Entries 100660
Mean 240.6
RMS 30.04

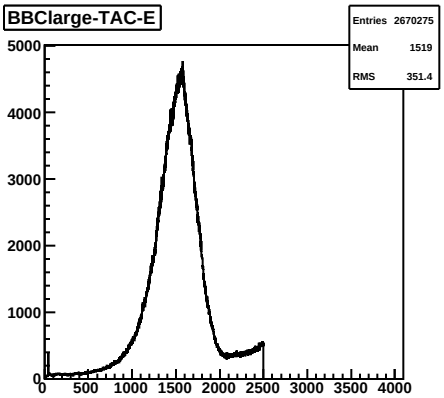

ZDC-TAC-W
Entries 80608
Mean 218.6
RMS 33.33

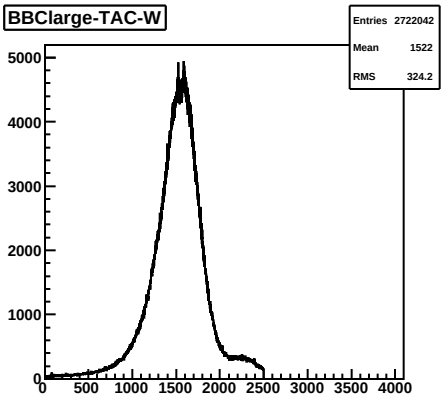

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5945
Mean 522.1
RMS 20.5

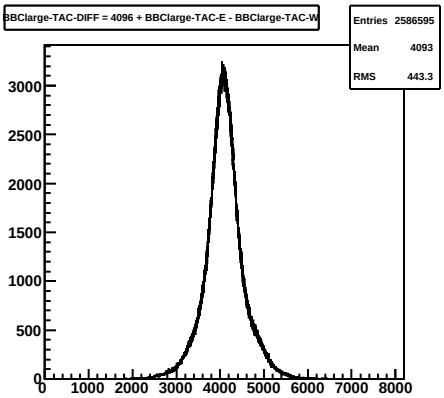

BBCsmall-TAC-E
Entries 2574805
Mean 1502
RMS 232.9

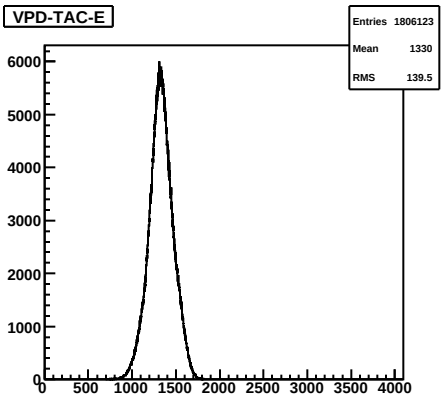

BBCsmall-TAC-W
Entries 2624941
Mean 1559
RMS 223.5

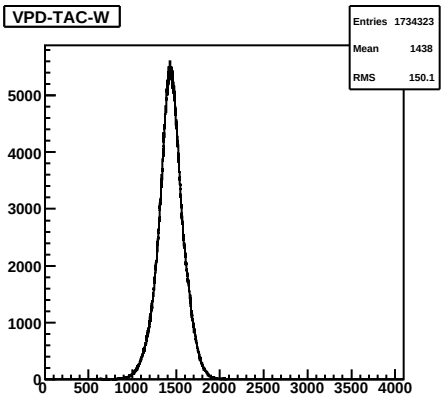

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2434523
Mean 4038
RMS 297.3

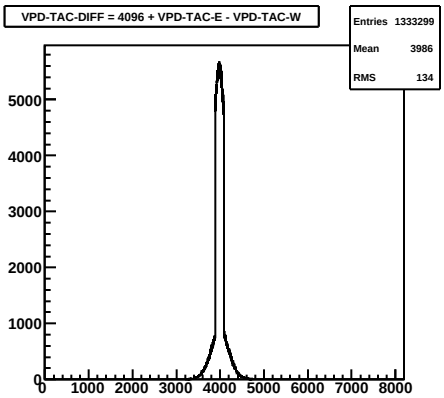

BBClarge-TAC-E
Entries 2670275
Mean 1519
RMS 351.4

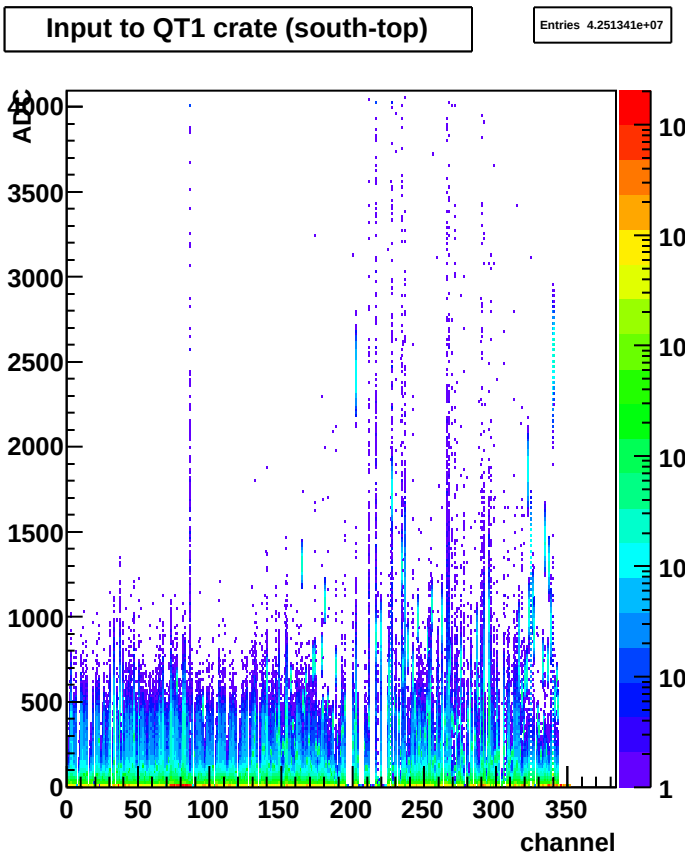

BBClarge-TAC-W
Entries 2722042
Mean 1522
RMS 324.2

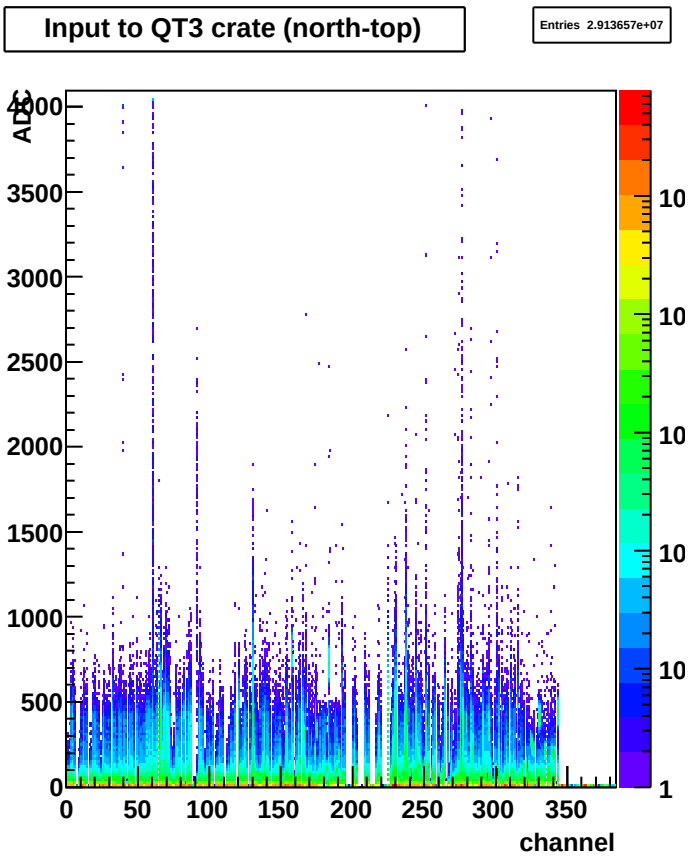

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2586595
Mean 4093
RMS 443.3

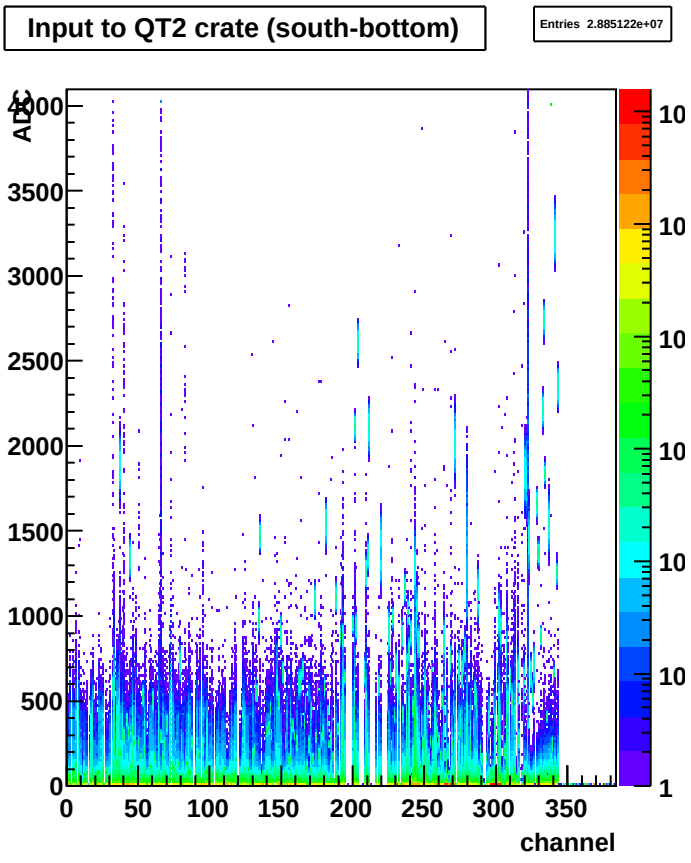

VPD-TAC-E
Entries 1806123
Mean 1330
RMS 139.5

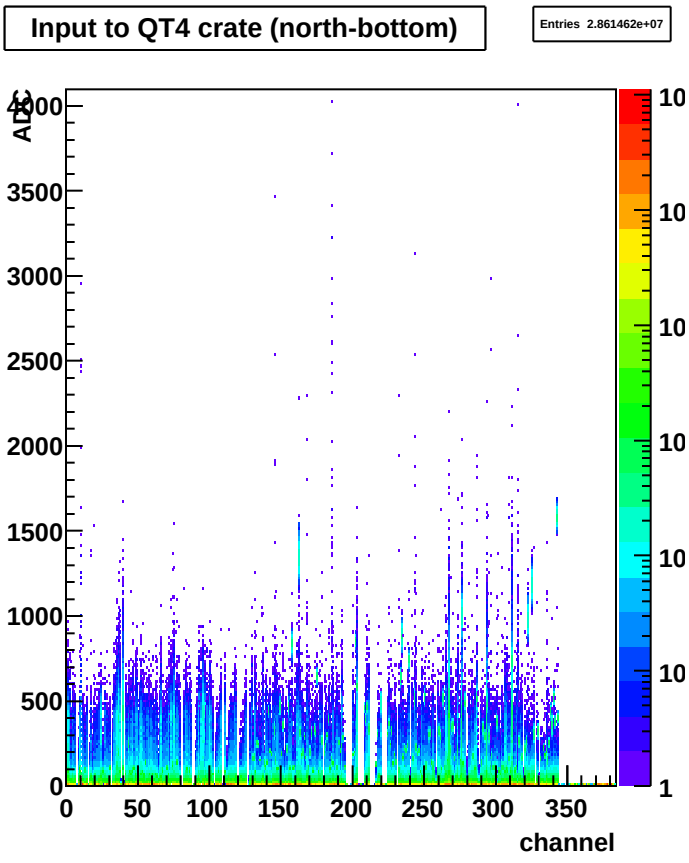

VPD-TAC-W
Entries 1734323
Mean 1438
RMS 150.1

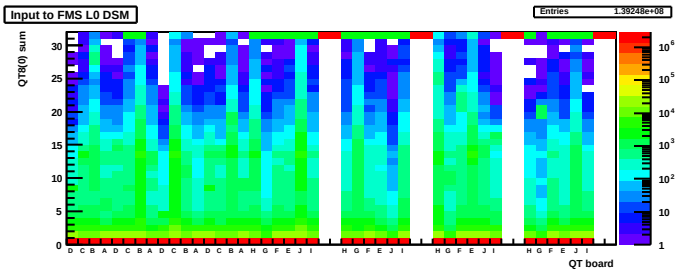

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1333299
Mean 3986
RMS 134

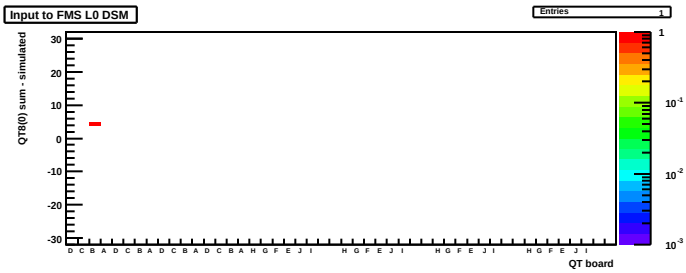

Input to QT1 crate (south-top)
Entries 4.251341e+07
ADC
channel

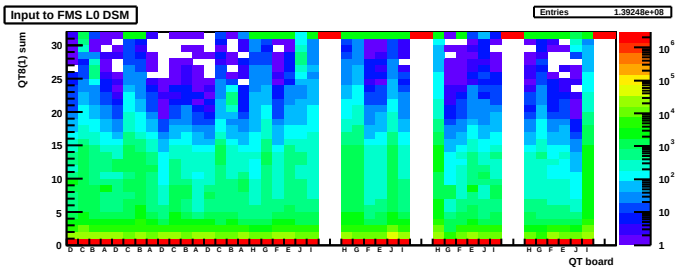

Input to QT3 crate (north-top)
Entries 2.913657e+07
ADC
channel

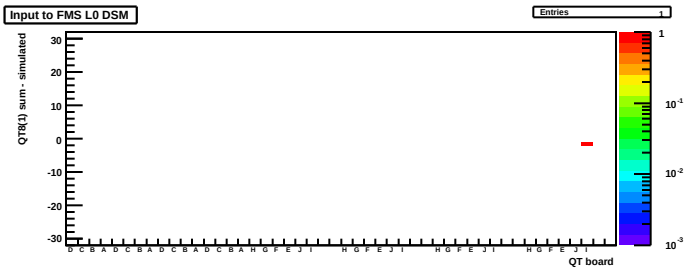

Input to QT2 crate (south-bottom)
Entries 2.885122e+07
ADC
channel

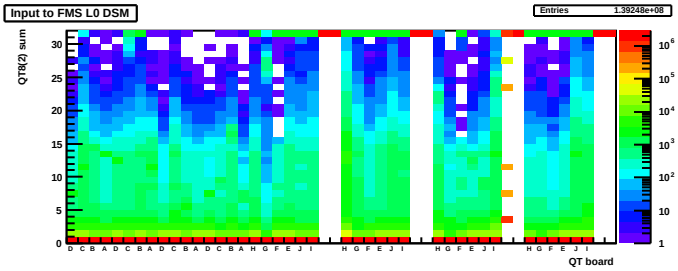

Input to QT4 crate (north-bottom)
Entries 2.861462e+07
ADC
channel

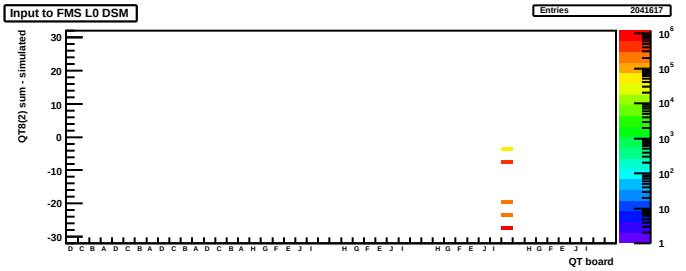

Input to FMS L0 DSM
Entries 1.39248e+08
QT8(0) sum
QT board

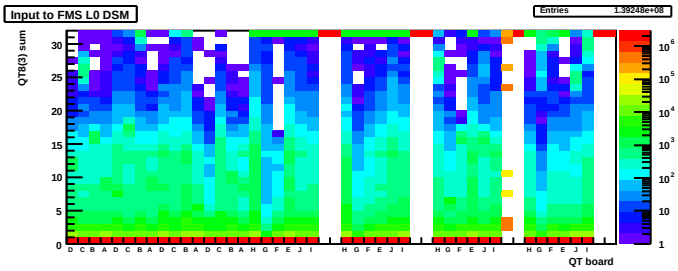

Input to FMS L0 DSM
Entries 1
QT8(0) sum - simulated
QT board

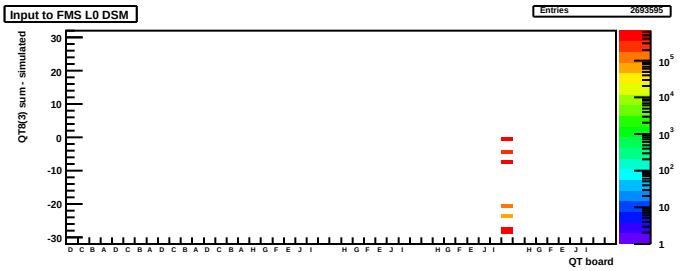

Input to FMS L0 DSM
Entries 1.39248e+08
QT8(1) sum
QT board

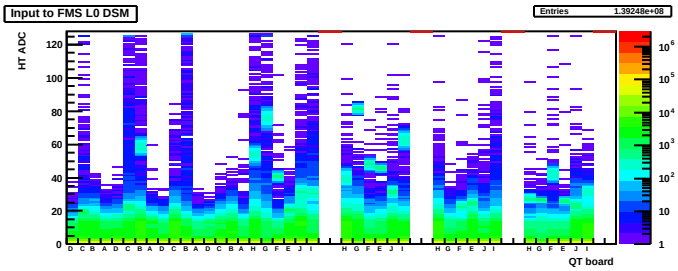

Input to FMS L0 DSM
Entries 1
QT8(1) sum - simulated
QT board

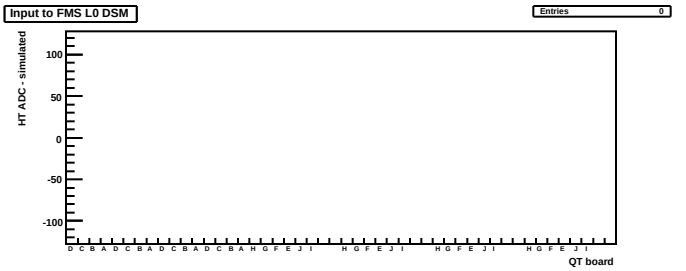

Input to FMS L0 DSM
Entries 1.39248e+08
QT8(2) sum
QT board

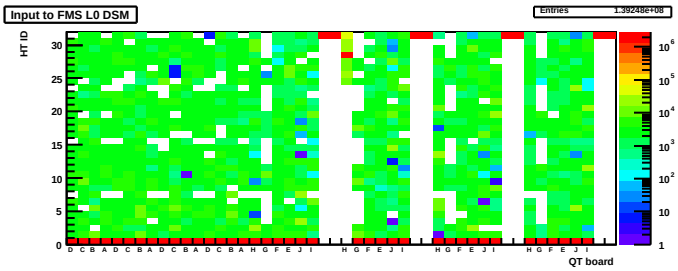

Input to FMS L0 DSM
Entries 2041617
QT8(2) sum - simulated
QT board

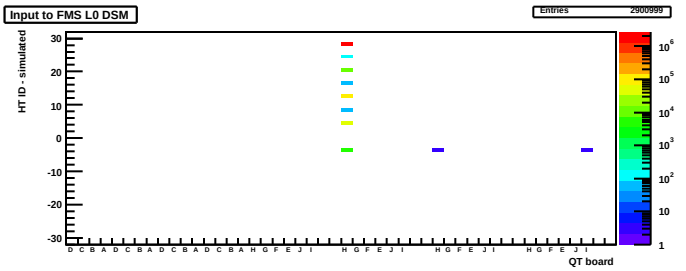

Input to FMS L0 DSM
Entries 1.39248e+08
QT8(3) sum
QT board

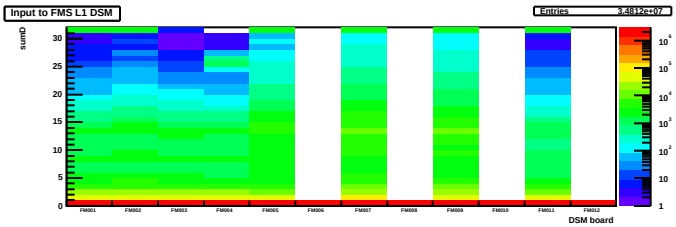

Input to FMS L0 DSM
Entries 2693595
QT8(3) sum - simulated
QT board

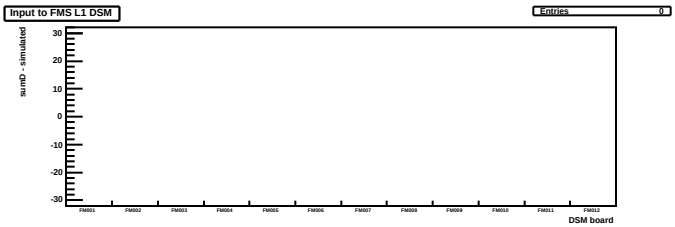

Input to FMS L0 DSM
Entries 1.39248e+08
HT ADC
QT board

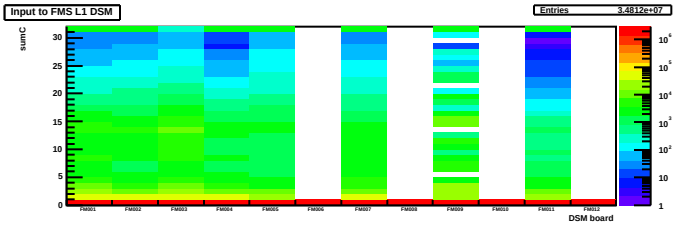

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

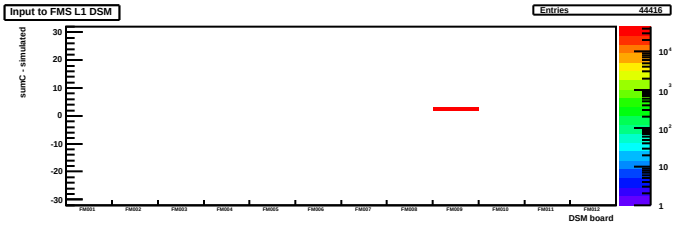

Input to FMS L0 DSM
Entries 1.39248e+08
HT ID
QT board

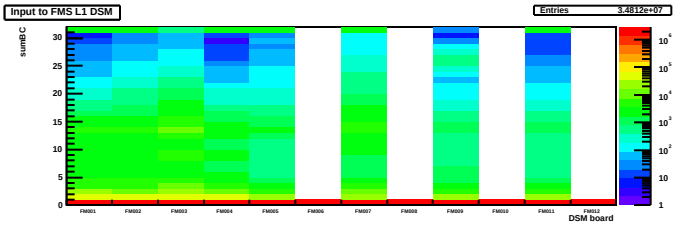

Input to FMS L0 DSM
Entries 2900999
HT ID - simulated
QT board

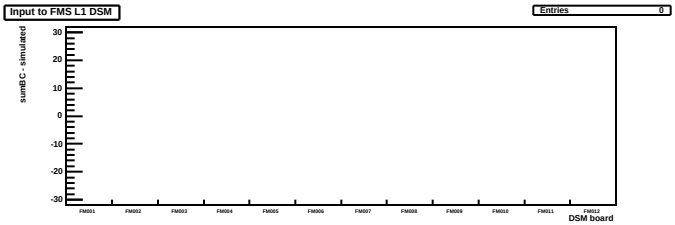

Input to FMS L1 DSM
Entries 3.4812e+07
sumD
DSM board

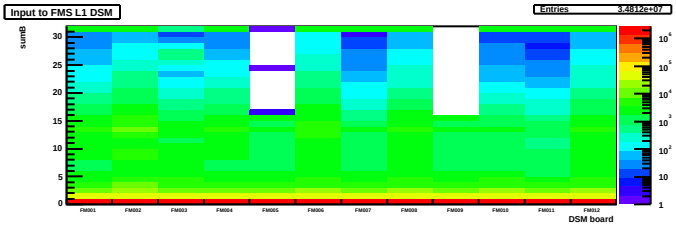

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

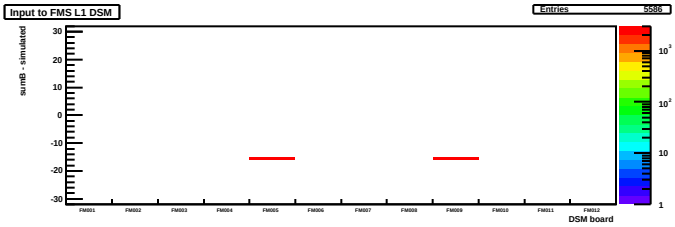

Input to FMS L1 DSM
Entries 3.4812e+07
sumC
DSM board

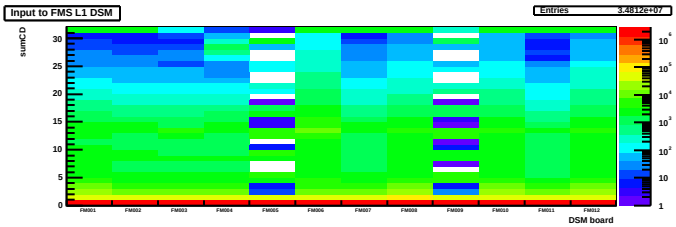

Input to FMS L1 DSM
Entries 44416
sumC - simulated
DSM board

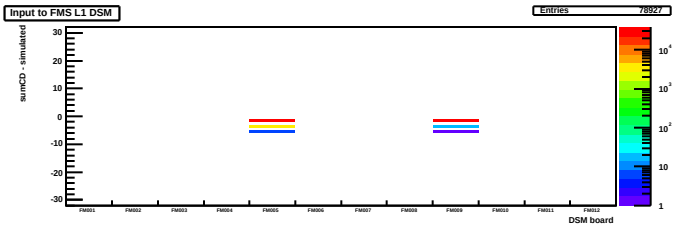

Input to FMS L1 DSM
Entries 3.4812e+07
sumBC
DSM board

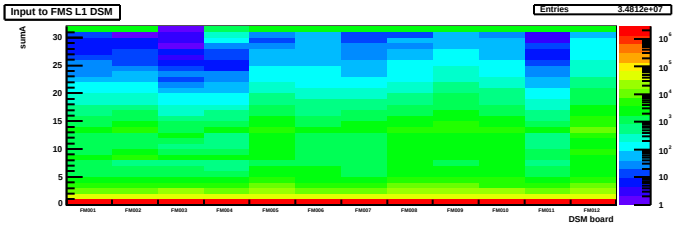

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

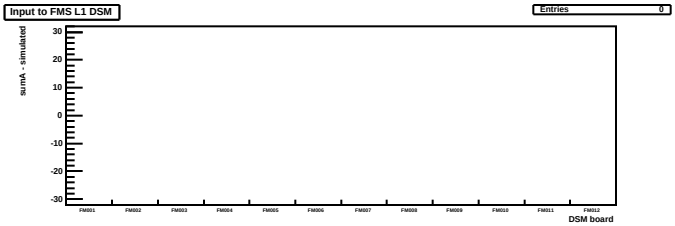

Input to FMS L1 DSM
Entries 3.4812e+07
sumB
DSM board

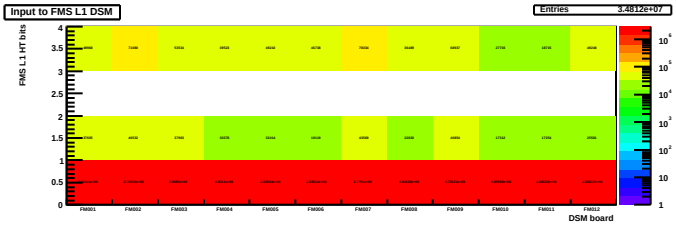

Input to FMS L1 DSM
Entries 5586
sumB - simulated
DSM board

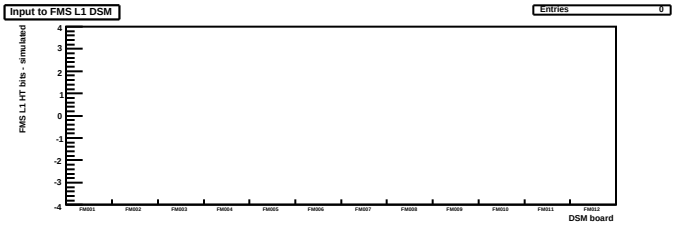

Input to FMS L1 DSM
Entries 3.4812e+07
sumCD
DSM board

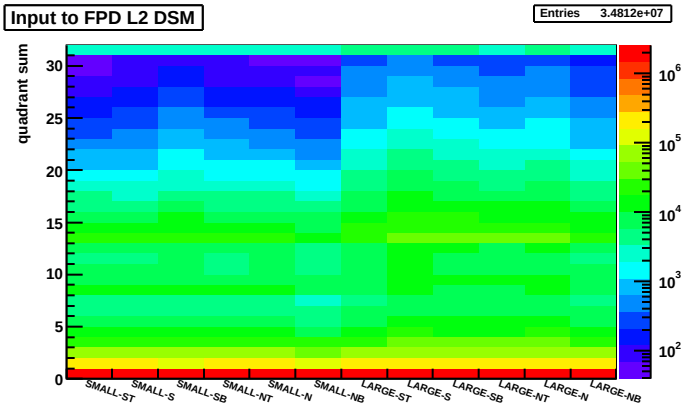

Input to FMS L1 DSM
Entries 78927
sumCD - simulated
DSM board

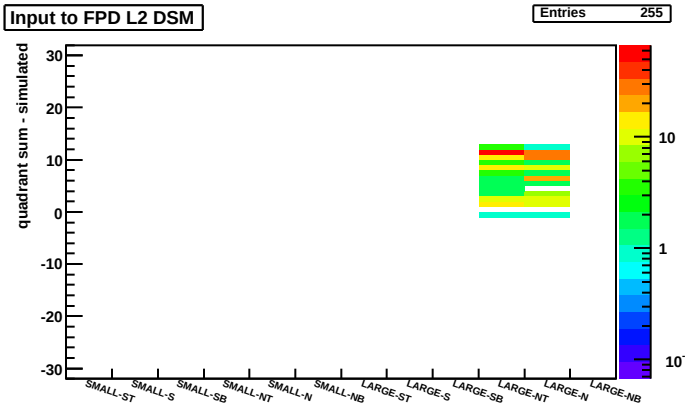

Input to FMS L1 DSM
Entries 3.4812e+07
sumA
DSM board

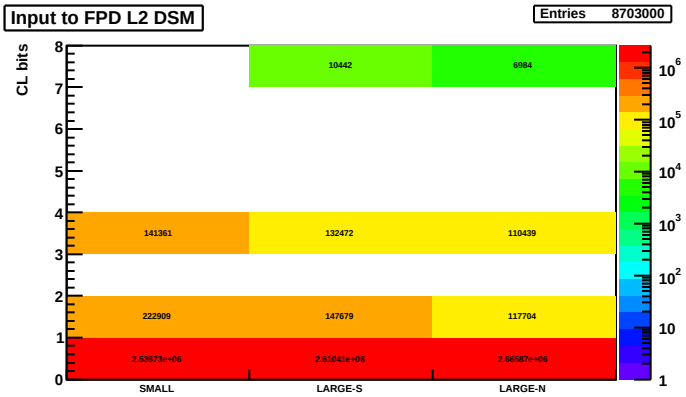

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

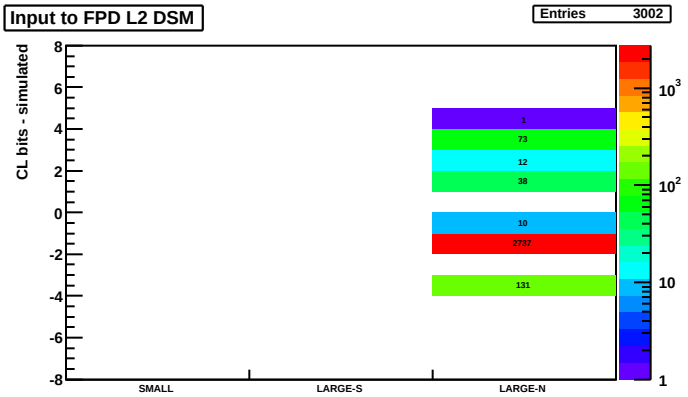

Input to FMS L1 DSM
Entries 3.4812e+07
FMS L1 HT bits
DSM board

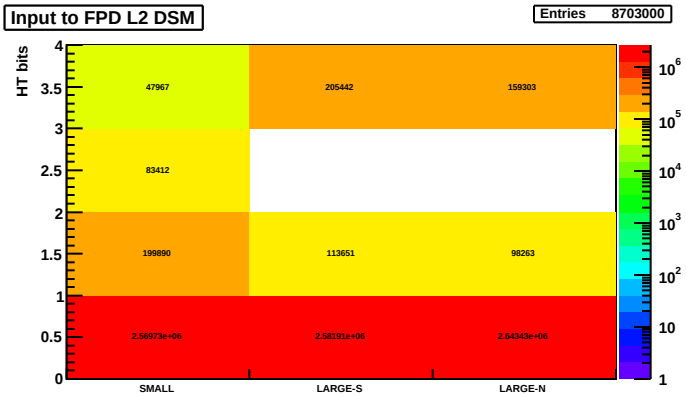

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

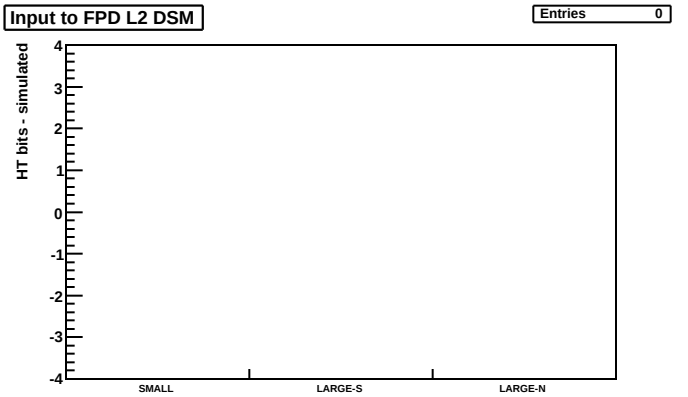

Input to FPD L2 DSM
Entries 3.4812e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

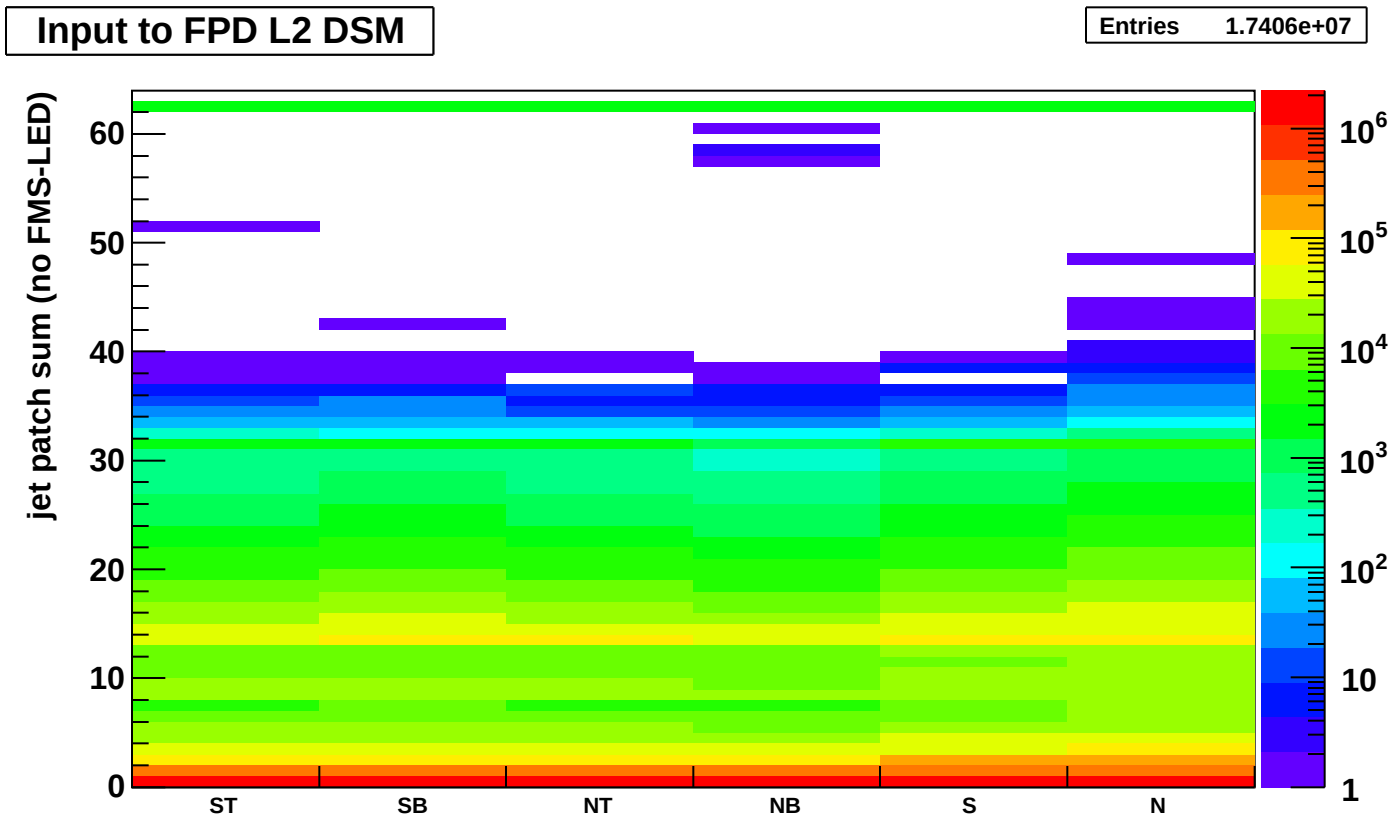

Input to FPD L2 DSM
Entries 255
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

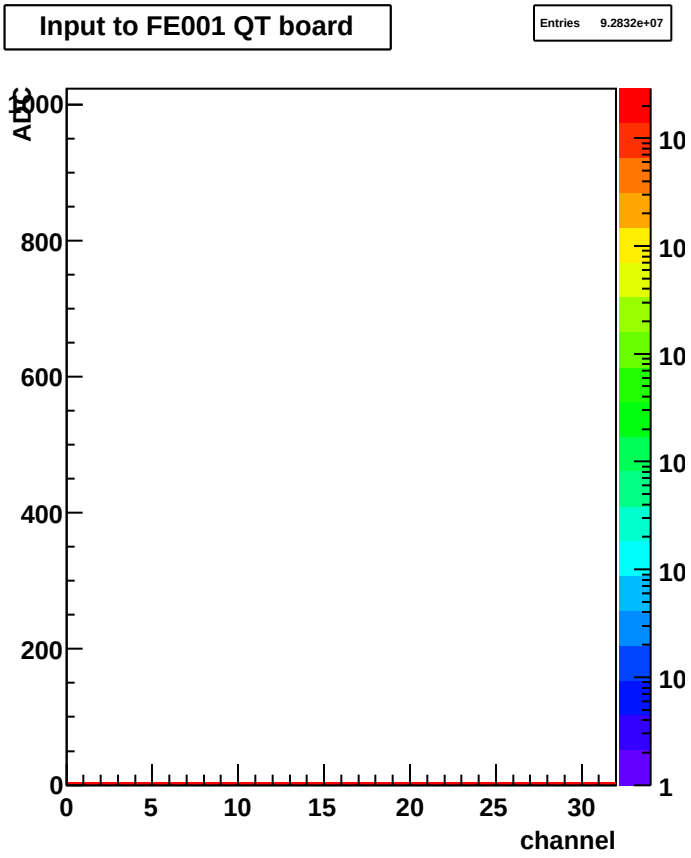

Input to FPD L2 DSM
Entries 8703000
CL bits
10442
6984
141361
132472
110439
222909
147679
117704
2.53673e+06
2.61041e+06
2.66587e+06
SMALL
LARGE-S
LARGE-N

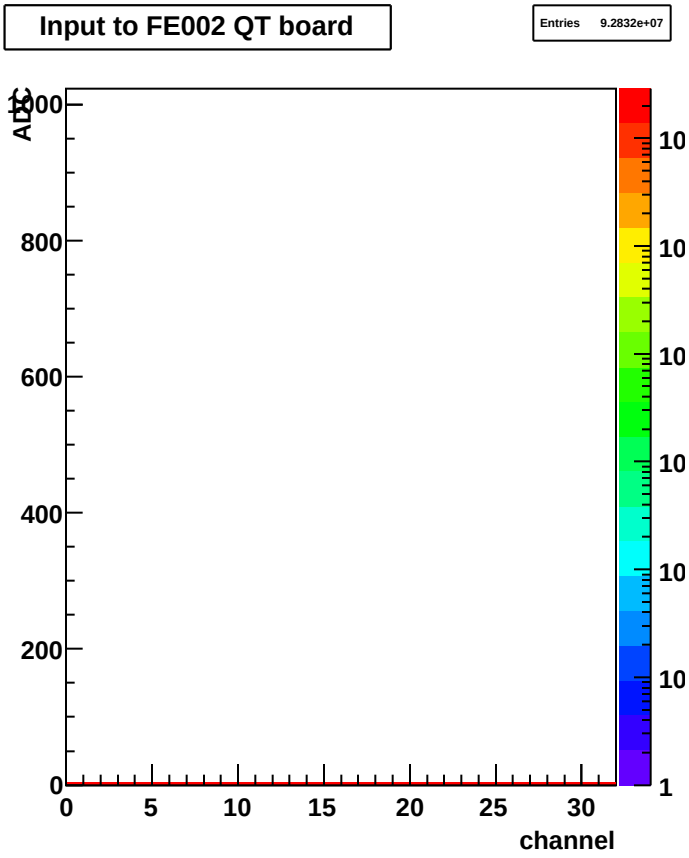

Input to FPD L2 DSM
Entries 3002
CL bits - simulated
1
73
12
38
10
2737
131
SMALL
LARGE-S
LARGE-N

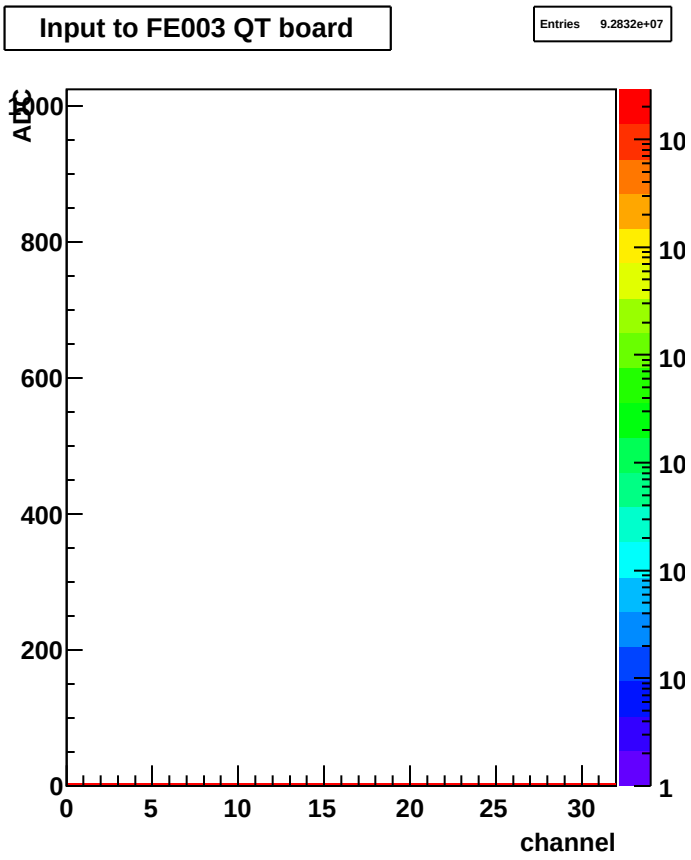

Input to FPD L2 DSM
Entries 8703000
HT bits
47967
205442
159303
83412
199890
113651
98263
2.56973e+06
2.58191e+06
2.64343e+06
SMALL
LARGE-S
LARGE-N

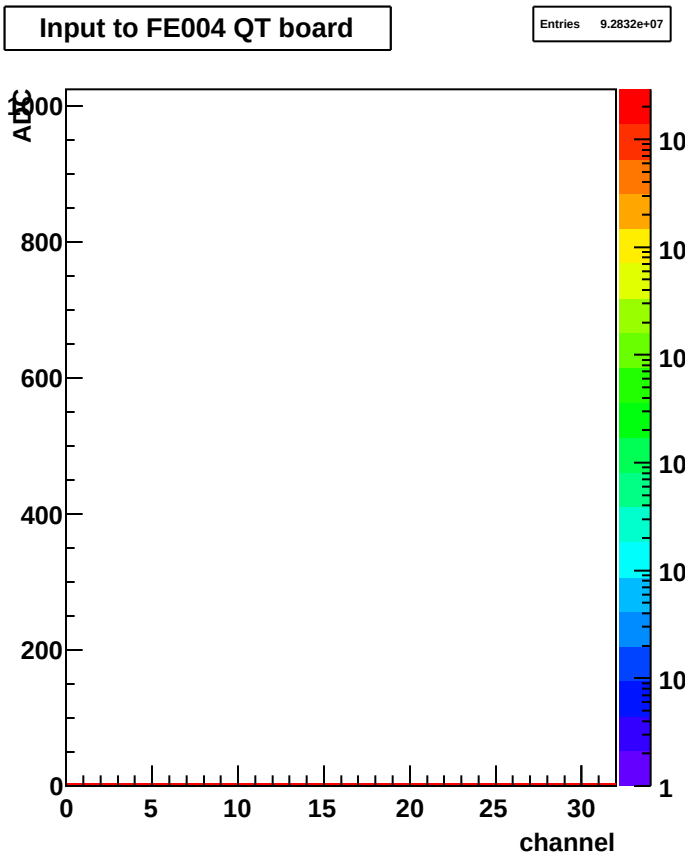

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

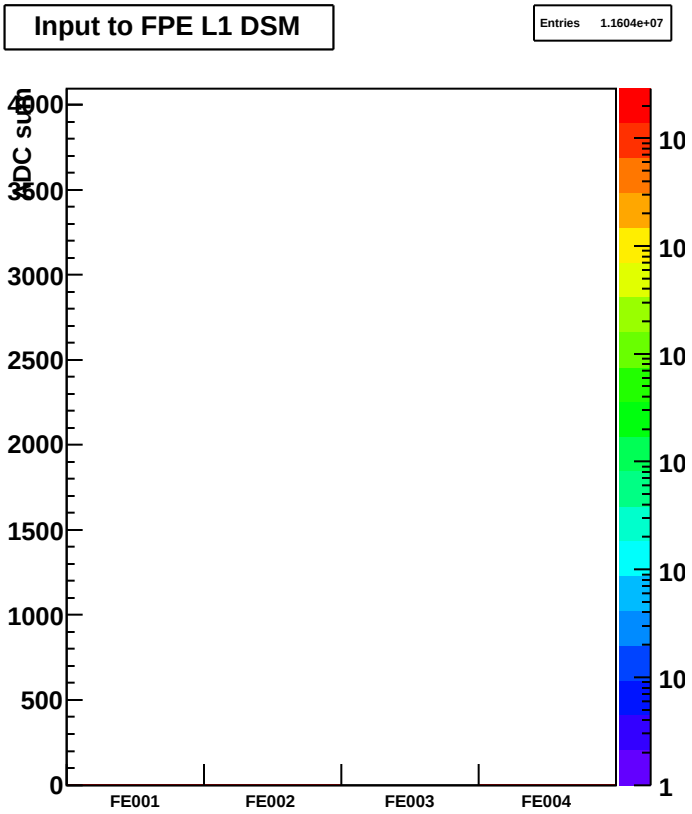

Input to FPD L2 DSM
Entries 1.7406e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

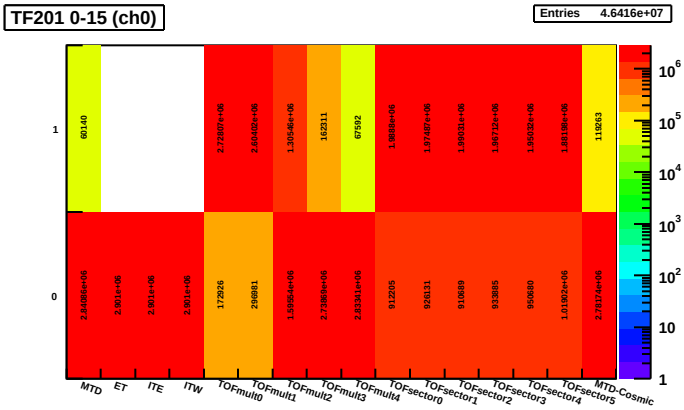

Input to FE001 QT board
Entries 9.2832e+07
ADC
channel

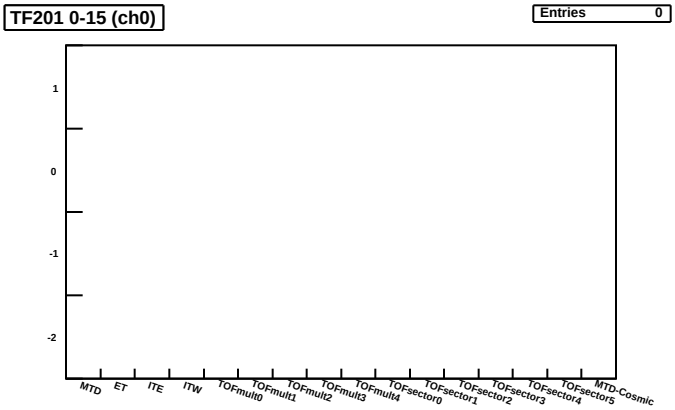

Input to FE002 QT board
Entries 9.2832e+07
ADC
channel

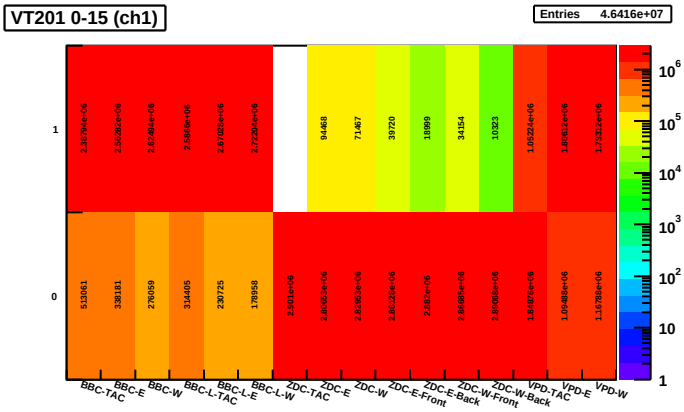

Input to FE003 QT board
Entries 9.2832e+07
ADC
channel

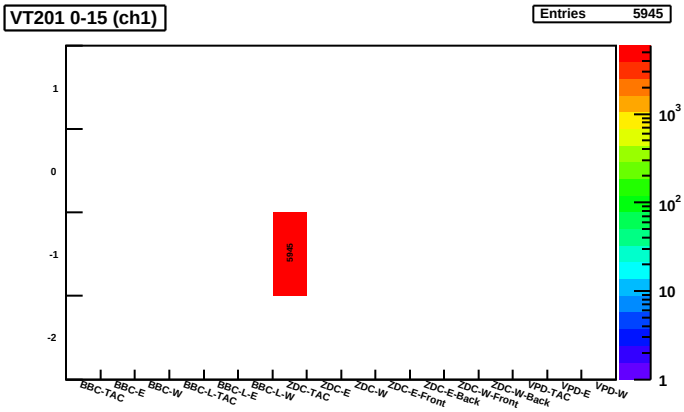

Input to FE004 QT board
Entries 9.2832e+07
ADC
channel

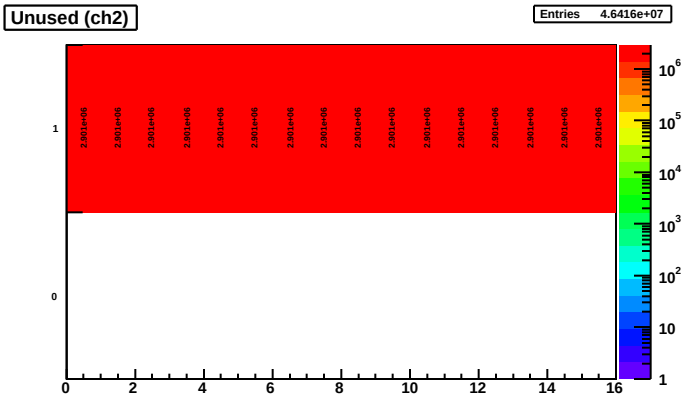

Input to FPE L1 DSM
Entries 1.1604e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

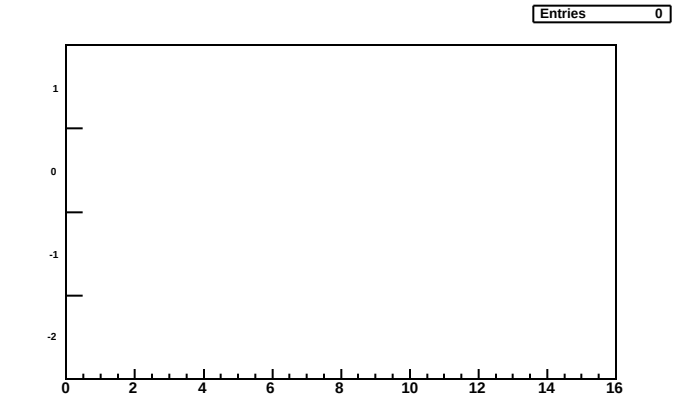

TF201 0-15 (ch0)
Entries 4.6416e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

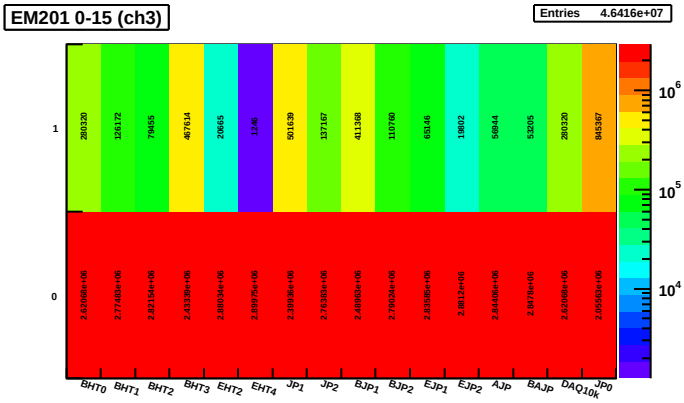

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

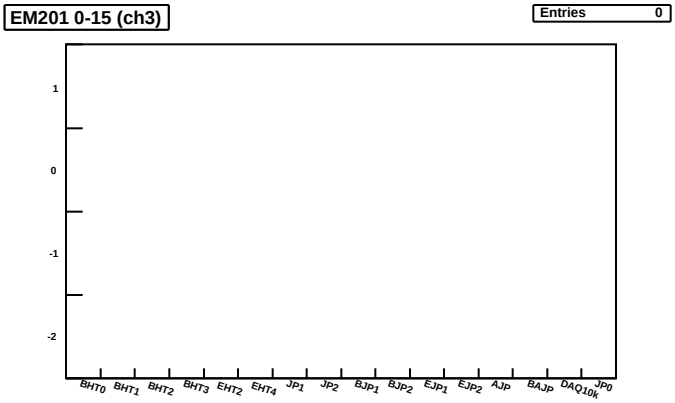

VT201 0-15 (ch1)
Entries 4.6416e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 5945
5945
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

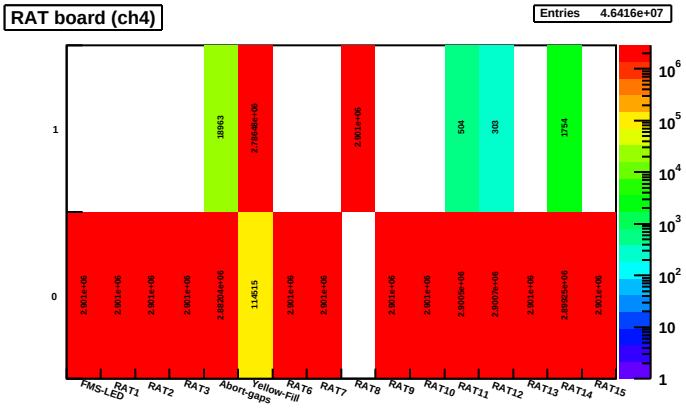

Unused (ch2)
Entries 4.6416e+07

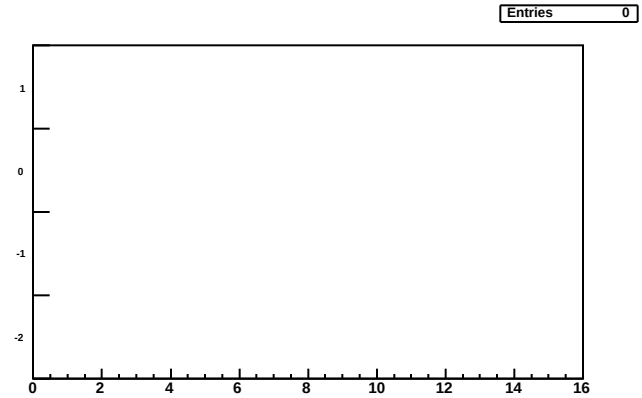

Entries 0


EM201 0-15 (ch3)
Entries 4.6416e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

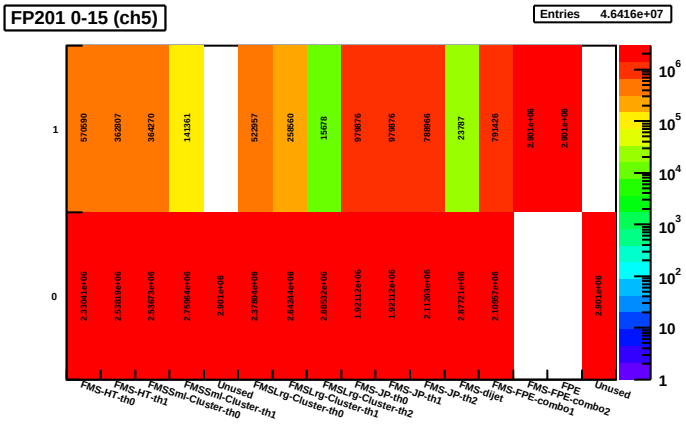

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

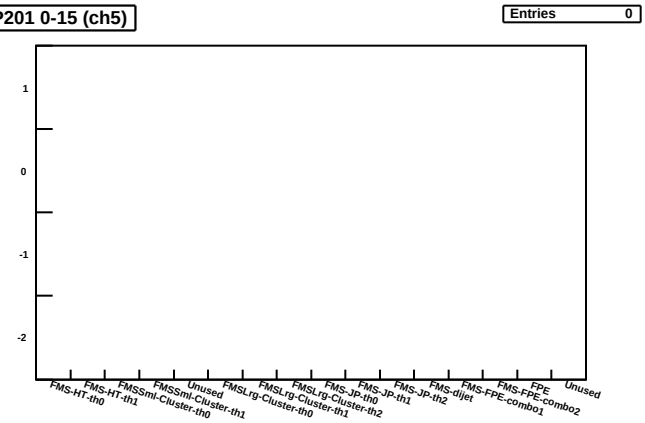




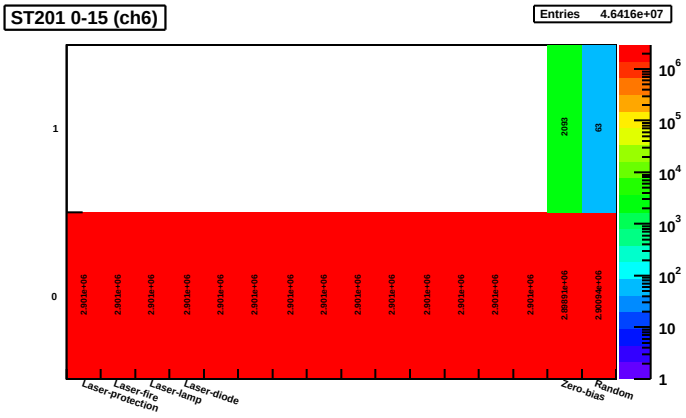
FP201 0-15 (ch5)

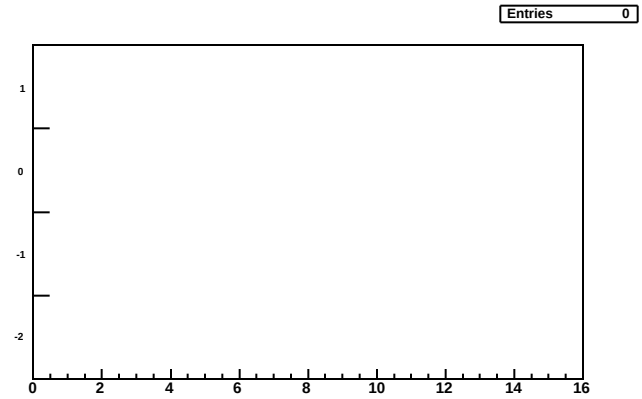
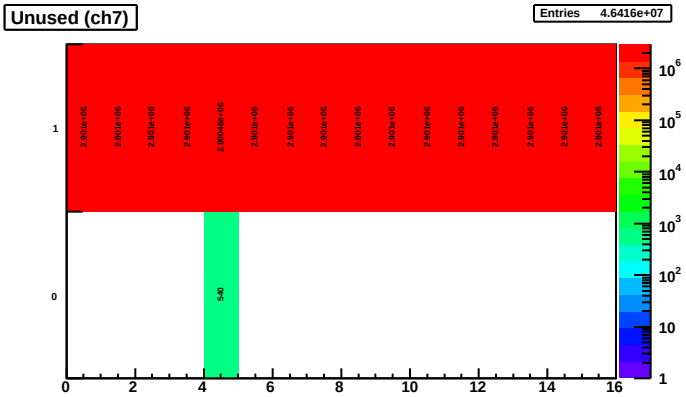
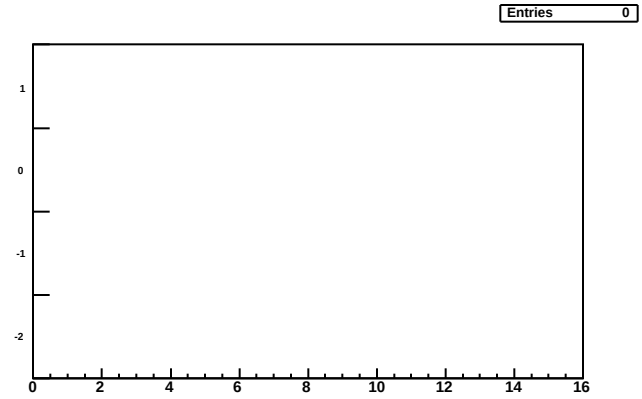


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)