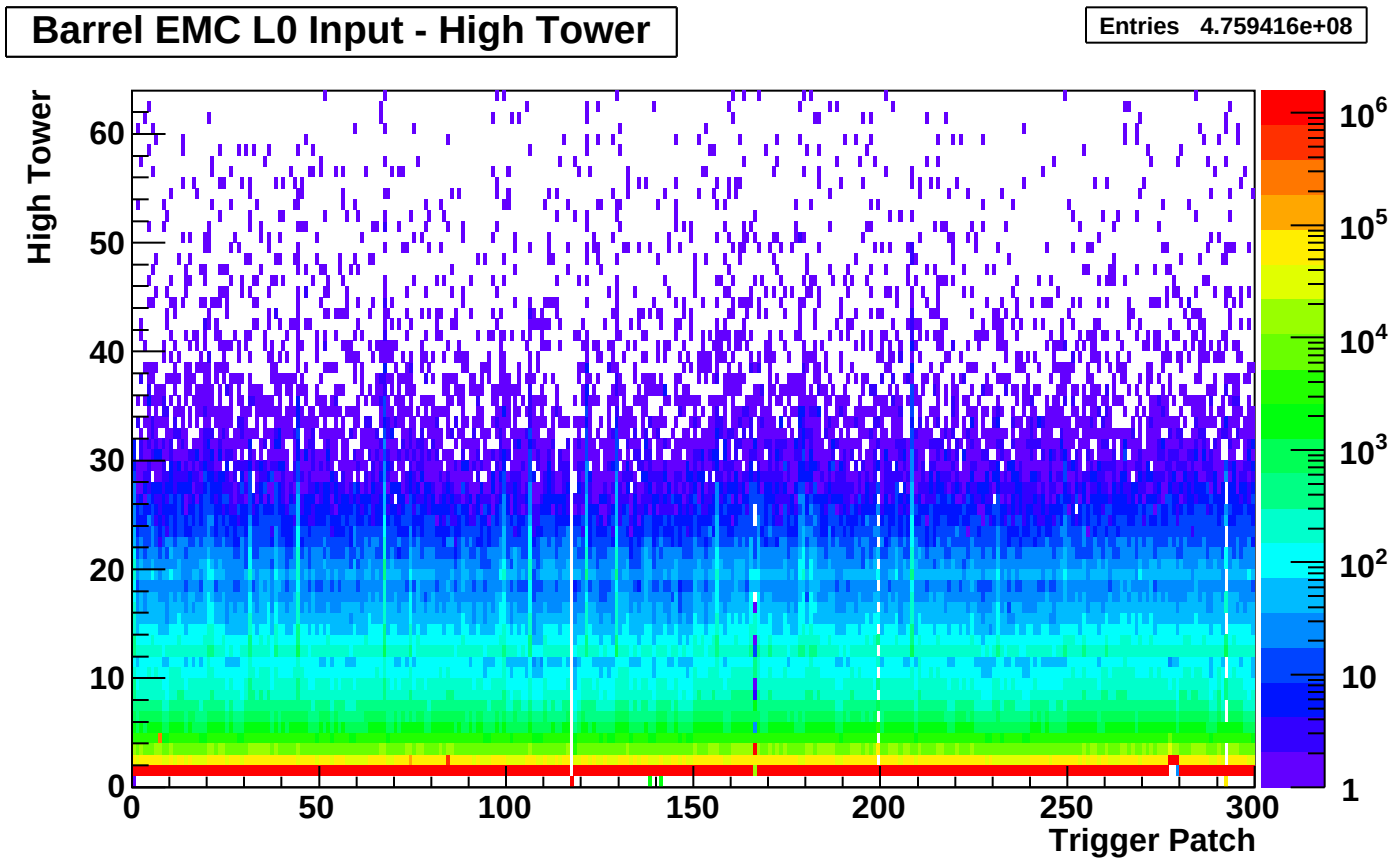
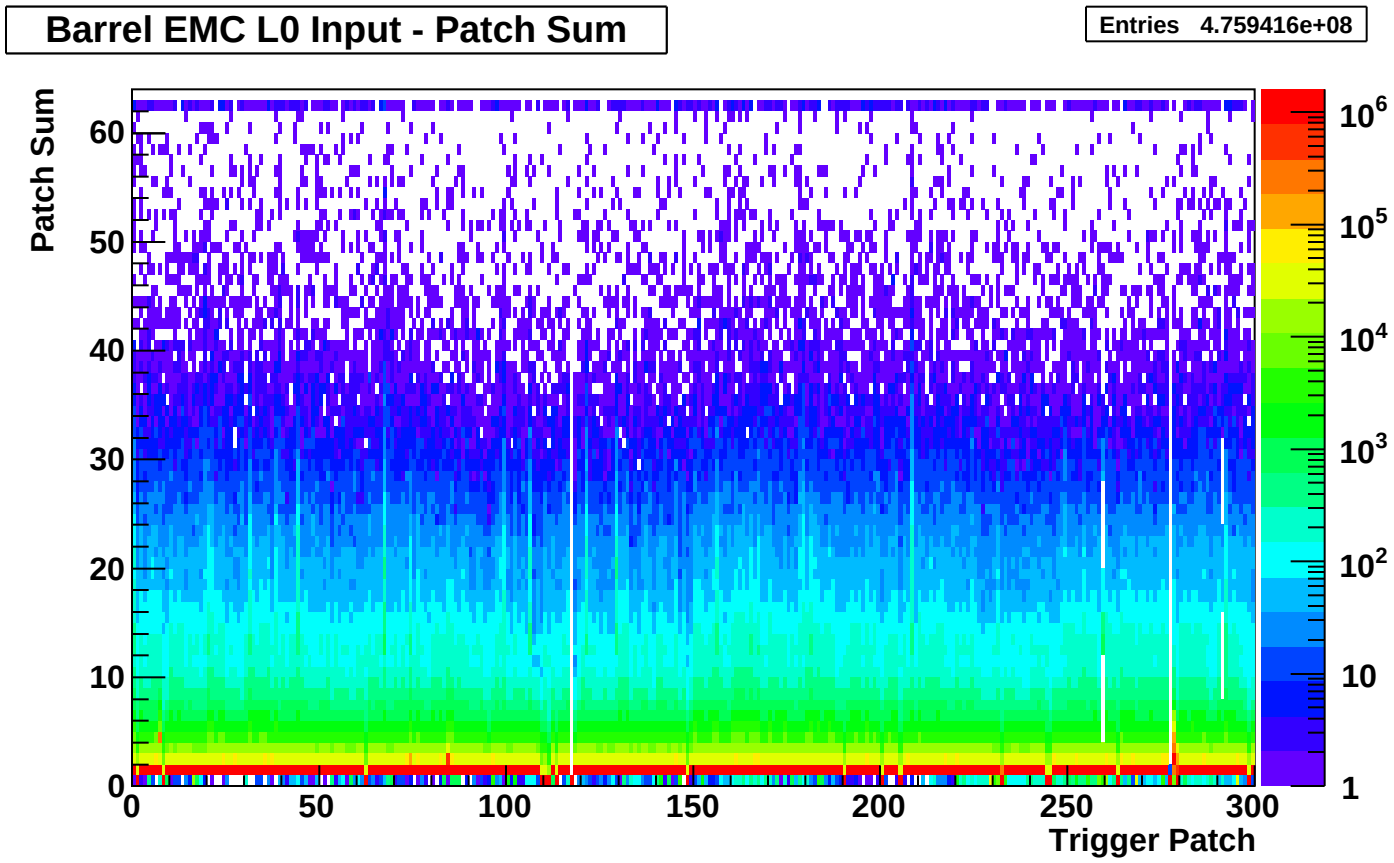
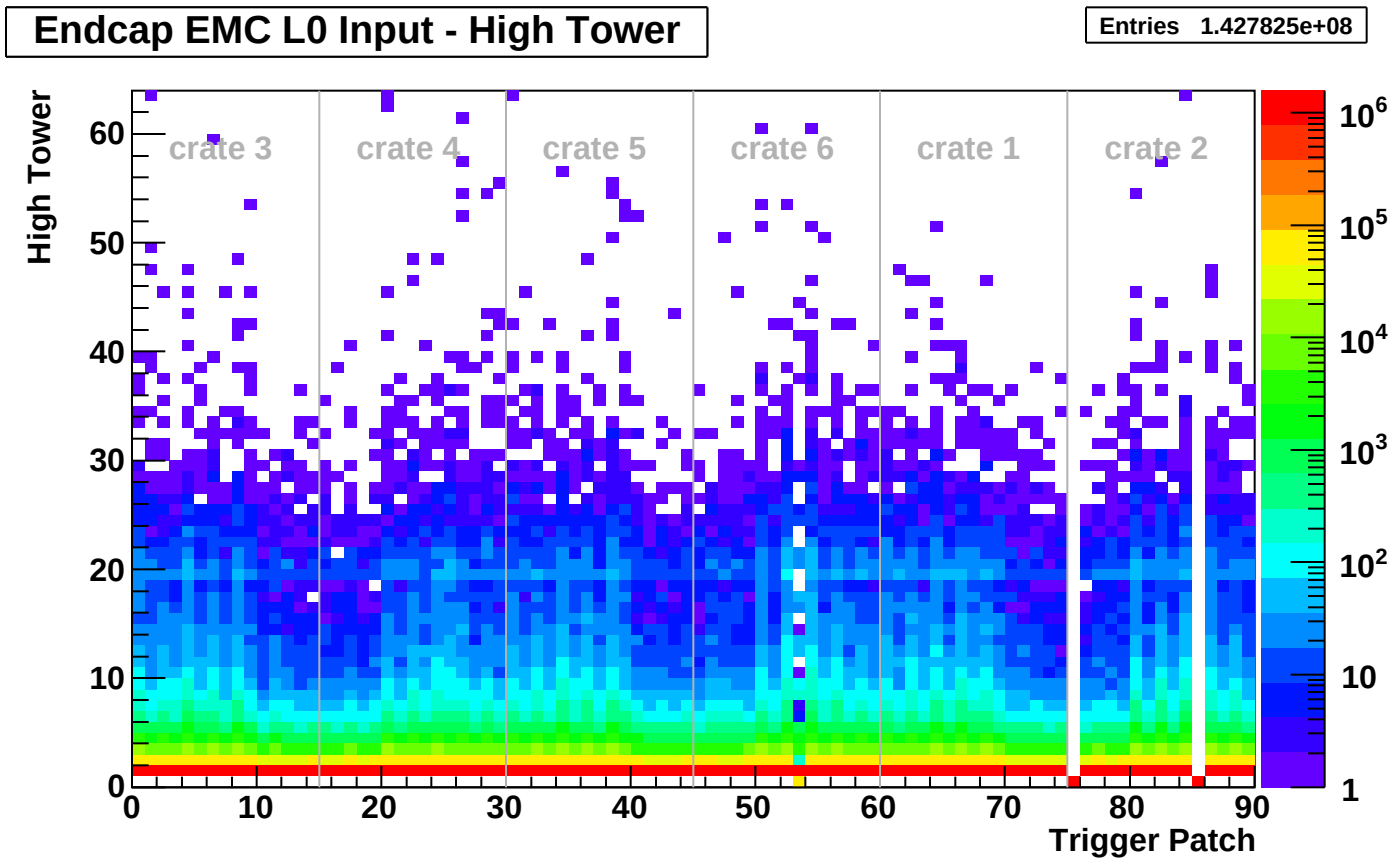
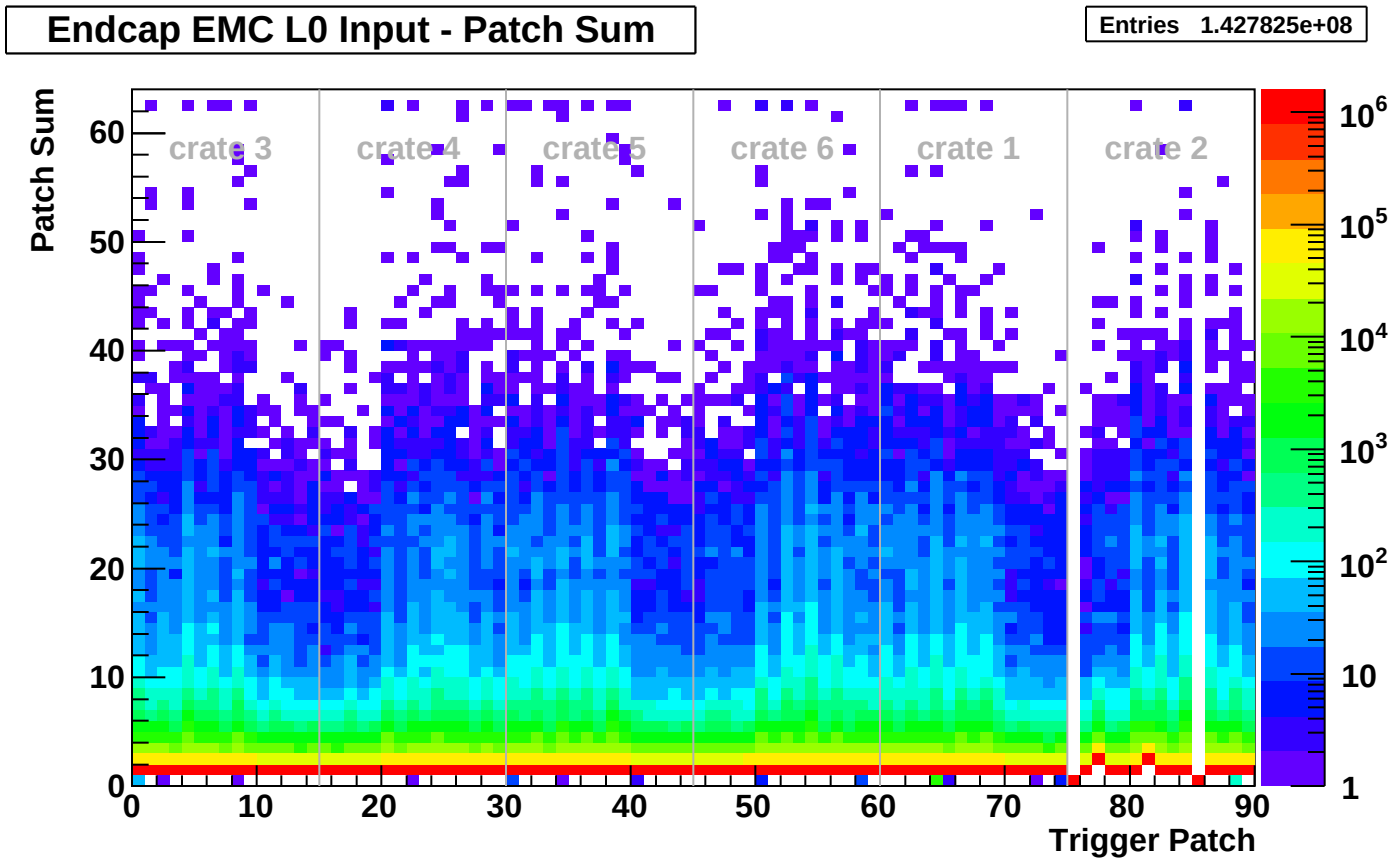
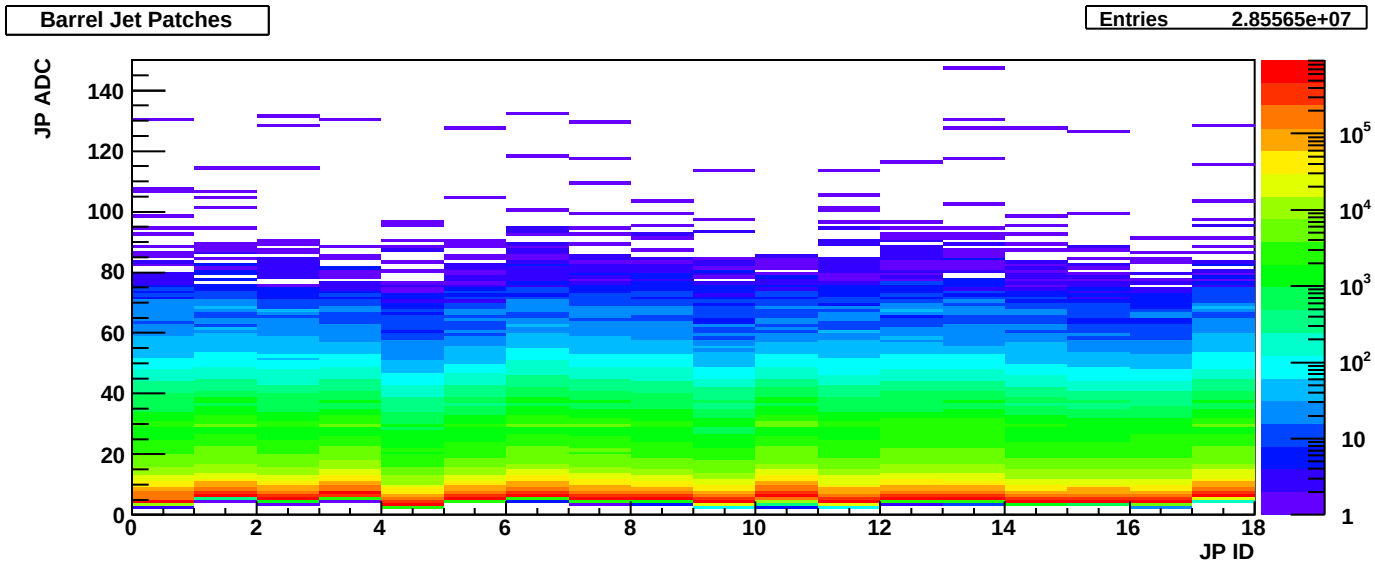
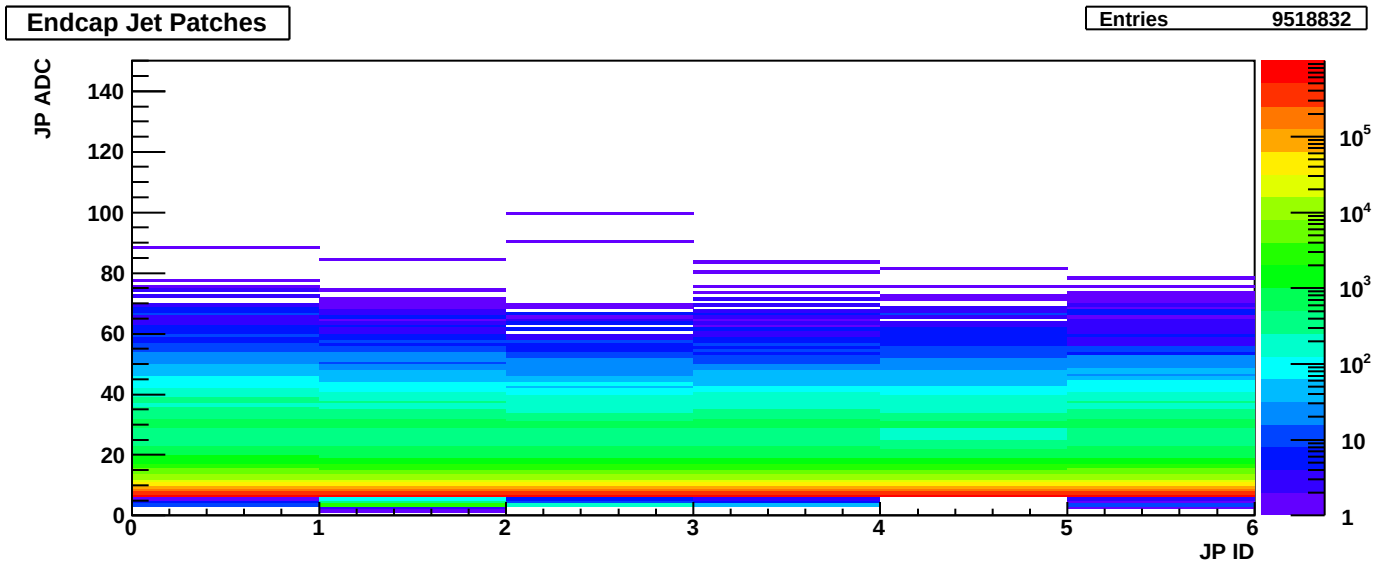

Barrel EMC L0 Input - High Tower
Entries 4.759416e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

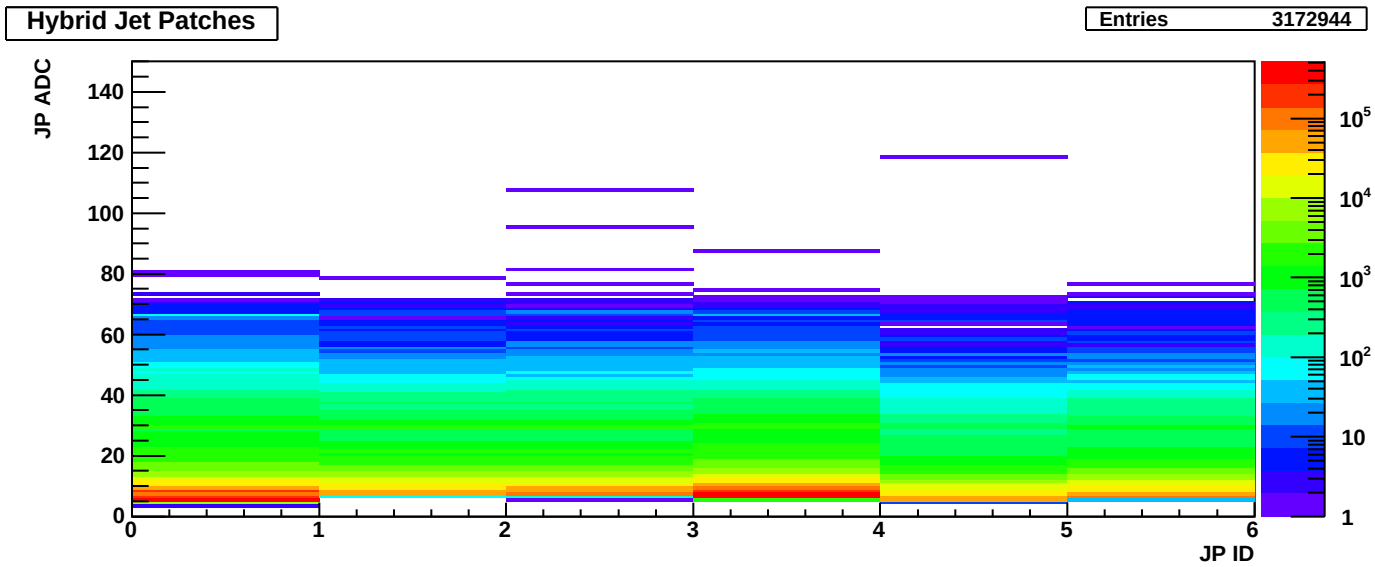

Barrel EMC L0 Input - Patch Sum
Entries 4.759416e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

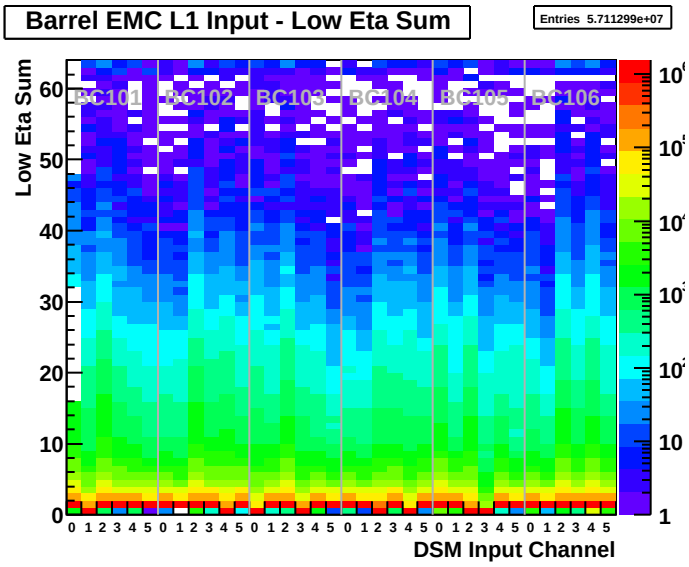

Endcap EMC L0 Input - High Tower
Entries 1.427825e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

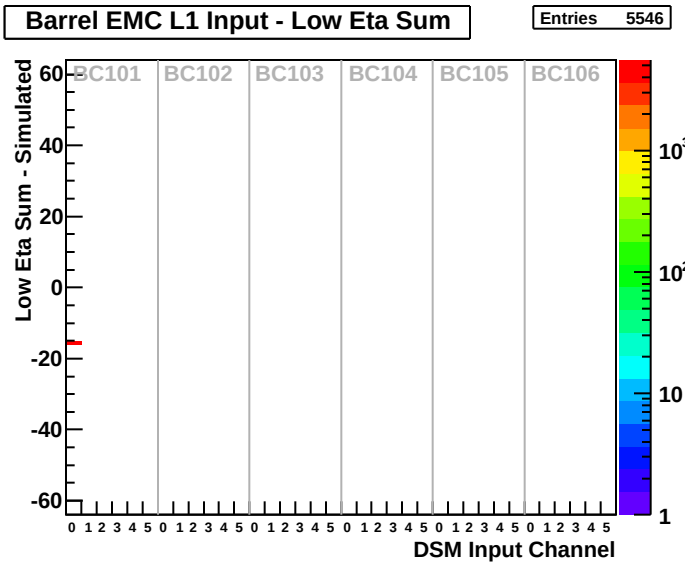

Endcap EMC L0 Input - Patch Sum
Entries 1.427825e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

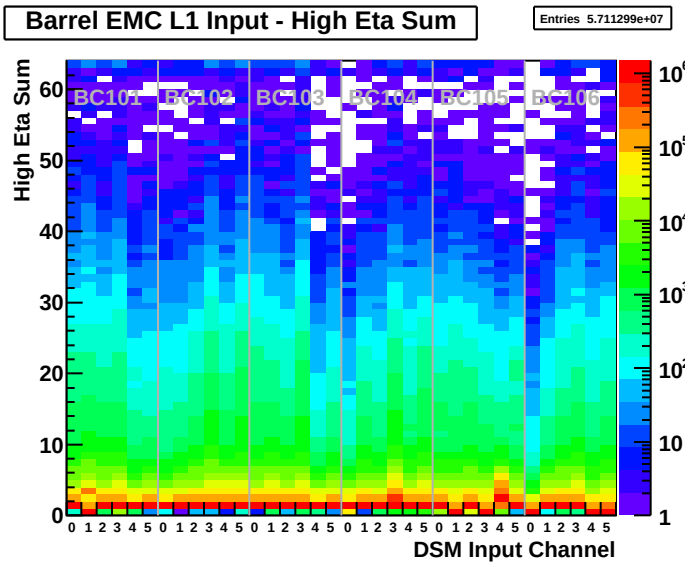

Barrel Jet Patches
Entries 2.85565e+07
JP ADC
JP ID

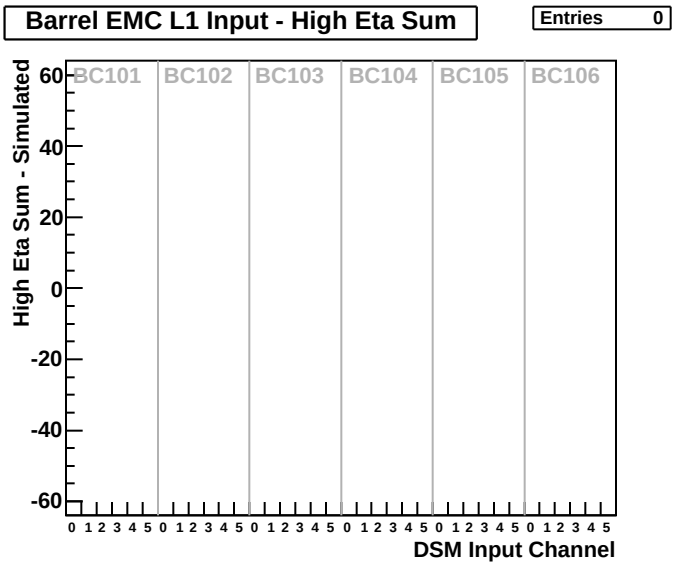

Endcap Jet Patches
Entries 9518832
JP ADC
JP ID

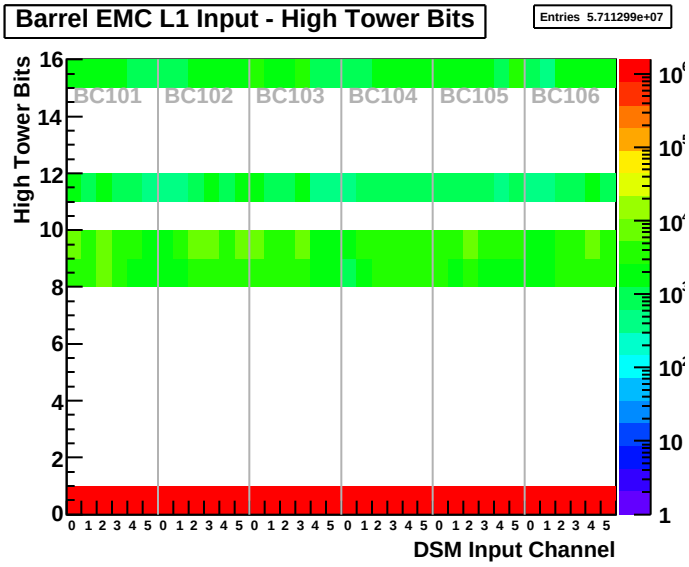

Hybrid Jet Patches
Entries 3172944
JP ADC
JP ID

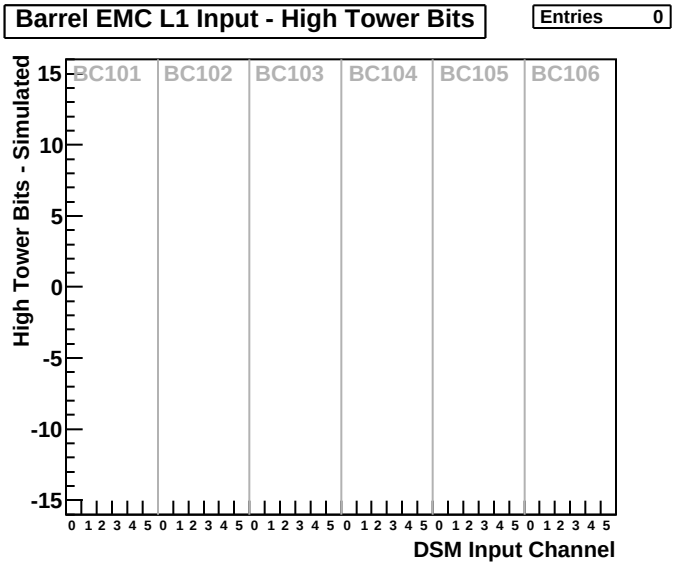

Barrel EMC L1 Input - Low Eta Sum
Entries 5.711299e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

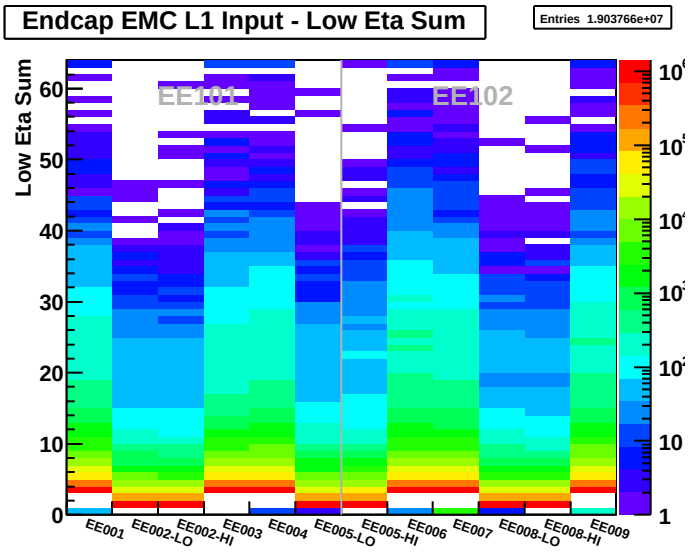

Barrel EMC L1 Input - Low Eta Sum
Entries 5546
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

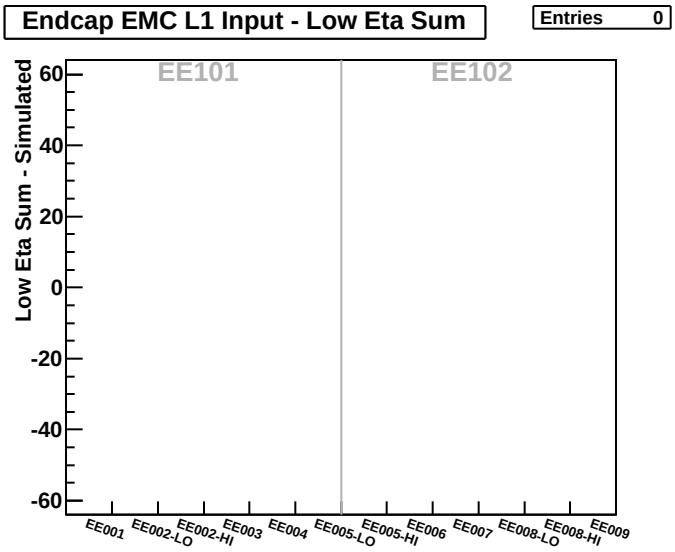

Barrel EMC L1 Input - High Eta Sum
Entries 5.711299e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

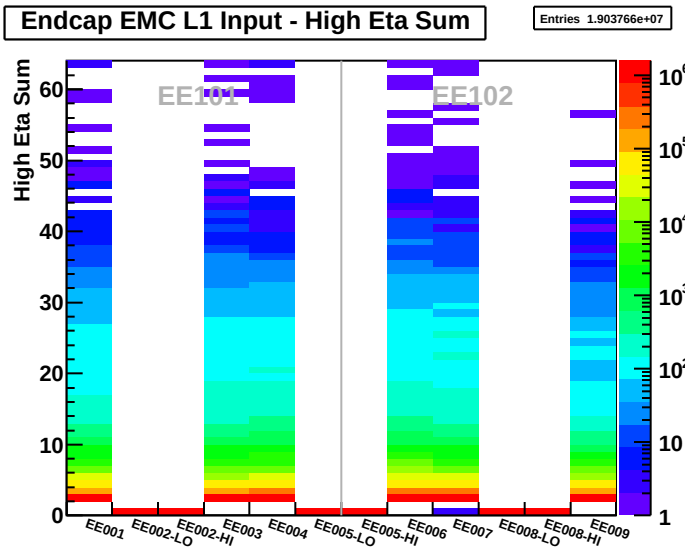

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

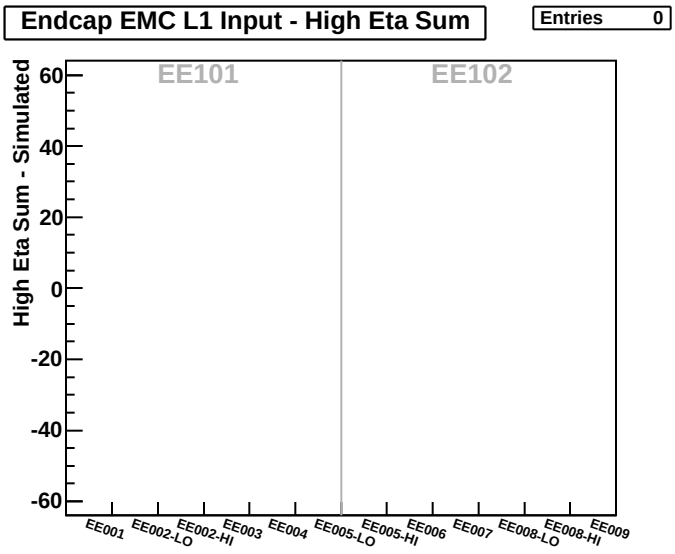

Barrel EMC L1 Input - High Tower Bits
Entries 5.711299e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

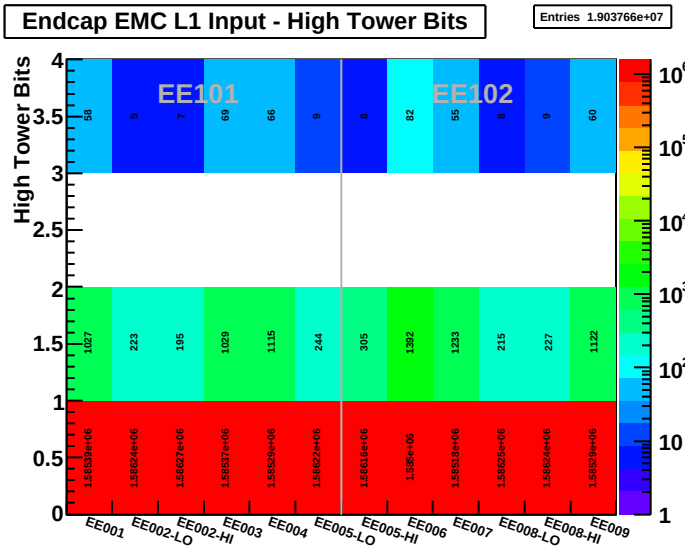

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

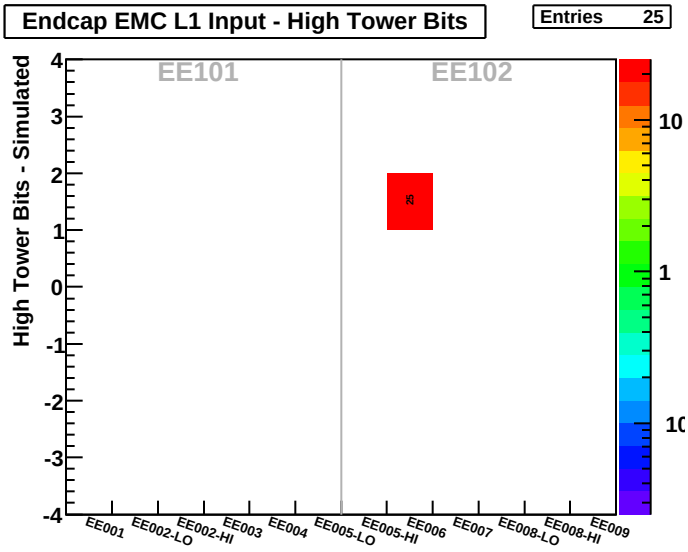

Endcap EMC L1 Input - Low Eta Sum
Entries 1.903766e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

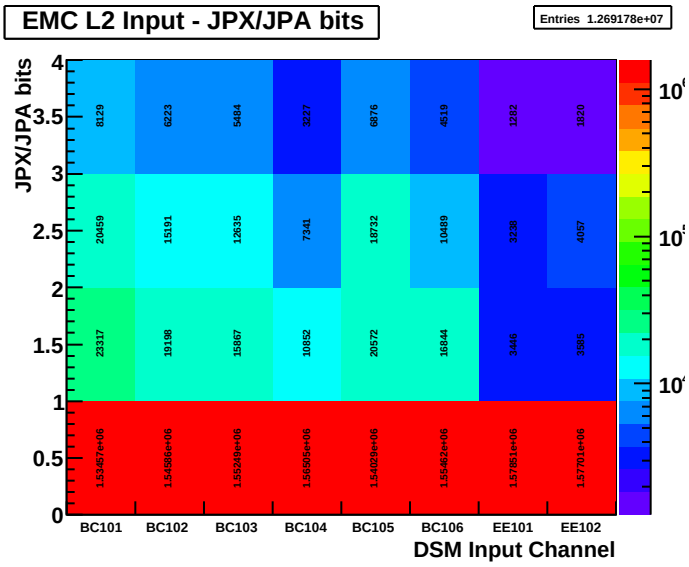

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

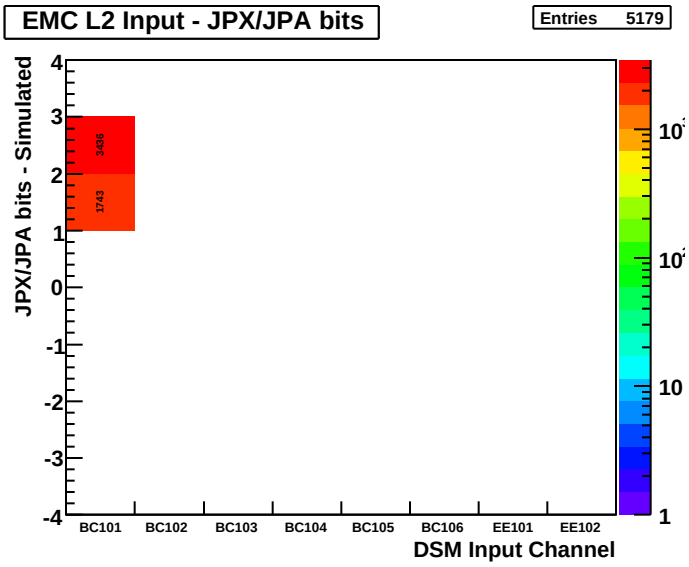

Endcap EMC L1 Input - High Eta Sum
Entries 1.903766e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

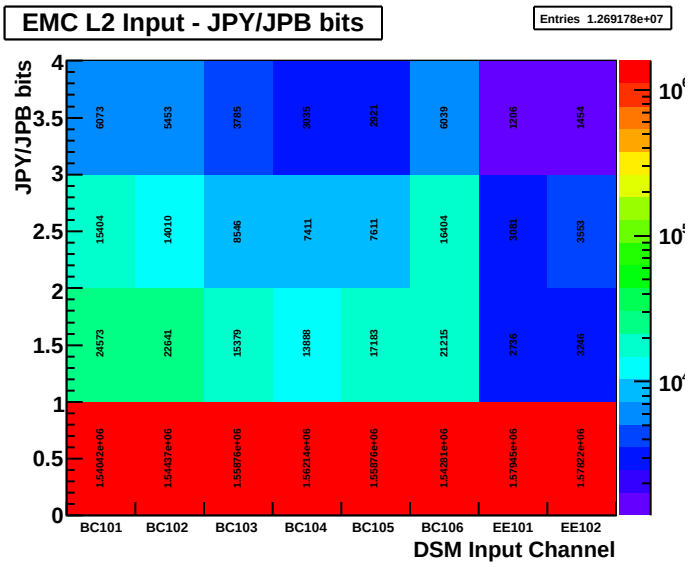

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

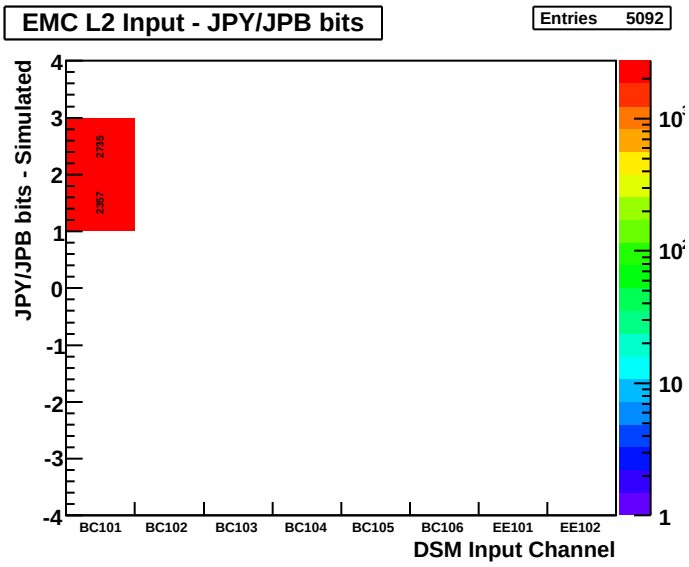

Endcap EMC L1 Input - High Tower Bits
Entries 1.903766e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

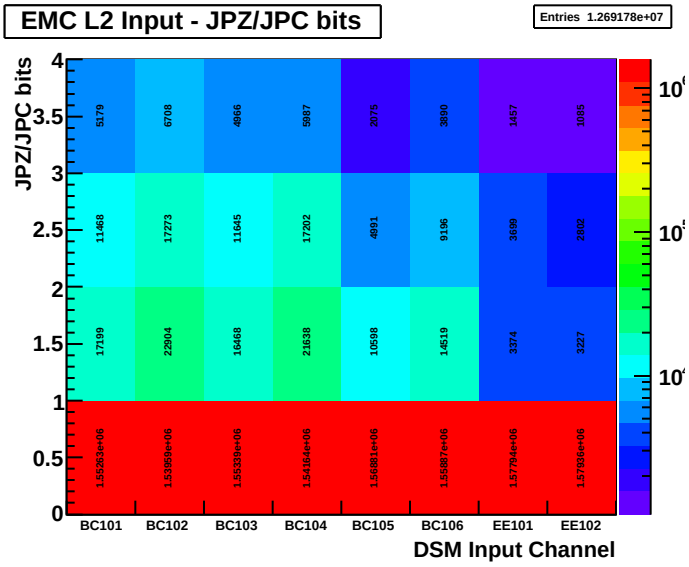

Endcap EMC L1 Input - High Tower Bits
Entries 25
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

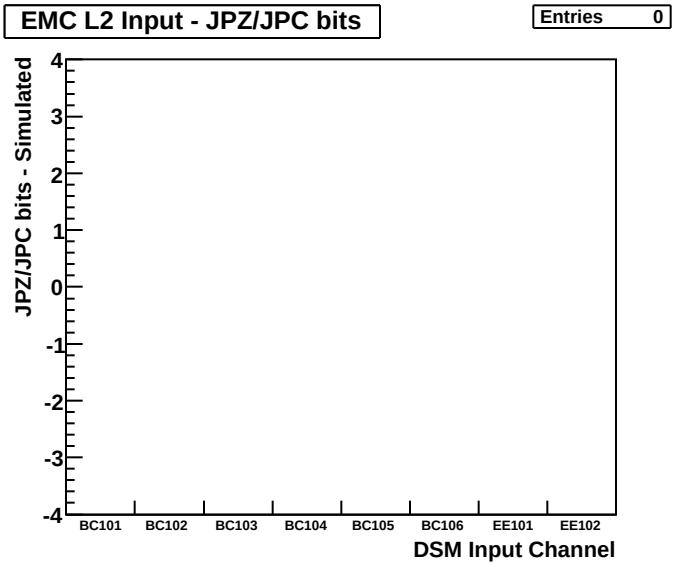

EMC L2 Input - JPX/JPA bits
Entries 1.269178e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
8129 6223 5484 3227 6876 4519 1282 1820
20459 15191 12635 7341 18732 10489 3238 4057
22317 19198 15867 10852 20572 16844 3446 3585
1.53457e+06 1.54586e+06 1.55249e+06 1.56905e+06 1.54029e+06 1.55462e+06 1.57851e+06 1.57701e+06

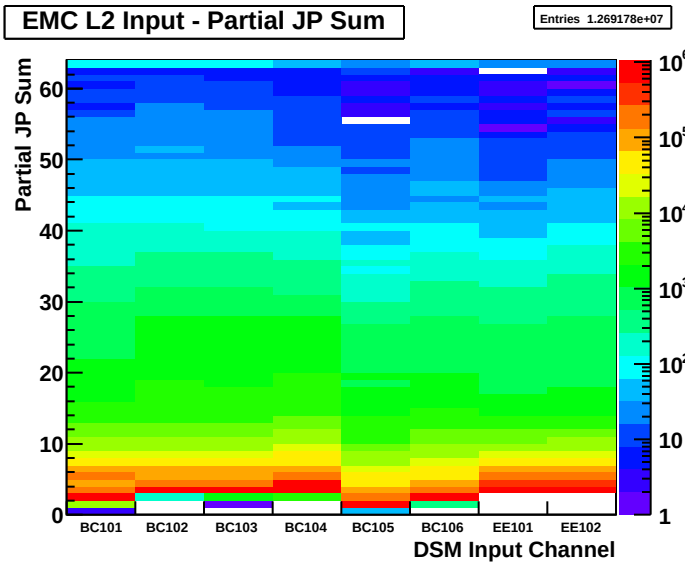

EMC L2 Input - JPX/JPA bits
Entries 5179
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3436
1743

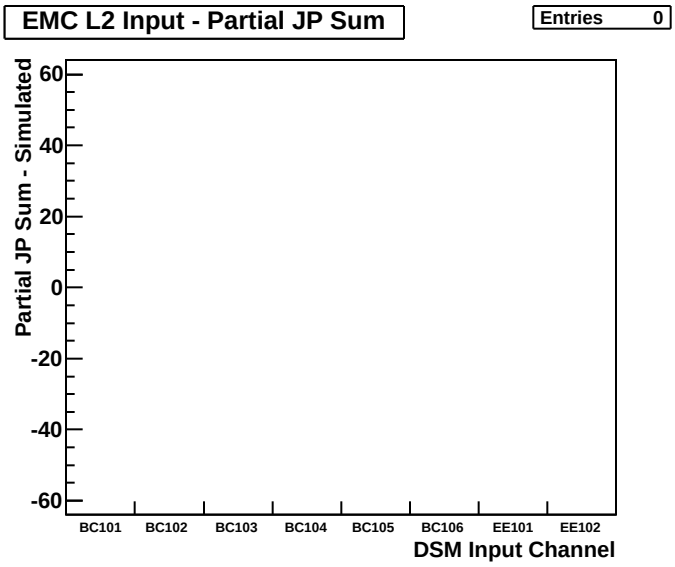

EMC L2 Input - JPY/JPB bits
Entries 1.269178e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6073 5453 3785 3035 2921 6039 1206 1454
15404 14010 8546 7411 7611 16404 3081 3553
24573 22641 15379 13888 17183 21215 2236 3246
1.54042e+06 1.54437e+06 1.55876e+06 1.56214e+06 1.55876e+06 1.54281e+06 1.57945e+06 1.57822e+06

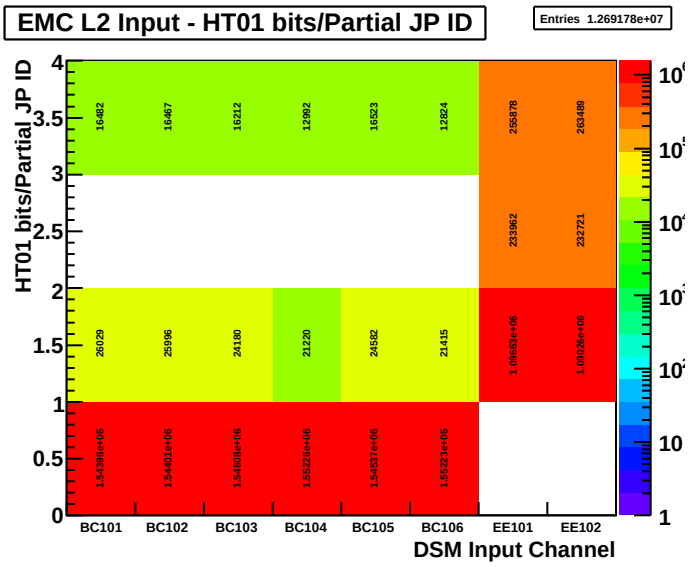

EMC L2 Input - JPY/JPB bits
Entries 5092
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2735
2357

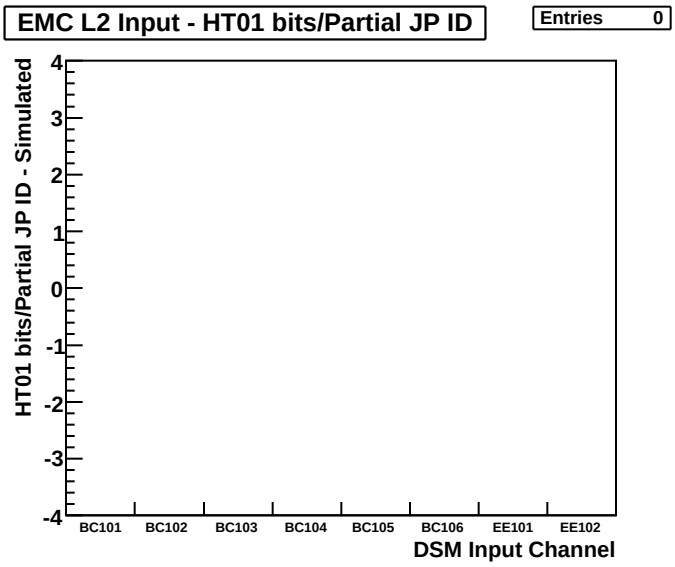

EMC L2 Input - JPZ/JPC bits
Entries 1.269178e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5179 6708 4966 5987 2075 3890 1457 1085
11468 17273 11645 17202 4991 9196 3699 2802
17199 22904 16468 21638 10598 14619 3374 3227
1.55263e+06 1.53959e+06 1.55339e+06 1.54164e+06 1.56881e+06 1.55887e+06 1.57794e+06 1.57936e+06

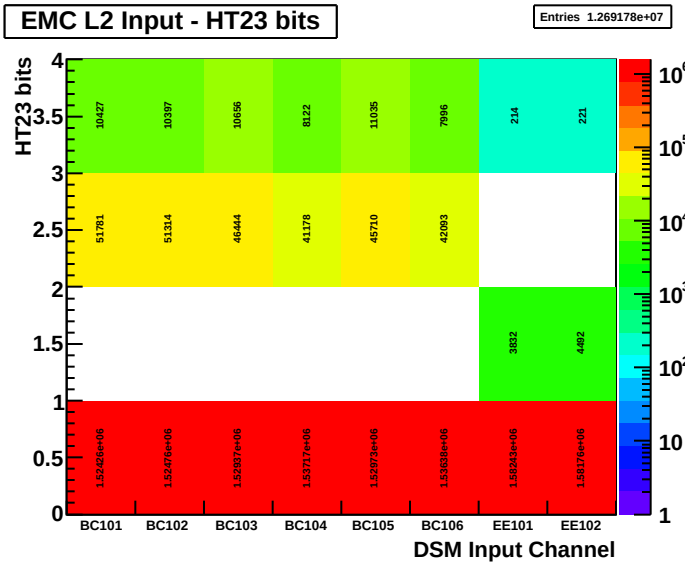

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

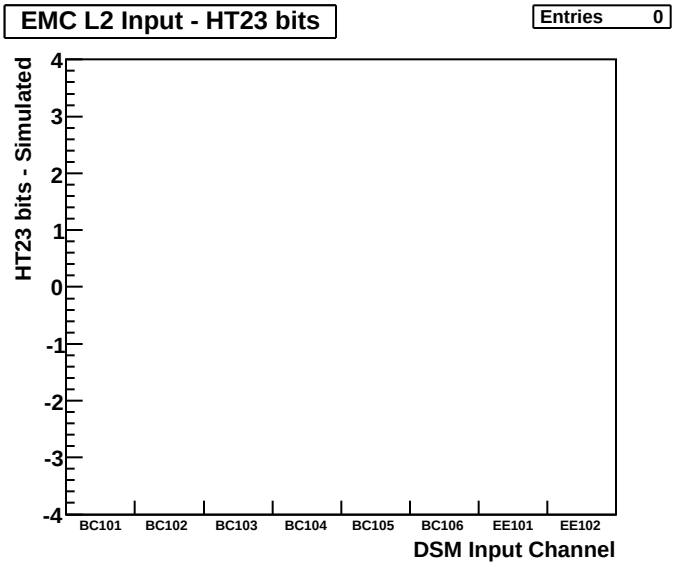

EMC L2 Input - Partial JP Sum
Entries 1.269178e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

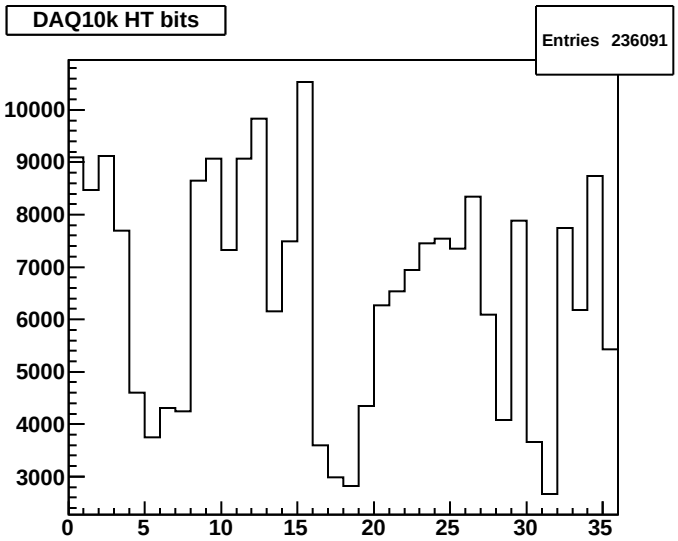

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

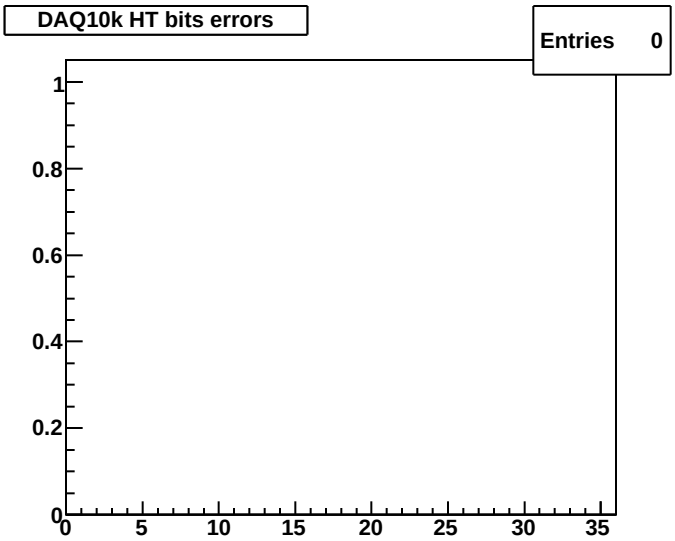

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.269178e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

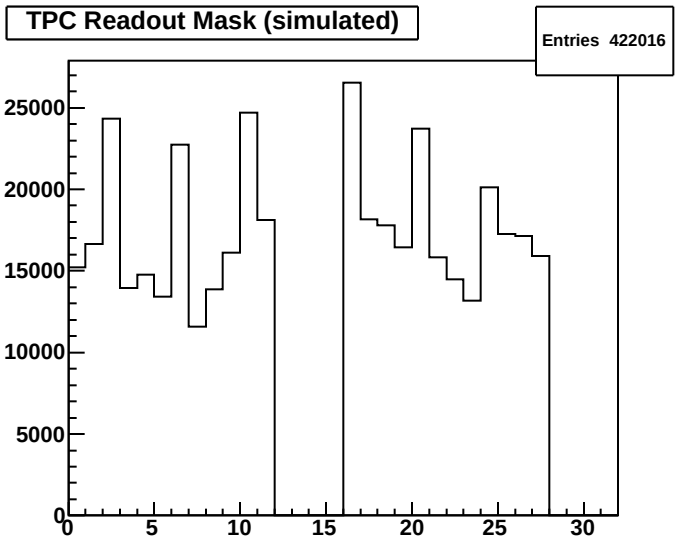

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

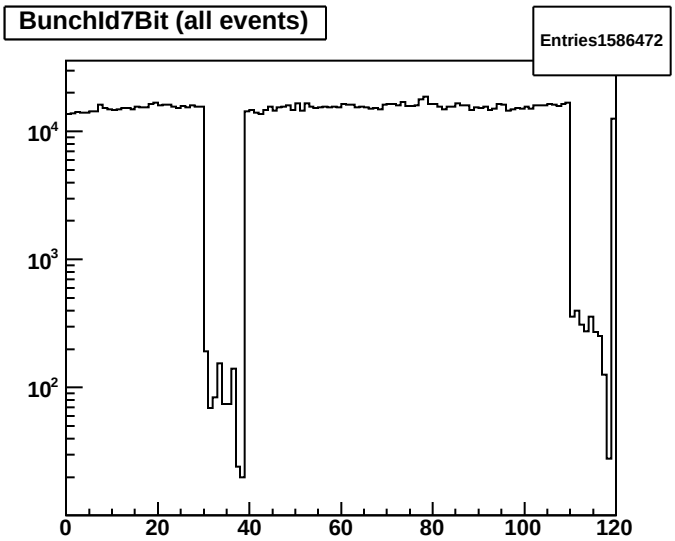

EMC L2 Input - HT23 bits
Entries 1.269178e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

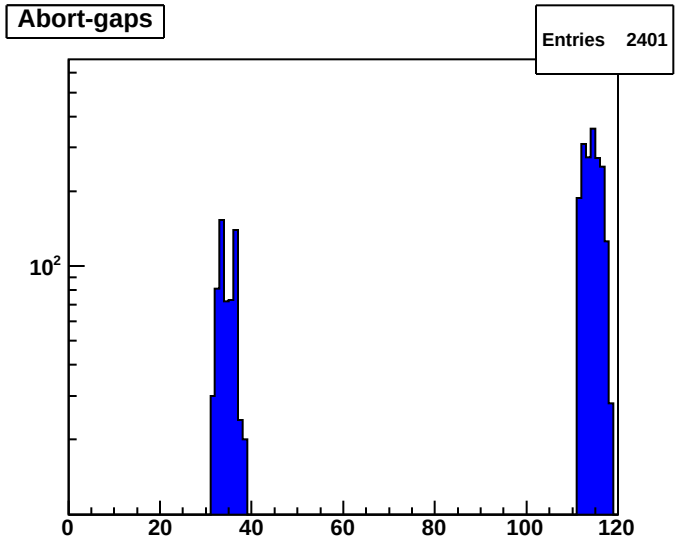

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

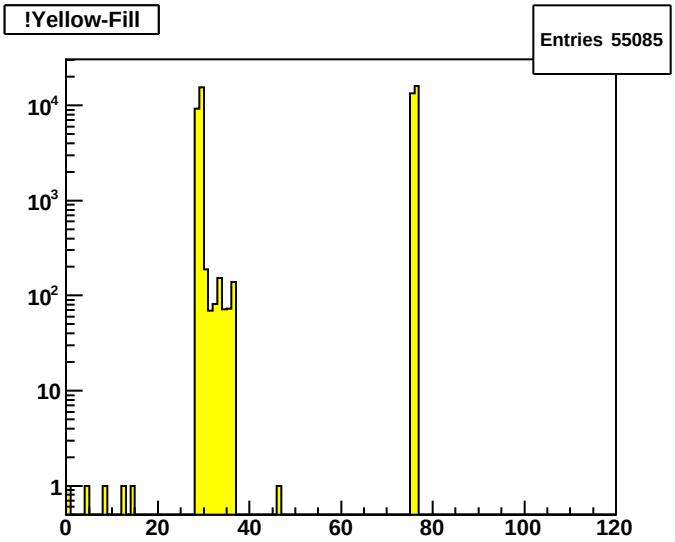

DAQ10k HT bits
Entries 236091

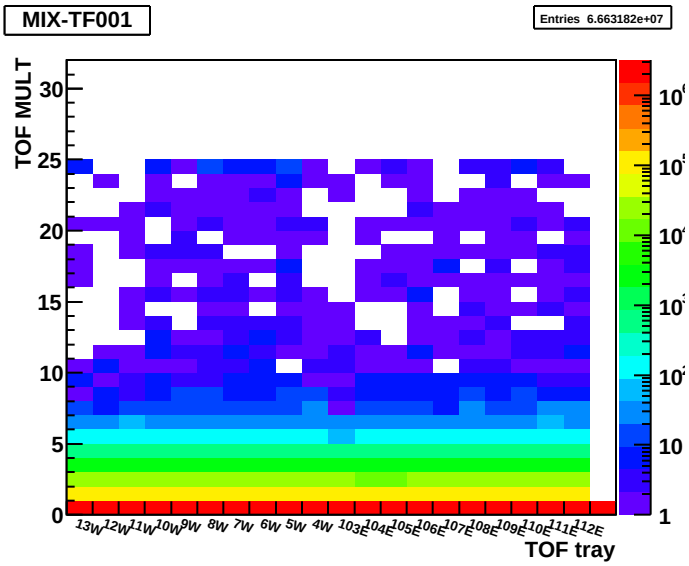

DAQ10k HT bits errors
Entries 0

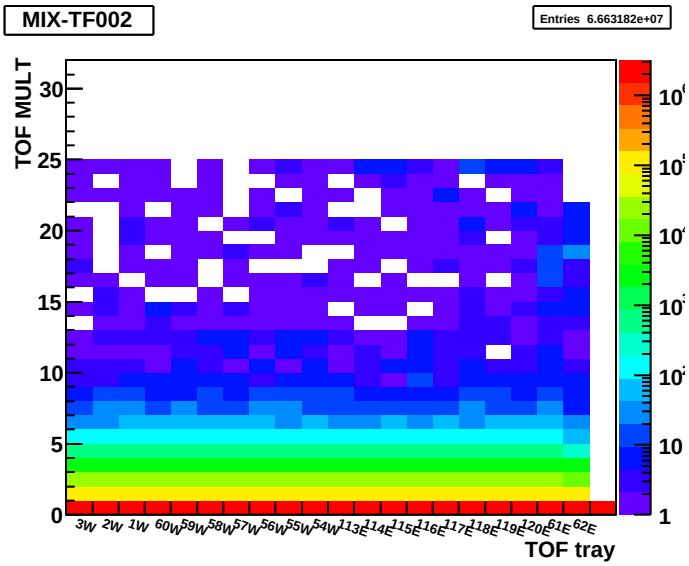

TPC Readout Mask (simulated)
Entries 422016

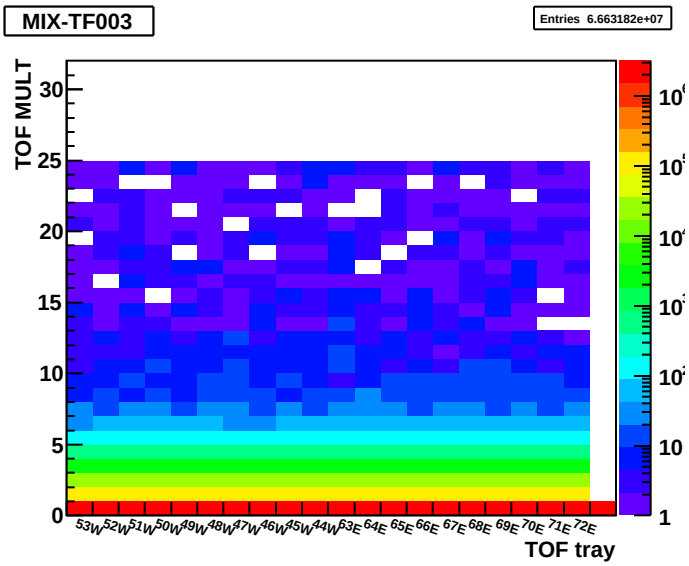

BunchId7Bit (all events)
Entries1586472

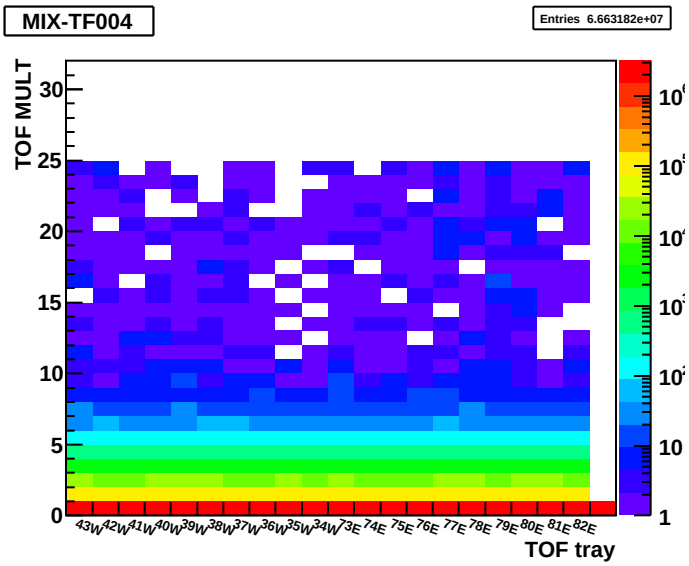

Abort-gaps
Entries 2401

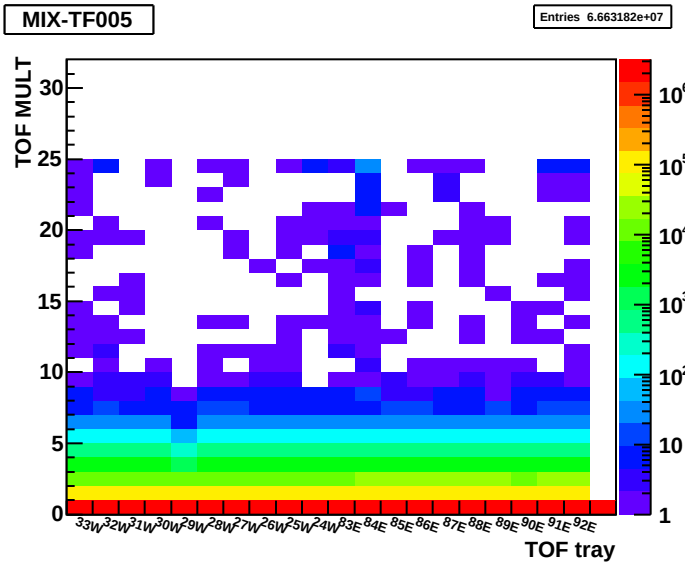

!Yellow-Fill
Entries 55085

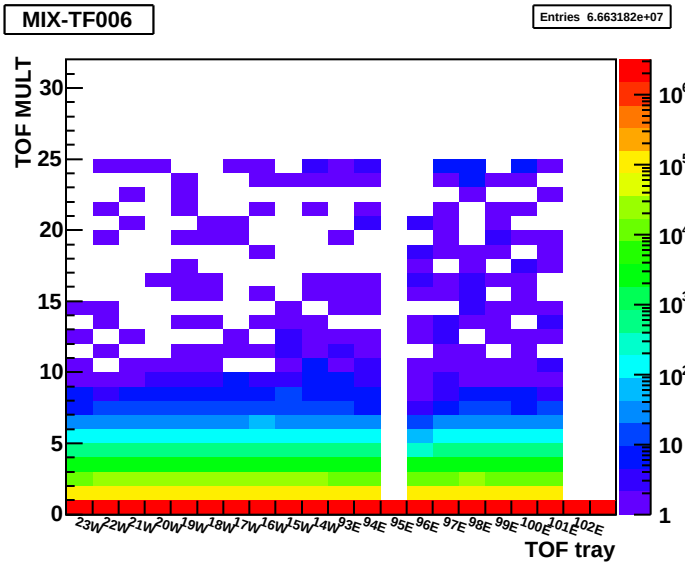

MIX-TF001
Entries 6.663182e+07
TOF MULT
TOF tray

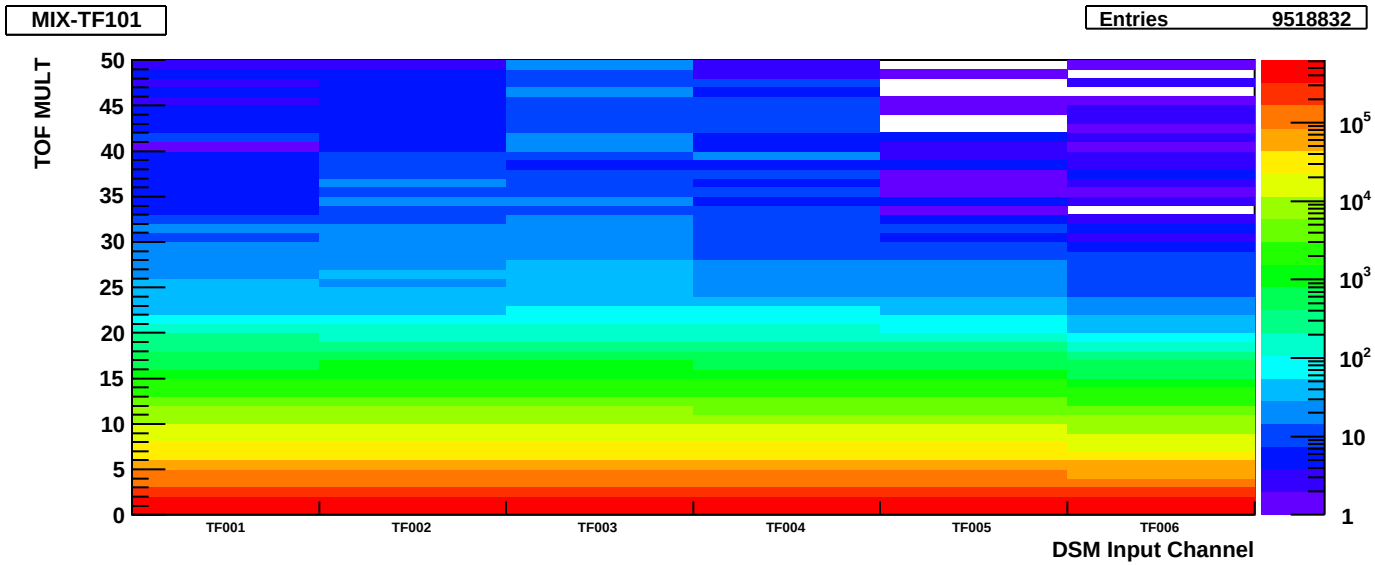

MIX-TF002
Entries 6.663182e+07
TOF MULT
TOF tray

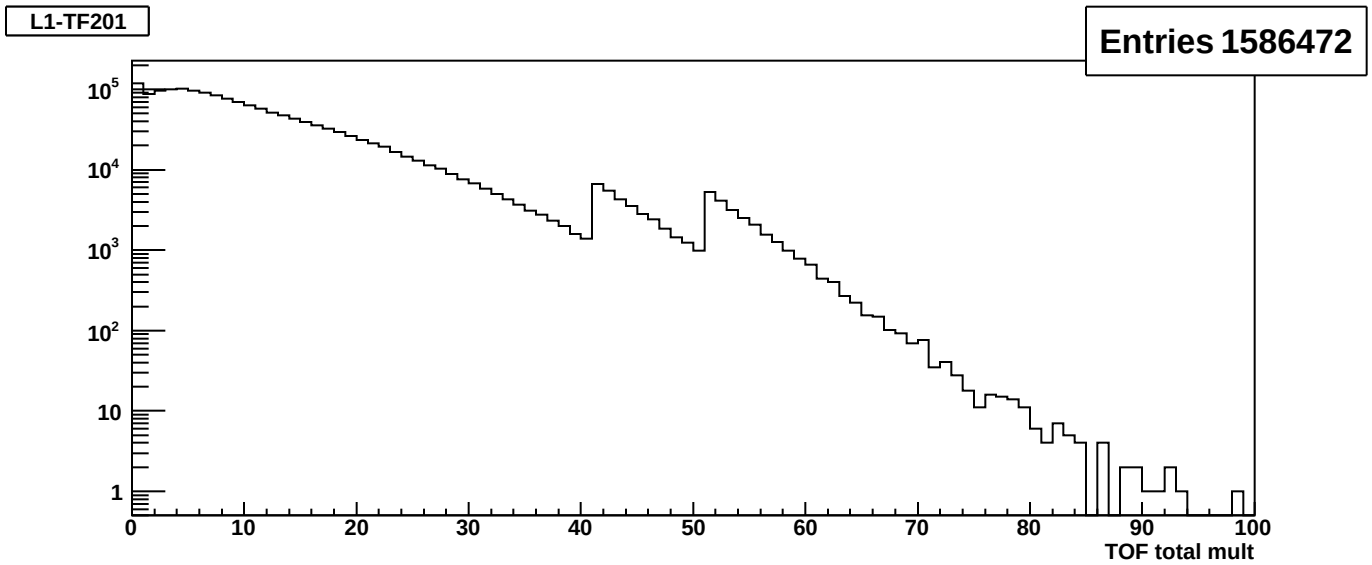

MIX-TF003
Entries 6.663182e+07
TOF MULT
TOF tray

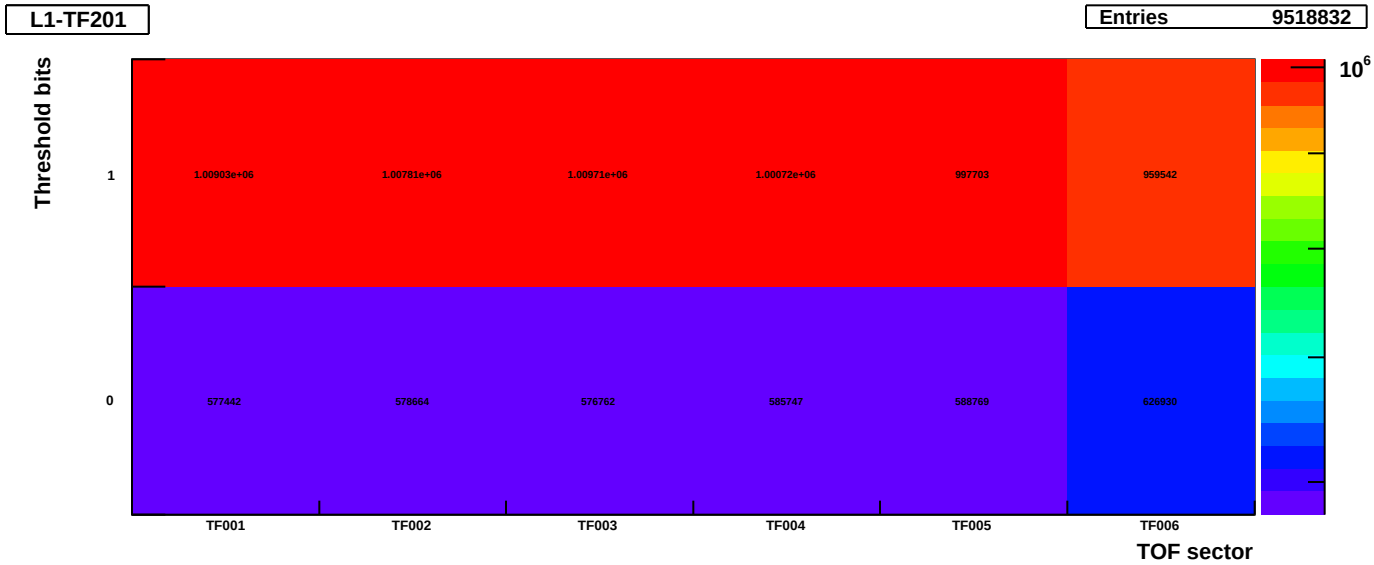

MIX-TF004
Entries 6.663182e+07
TOF MULT
TOF tray

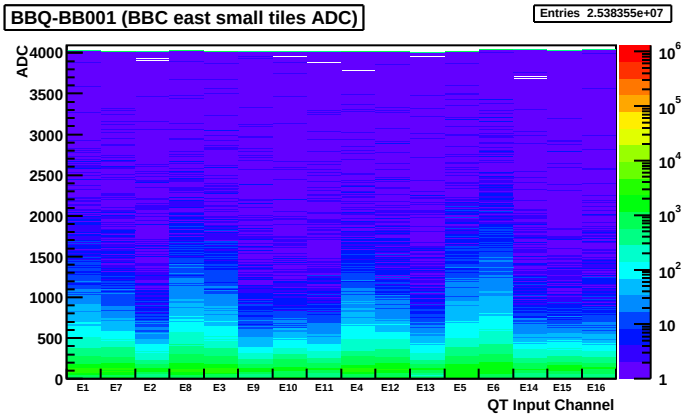

MIX-TF005
Entries 6.663182e+07
TOF MULT
TOF tray

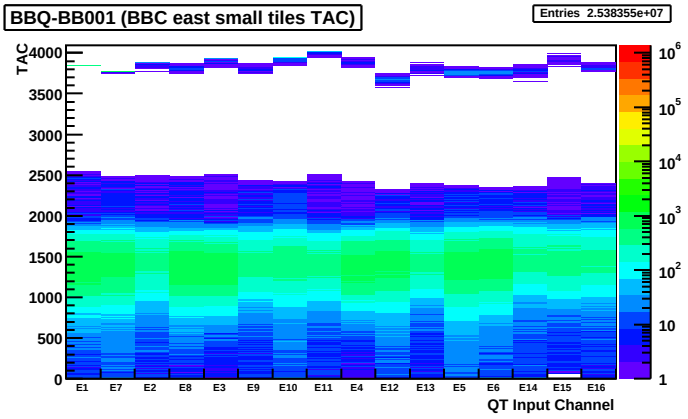

MIX-TF006
Entries 6.663182e+07
TOF MULT
TOF tray

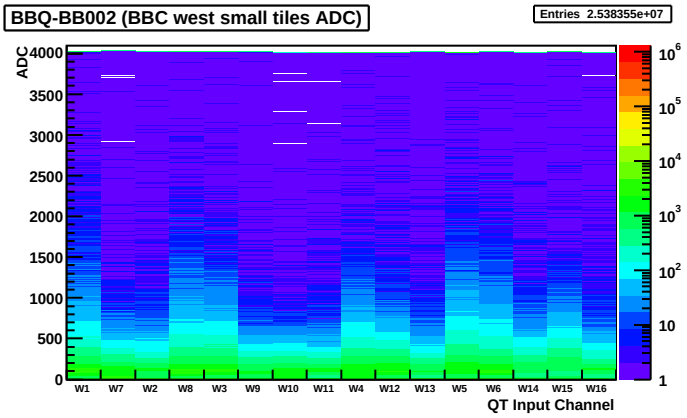

MIX-TF101
Entries 9518832
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

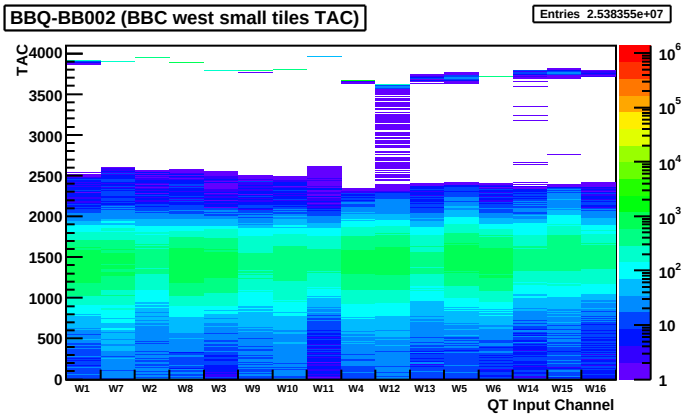

L1-TF201
Entries 1586472
TOF total mult

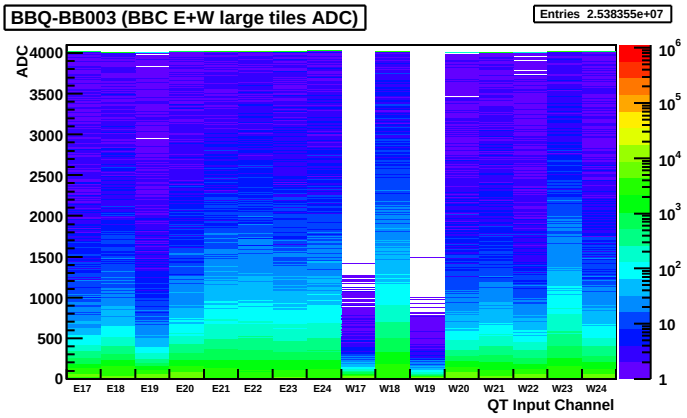

L1-TF201
Entries 9518832
Threshold bits
1.00903e+06
1.00781e+06
1.00971e+06
1.00072e+06
997703
959542
577442
578664
576762
585747
588769
626930
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

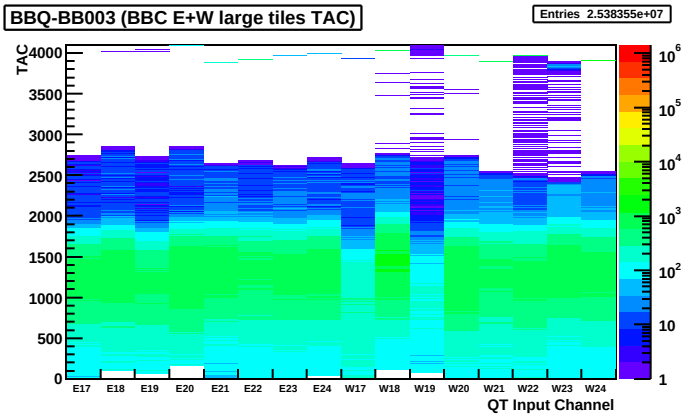

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.538355e+07
ADC
QT Input Channel

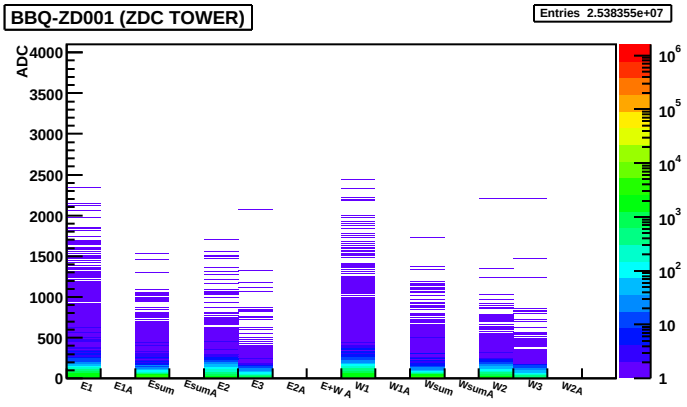

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.538355e+07
TAC
QT Input Channel

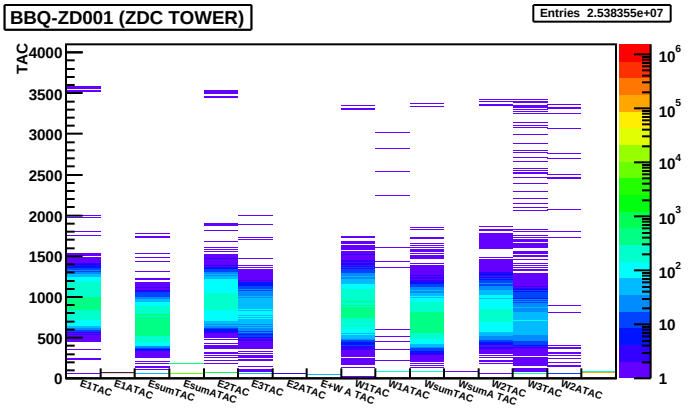

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.538355e+07
ADC
QT Input Channel

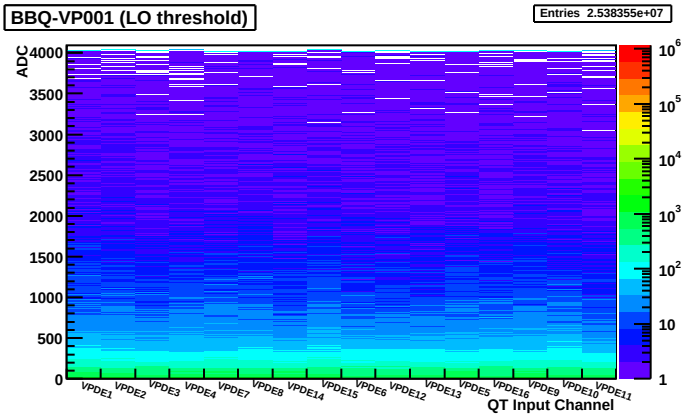

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.538355e+07
TAC
QT Input Channel

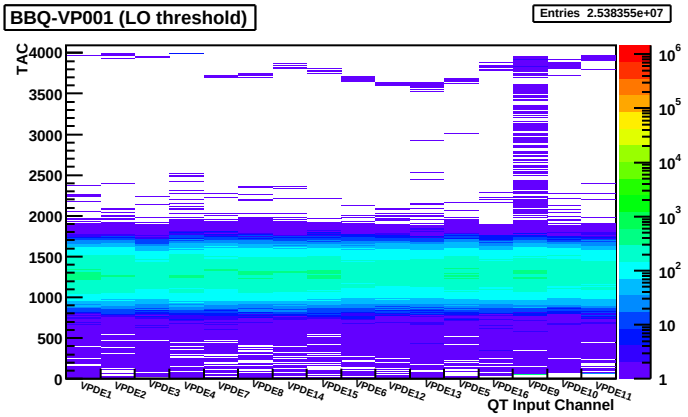

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.538355e+07
ADC
QT Input Channel

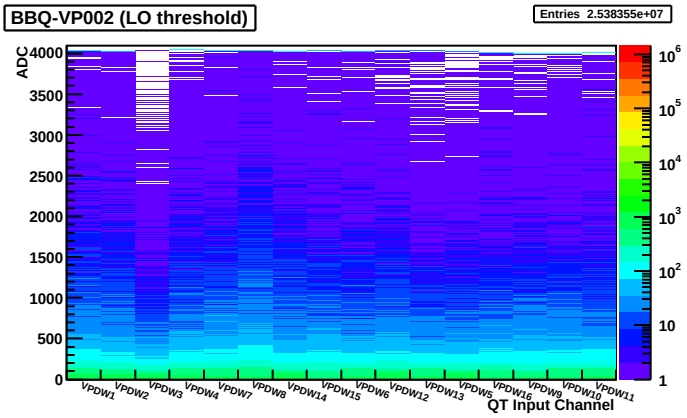

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.538355e+07
TAC
QT Input Channel

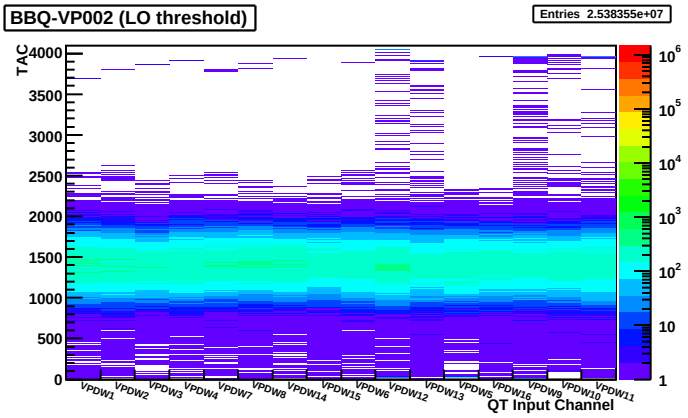

BBQ-ZD001 (ZDC TOWER)
Entries 2.538355e+07
ADC

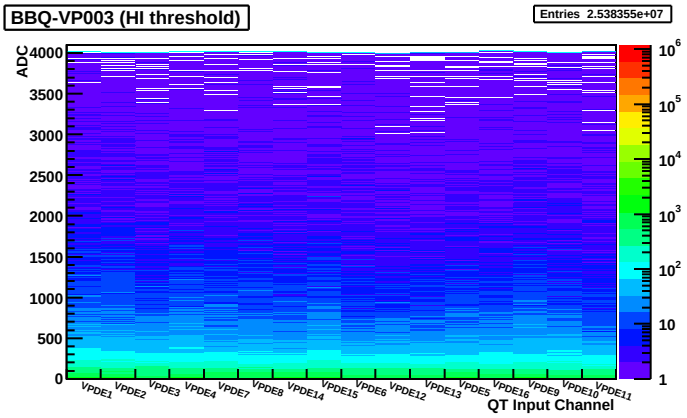

BBQ-ZD001 (ZDC TOWER)
Entries 2.538355e+07
TAC

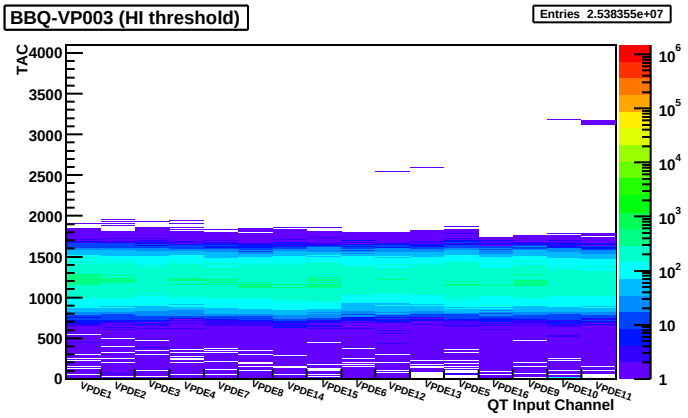

BBQ-VP001 (LO threshold)
Entries 2.538355e+07
ADC
QT Input Channel

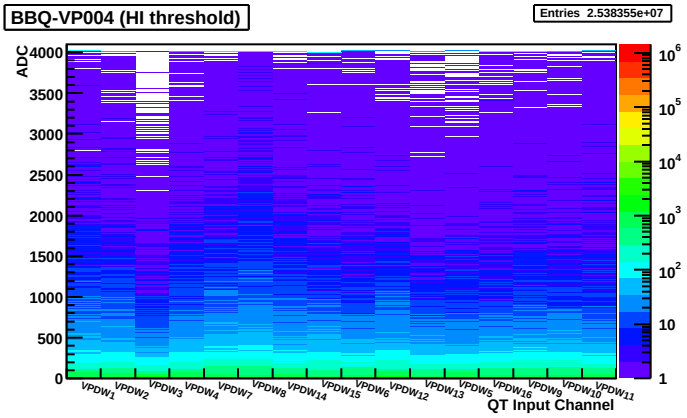

BBQ-VP001 (LO threshold)
Entries 2.538355e+07
TAC
QT Input Channel

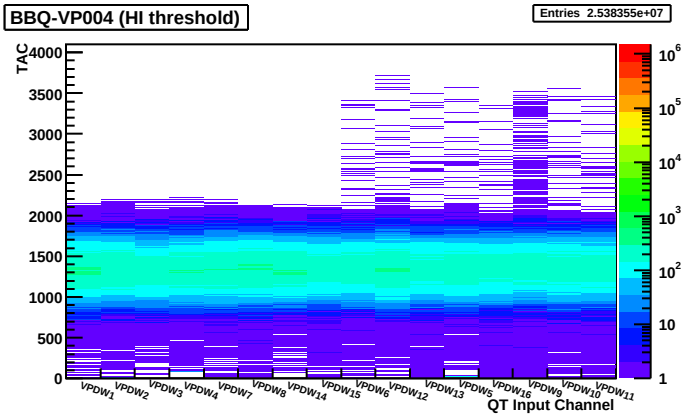

BBQ-VP002 (LO threshold)
Entries 2.538355e+07
ADC
QT Input Channel

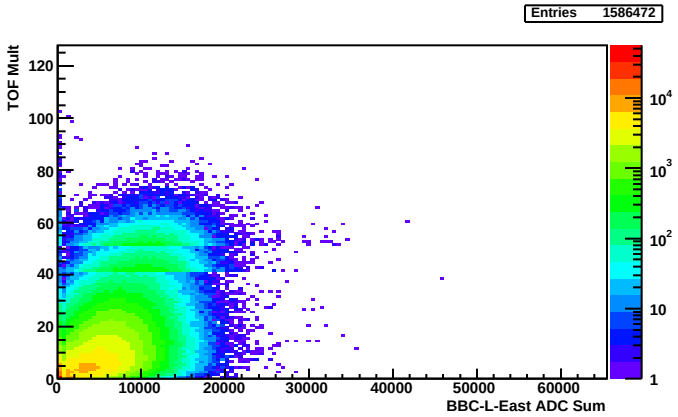

BBQ-VP002 (LO threshold)
Entries 2.538355e+07
TAC
QT Input Channel

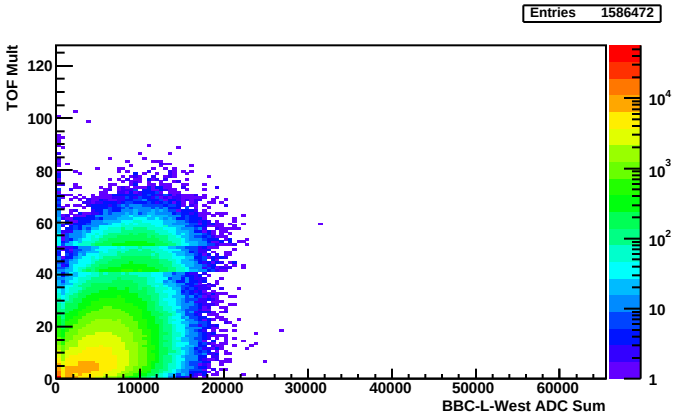

BBQ-VP003 (HI threshold)
Entries 2.538355e+07
ADC
QT Input Channel

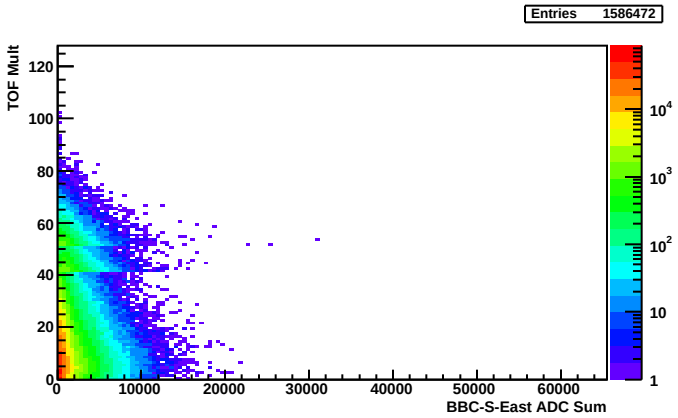

BBQ-VP003 (HI threshold)
Entries 2.538355e+07
TAC
QT Input Channel

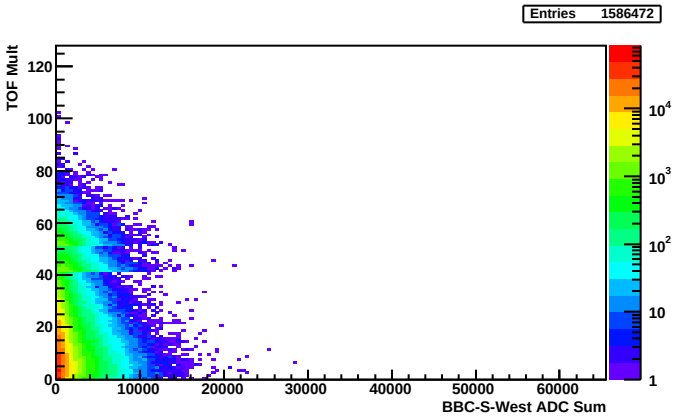

BBQ-VP004 (HI threshold)
Entries 2.538355e+07
ADC
QT Input Channel

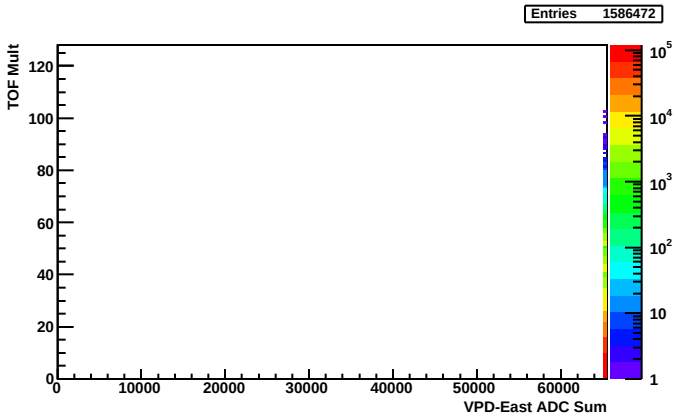

BBQ-VP004 (HI threshold)
Entries 2.538355e+07
TAC
QT Input Channel

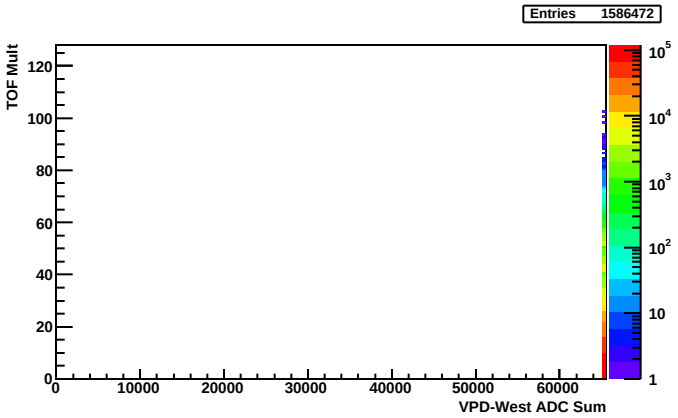

Entries 1586472
TOF Mult
BBC-L-East ADC Sum

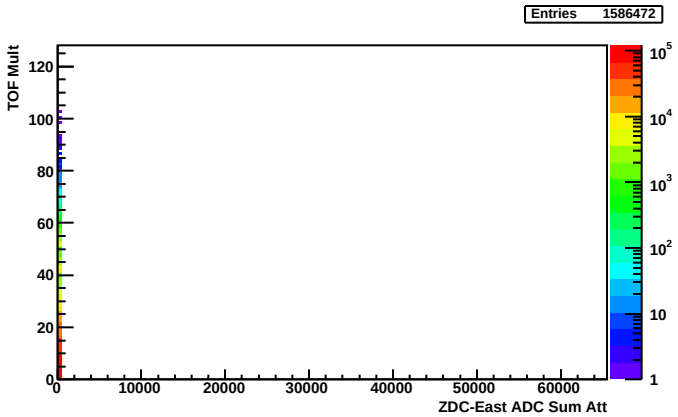

Entries 1586472
TOF Mult
BBC-L-West ADC Sum

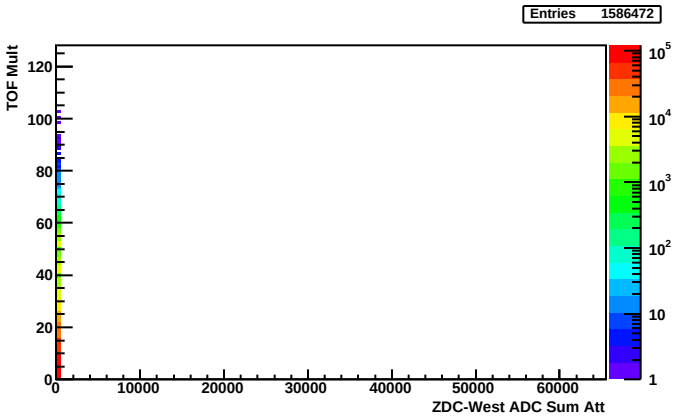

Entries 1586472
TOF Mult
BBC-S-East ADC Sum

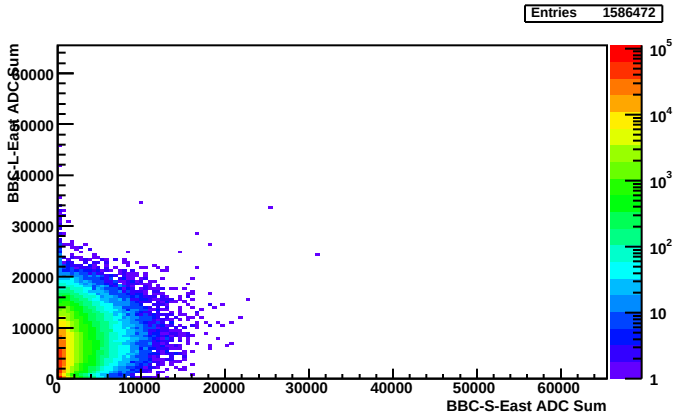

Entries 1586472
TOF Mult
BBC-S-West ADC Sum

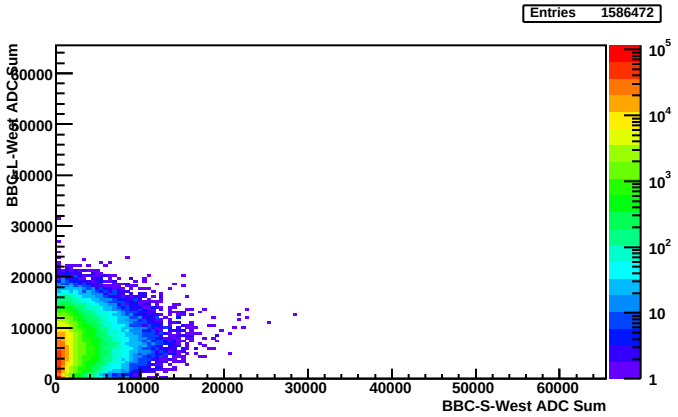

Entries 1586472
TOF Mult
VPD-East ADC Sum

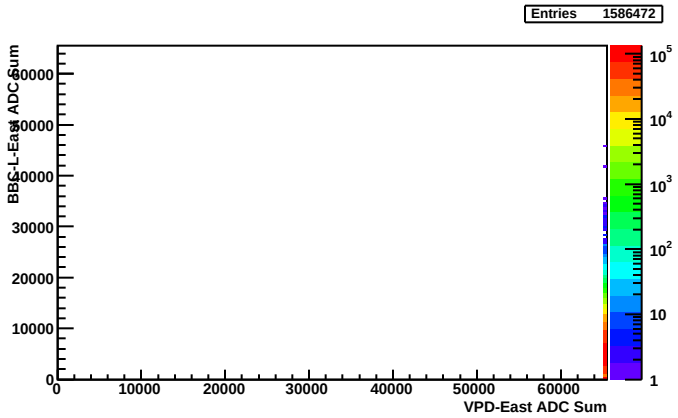

Entries 1586472
TOF Mult
VPD-West ADC Sum

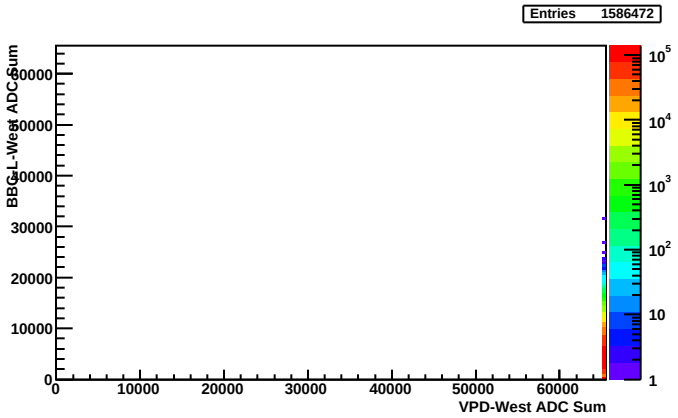

Entries 1586472
TOF Mult
ZDC-East ADC Sum Att

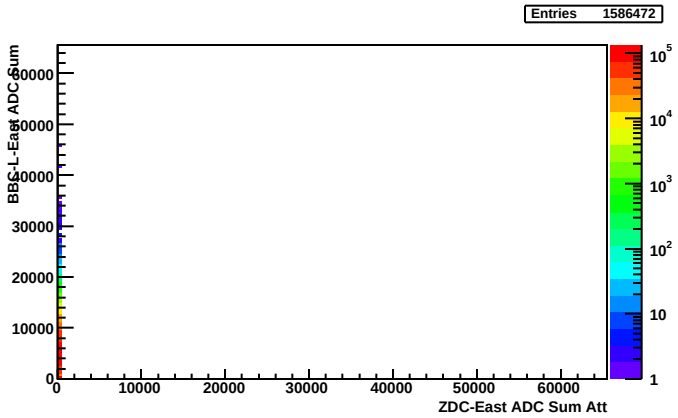

Entries 1586472
TOF Mult
ZDC-West ADC Sum Att

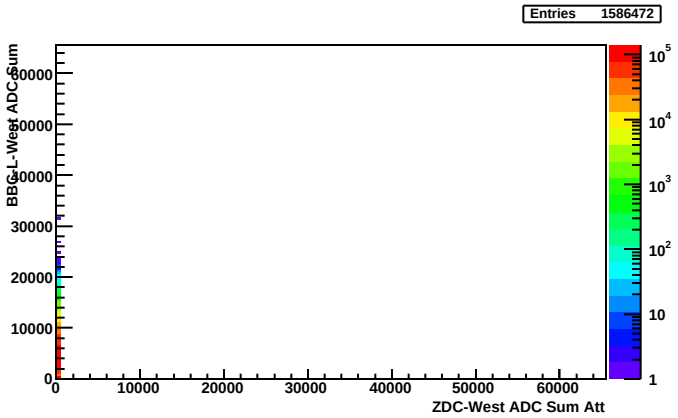

Entries 1586472
BBC-L-East ADC Sum
BBC-S-East ADC Sum

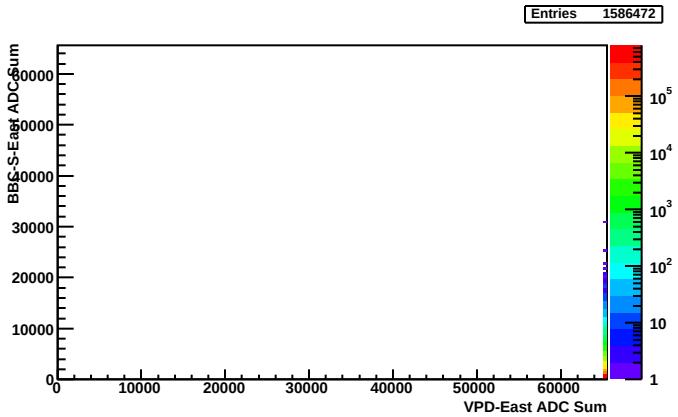

Entries 1586472
BBC-L-West ADC Sum
BBC-S-West ADC Sum

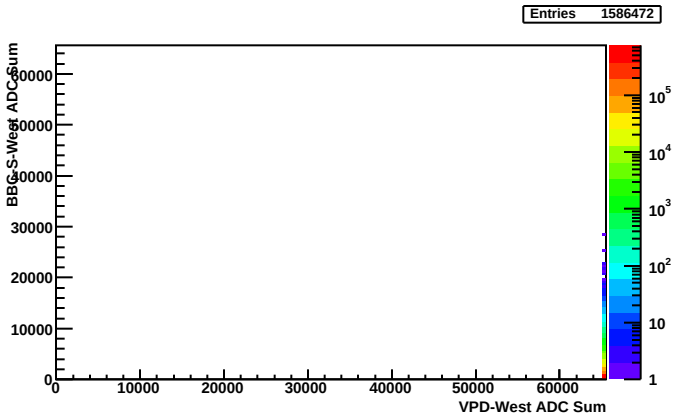

Entries 1586472
BBC-L-East ADC Sum
VPD-East ADC Sum

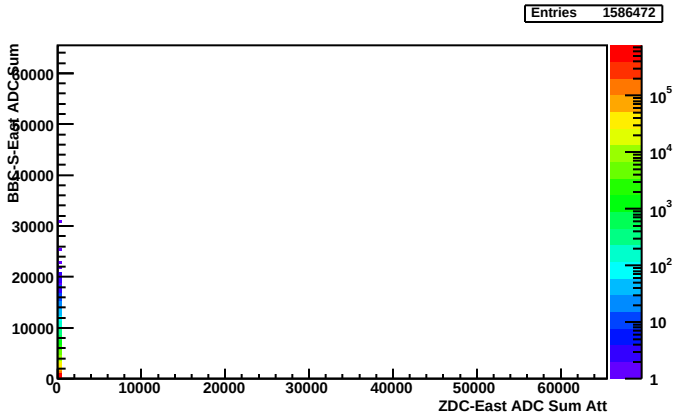

Entries 1586472
BBC-L-West ADC Sum
VPD-West ADC Sum

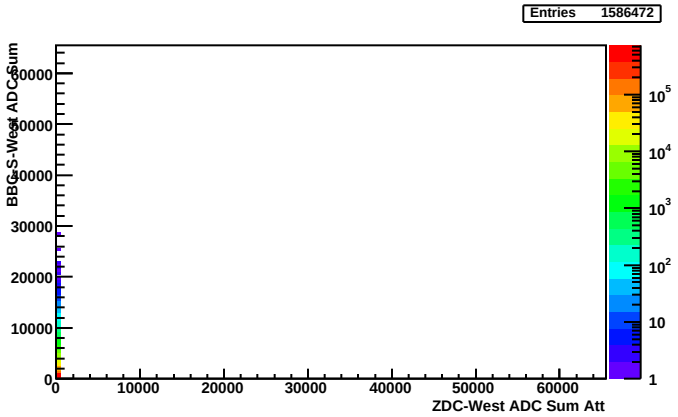

Entries 1586472
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

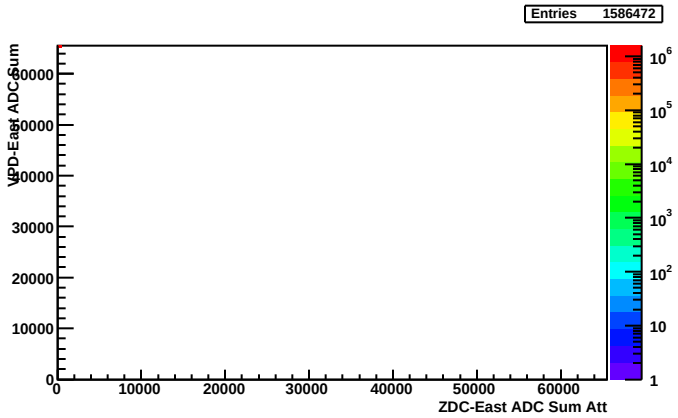

Entries 1586472
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

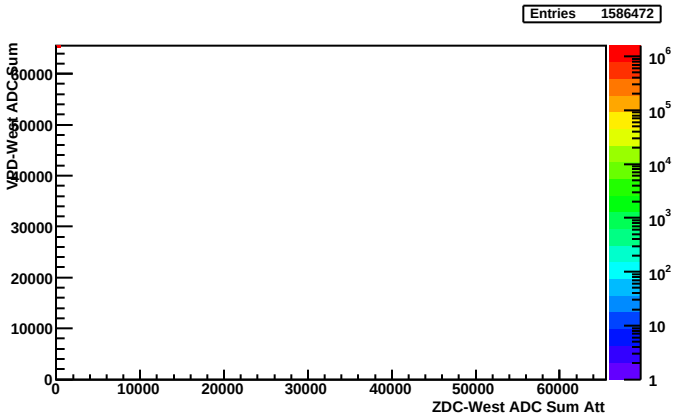

Entries 1586472
BBC-S-East ADC Sum
VPD-East ADC Sum

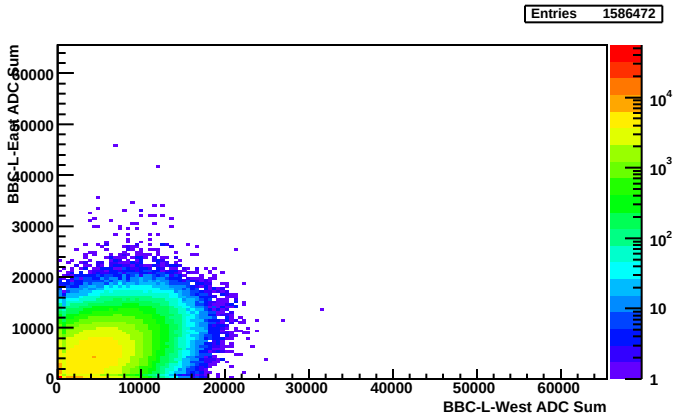

Entries 1586472
BBC-S-West ADC Sum
VPD-West ADC Sum

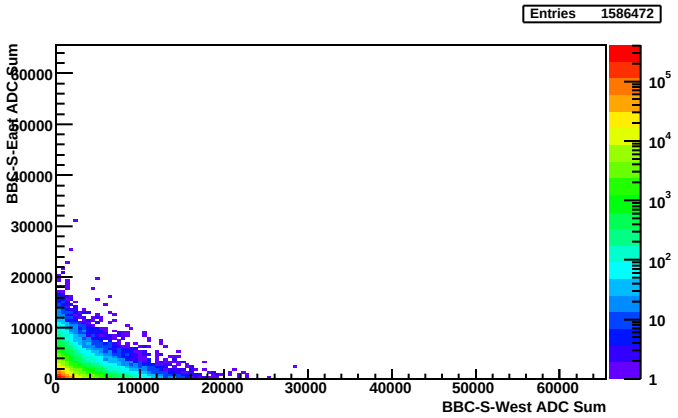

Entries 1586472
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

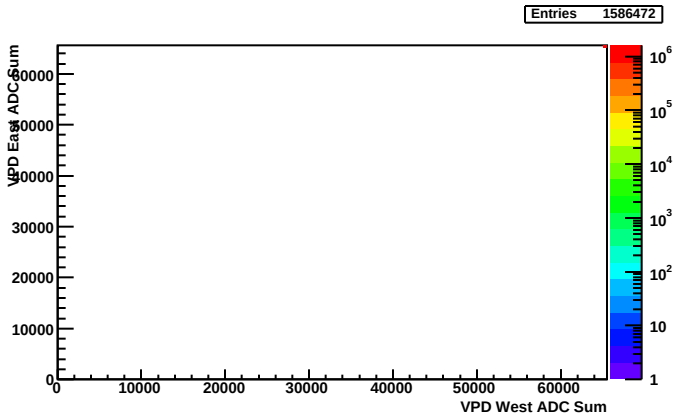

Entries 1586472
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

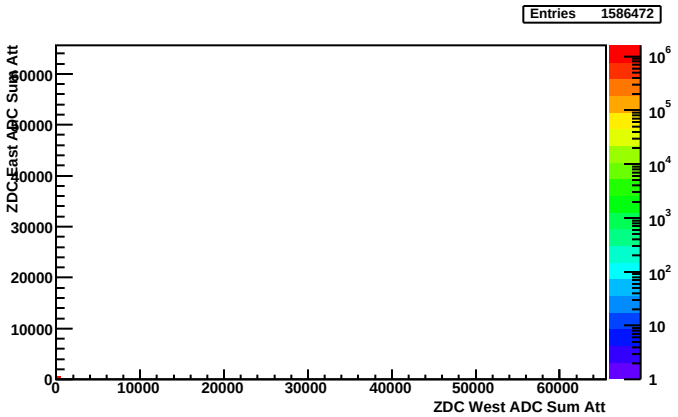

Entries 1586472
VPD-East ADC Sum
ZDC-East ADC Sum Att

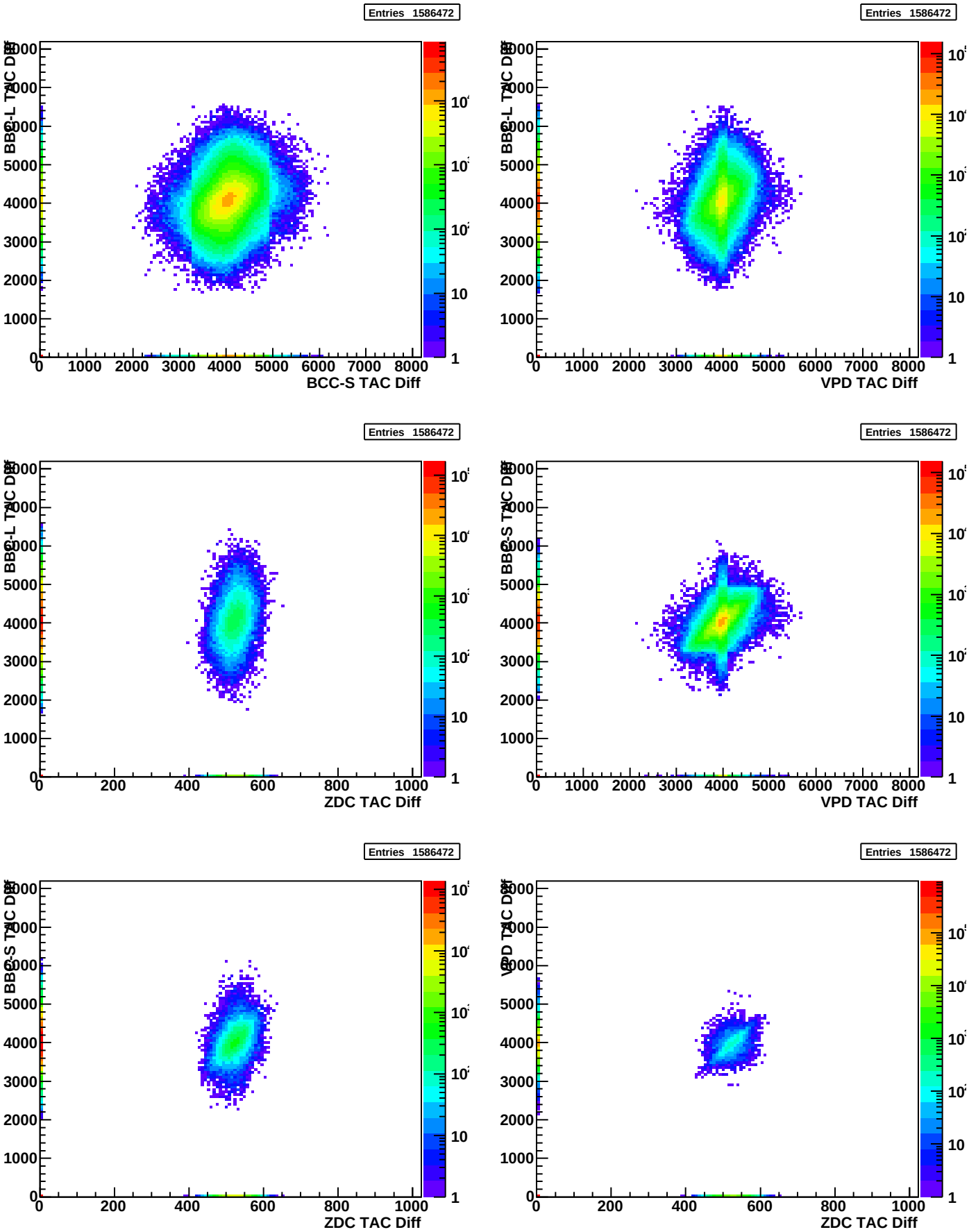

Entries 1586472
VPD-West ADC Sum
ZDC-West ADC Sum Att

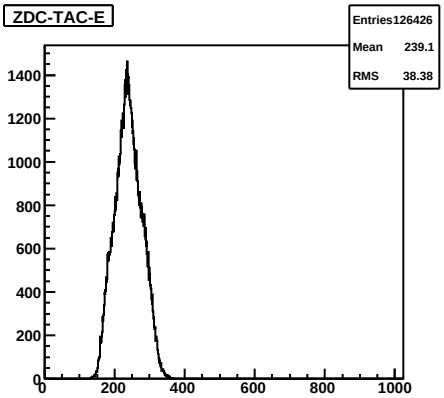

Entries 1586472
BBC-L-East ADC Sum
BBC-L-West ADC Sum

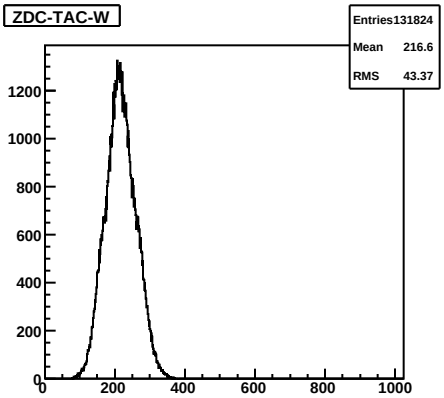

Entries 1586472
BBC-S-East ADC Sum
BBC-S-West ADC Sum

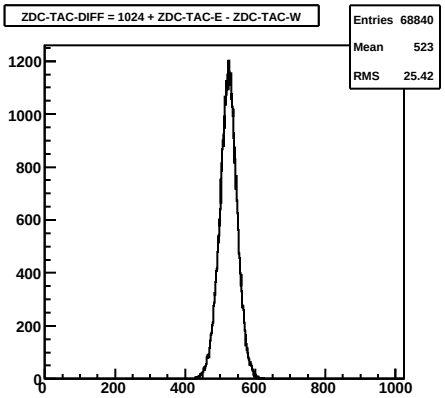

Entries 1586472
VPD East ADC Sum
VPD West ADC Sum

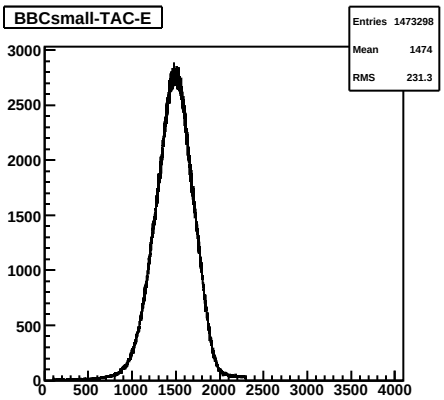

Entries 1586472
ZDC East ADC Sum Att
ZDC West ADC Sum Att

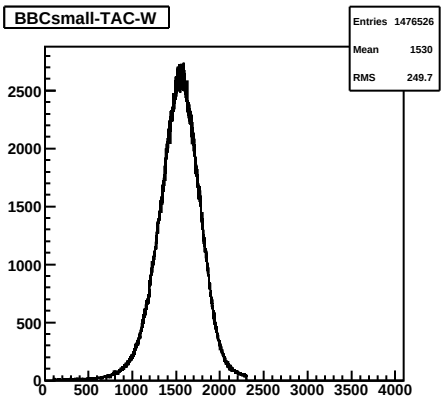

Entries 1586472
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1586472
BBC-L TAC Diff
VPD TAC Diff
Entries 1586472
BBC-L TAC Diff
ZDC TAC Diff
Entries 1586472
BBC-S TAC Diff
VPD TAC Diff
Entries 1586472
BBC-S TAC Diff
ZDC TAC Diff
Entries 1586472
VPD TAC Diff
ZDC TAC Diff

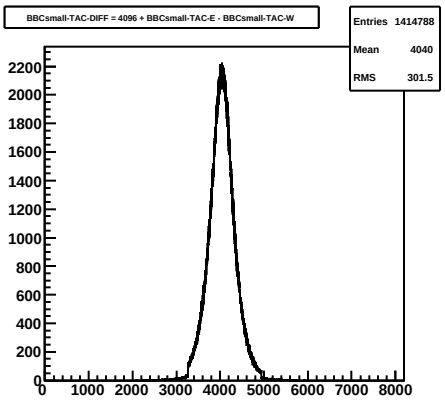

ZDC-TAC-E
Entries126426
Mean 239.1
RMS 38.38

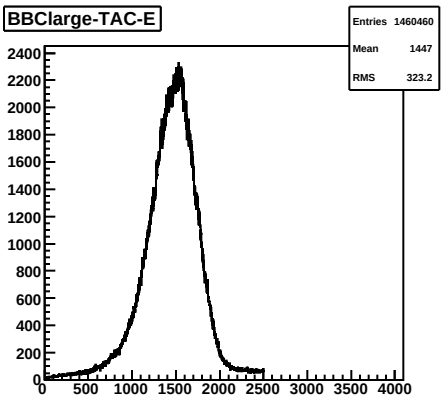

ZDC-TAC-W
Entries131824
Mean 216.6
RMS 43.37

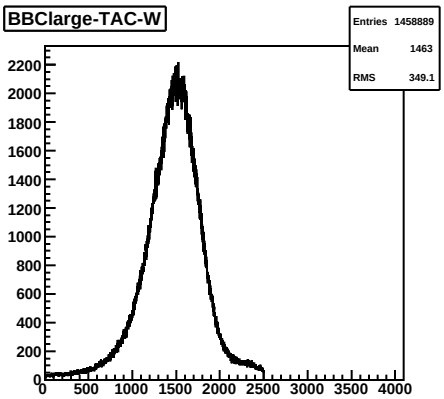

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 68840
Mean 523
RMS 25.42

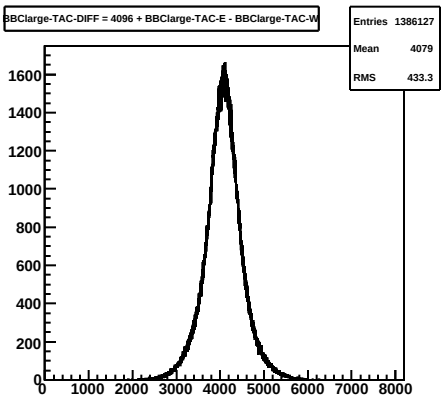

BBCsmall-TAC-E
Entries 1473298
Mean 1474
RMS 231.3

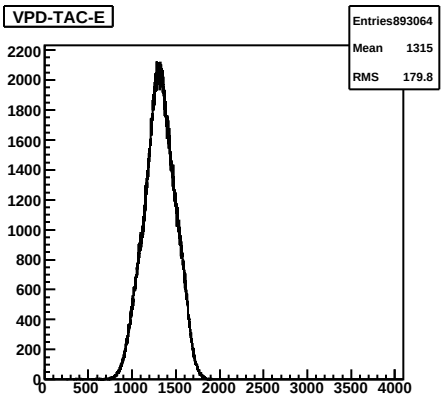

BBCsmall-TAC-W
Entries 1476526
Mean 1530
RMS 249.7

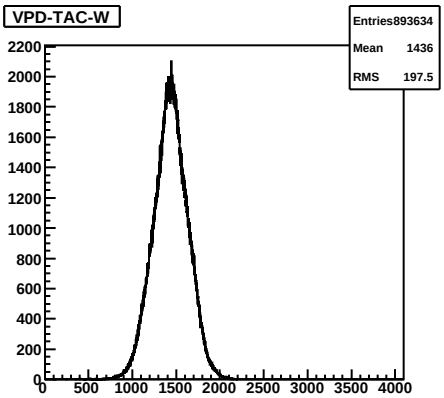

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1414788
Mean 4040
RMS 301.5

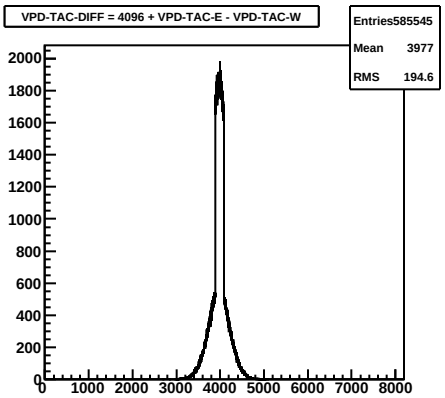

BBClarge-TAC-E
Entries 1460460
Mean 1447
RMS 323.2

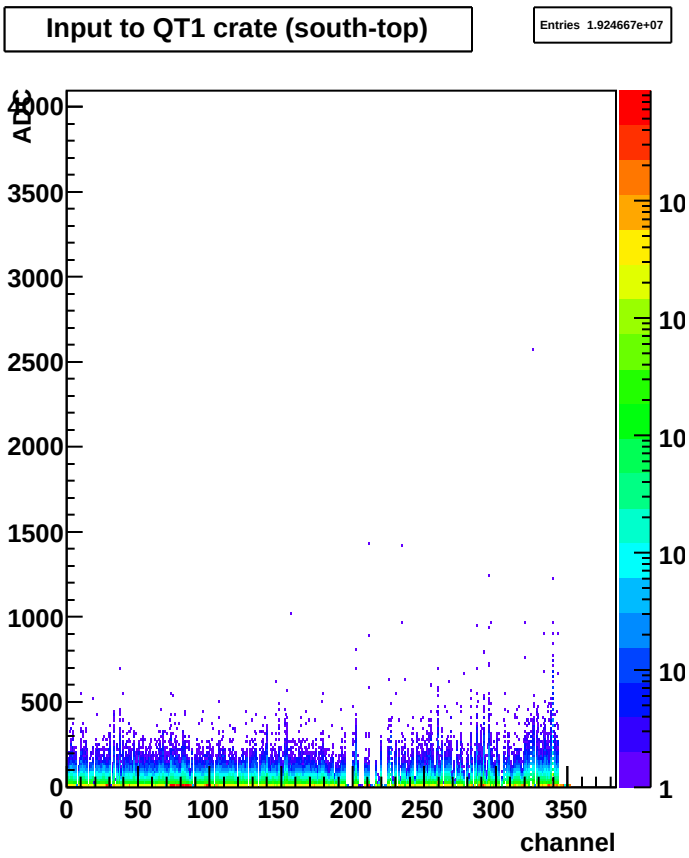

BBClarge-TAC-W
Entries 1458889
Mean 1463
RMS 349.1

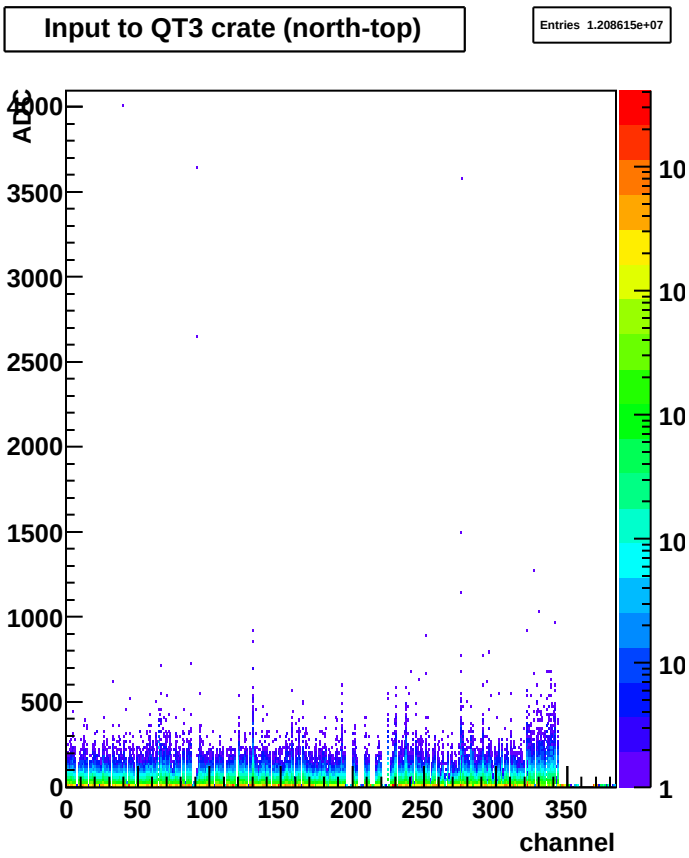

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1386127
Mean 4079
RMS 433.3

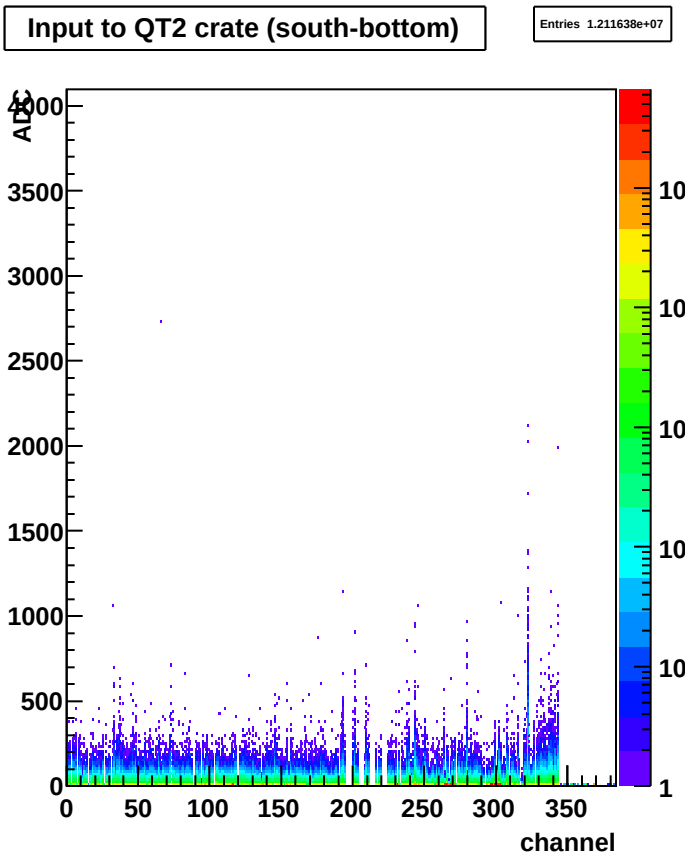

VPD-TAC-E
Entries893064
Mean 1315
RMS 179.8

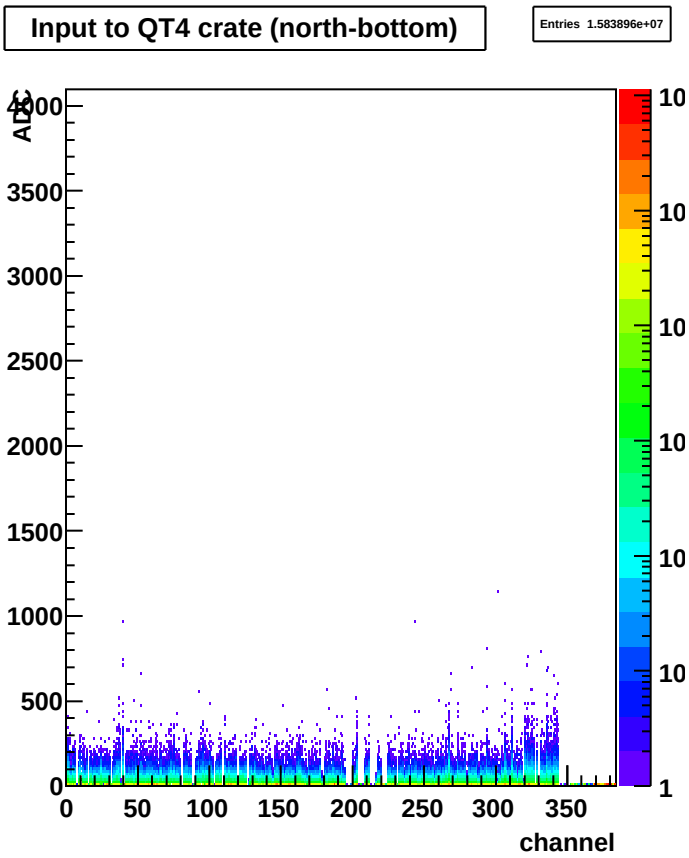

VPD-TAC-W
Entries893634
Mean 1436
RMS 197.5

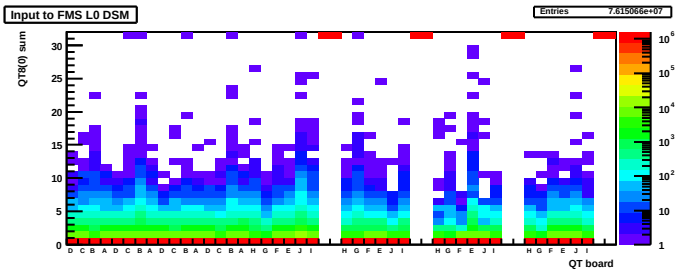

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries585545
Mean 3977
RMS 194.6

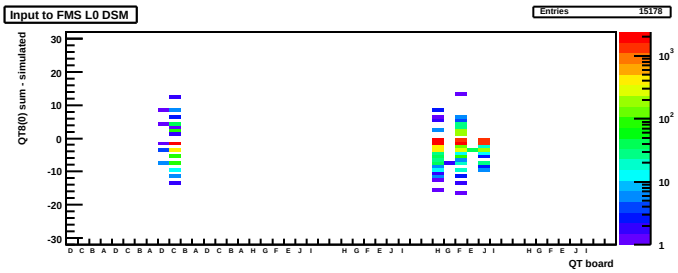

Input to QT1 crate (south-top)
Entries 1.924667e+07
ADC
channel

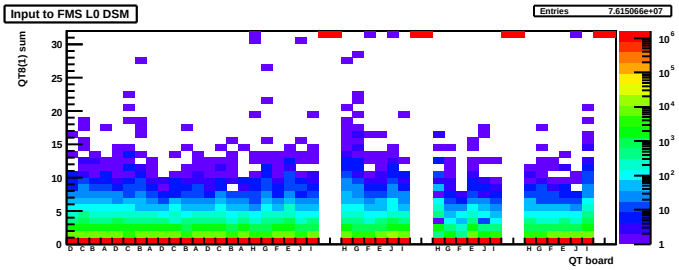

Input to QT3 crate (north-top)
Entries 1.208615e+07
ADC
channel

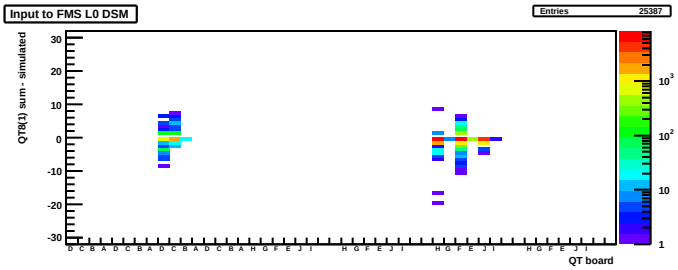

Input to QT2 crate (south-bottom)
Entries 1.211638e+07
ADC
channel

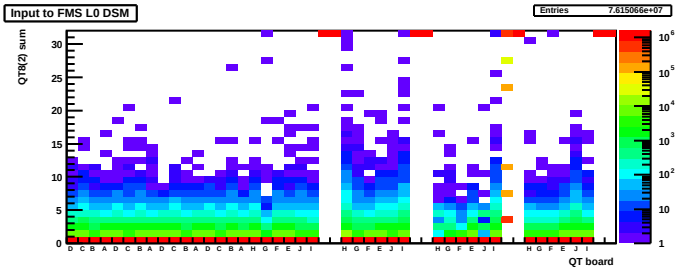

Input to QT4 crate (north-bottom)
Entries 1.583896e+07
ADC
channel

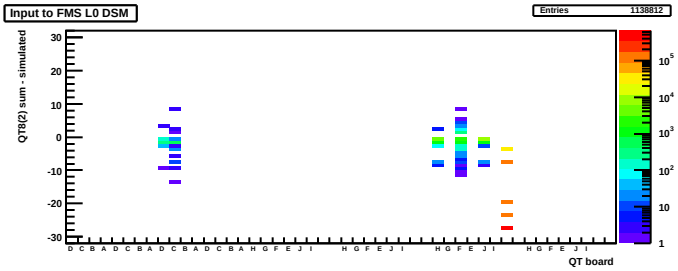

Input to FMS L0 DSM
Entries 7.615066e+07
QT8(0) sum
QT board

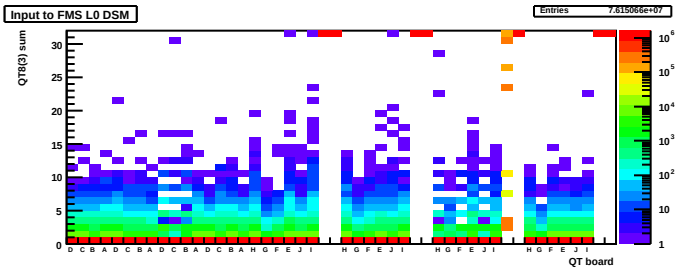

Input to FMS L0 DSM
Entries 15178
QT8(0) sum - simulated
QT board

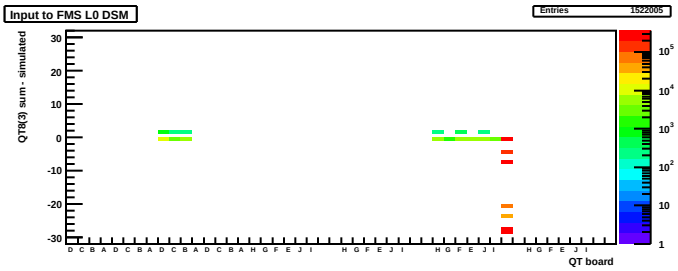

Input to FMS L0 DSM
Entries 7.615066e+07
QT8(1) sum
QT board

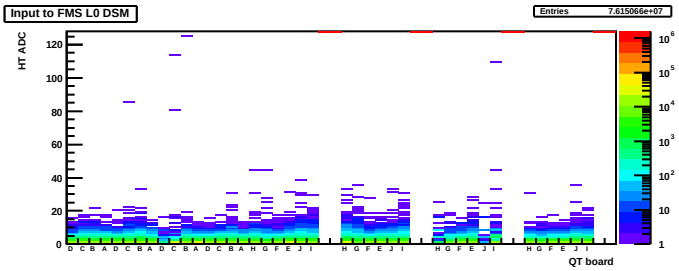

Input to FMS L0 DSM
Entries 25387
QT8(1) sum - simulated
QT board

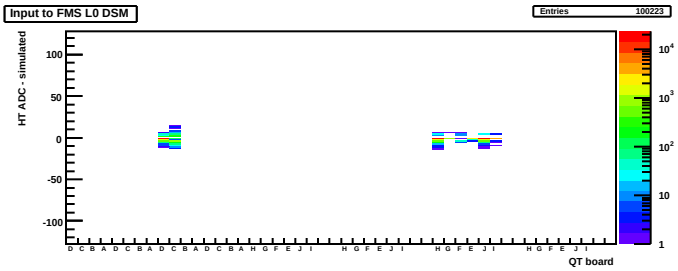

Input to FMS L0 DSM
Entries 7.615066e+07
QT8(2) sum
QT board

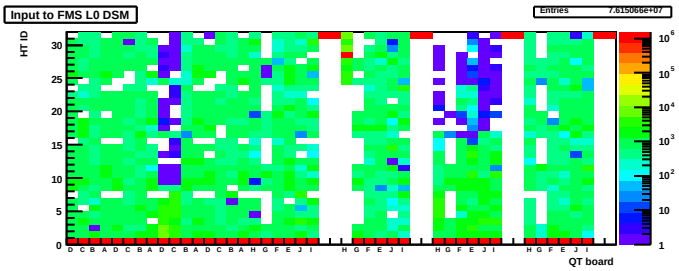

Input to FMS L0 DSM
Entries 1138812
QT8(2) sum - simulated
QT board

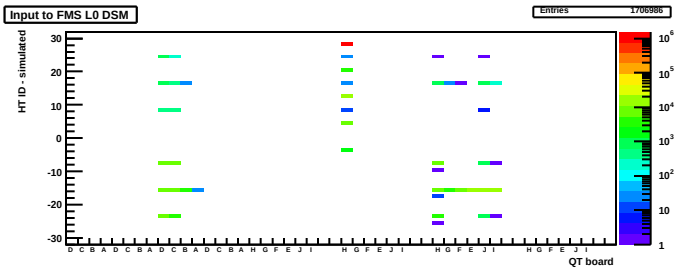

Input to FMS L0 DSM
Entries 7.615066e+07
QT8(3) sum
QT board

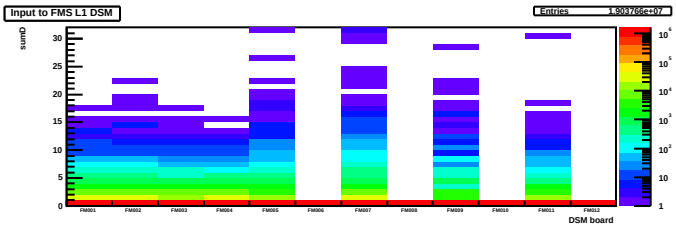

Input to FMS L0 DSM
Entries 1522005
QT8(3) sum - simulated
QT board

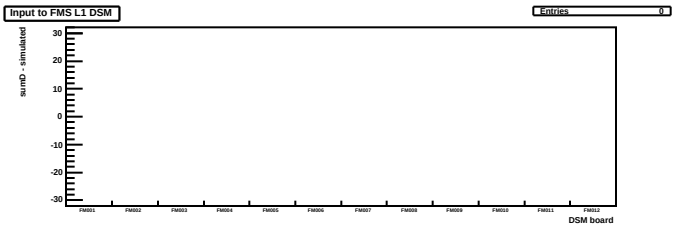

Input to FMS L0 DSM
Entries 7.615066e+07
HT ADC
QT board

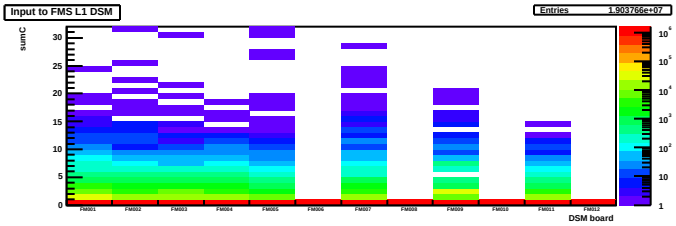

Input to FMS L0 DSM
Entries 100223
HT ADC - simulated
QT board

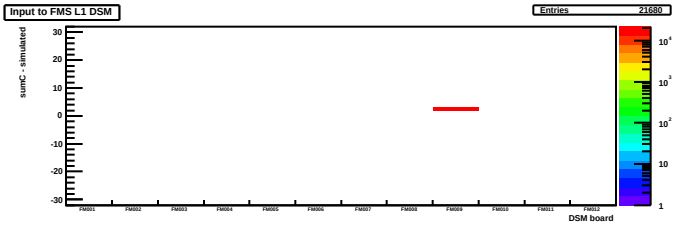

Input to FMS L0 DSM
Entries 7.615066e+07
HT ID
QT board

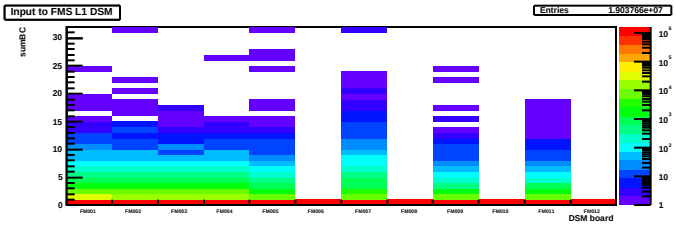

Input to FMS L0 DSM
Entries 1706986
HT ID - simulated
QT board

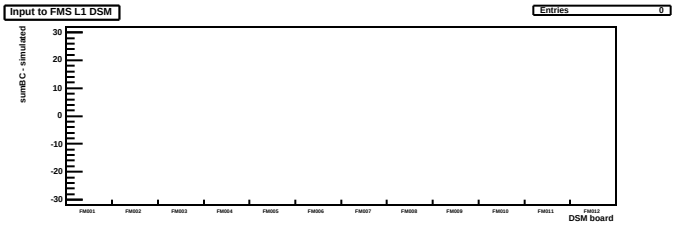

Input to FMS L1 DSM
Entries 1.903766e+07
sumD
DSM board

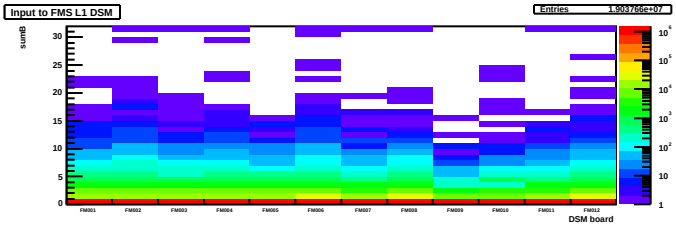

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

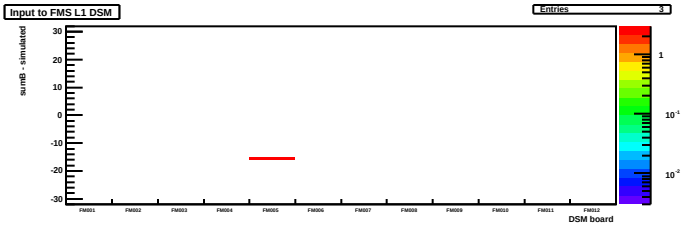

Input to FMS L1 DSM
Entries 1.903766e+07
sumC
DSM board

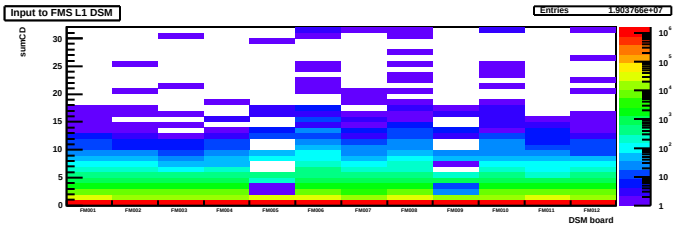

Input to FMS L1 DSM
Entries 21680
sumC - simulated
DSM board

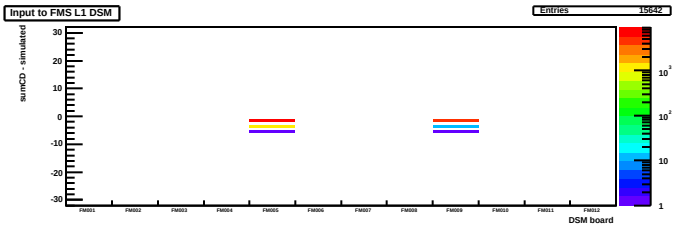

Input to FMS L1 DSM
Entries 1.903766e+07
sumBC
DSM board

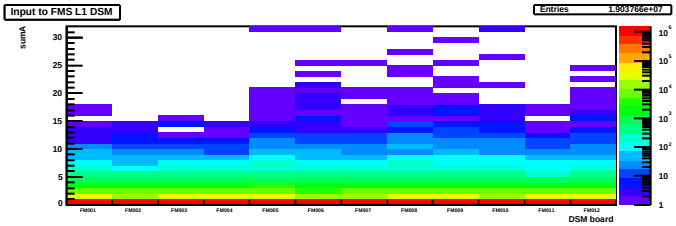

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

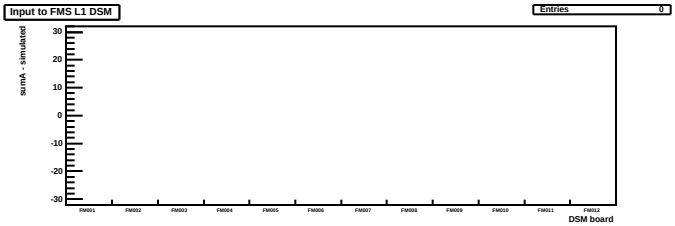

Input to FMS L1 DSM
Entries 1.903766e+07
sumB
DSM board

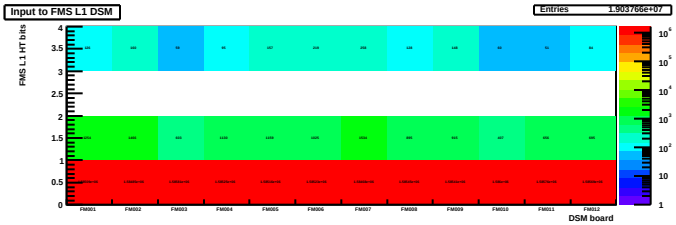

Input to FMS L1 DSM
Entries 3
sumB - simulated
DSM board

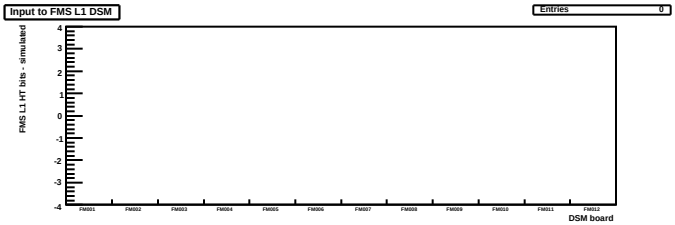

Input to FMS L1 DSM
Entries 1.903766e+07
sumCD
DSM board

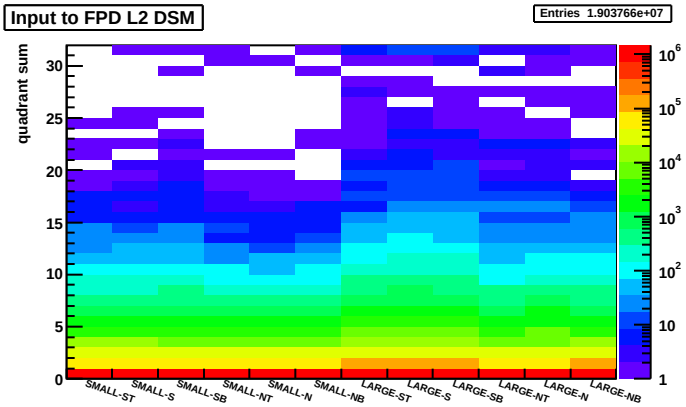

Input to FMS L1 DSM
Entries 15642
sumCD - simulated
DSM board

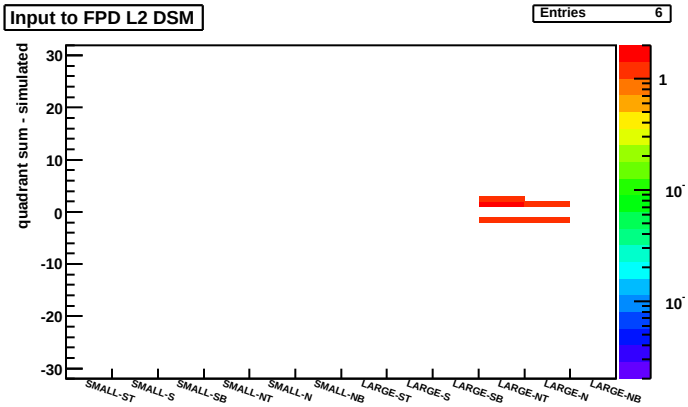

Input to FMS L1 DSM
Entries 1.903766e+07
sumA
DSM board

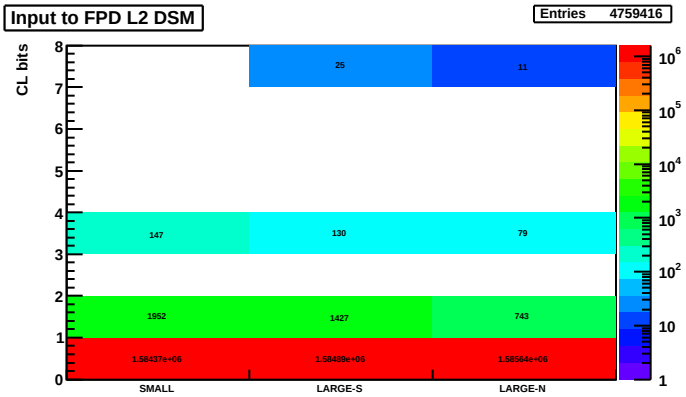

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

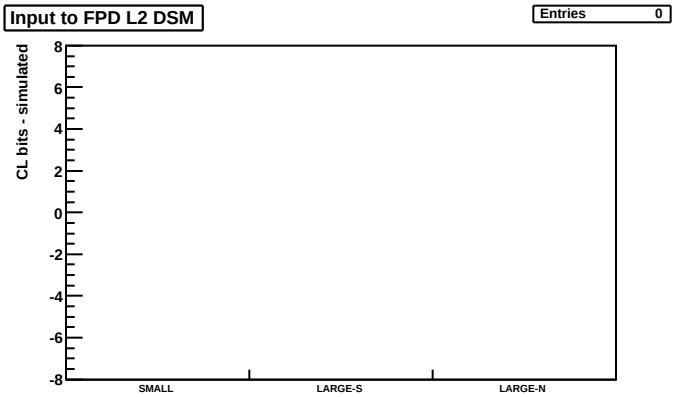

Input to FMS L1 DSM
Entries 1.903766e+07
FMS L1 HT bits
DSM board

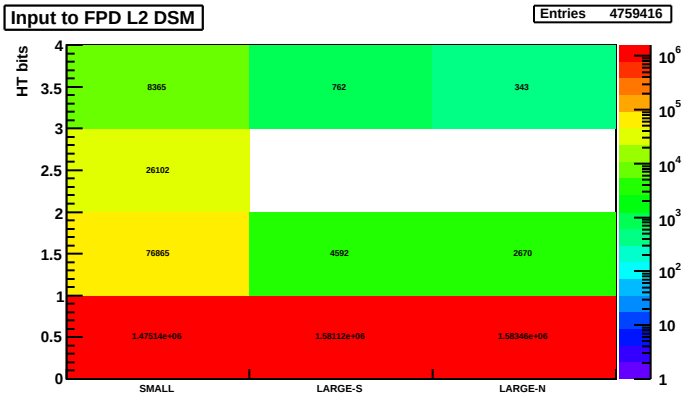

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

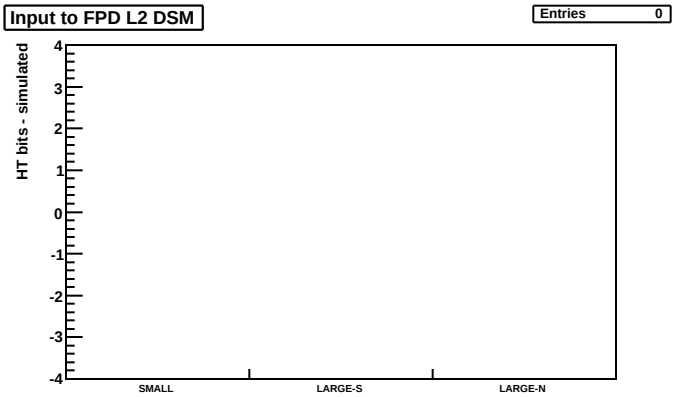

Input to FPD L2 DSM
Entries 1.903766e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

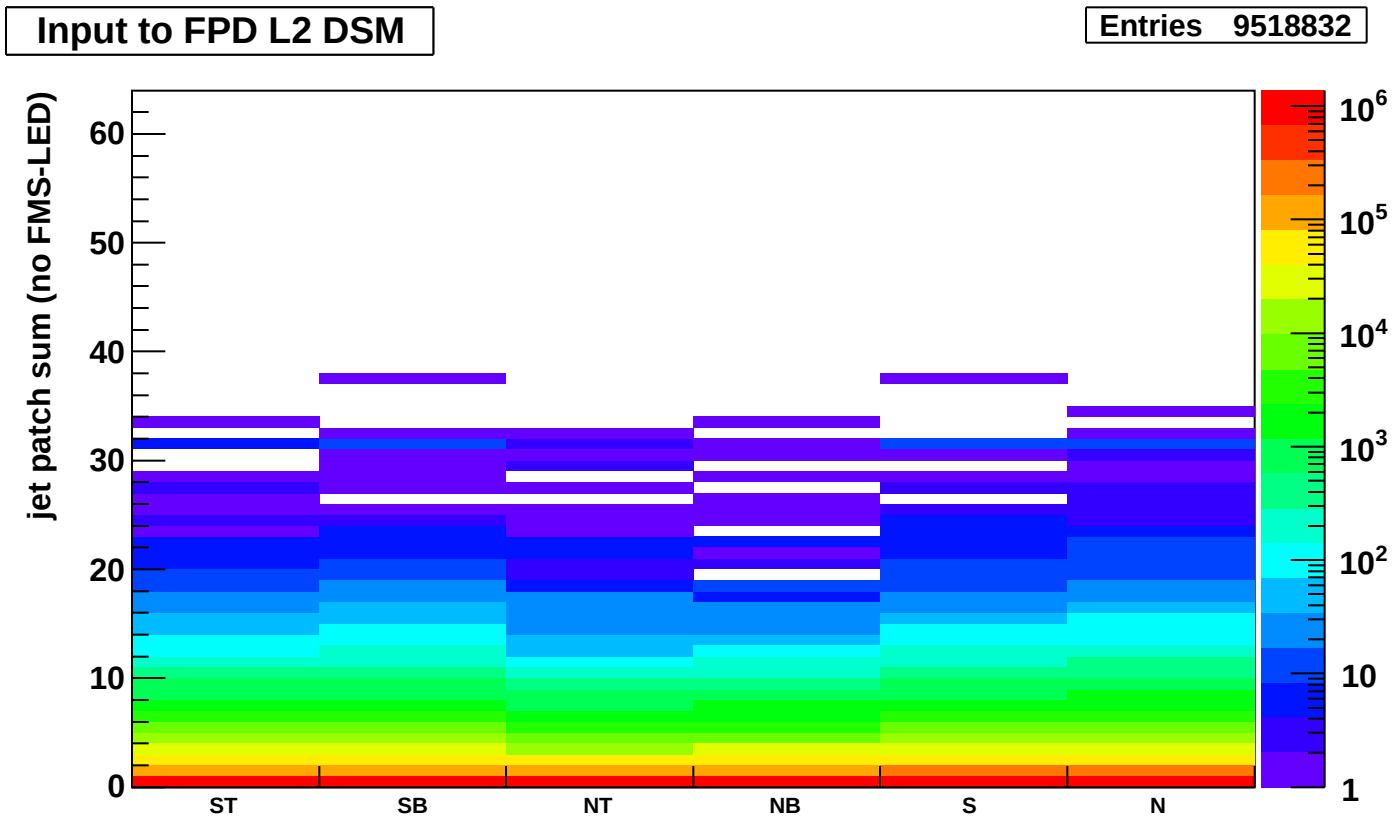

Input to FPD L2 DSM
Entries 6
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

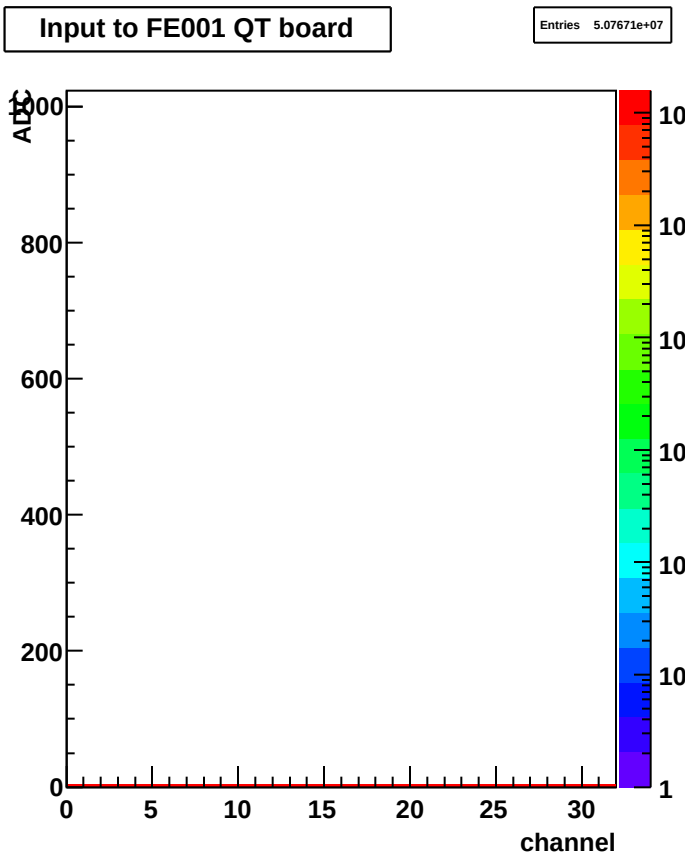

Input to FPD L2 DSM
Entries 4759416
CL bits
25
11
147
130
79
1952
1427
743
1.58437e+06
1.58489e+06
1.58564e+06
SMALL
LARGE-S
LARGE-N

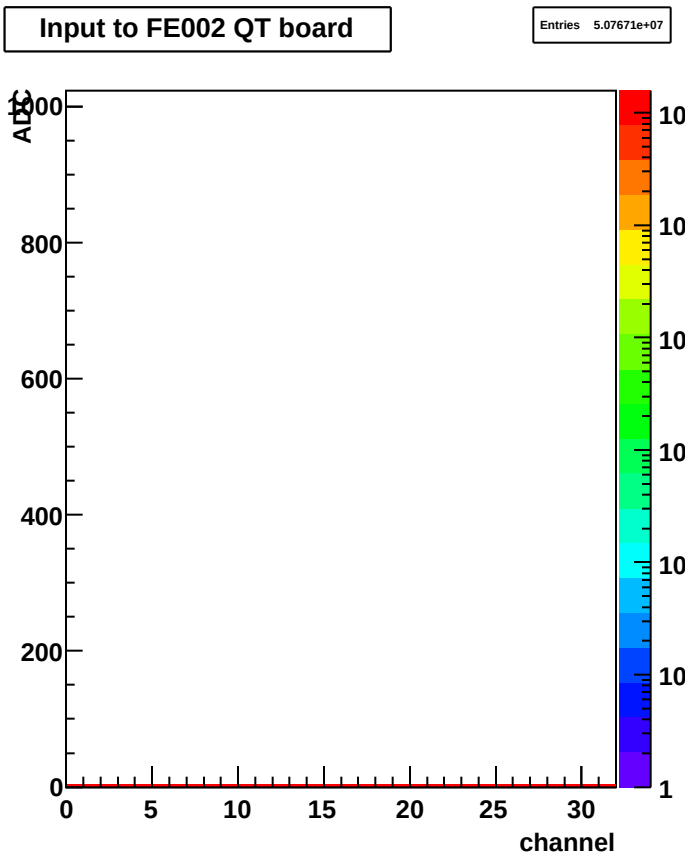

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

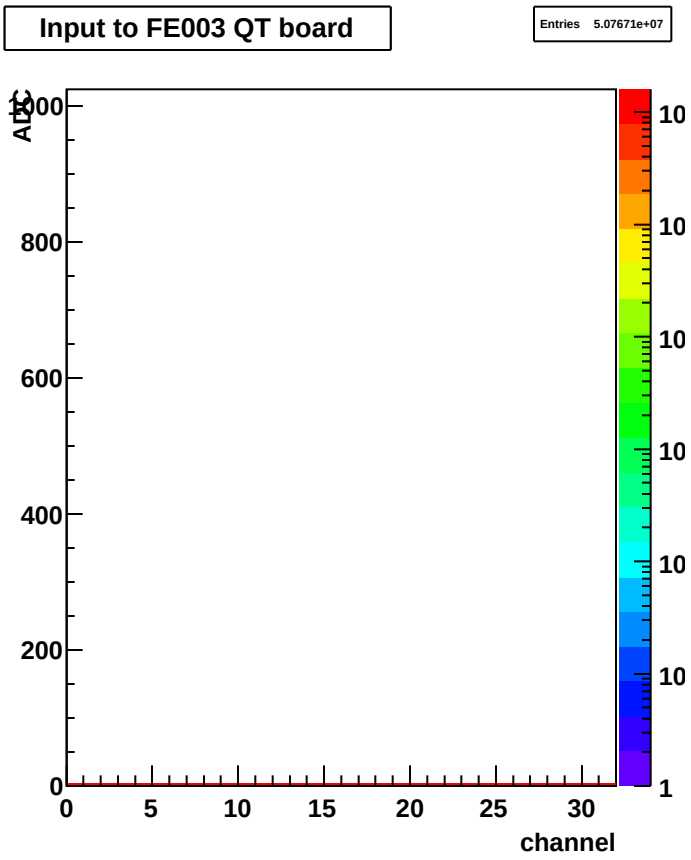

Input to FPD L2 DSM
Entries 4759416
HT bits
8365
762
343
26102
76865
4592
2670
1.47514e+06
1.58112e+06
1.58346e+06
SMALL
LARGE-S
LARGE-N

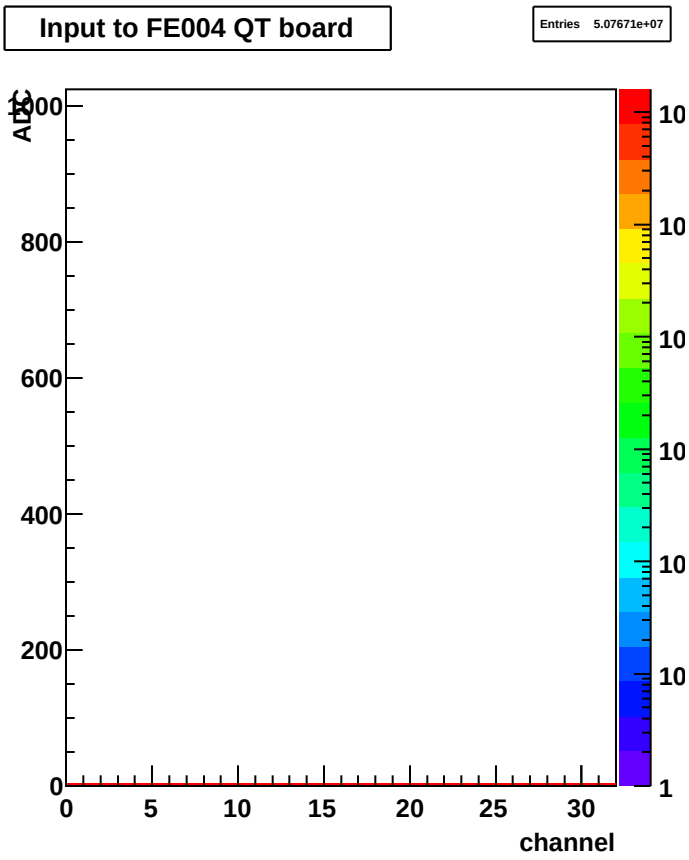

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

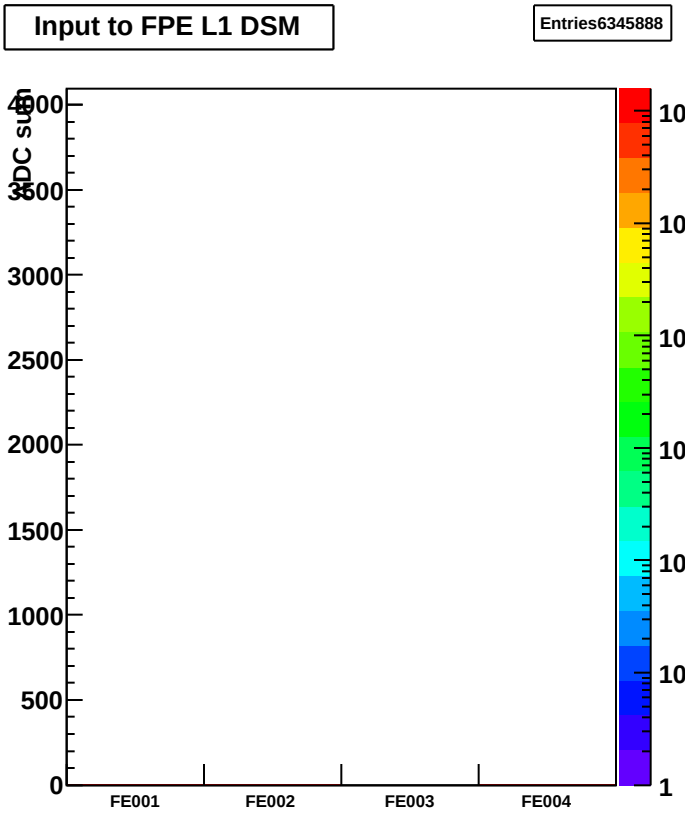

Input to FPD L2 DSM
Entries 9518832
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

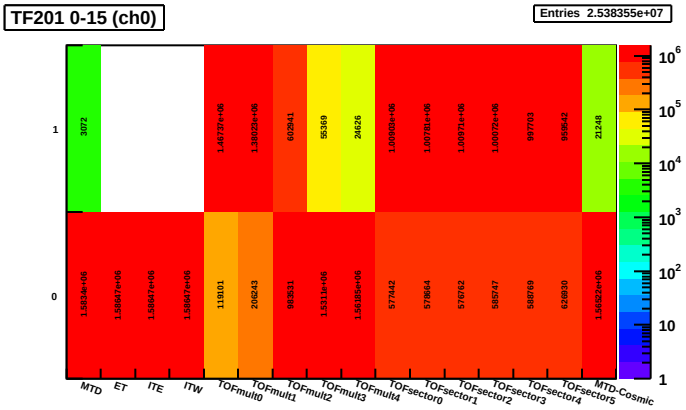

Input to FE001 QT board
Entries 5.07671e+07
ADC
channel

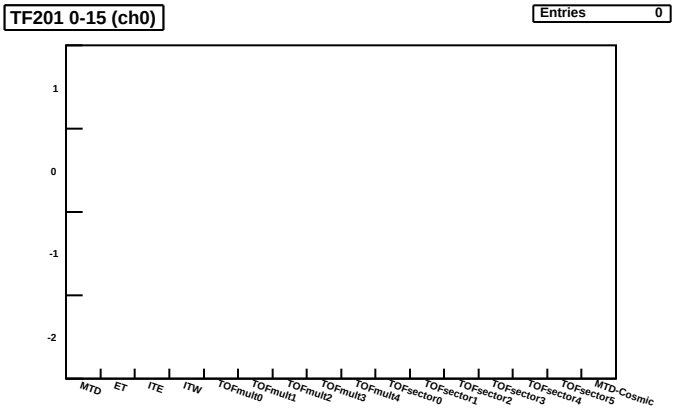

Input to FE002 QT board
Entries 5.07671e+07
ADC
channel

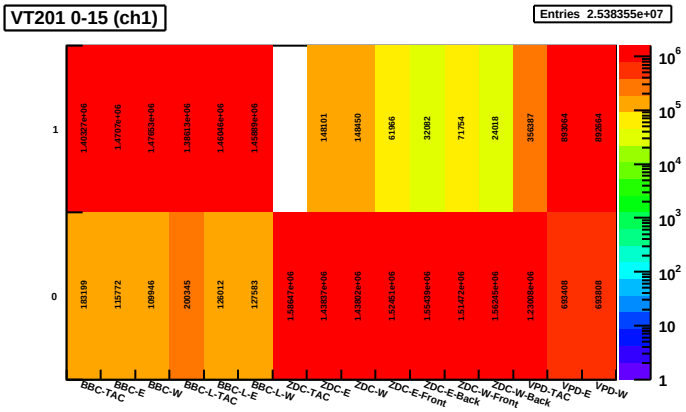

Input to FE003 QT board
Entries 5.07671e+07
ADC
channel

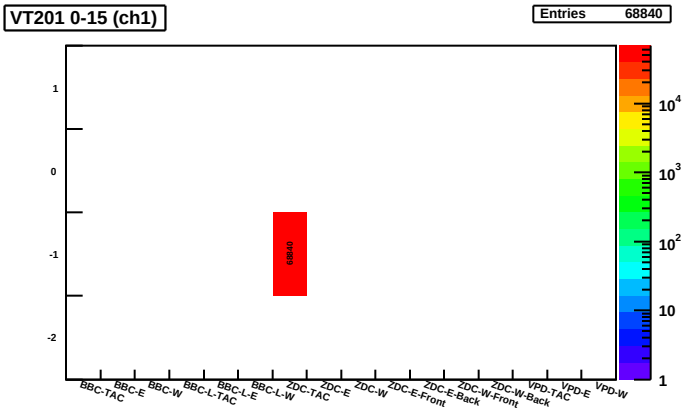

Input to FE004 QT board
Entries 5.07671e+07
ADC
channel

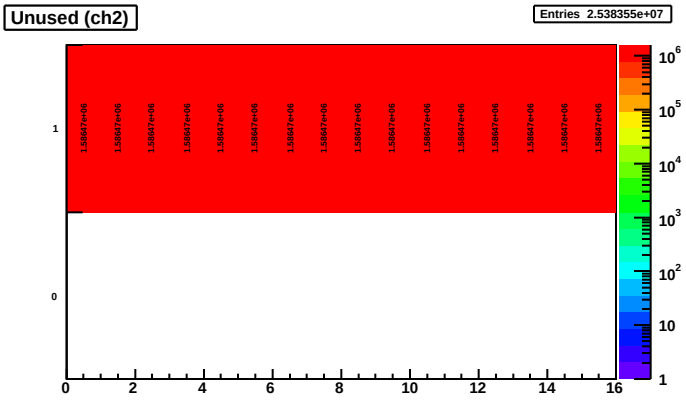

Input to FPE L1 DSM
Entries6345888
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

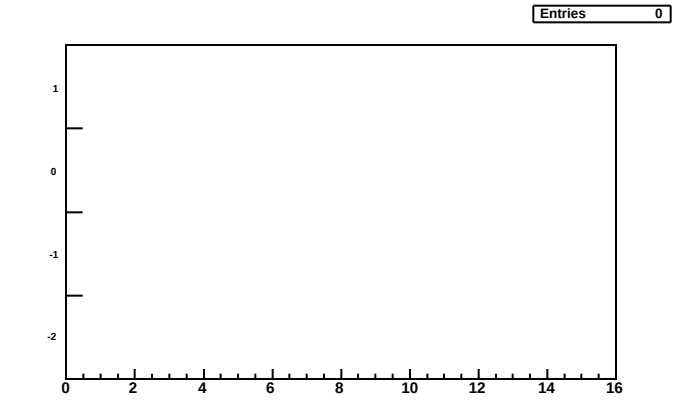

TF201 0-15 (ch0)
Entries 2.538355e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

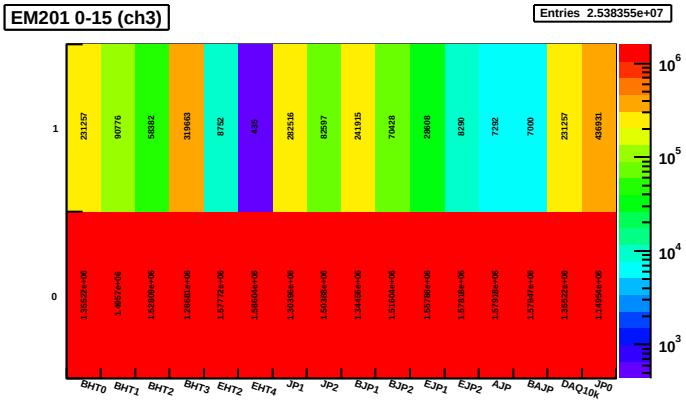

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

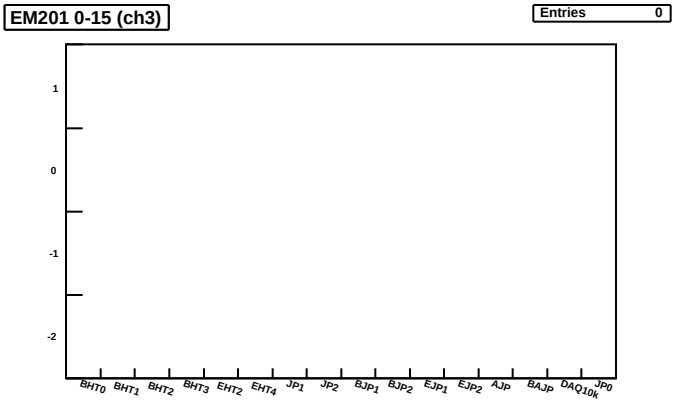

VT201 0-15 (ch1)
Entries 2.538355e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 68840
68840
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

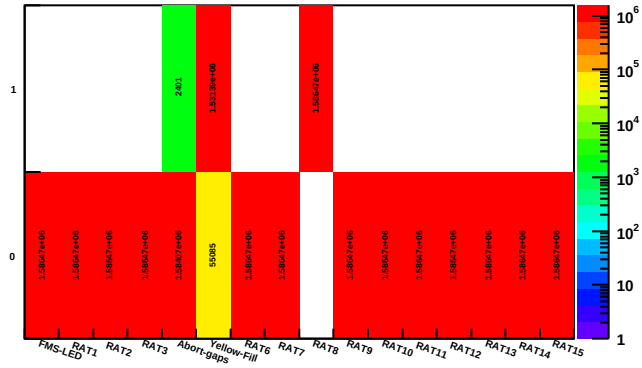

Unused (ch2)
Entries 2.538355e+07

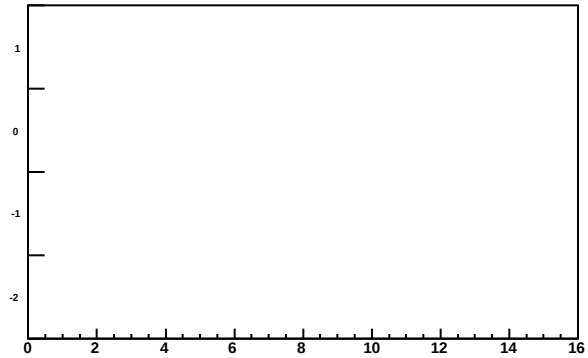

Entries 0


EM201 0-15 (ch3)
Entries 2.538355e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

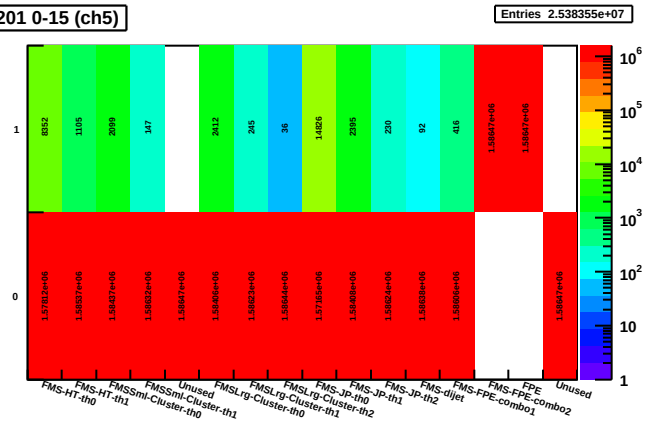

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

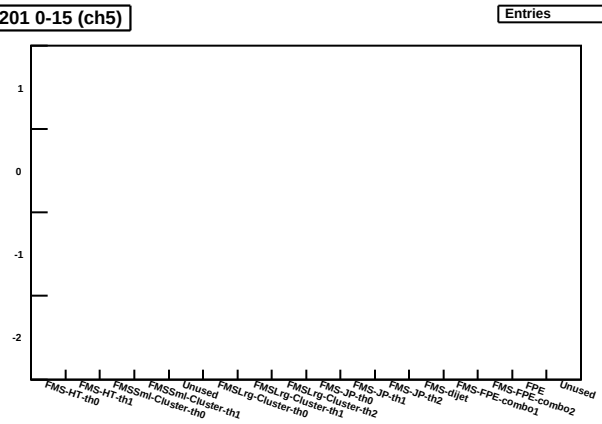




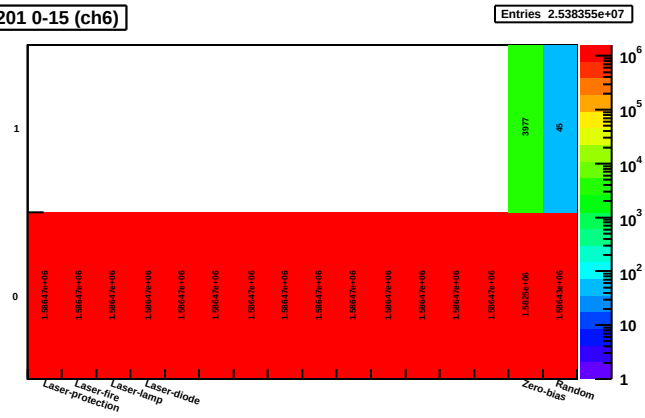
FP201 0-15 (ch5)

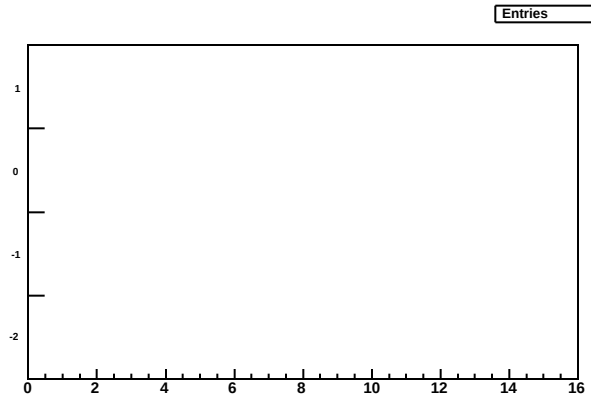
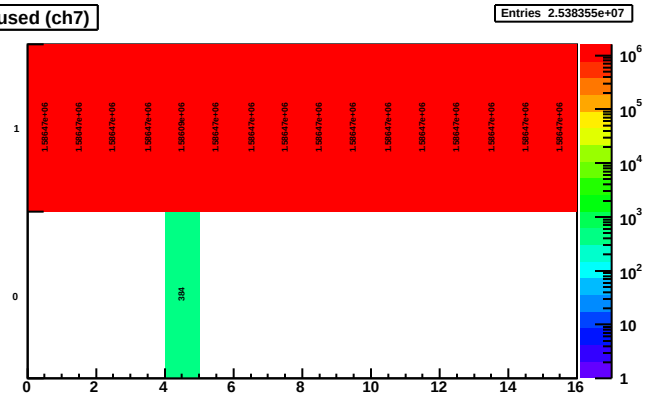
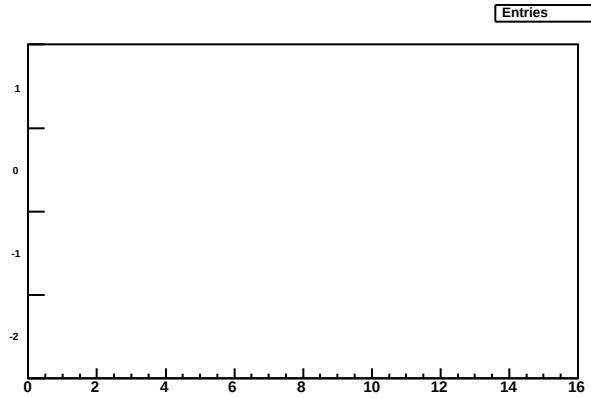


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)