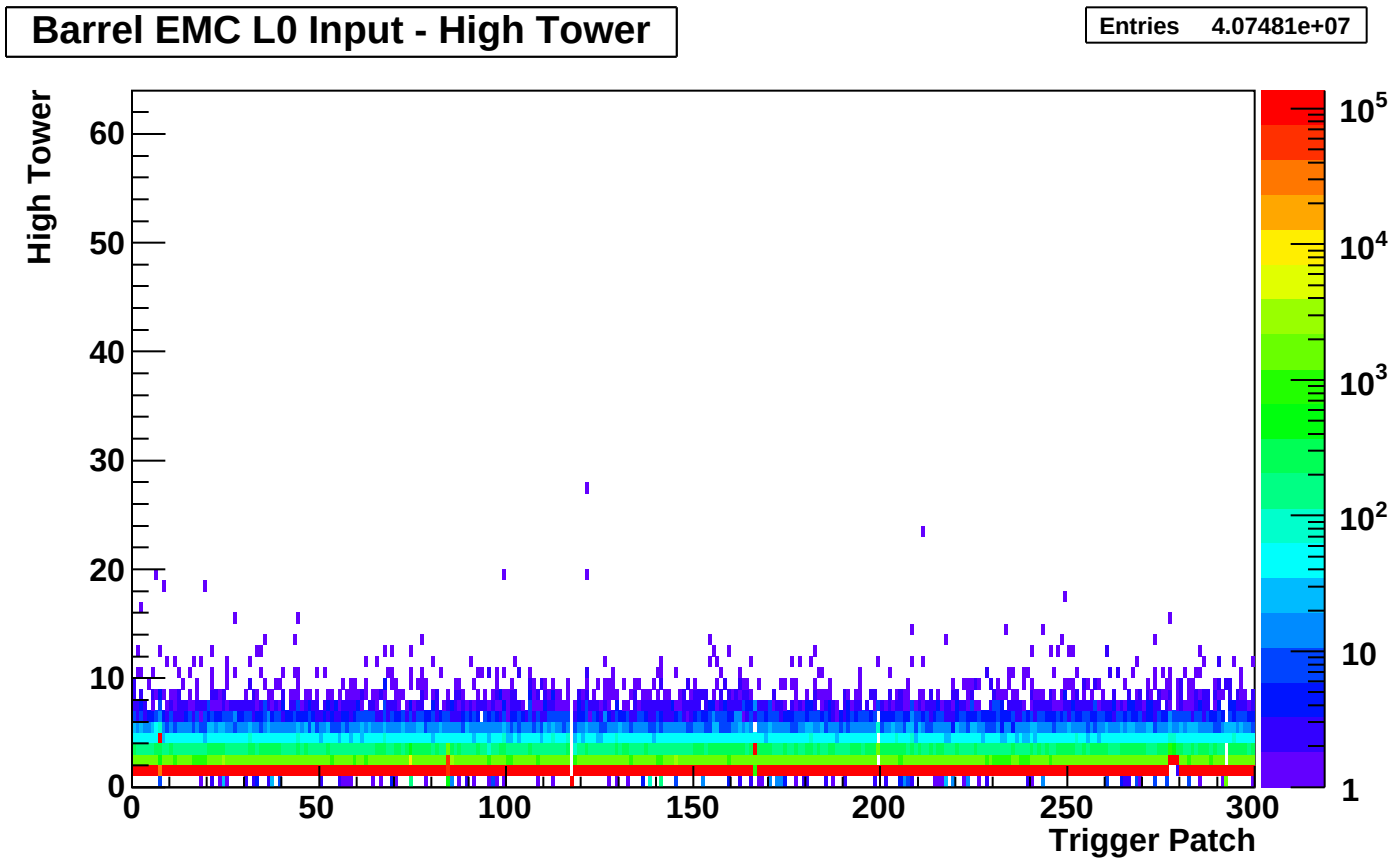
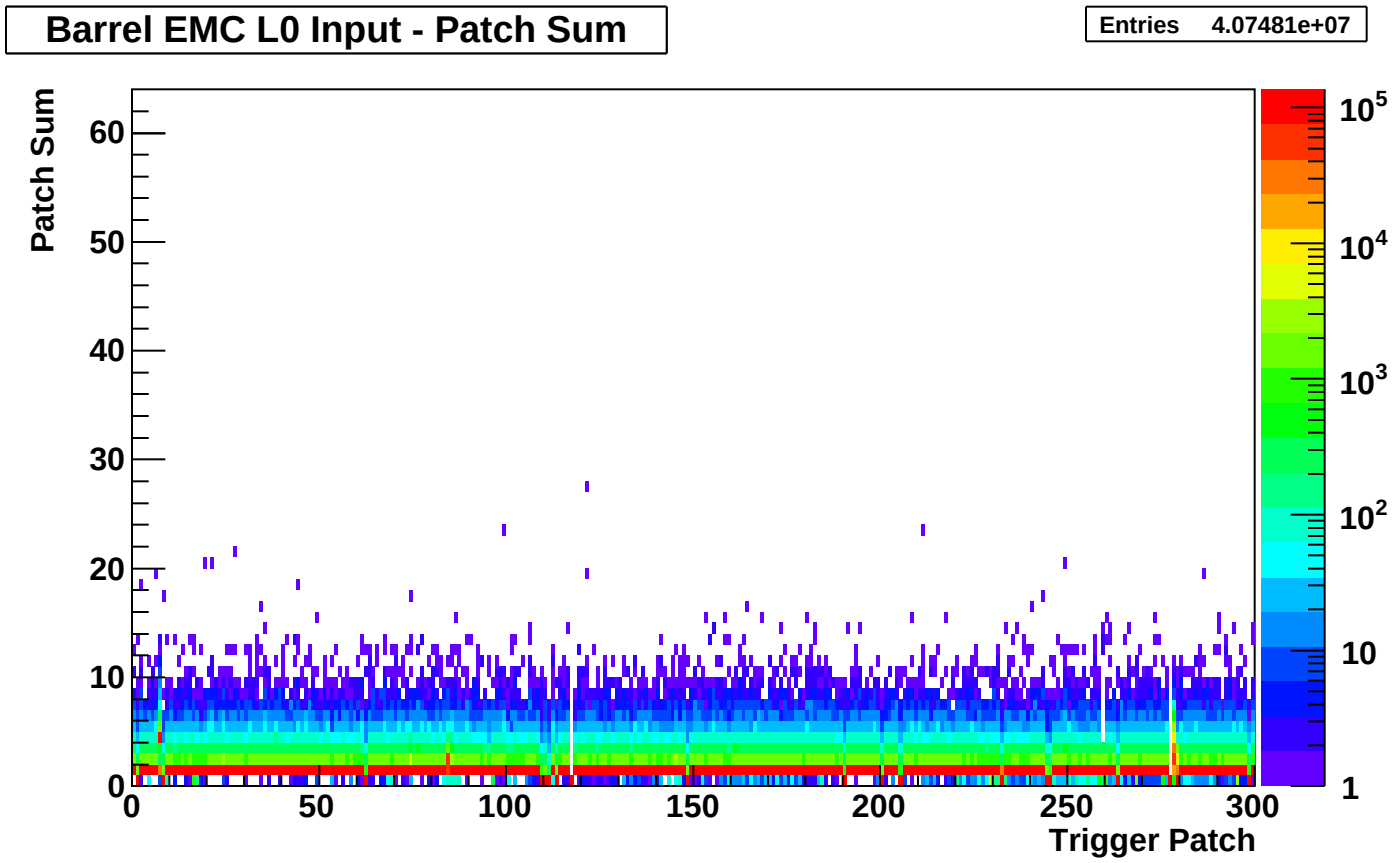
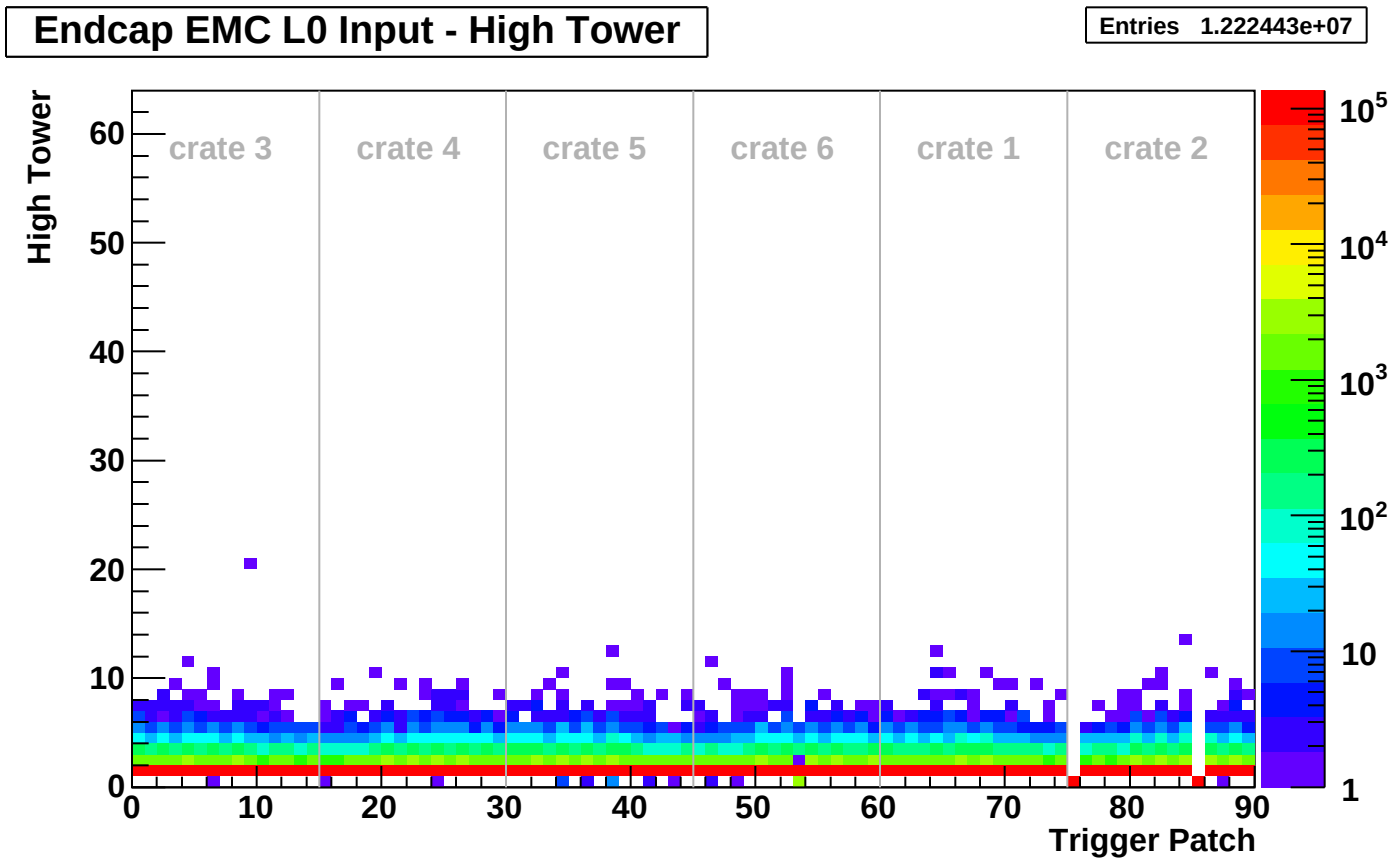
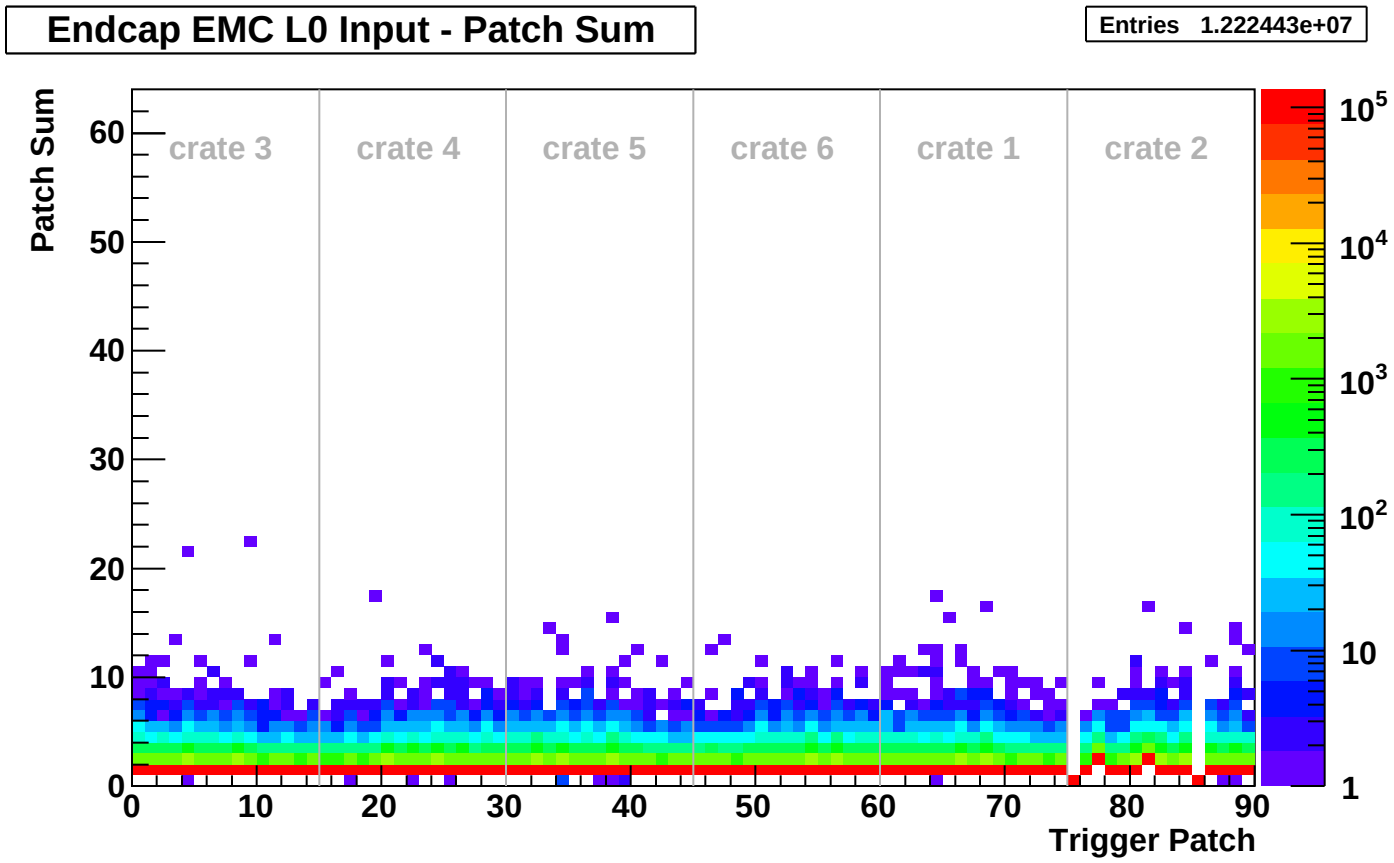
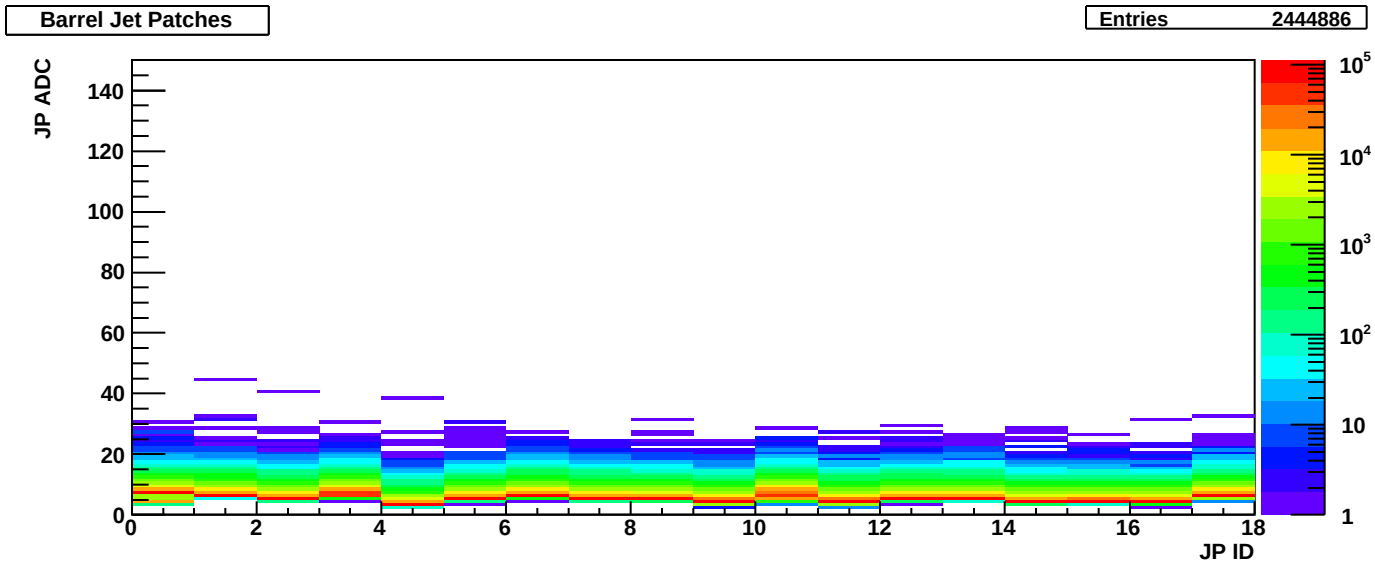
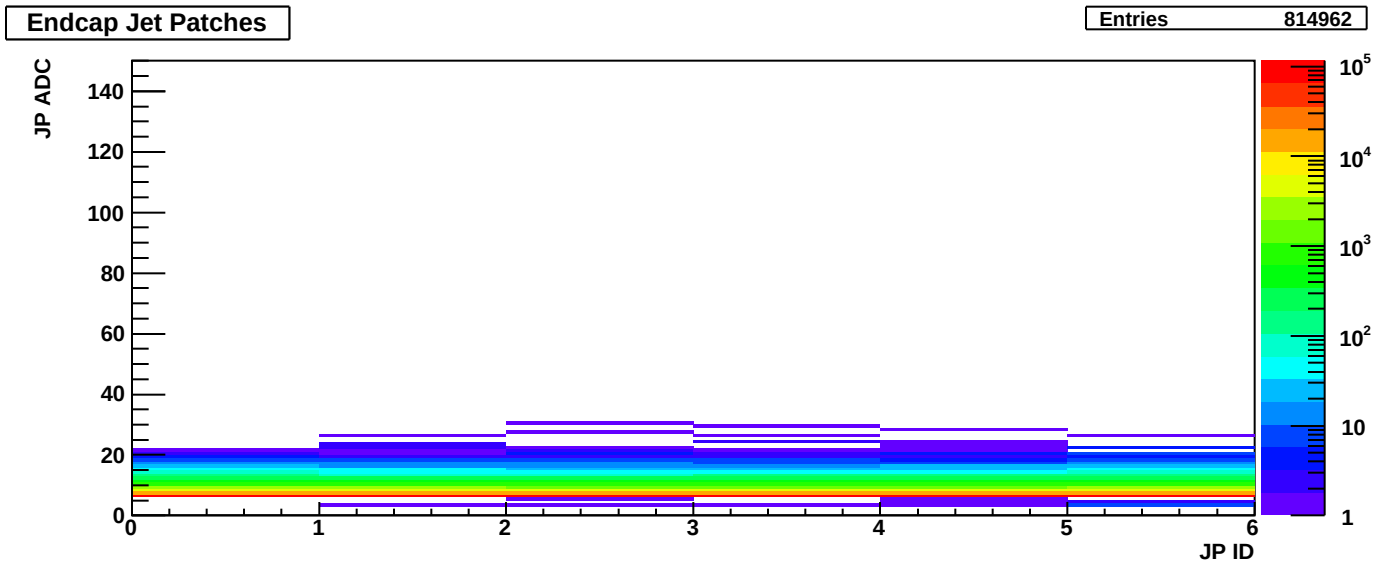

Barrel EMC L0 Input - High Tower
Entries 4.07481e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

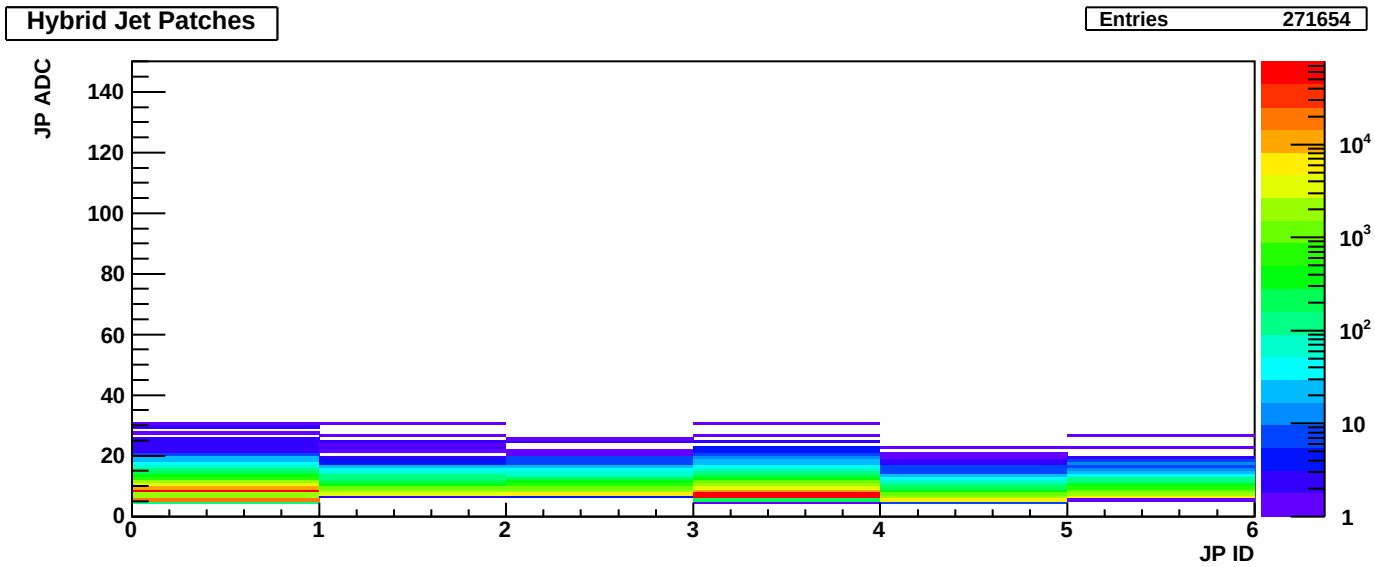

Barrel EMC L0 Input - Patch Sum
Entries 4.07481e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

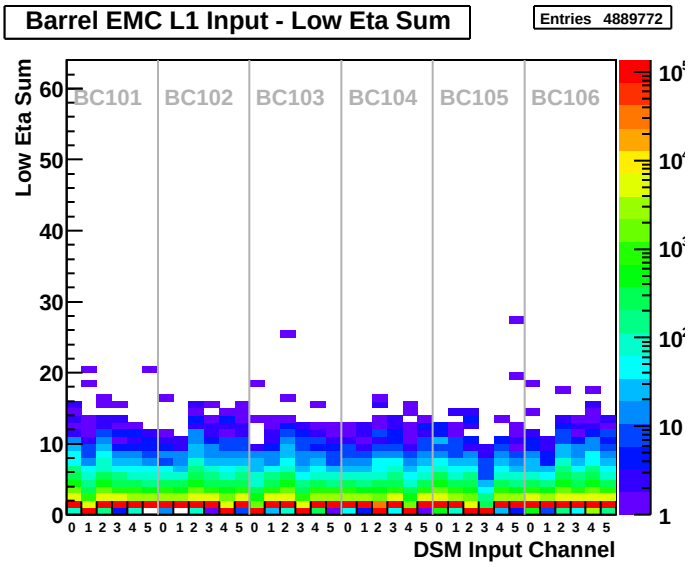

Endcap EMC L0 Input - High Tower
Entries 1.222443e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

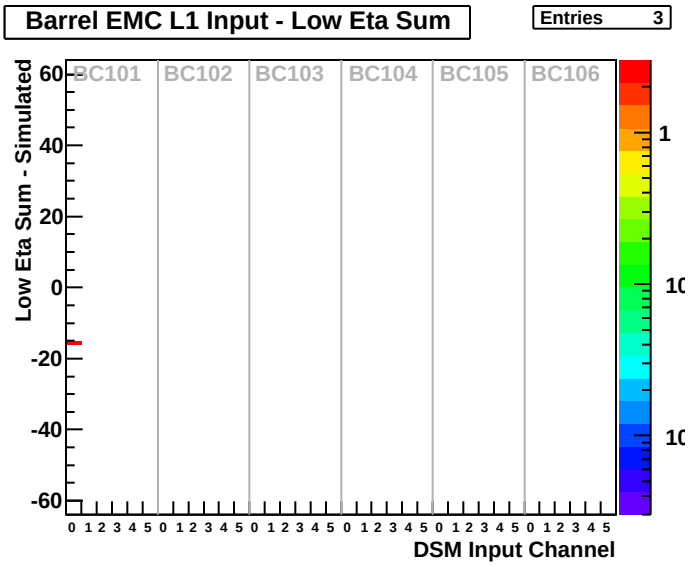

Endcap EMC L0 Input - Patch Sum
Entries 1.222443e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

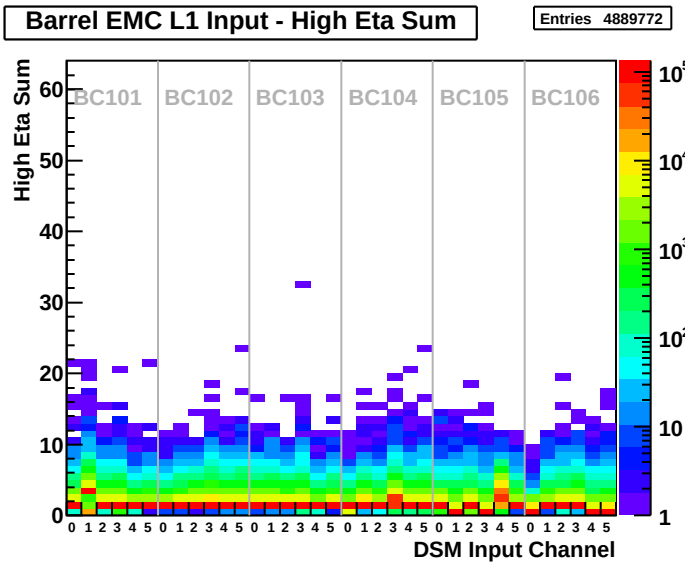

Barrel Jet Patches
Entries 2444886
JP ADC
JP ID

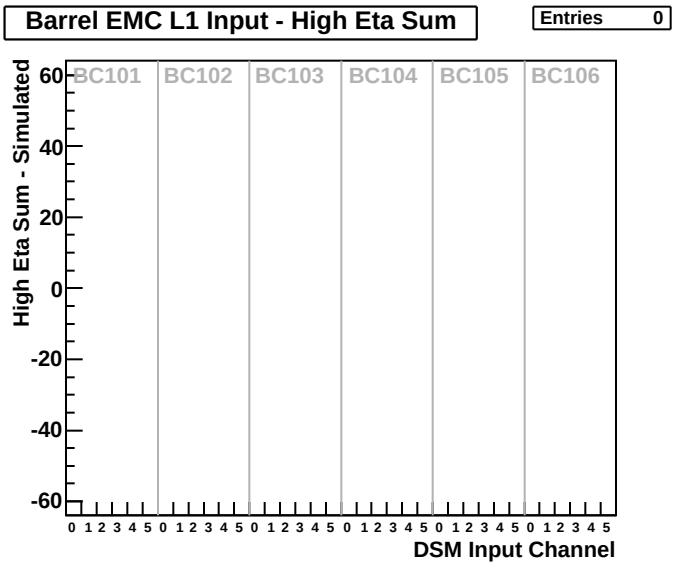

Endcap Jet Patches
Entries 814962
JP ADC
JP ID

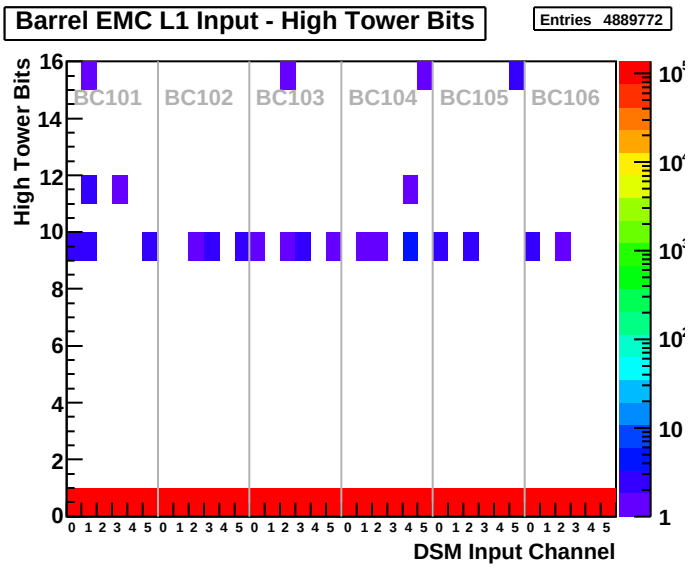

Hybrid Jet Patches
Entries 271654
JP ADC
JP ID

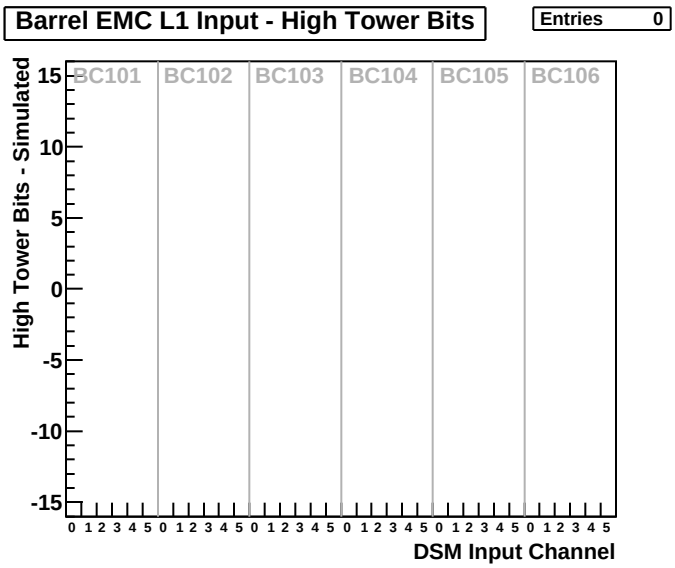

Barrel EMC L1 Input - Low Eta Sum
Entries 4889772
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

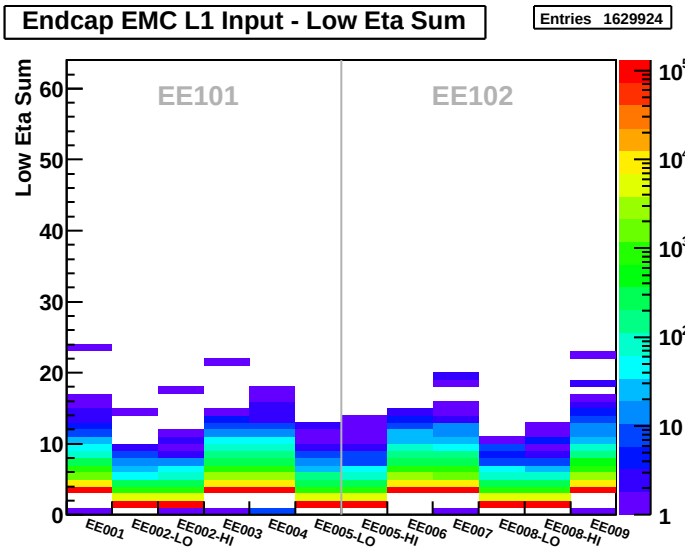

Barrel EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

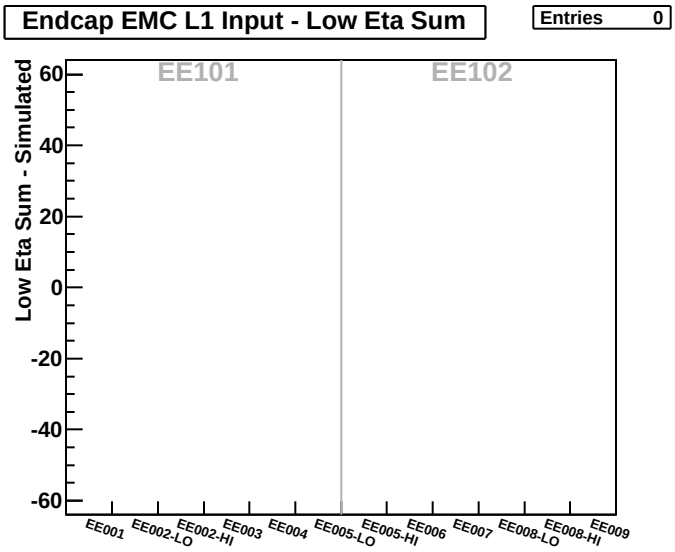

Barrel EMC L1 Input - High Eta Sum
Entries 4889772
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

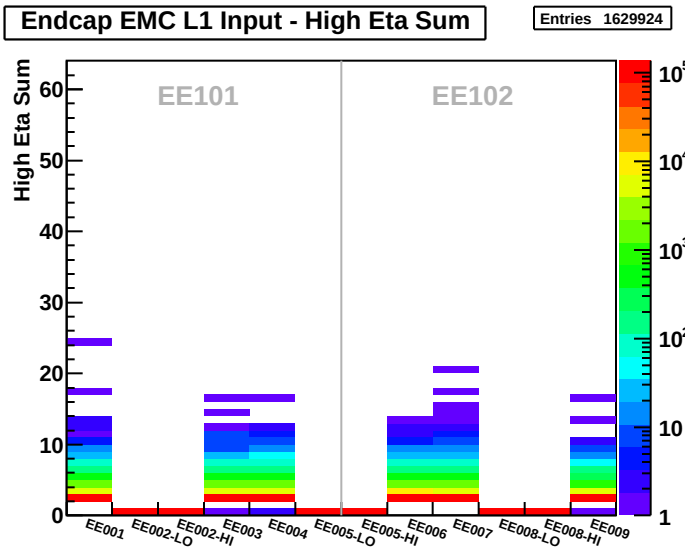

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

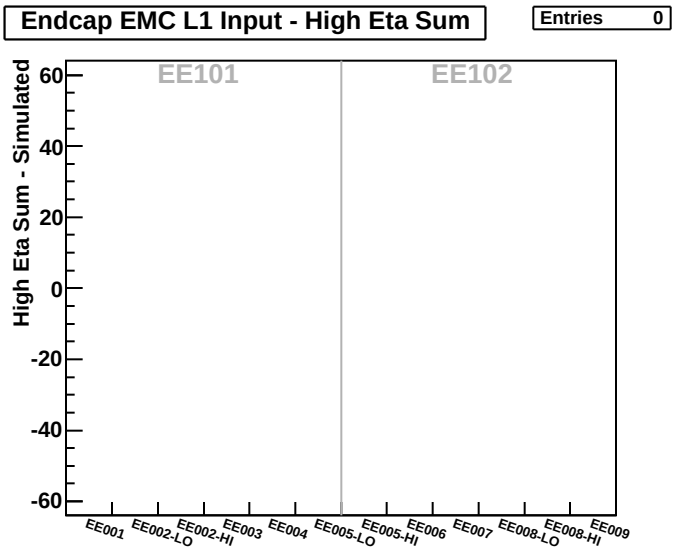

Barrel EMC L1 Input - High Tower Bits
Entries 4889772
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

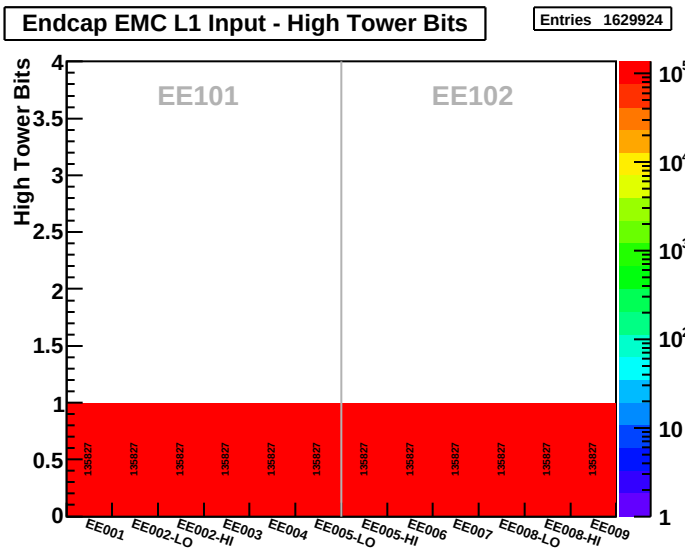

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

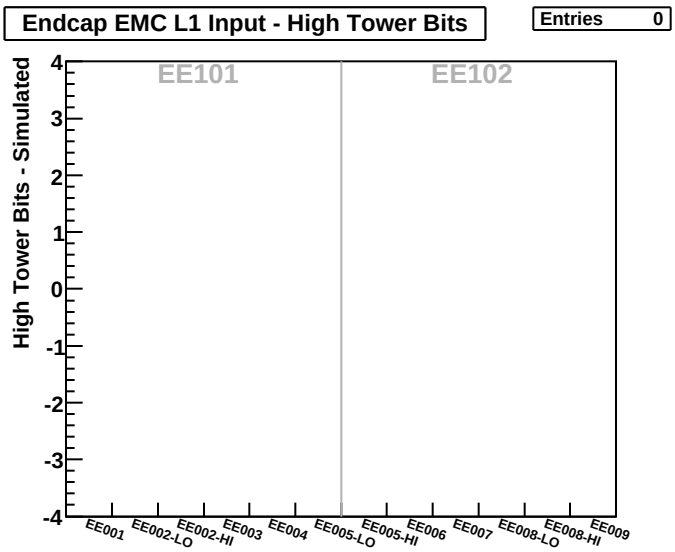

Endcap EMC L1 Input - Low Eta Sum
Entries 1629924
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

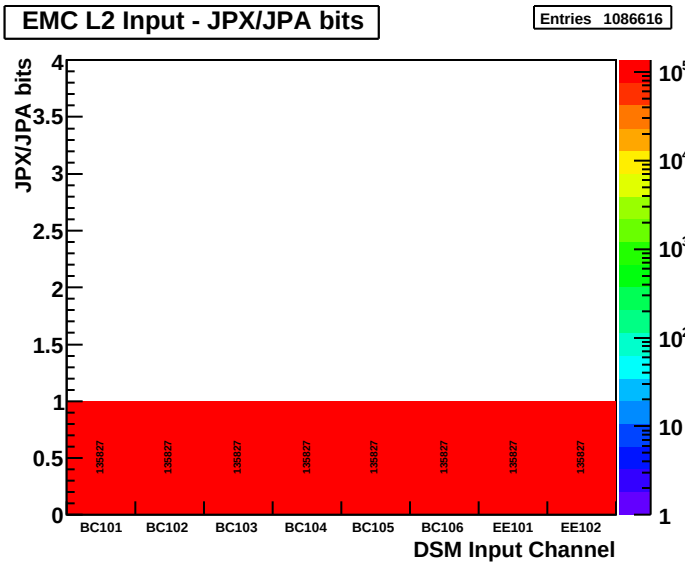

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

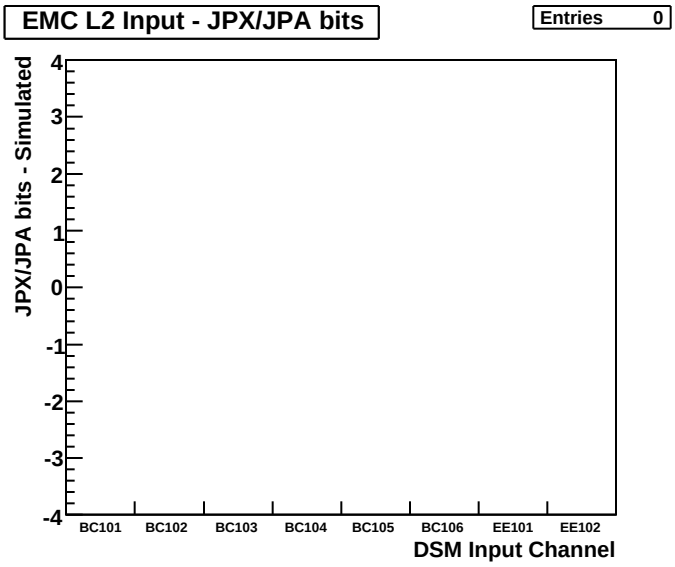

Endcap EMC L1 Input - High Eta Sum
Entries 1629924
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

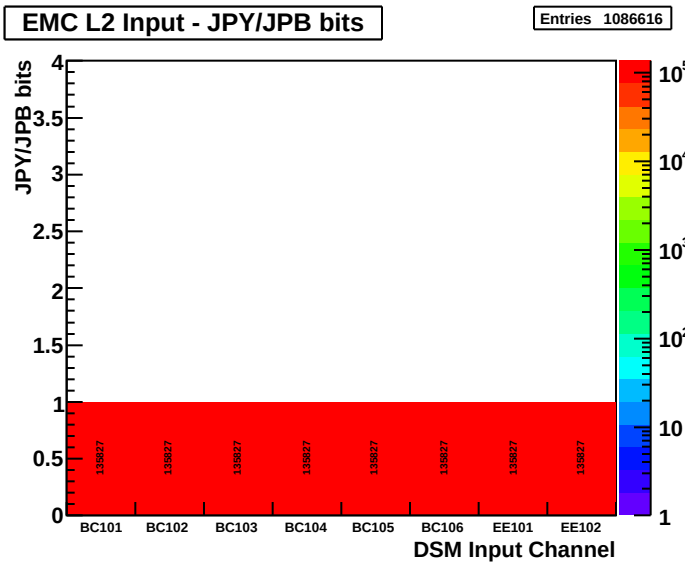

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

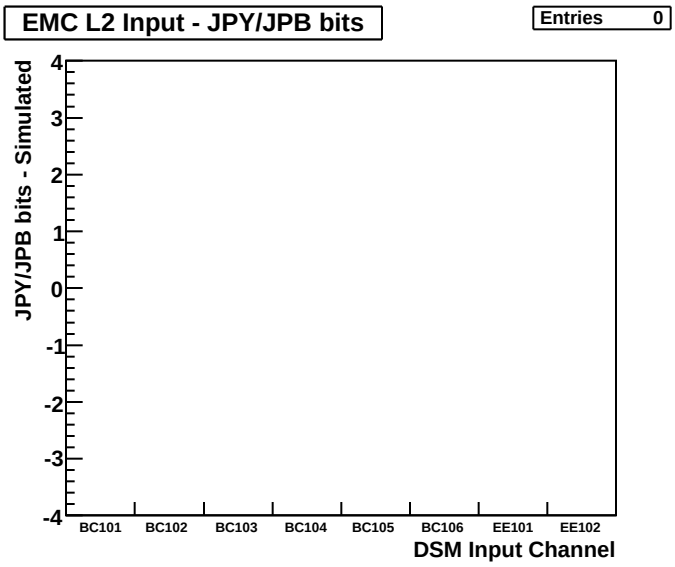

Endcap EMC L1 Input - High Tower Bits
Entries 1629924
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

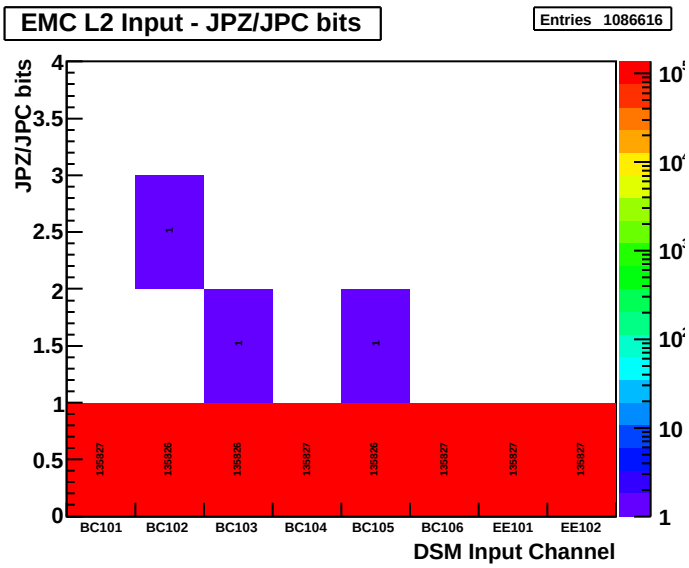

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

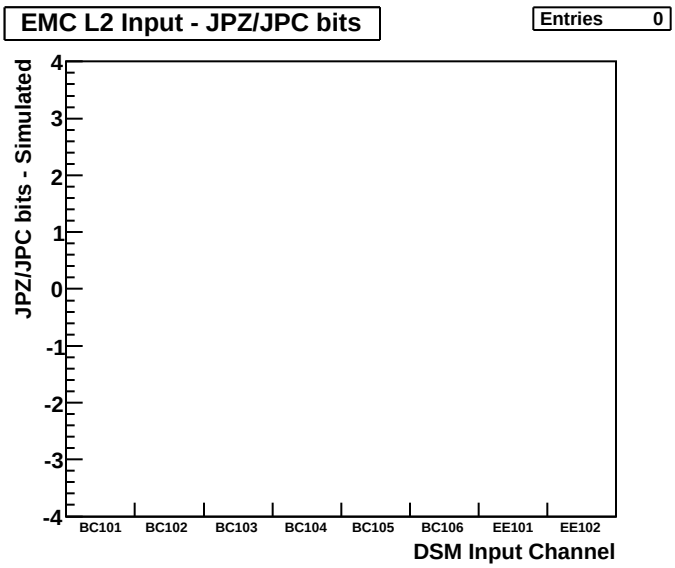

EMC L2 Input - JPX/JPA bits
Entries 1086616
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

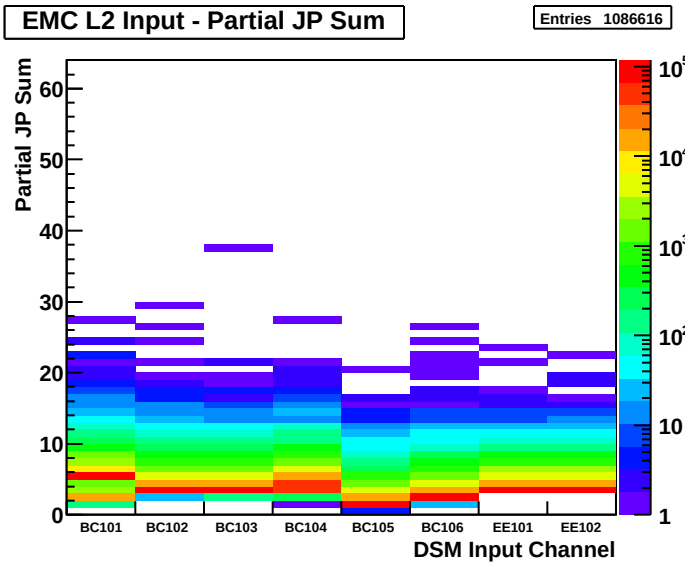

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

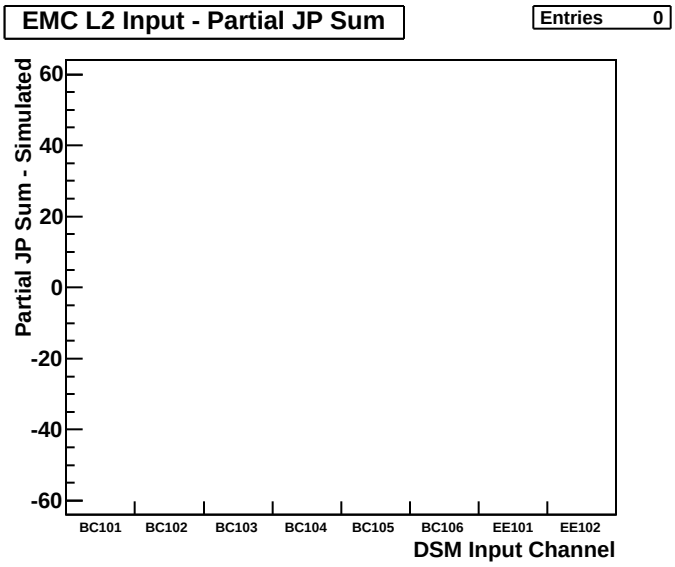

EMC L2 Input - JPY/JPB bits
Entries 1086616
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

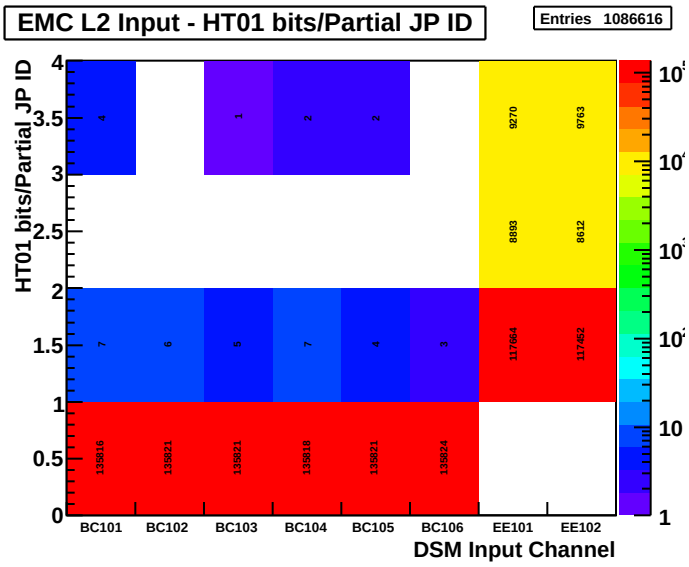

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

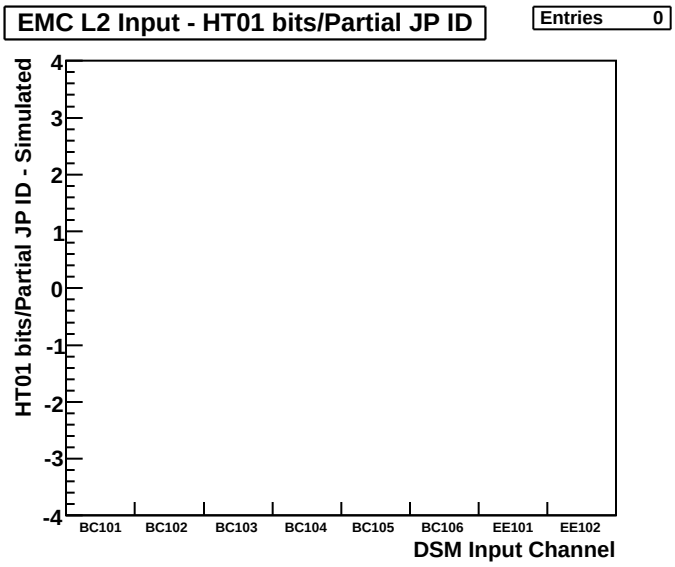

EMC L2 Input - JPZ/JPC bits
Entries 1086616
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

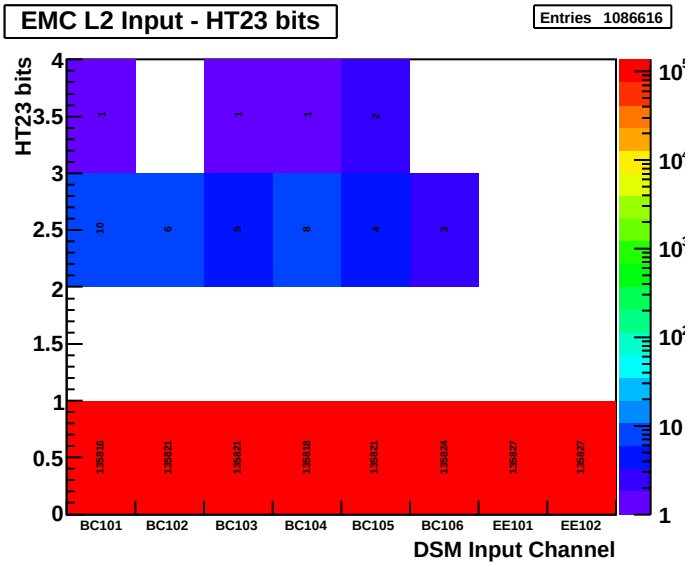

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

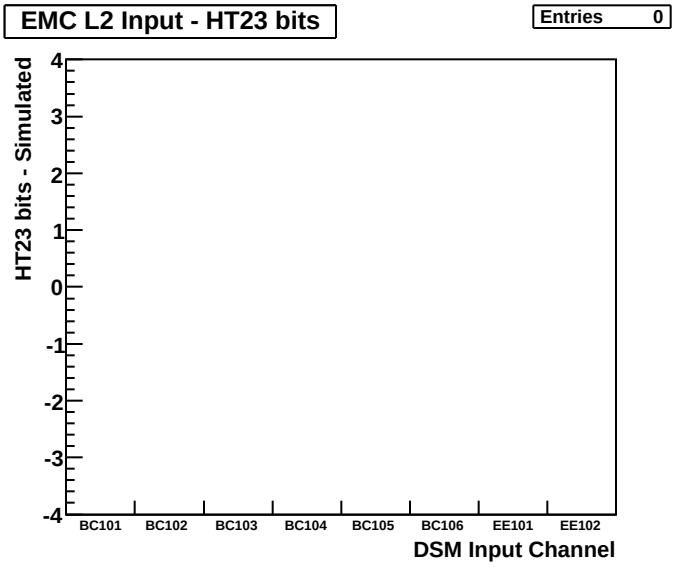

EMC L2 Input - Partial JP Sum
Entries 1086616
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

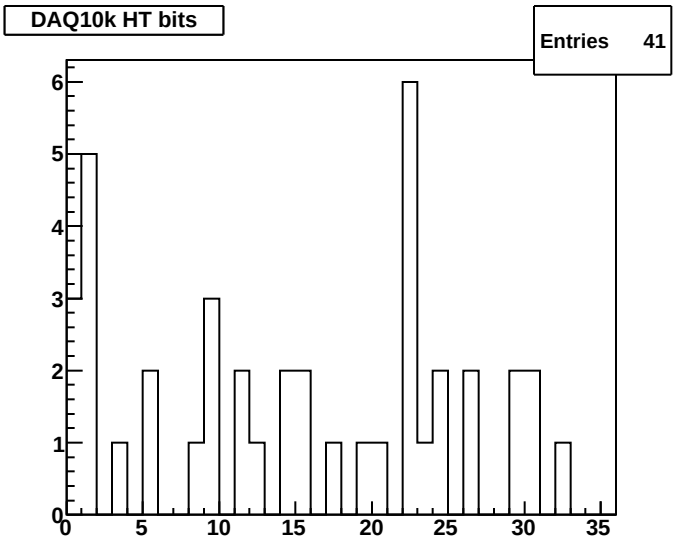

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

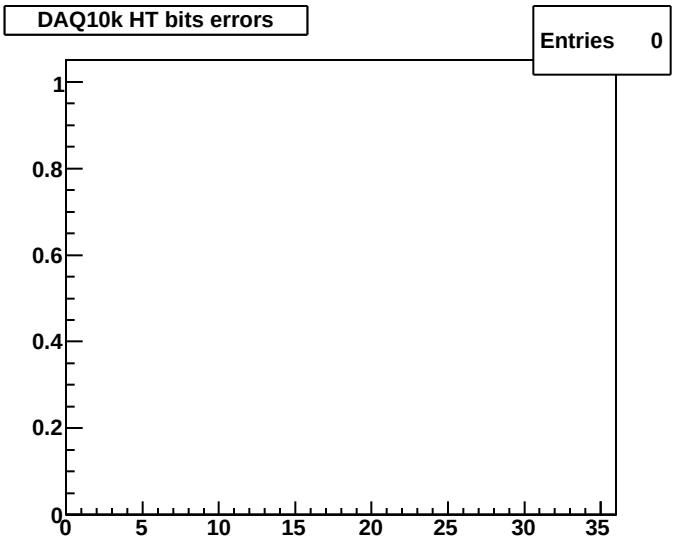

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1086616
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

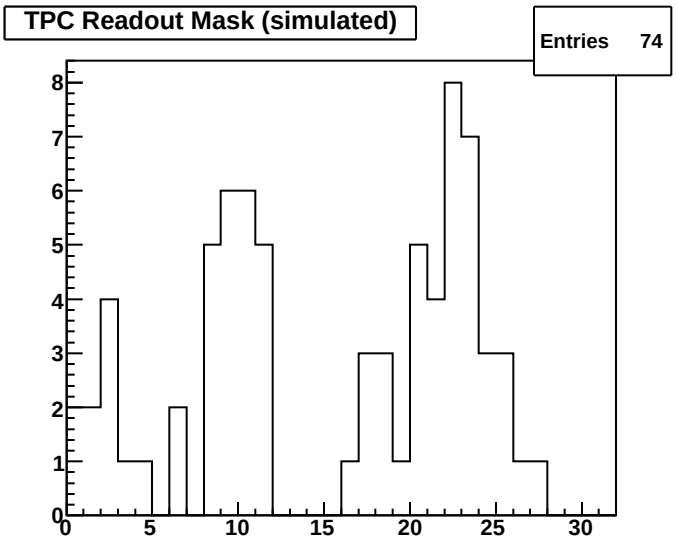

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

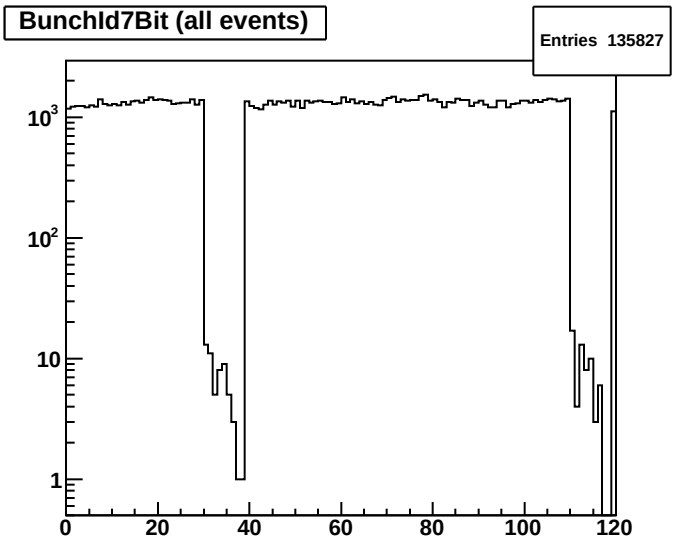

EMC L2 Input - HT23 bits
Entries 1086616
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

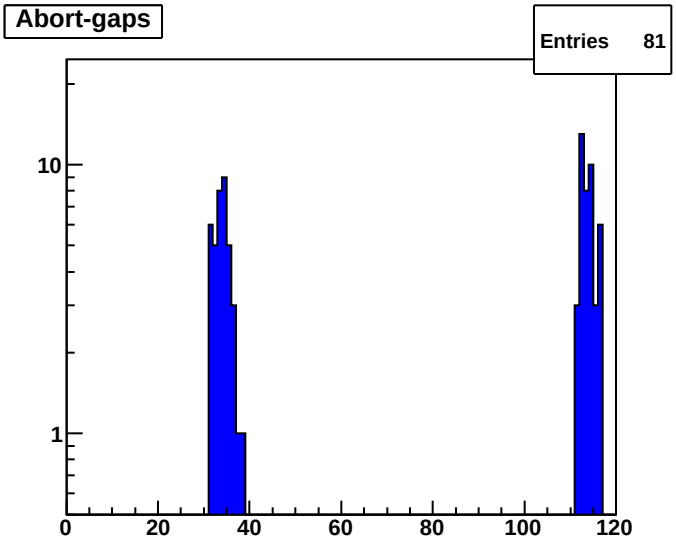

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

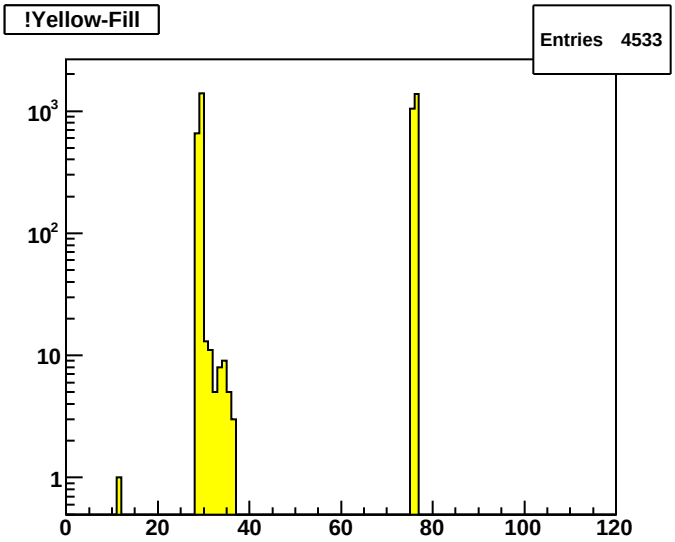

DAQ10k HT bits
Entries 41

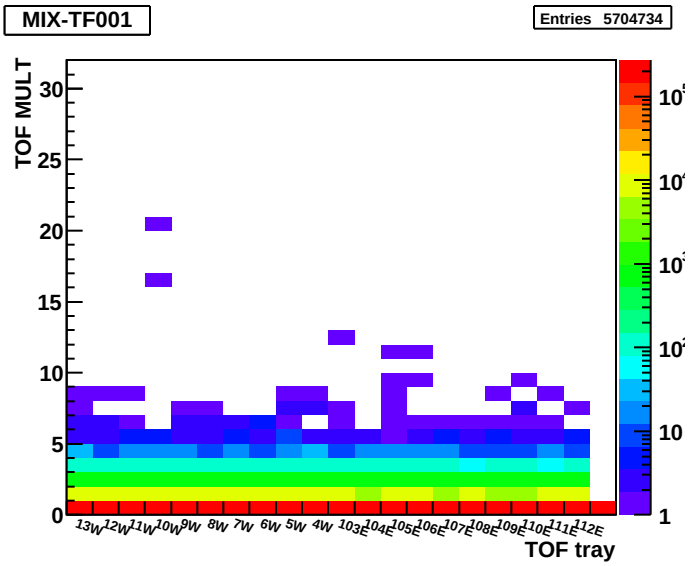

DAQ10k HT bits errors
Entries 0

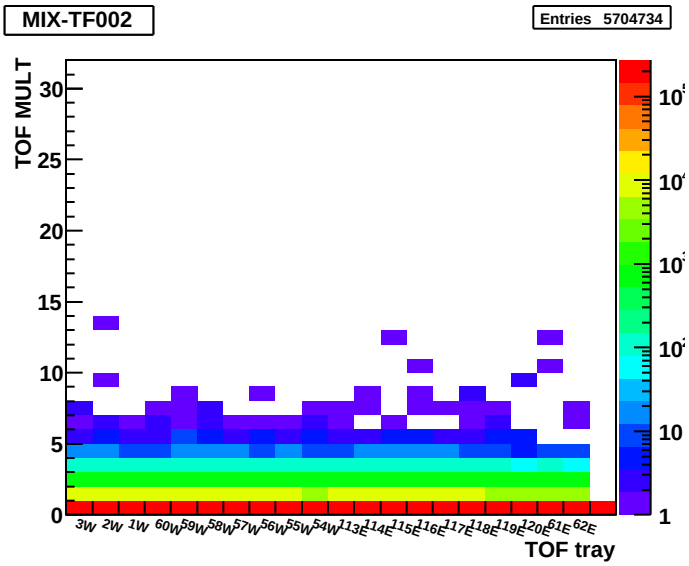

TPC Readout Mask (simulated)
Entries 74

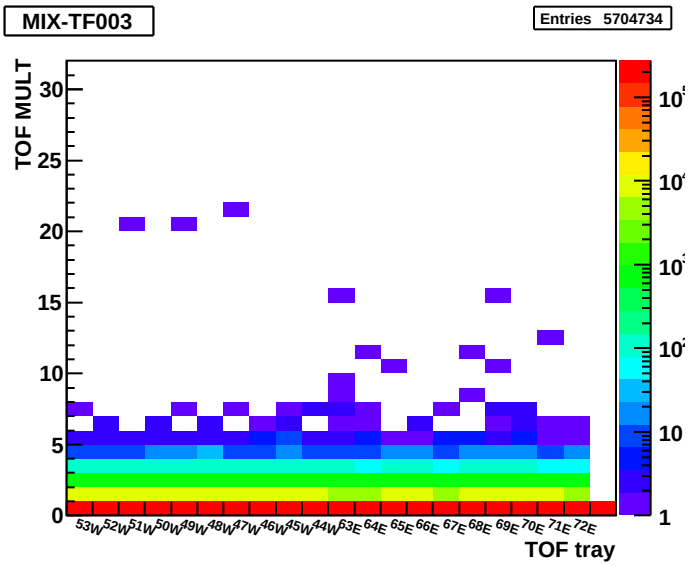

BunchId7Bit (all events)
Entries 135827

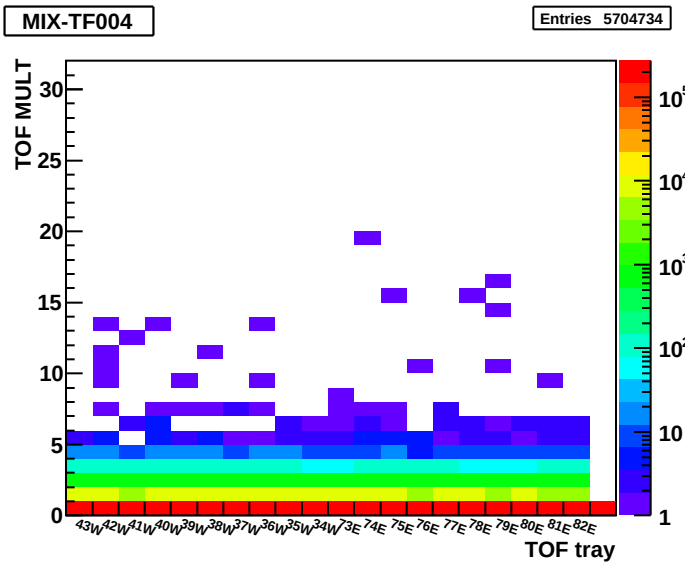

Abort-gaps
Entries 81

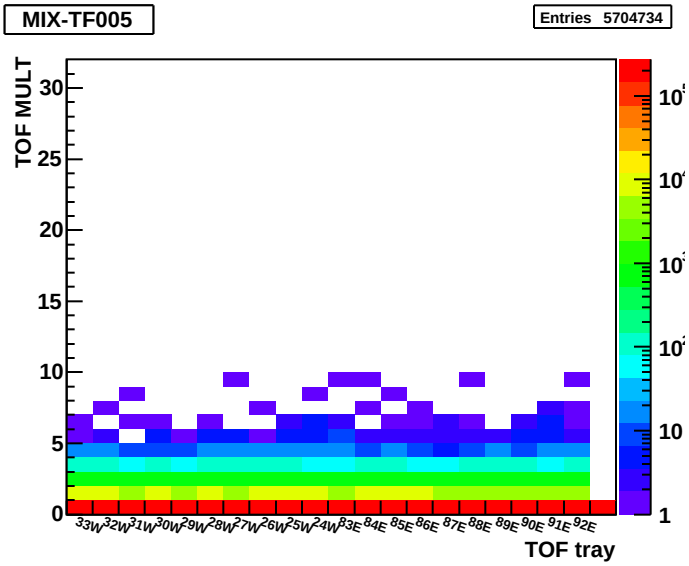

!Yellow-Fill
Entries 4533

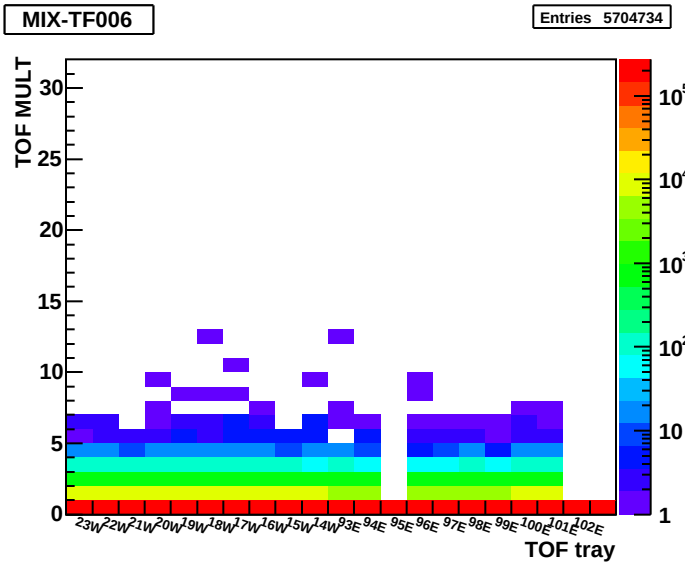

MIX-TF001
Entries 5704734
TOF MULT
TOF tray

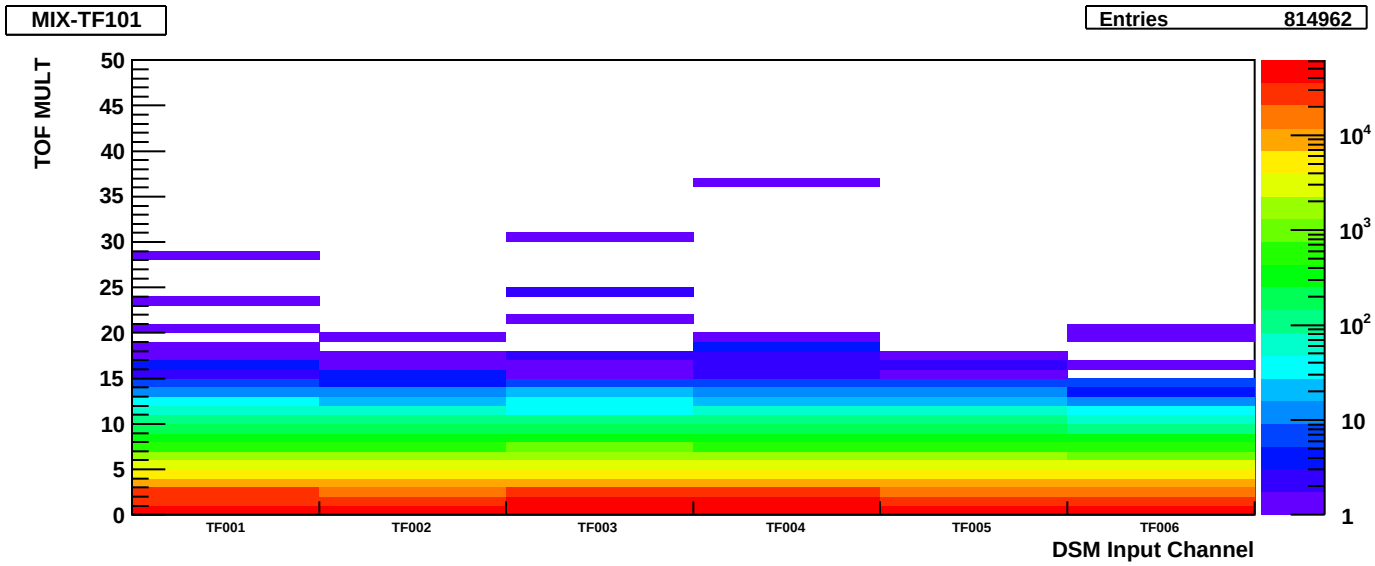

MIX-TF002
Entries 5704734
TOF MULT
TOF tray

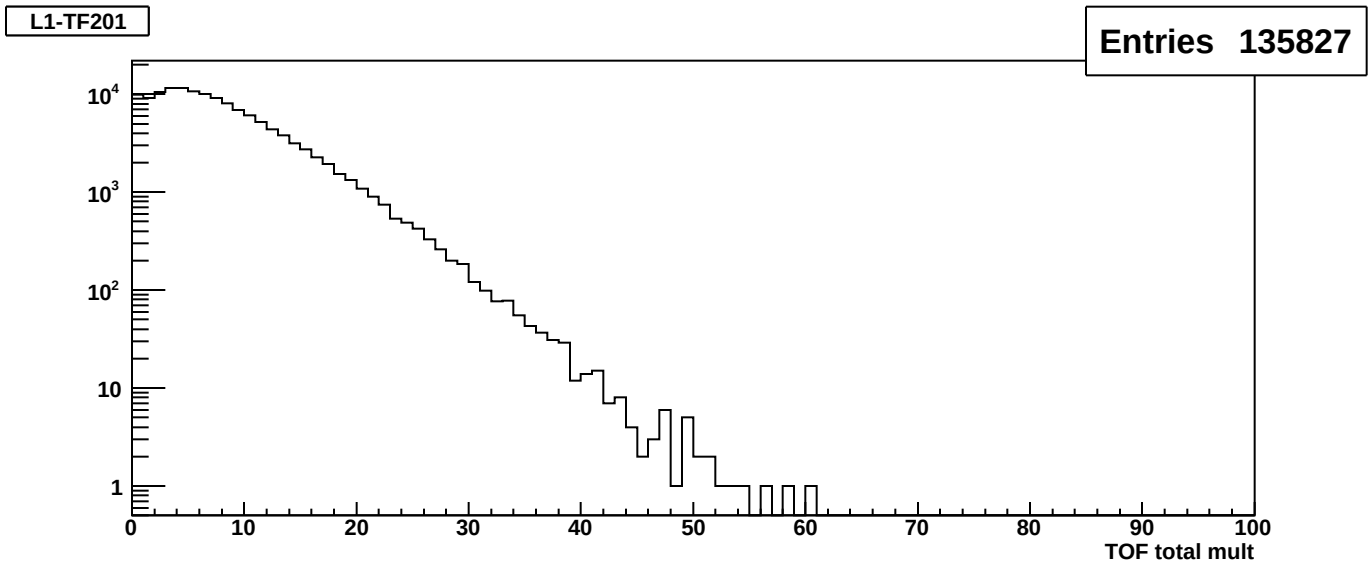

MIX-TF003
Entries 5704734
TOF MULT
TOF tray

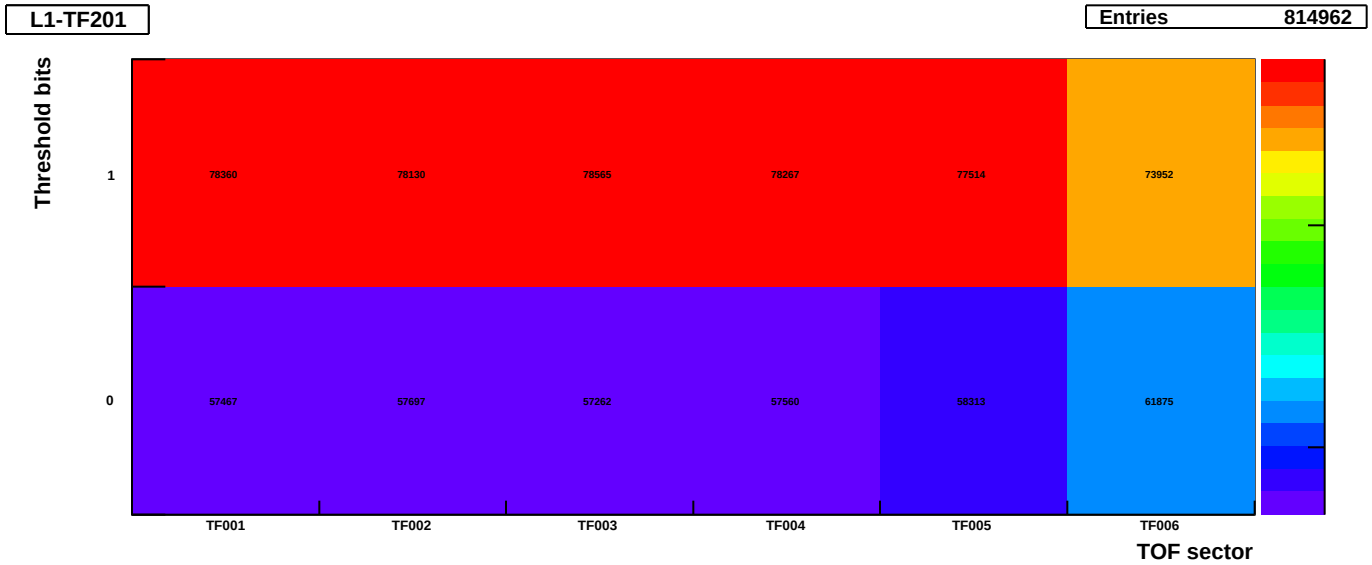

MIX-TF004
Entries 5704734
TOF MULT
TOF tray

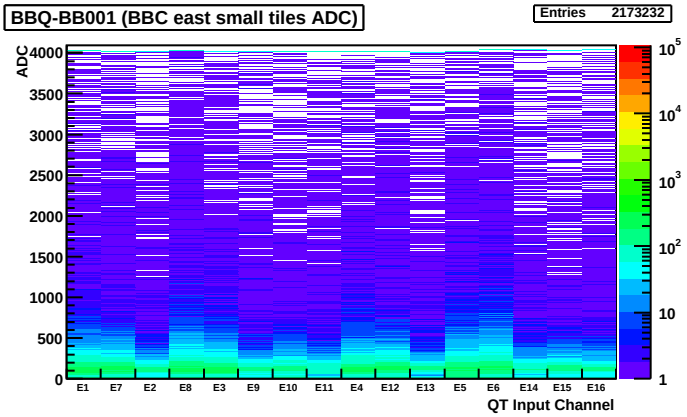

MIX-TF005
Entries 5704734
TOF MULT
TOF tray

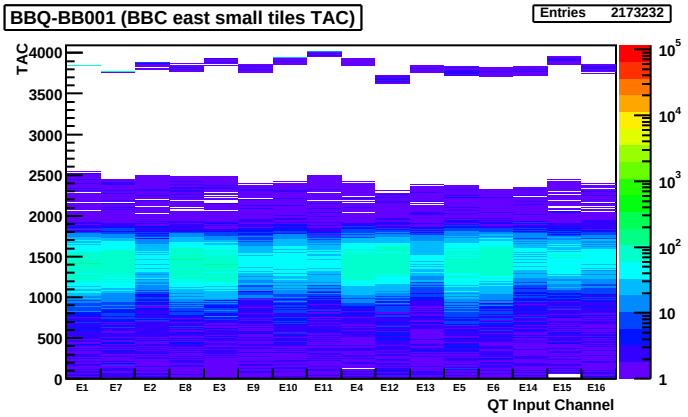

MIX-TF006
Entries 5704734
TOF MULT
TOF tray

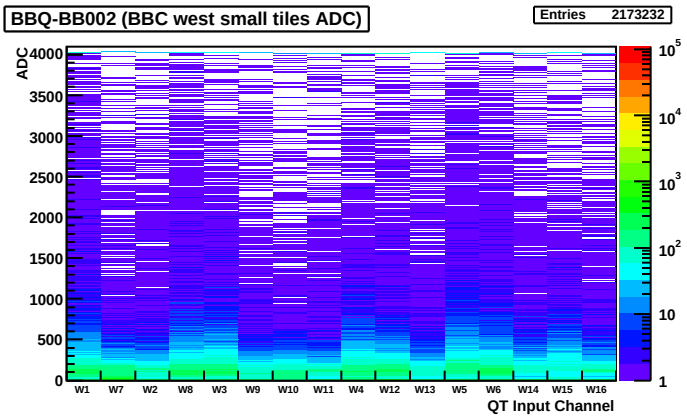

MIX-TF101
Entries 814962
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

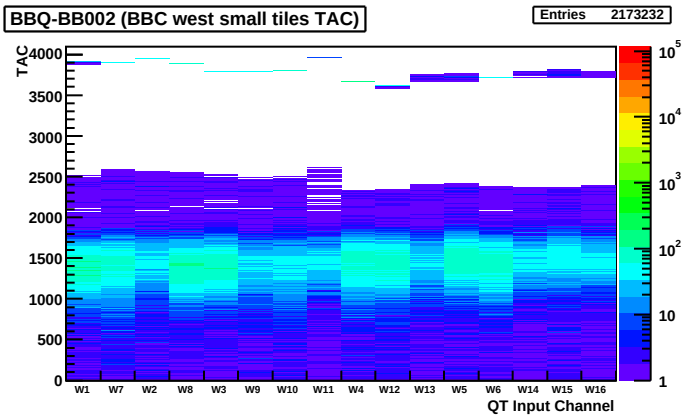

L1-TF201
Entries 135827
TOF total mult

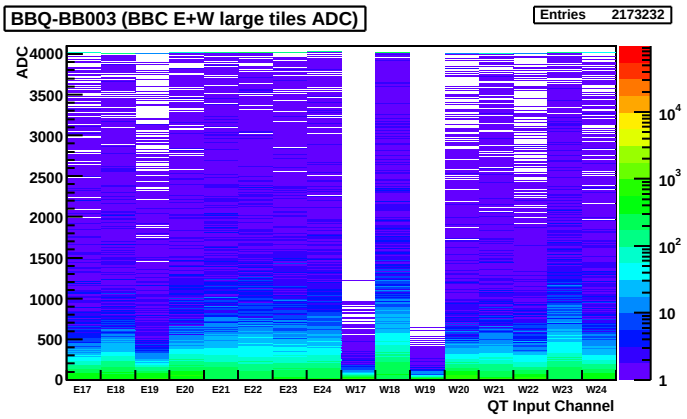

L1-TF201
Entries 814962
Threshold bits
78360
78130
78565
78267
77514
73952
57467
57697
57262
57560
58313
61875
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

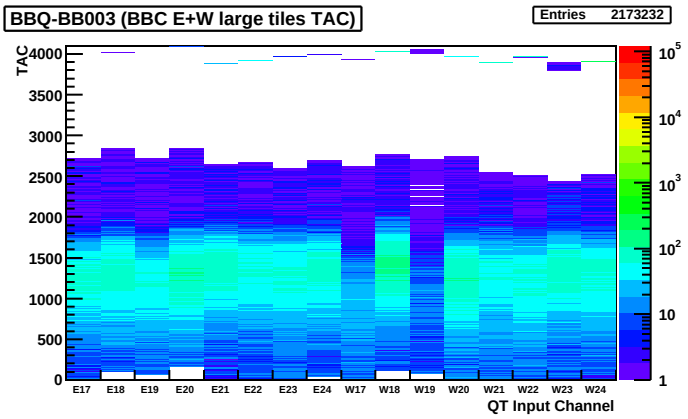

BBQ-BB001 (BBC east small tiles ADC)
Entries 2173232
ADC
QT Input Channel

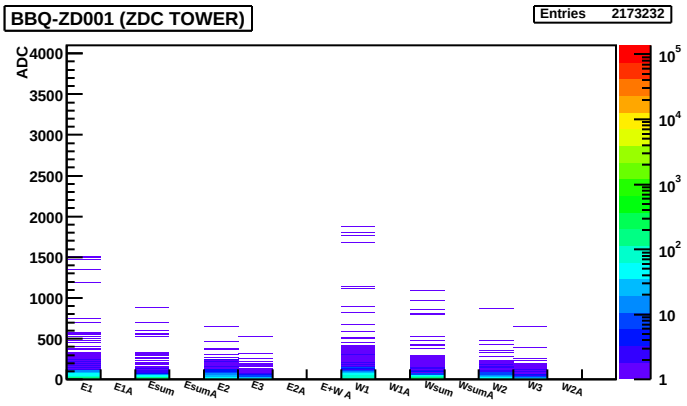

BBQ-BB001 (BBC east small tiles TAC)
Entries 2173232
TAC
QT Input Channel

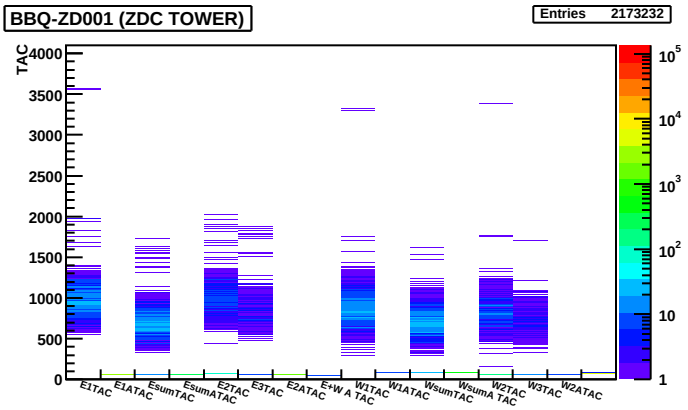

BBQ-BB002 (BBC west small tiles ADC)
Entries 2173232
ADC
QT Input Channel

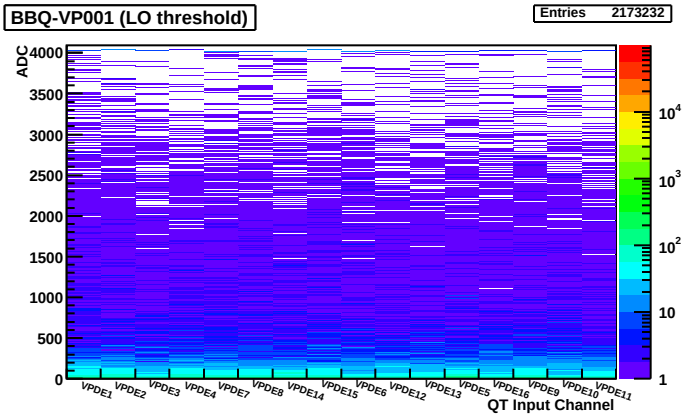

BBQ-BB002 (BBC west small tiles TAC)
Entries 2173232
TAC
QT Input Channel

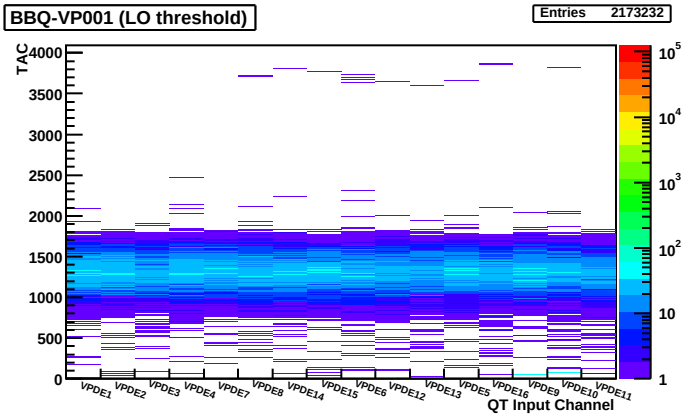

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2173232
ADC
QT Input Channel

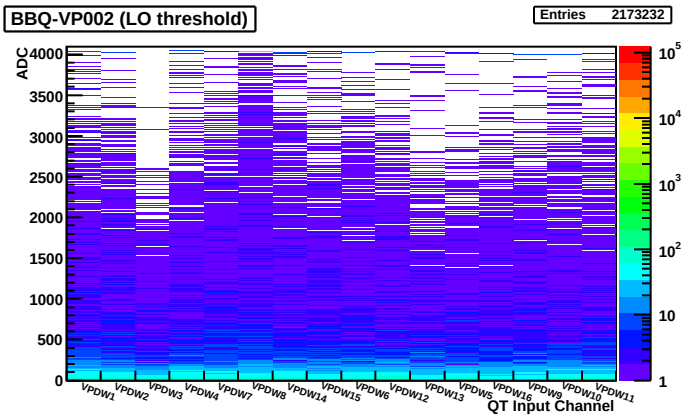

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2173232
TAC
QT Input Channel

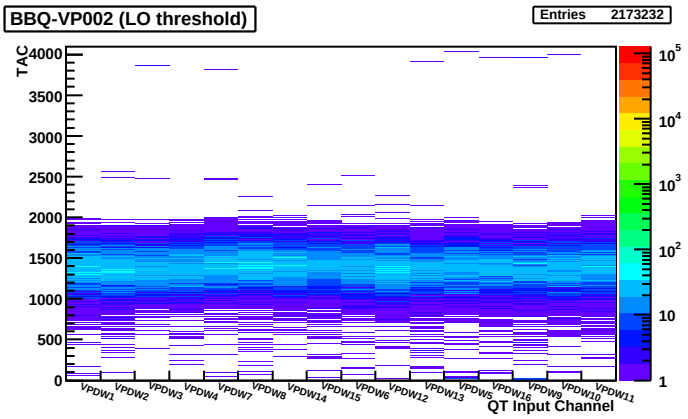

BBQ-ZD001 (ZDC TOWER)
Entries 2173232
ADC

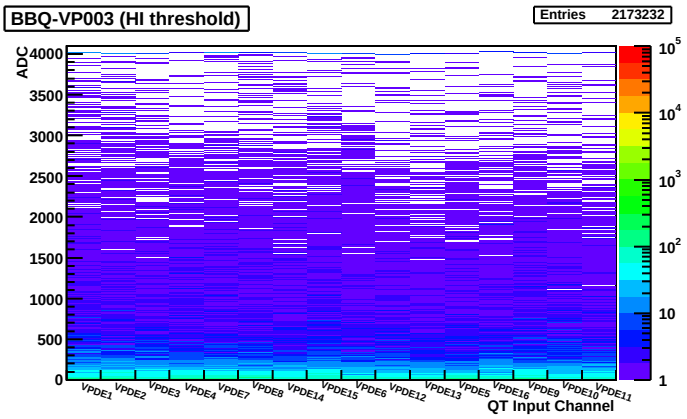

BBQ-ZD001 (ZDC TOWER)
Entries 2173232
TAC

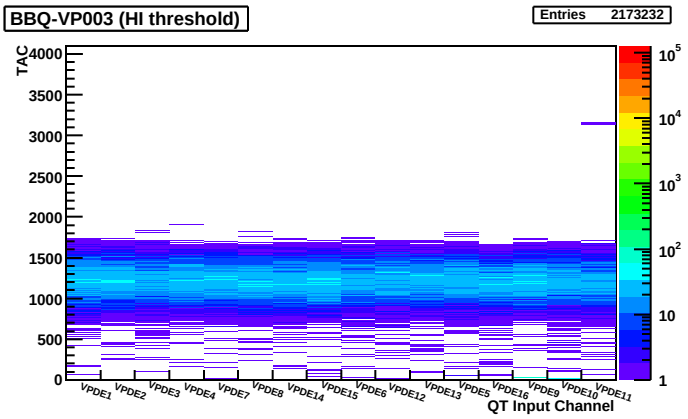

BBQ-VP001 (LO threshold)
Entries 2173232
ADC
QT Input Channel

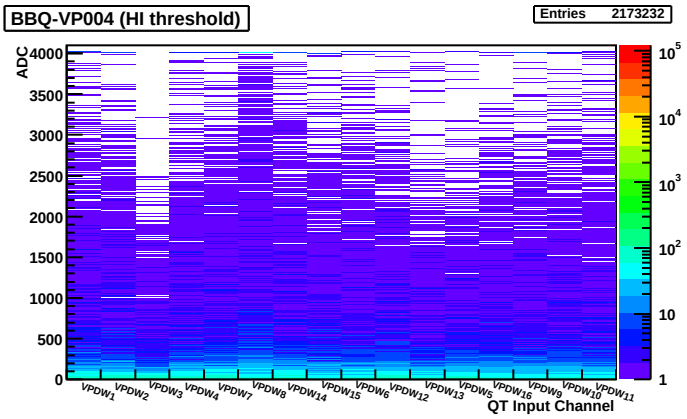

BBQ-VP001 (LO threshold)
Entries 2173232
TAC
QT Input Channel

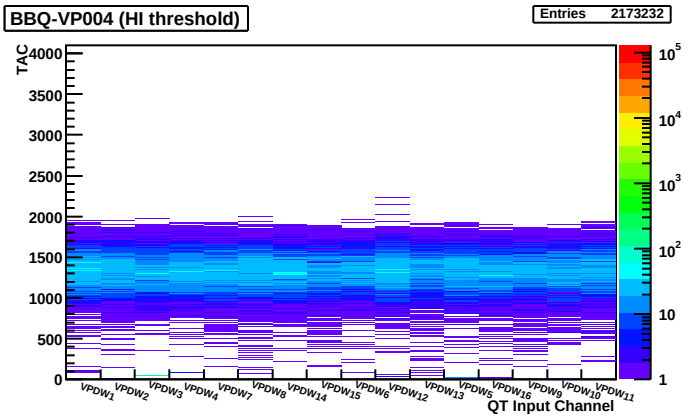

BBQ-VP002 (LO threshold)
Entries 2173232
ADC
QT Input Channel

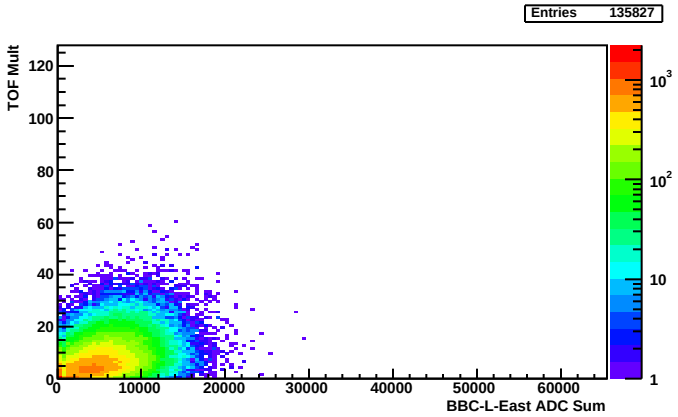

BBQ-VP002 (LO threshold)
Entries 2173232
TAC
QT Input Channel

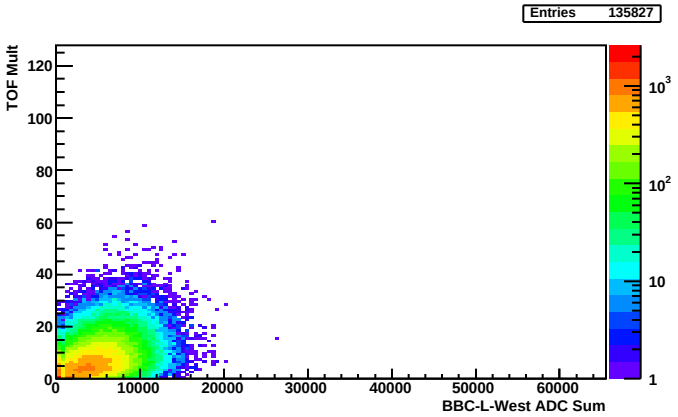

BBQ-VP003 (HI threshold)
Entries 2173232
ADC
QT Input Channel

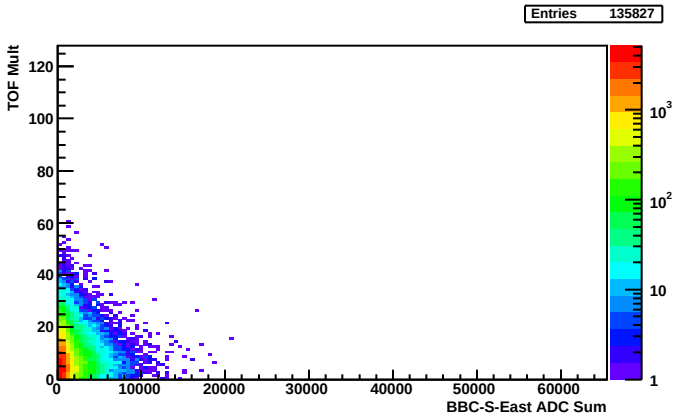

BBQ-VP003 (HI threshold)
Entries 2173232
TAC
QT Input Channel

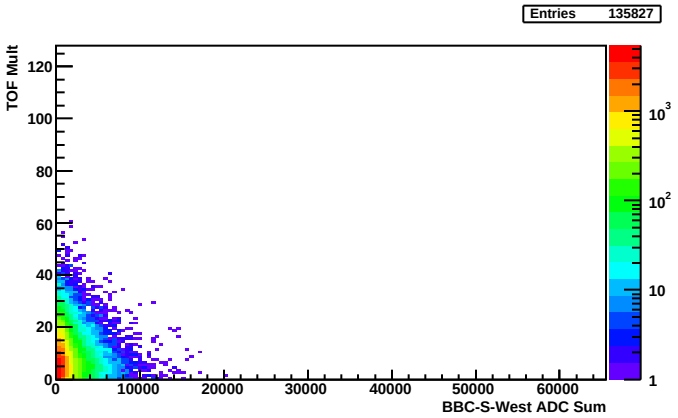

BBQ-VP004 (HI threshold)
Entries 2173232
ADC
QT Input Channel

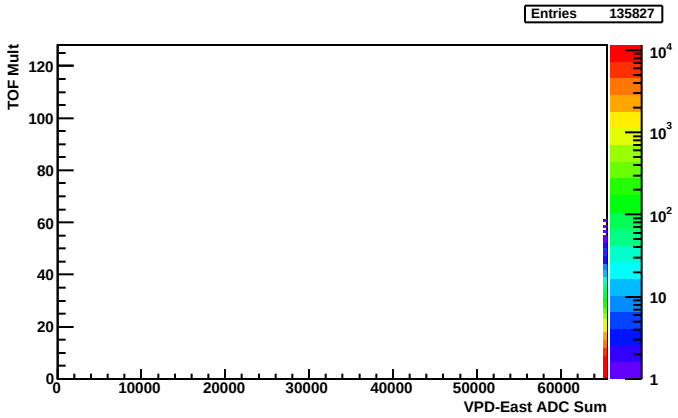

BBQ-VP004 (HI threshold)
Entries 2173232
TAC
QT Input Channel

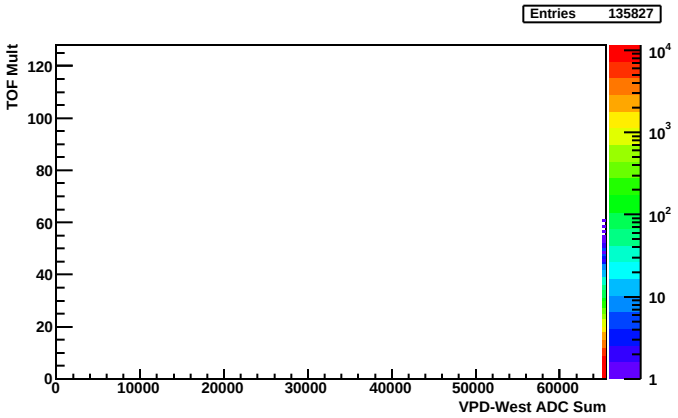

Entries 135827
TOF Mult
BBC-L-East ADC Sum

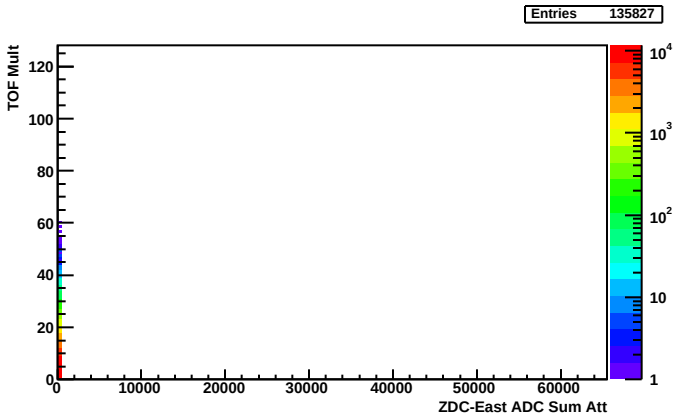

Entries 135827
TOF Mult
BBC-L-West ADC Sum

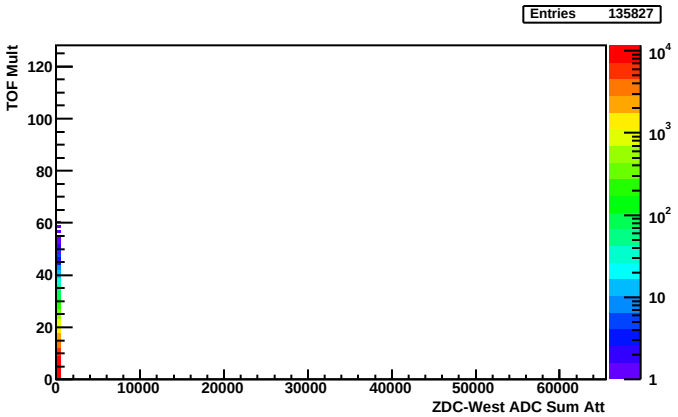

Entries 135827
TOF Mult
BBC-S-East ADC Sum

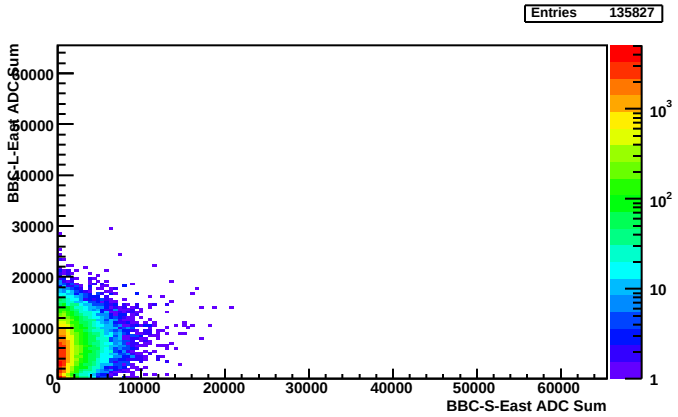

Entries 135827
TOF Mult
BBC-S-West ADC Sum

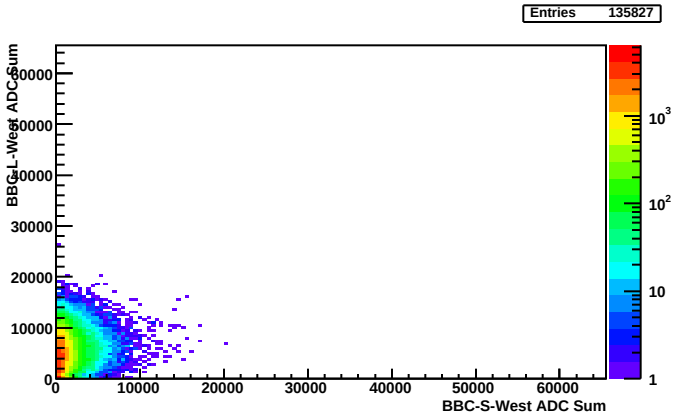

Entries 135827
TOF Mult
VPD-East ADC Sum

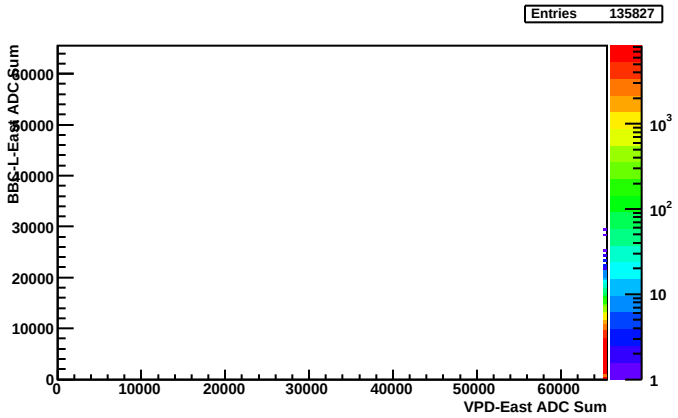

Entries 135827
TOF Mult
VPD-West ADC Sum

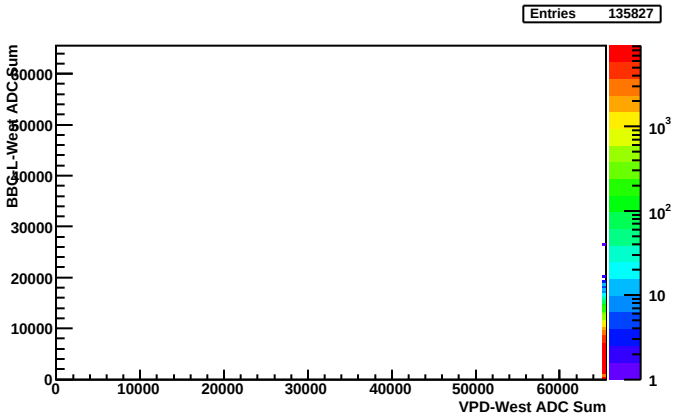

Entries 135827
TOF Mult
ZDC-East ADC Sum Att

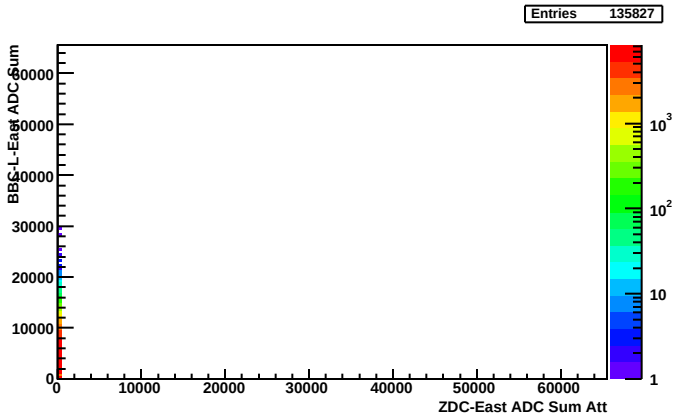

Entries 135827
TOF Mult
ZDC-West ADC Sum Att

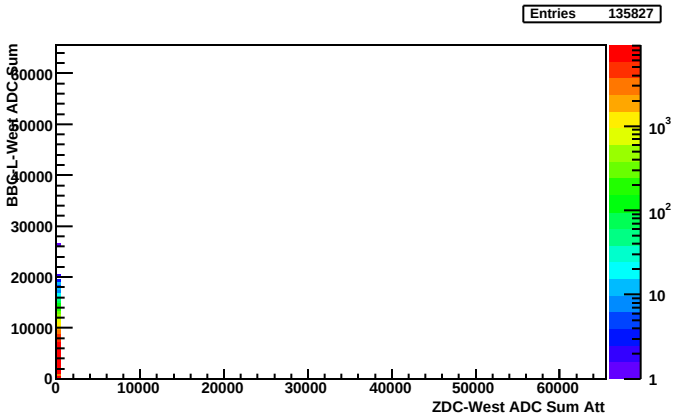

Entries 135827
BBC-L-East ADC Sum
BBC-S-East ADC Sum

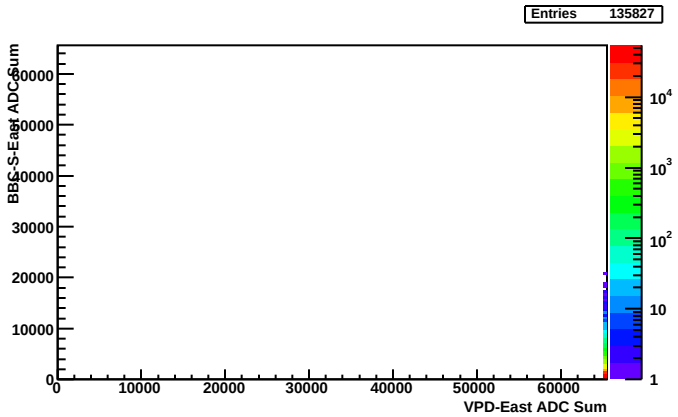

Entries 135827
BBC-L-West ADC Sum
BBC-S-West ADC Sum

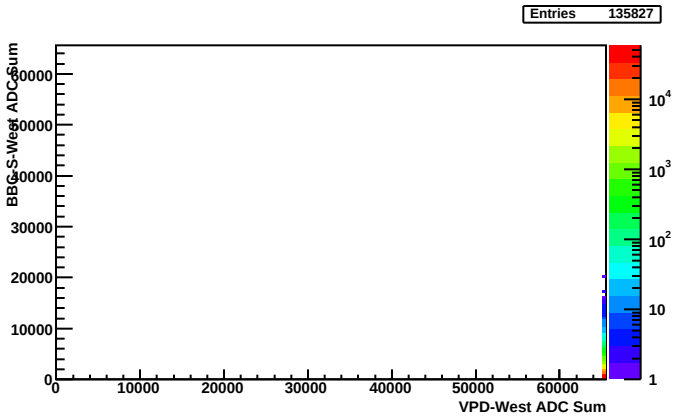

Entries 135827
BBC-L-East ADC Sum
VPD-East ADC Sum

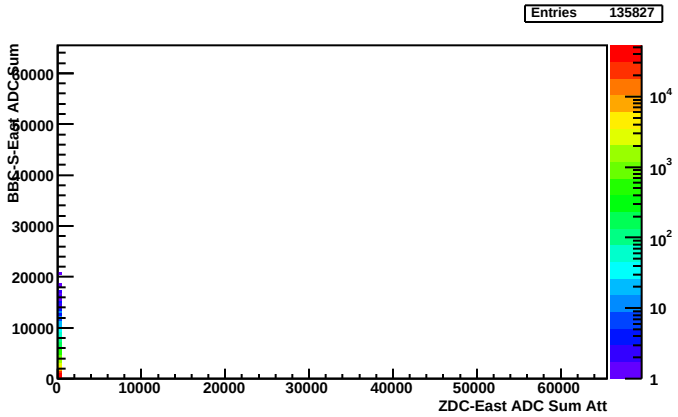

Entries 135827
BBC-L-West ADC Sum
VPD-West ADC Sum

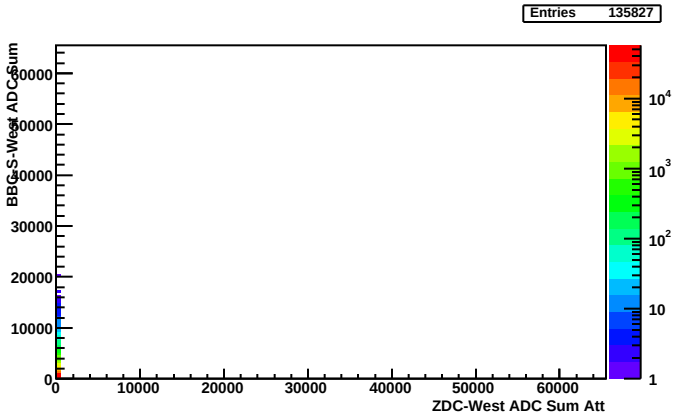

Entries 135827
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

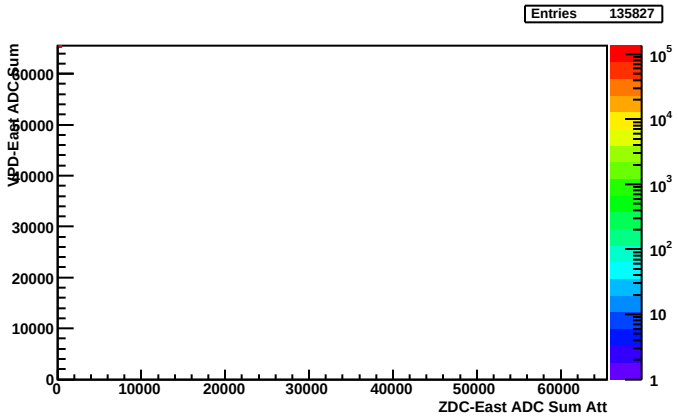

Entries 135827
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

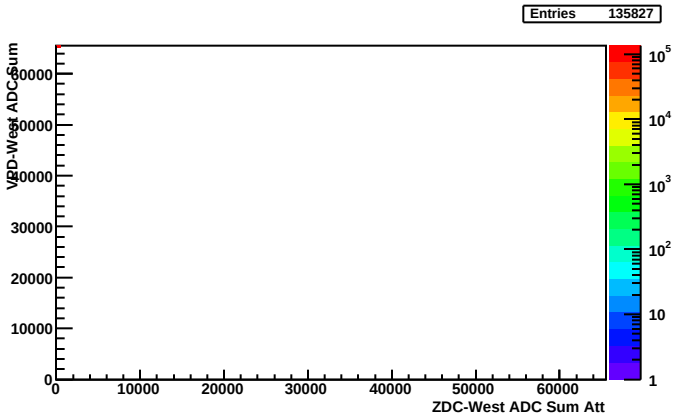

Entries 135827
BBC-S-East ADC Sum
VPD-East ADC Sum

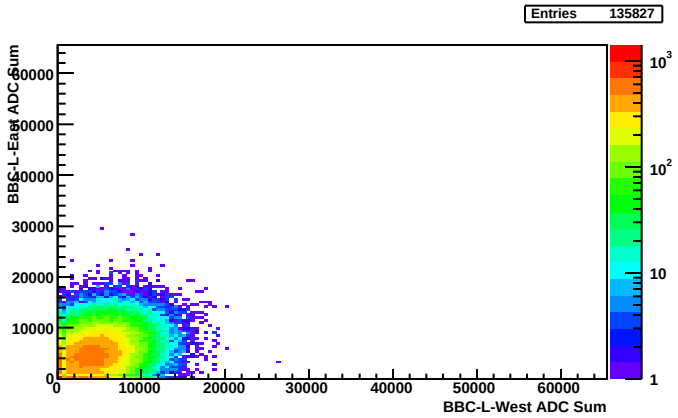

Entries 135827
BBC-S-West ADC Sum
VPD-West ADC Sum

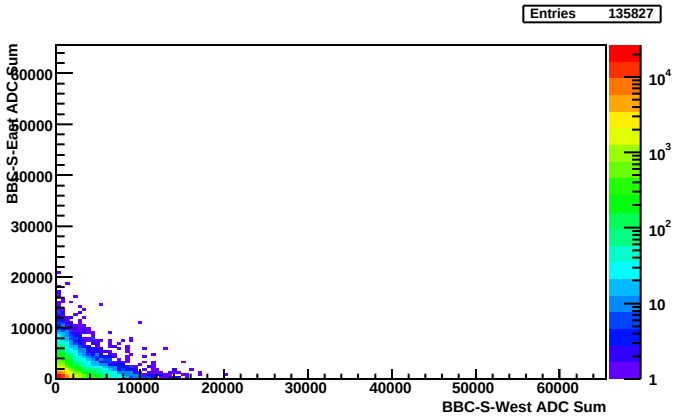

Entries 135827
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

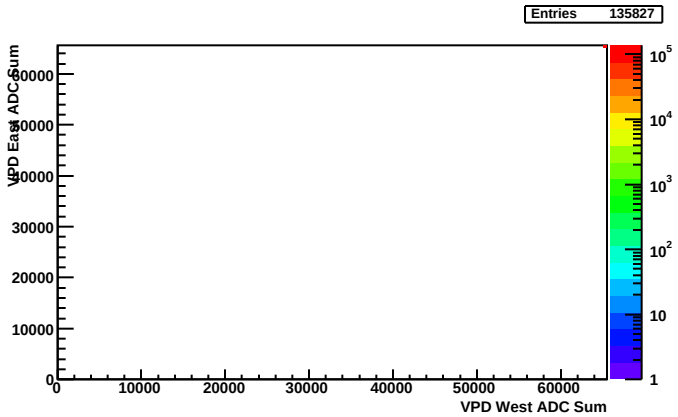

Entries 135827
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

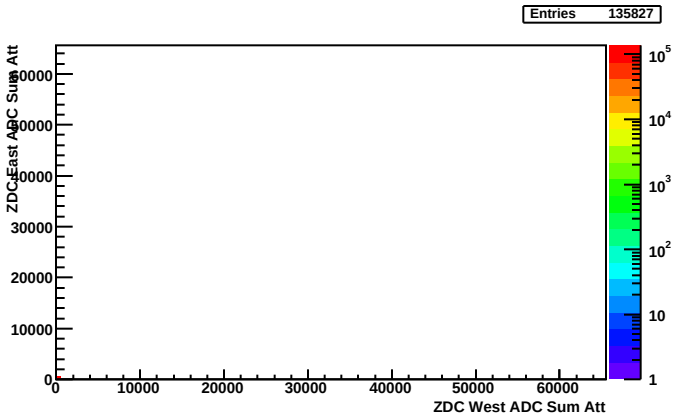

Entries 135827
VPD-East ADC Sum
ZDC-East ADC Sum Att

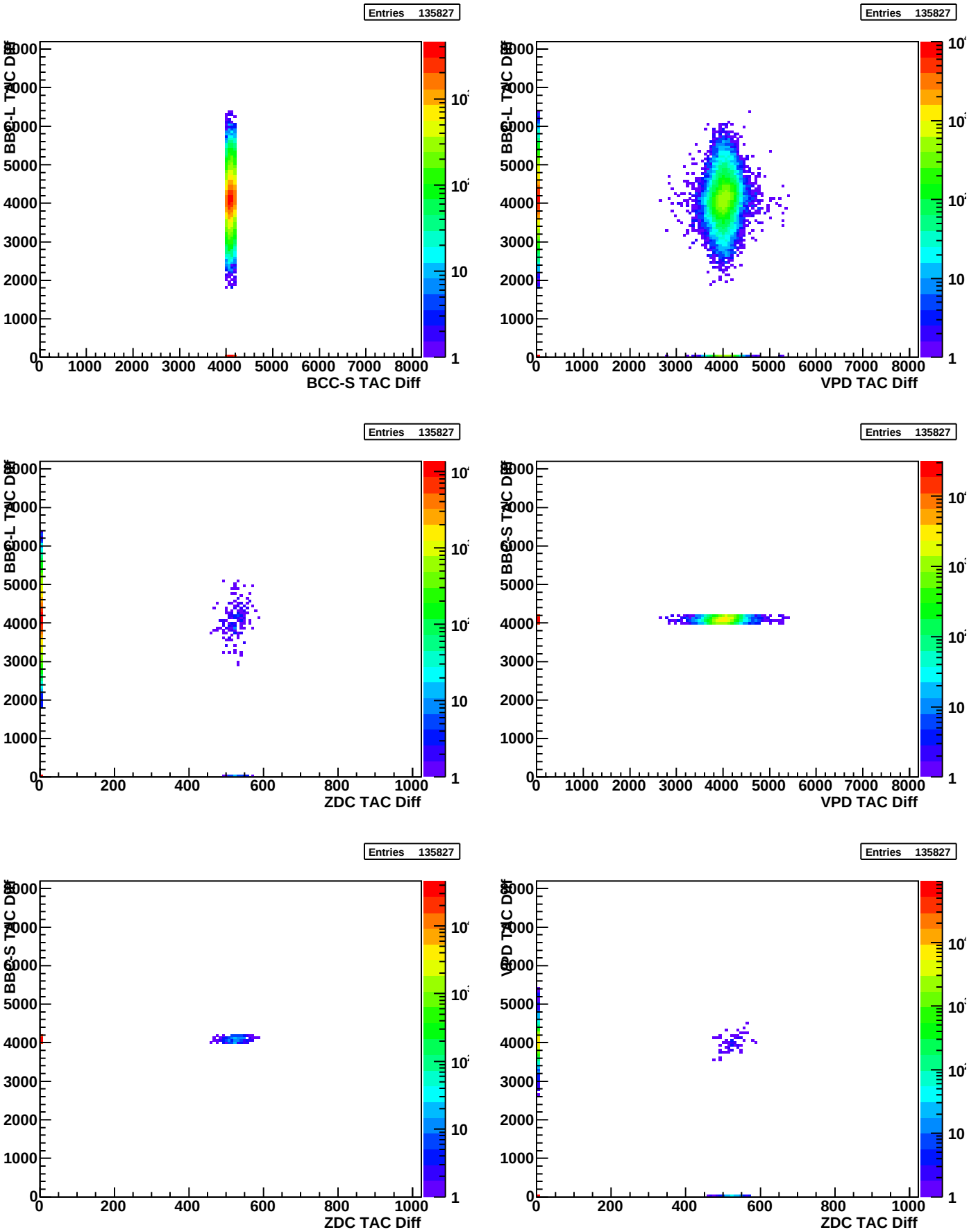

Entries 135827
VPD-West ADC Sum
ZDC-West ADC Sum Att

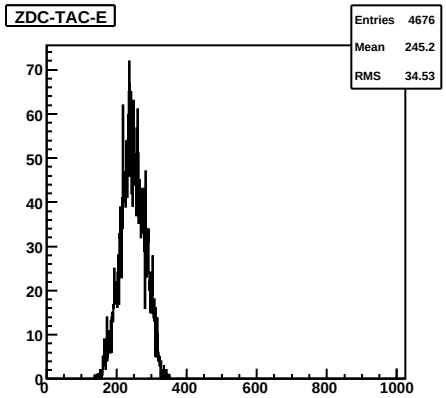

Entries 135827
BBC-L-East ADC Sum
BBC-L-West ADC Sum

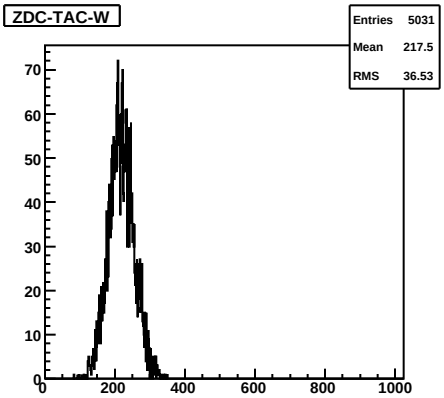

Entries 135827
BBC-S-East ADC Sum
BBC-S-West ADC Sum

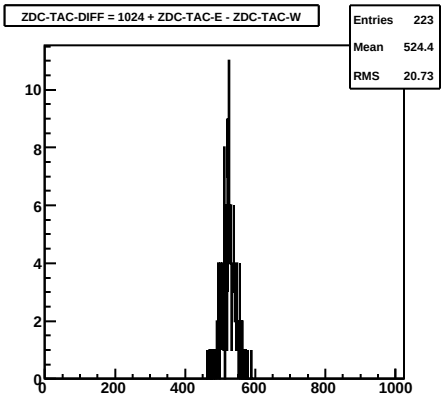

Entries 135827
VPD East ADC Sum
VPD West ADC Sum

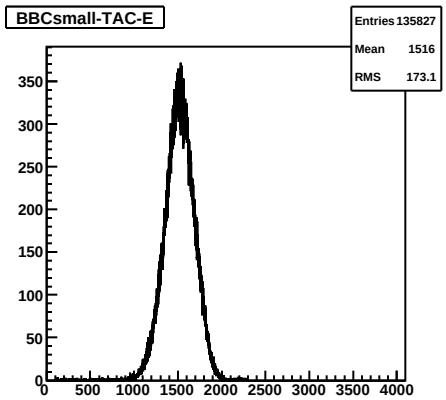

Entries 135827
ZDC East ADC Sum Att
ZDC West ADC Sum Att

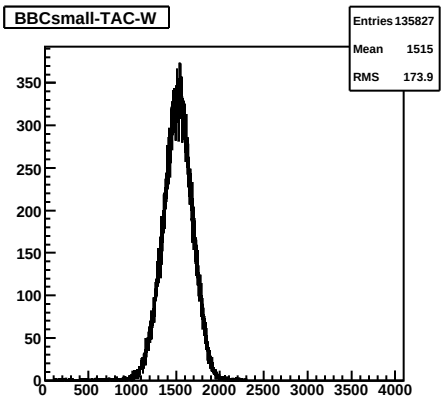

Entries 135827
BBC-L TAC Diff
BCC-S TAC Diff
Entries 135827
BBC-L TAC Diff
VPD TAC Diff
Entries 135827
BBC-L TAC Diff
ZDC TAC Diff
Entries 135827
BBC-S TAC Diff
VPD TAC Diff
Entries 135827
BBC-S TAC Diff
ZDC TAC Diff
Entries 135827
VPD TAC Diff
ZDC TAC Diff

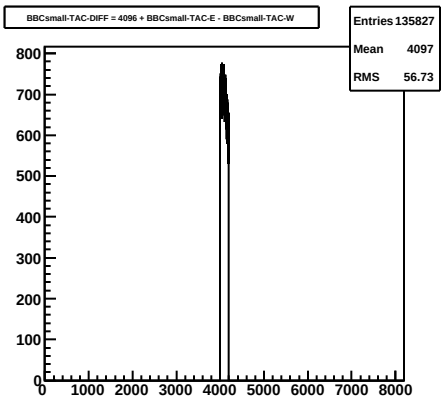

ZDC-TAC-E
Entries 4676
Mean 245.2
RMS 34.53

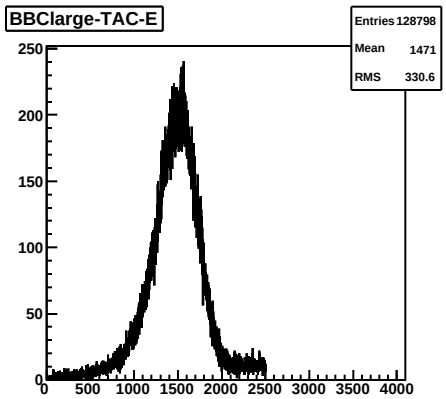

ZDC-TAC-W
Entries 5031
Mean 217.5
RMS 36.53

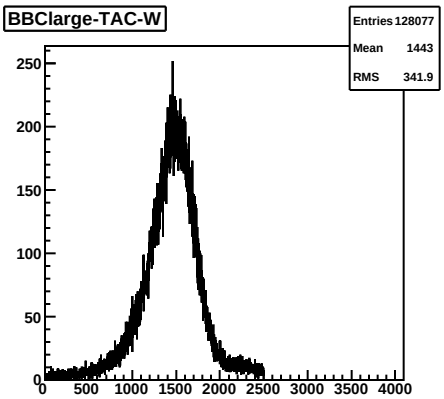

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 223
Mean 524.4
RMS 20.73

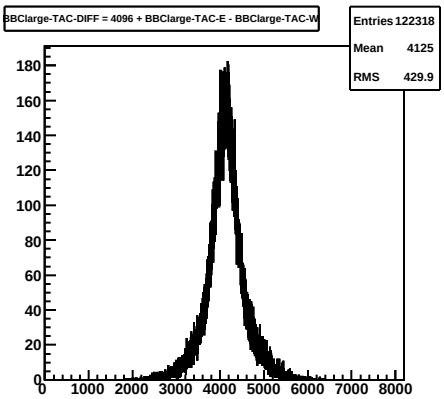

BBCsmall-TAC-E
Entries 135827
Mean 1516
RMS 173.1

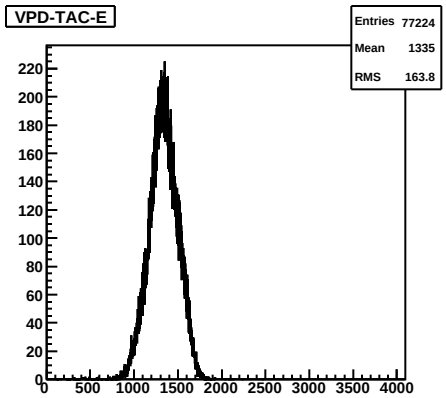

BBCsmall-TAC-W
Entries 135827
Mean 1515
RMS 173.9

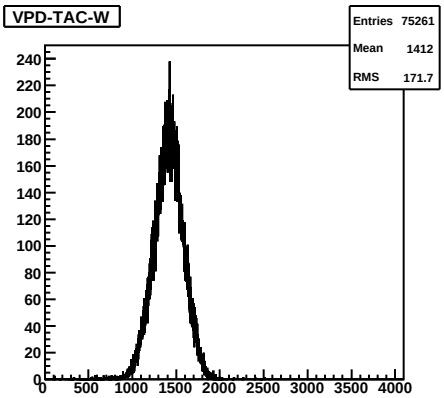

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 135827
Mean 4097
RMS 56.73

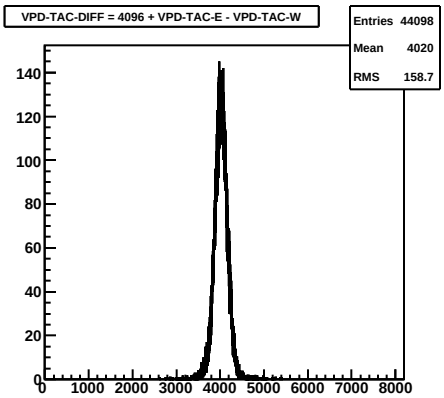

BBClarge-TAC-E
Entries 128798
Mean 1471
RMS 330.6

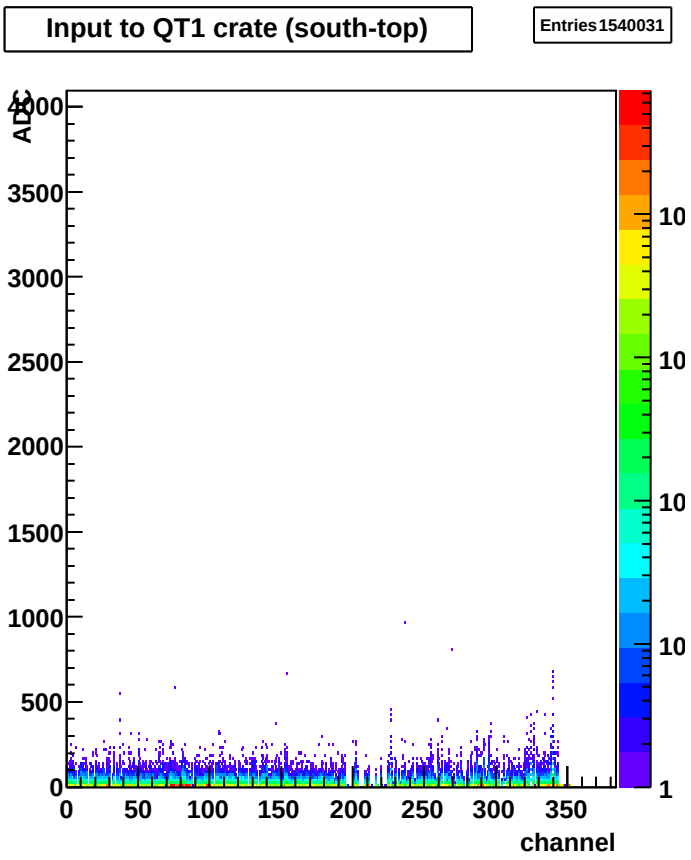

BBClarge-TAC-W
Entries 128077
Mean 1443
RMS 341.9

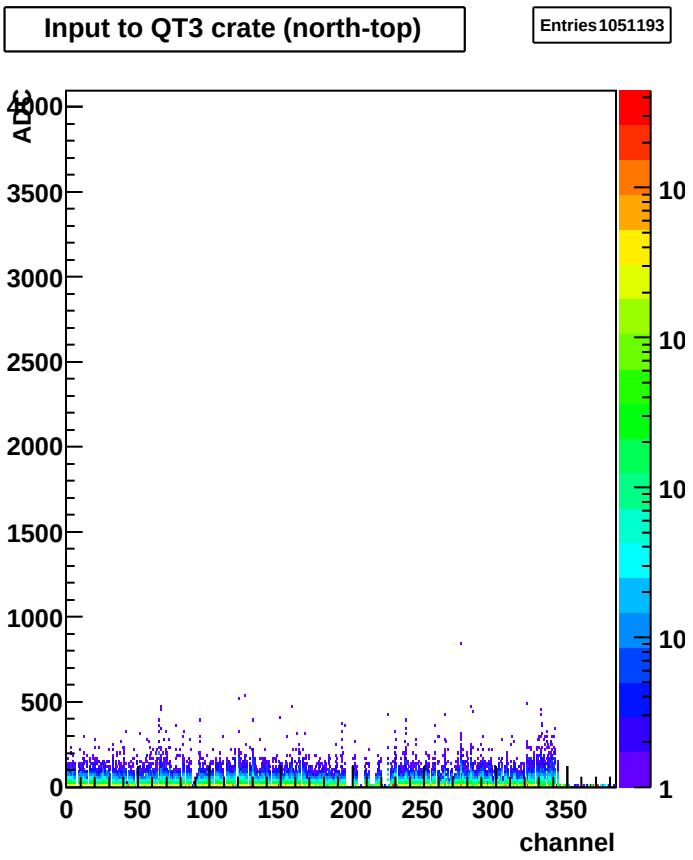

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 122318
Mean 4125
RMS 429.9

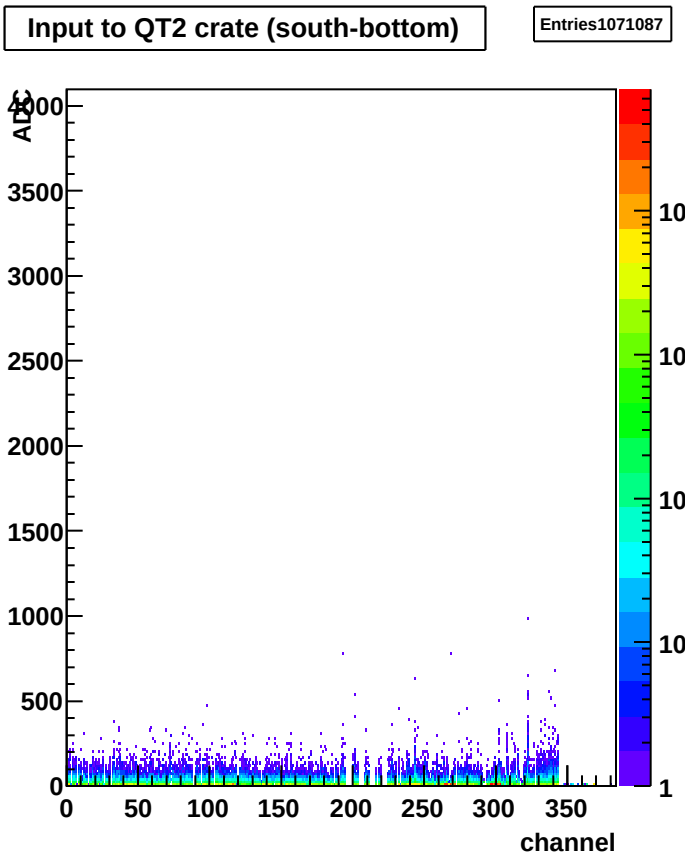

VPD-TAC-E
Entries 77224
Mean 1335
RMS 163.8

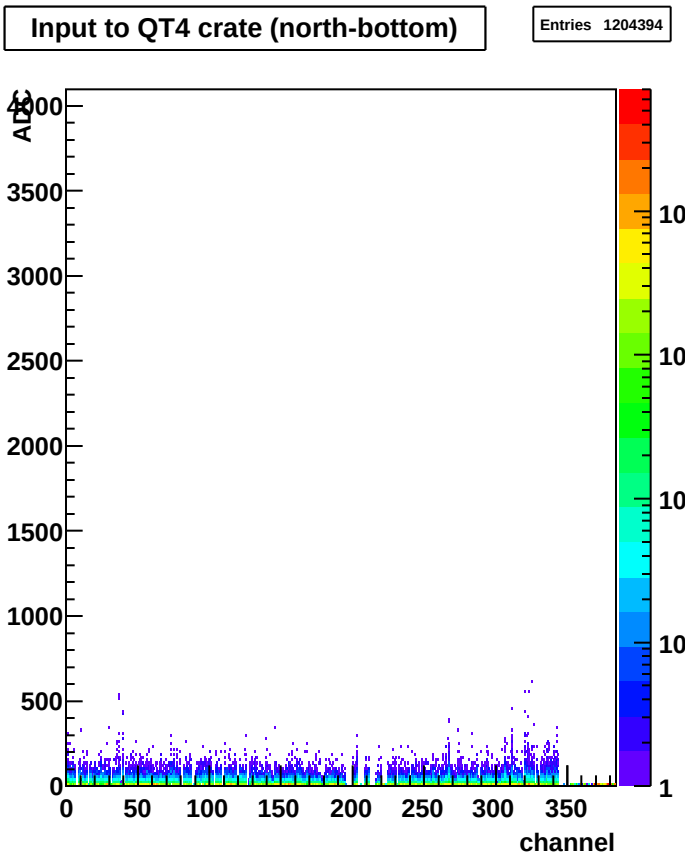

VPD-TAC-W
Entries 75261
Mean 1412
RMS 171.7

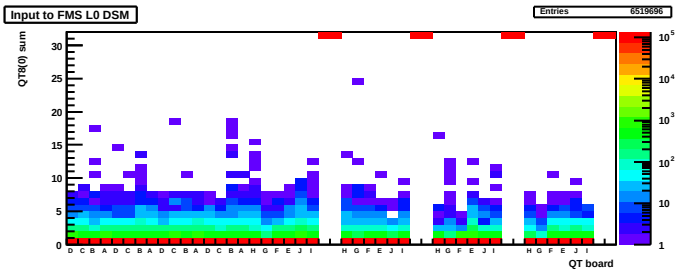

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 44098
Mean 4020
RMS 158.7

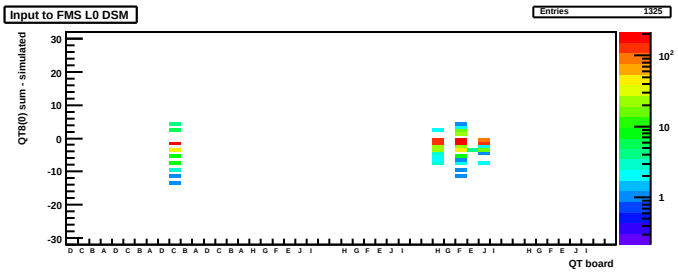

Input to QT1 crate (south-top)
Entries 1540031
ADC
channel

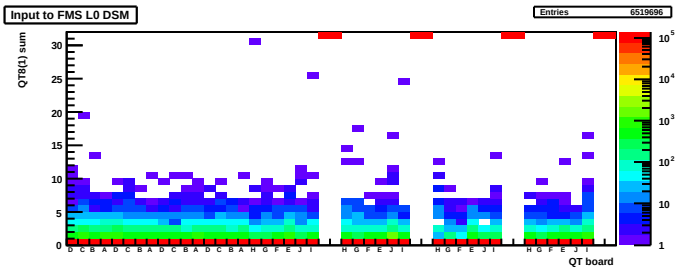

Input to QT3 crate (north-top)
Entries 1051193
ADC
channel

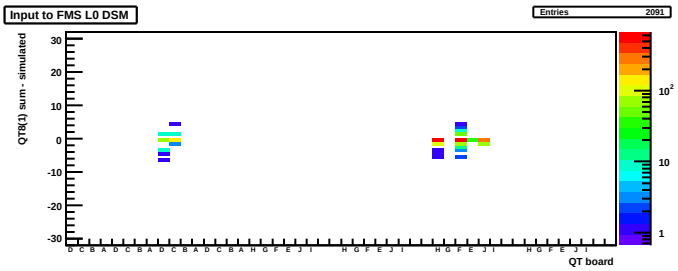

Input to QT2 crate (south-bottom)
Entries 1071087
ADC
channel

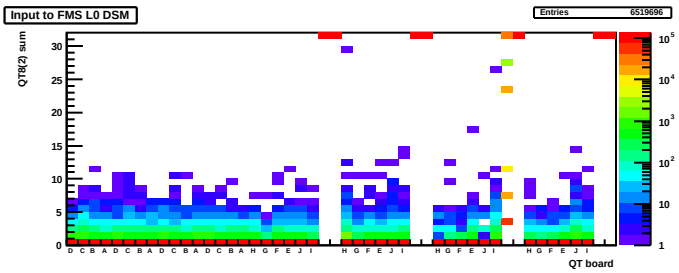

Input to QT4 crate (north-bottom)
Entries 1204394
ADC
channel

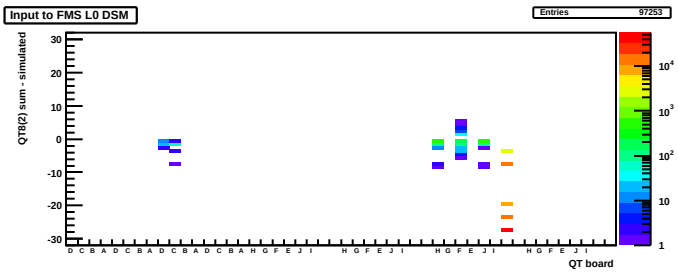

Input to FMS L0 DSM
Entries 6519696
QT8(0) sum
QT board

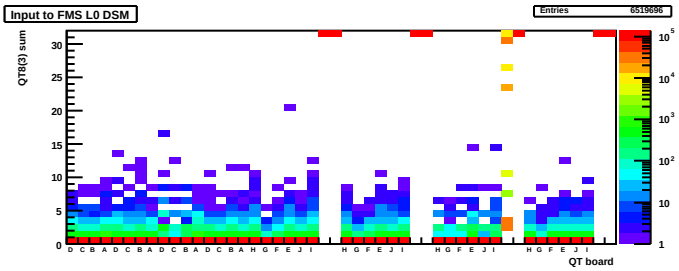

Input to FMS L0 DSM
Entries 1325
QT8(0) sum - simulated
QT board

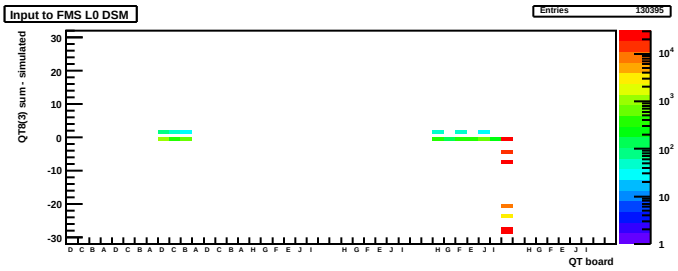

Input to FMS L0 DSM
Entries 6519696
QT8(1) sum
QT board

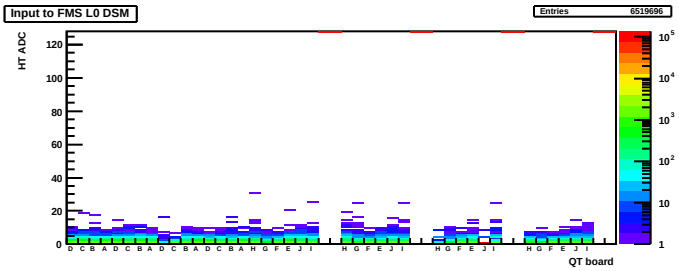

Input to FMS L0 DSM
Entries 2091
QT8(1) sum - simulated
QT board

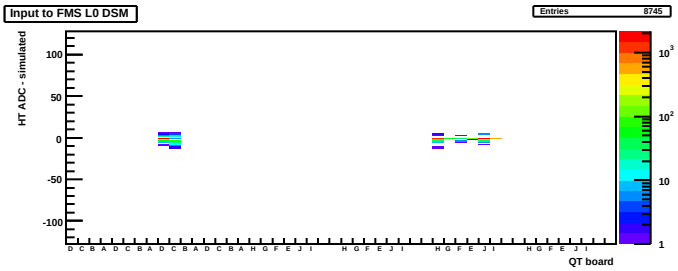

Input to FMS L0 DSM
Entries 6519696
QT8(2) sum
QT board

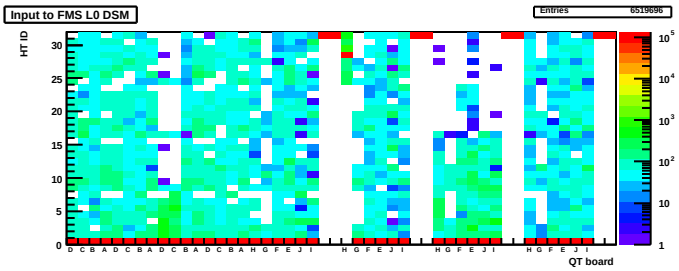

Input to FMS L0 DSM
Entries 97253
QT8(2) sum - simulated
QT board

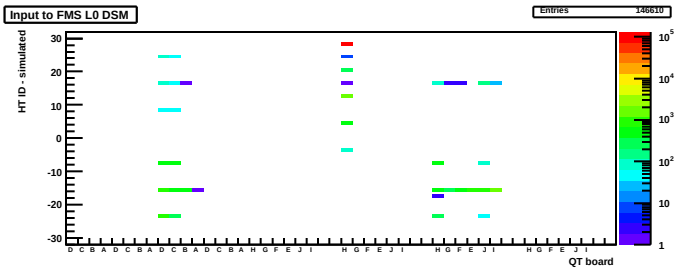

Input to FMS L0 DSM
Entries 6519696
QT8(3) sum
QT board

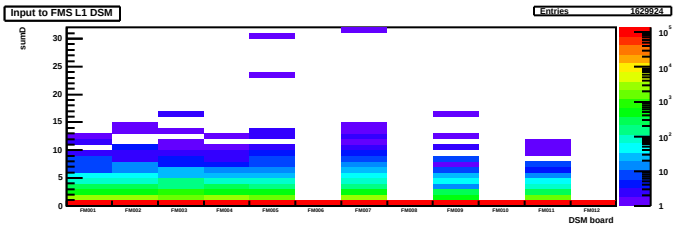

Input to FMS L0 DSM
Entries 130395
QT8(3) sum - simulated
QT board

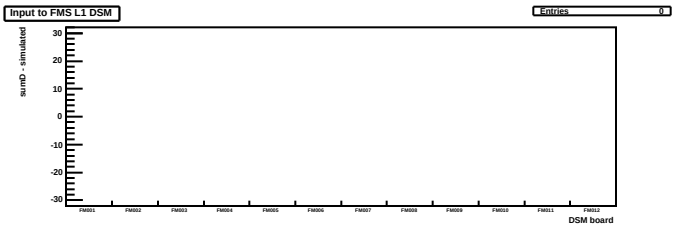

Input to FMS L0 DSM
Entries 6519696
HT ADC
QT board

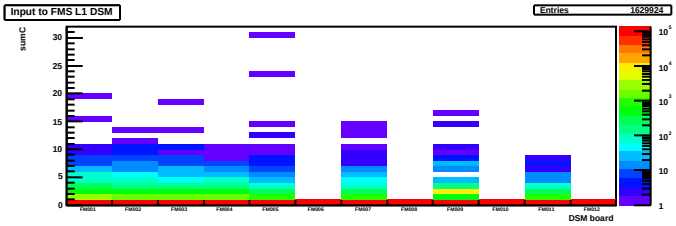

Input to FMS L0 DSM
Entries 8745
HT ADC - simulated
QT board

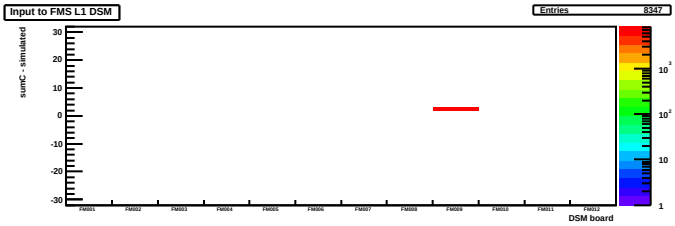

Input to FMS L0 DSM
Entries 6519696
HT ID
QT board

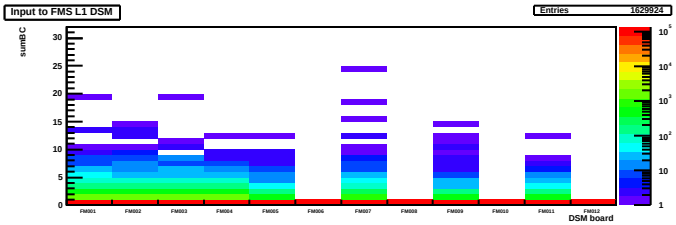

Input to FMS L0 DSM
Entries 146610
HT ID - simulated
QT board

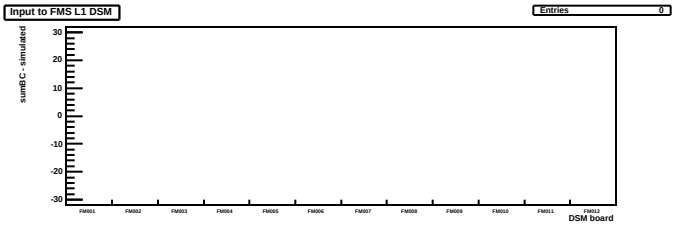

Input to FMS L1 DSM
Entries 1629924
sumD
DSM board

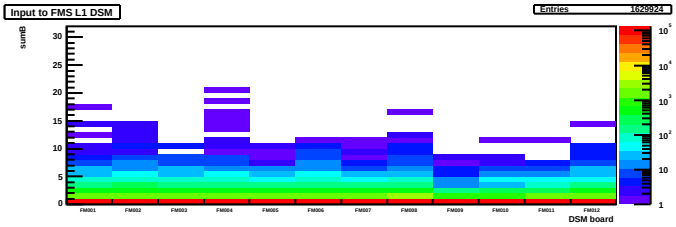

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

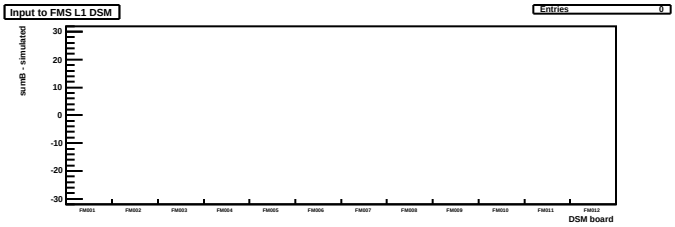

Input to FMS L1 DSM
Entries 1629924
sumC
DSM board

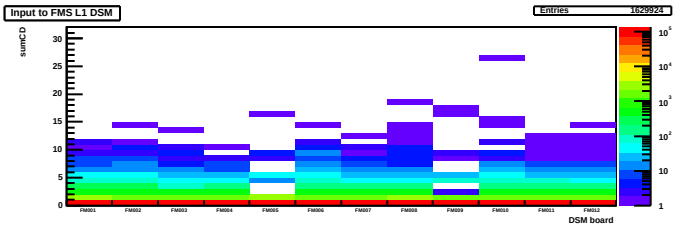

Input to FMS L1 DSM
Entries 8347
sumC - simulated
DSM board

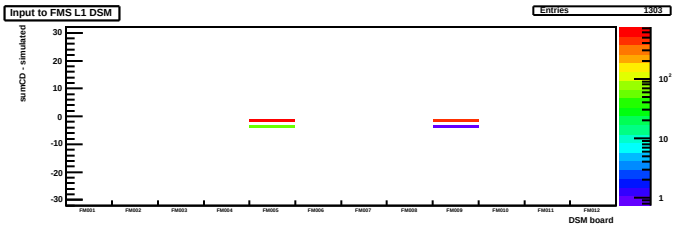

Input to FMS L1 DSM
Entries 1629924
sumBC
DSM board

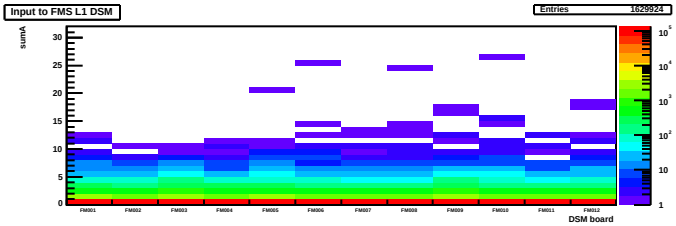

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

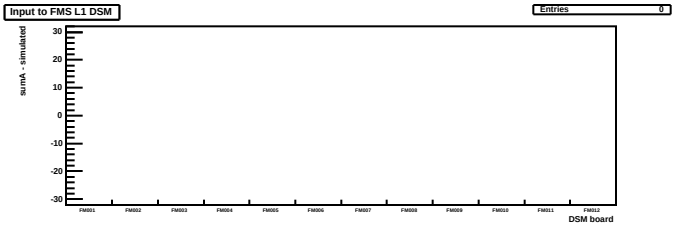

Input to FMS L1 DSM
Entries 1629924
sumB
DSM board

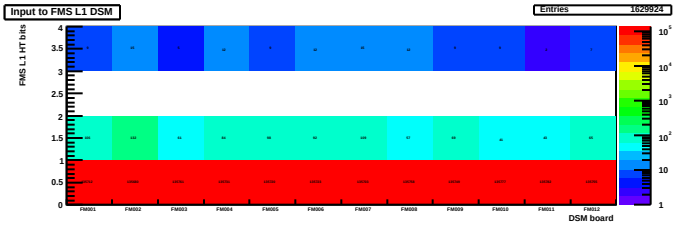

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

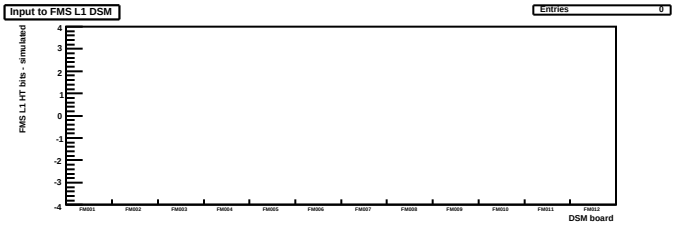

Input to FMS L1 DSM
Entries 1629924
sumCD
DSM board

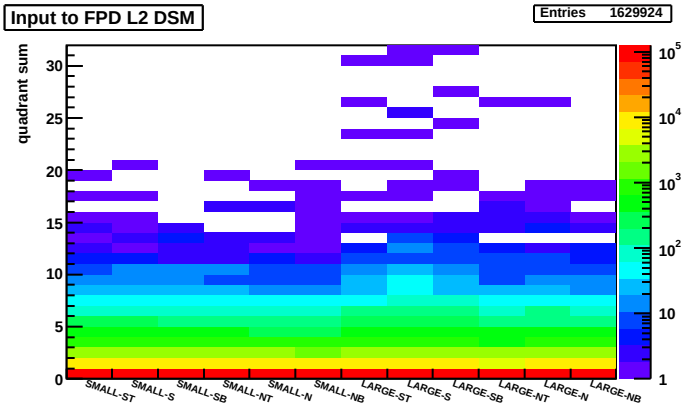

Input to FMS L1 DSM
Entries 1303
sumCD - simulated
DSM board

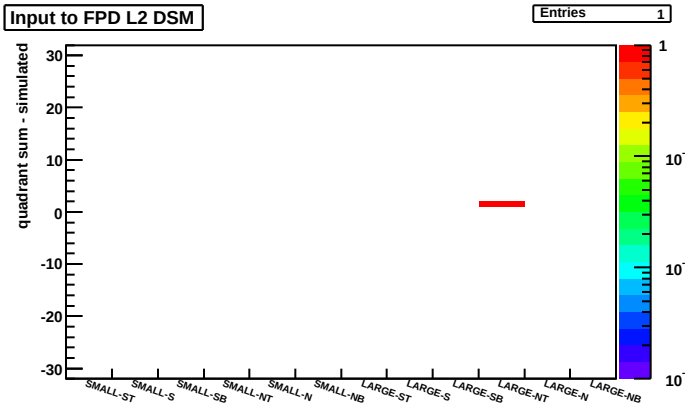

Input to FMS L1 DSM
Entries 1629924
sumA
DSM board

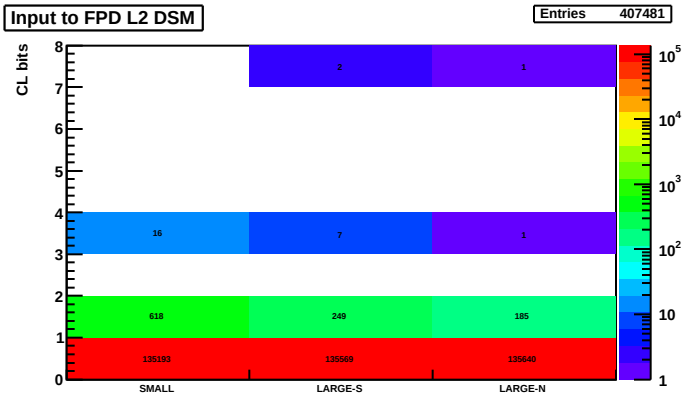

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

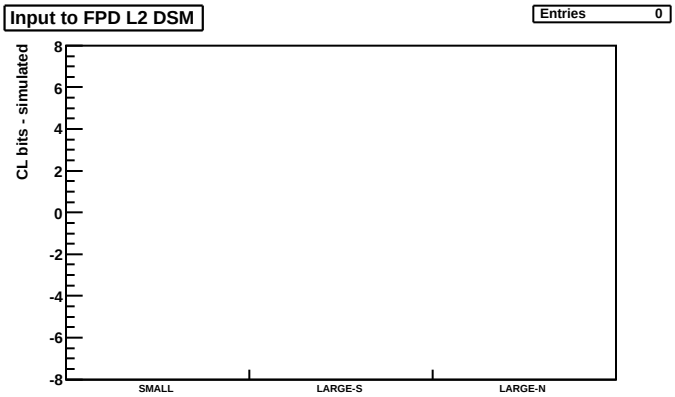

Input to FMS L1 DSM
Entries 1629924
FMS L1 HT bits
DSM board

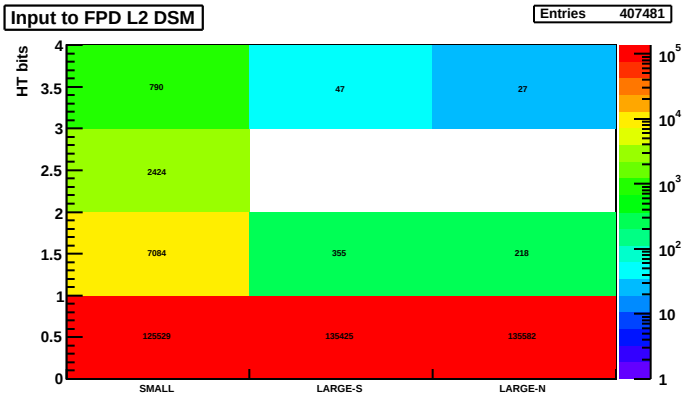

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

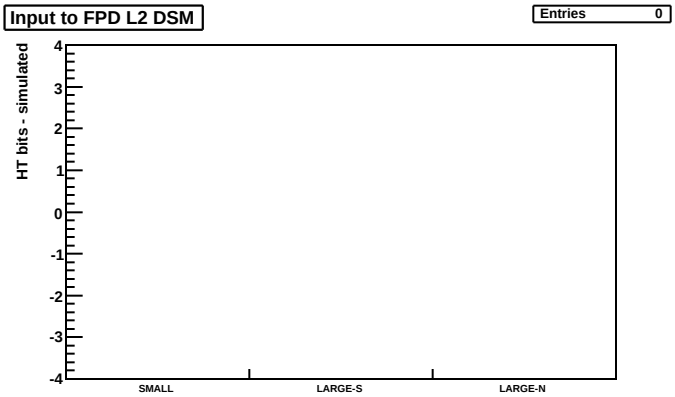

Input to FPD L2 DSM
Entries 1629924
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

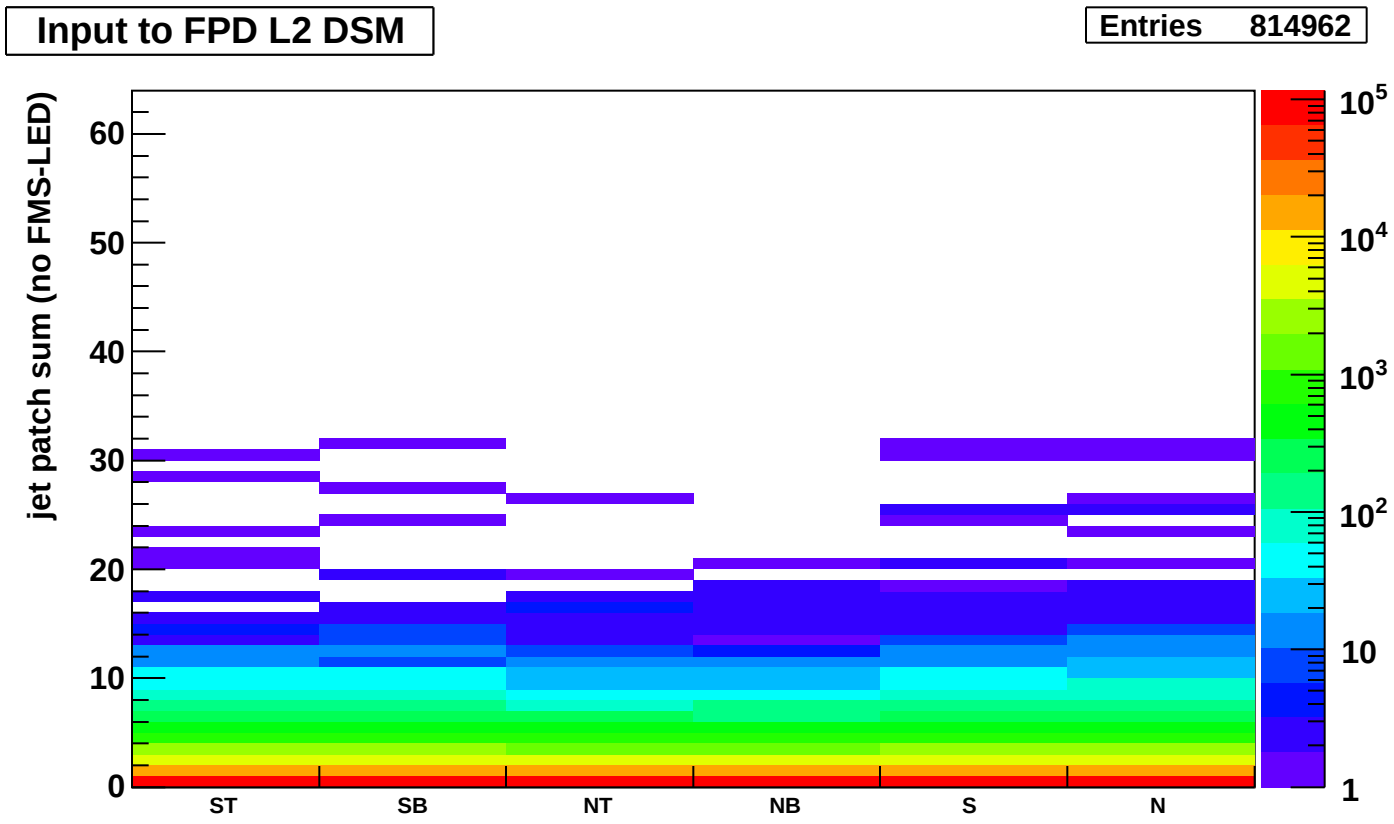

Input to FPD L2 DSM
Entries 1
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

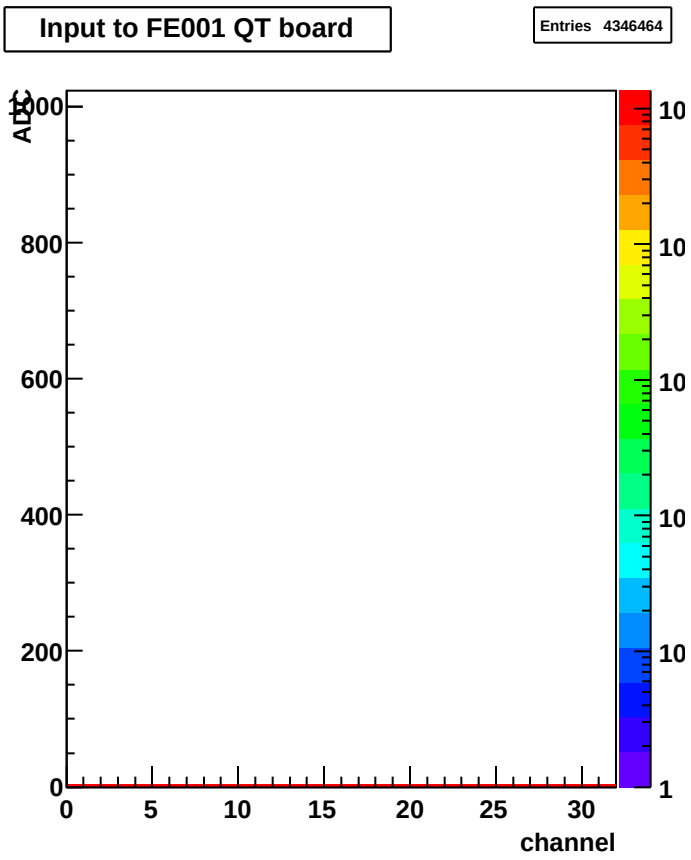

Input to FPD L2 DSM
Entries 407481
CL bits
2
1
16
7
1
618
249
185
135193
135569
135640
SMALL
LARGE-S
LARGE-N

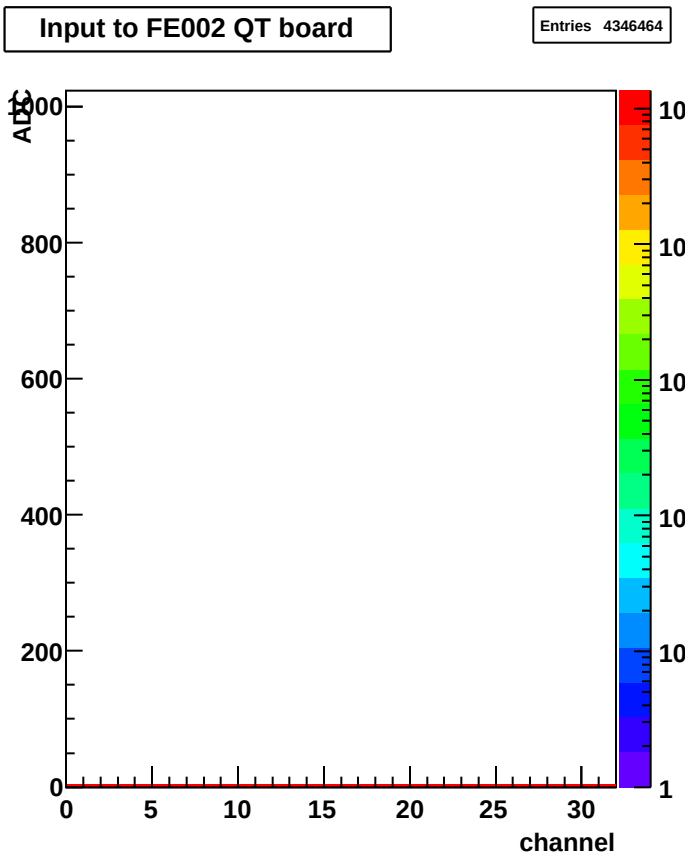

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

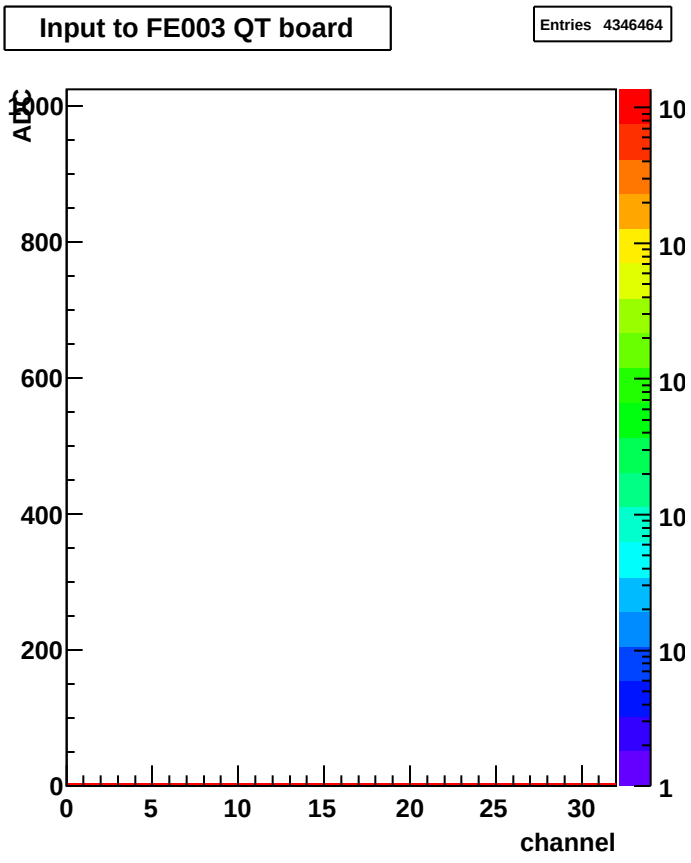

Input to FPD L2 DSM
Entries 407481
HT bits
790
47
27
2424
7084
355
218
125529
135425
135582
SMALL
LARGE-S
LARGE-N

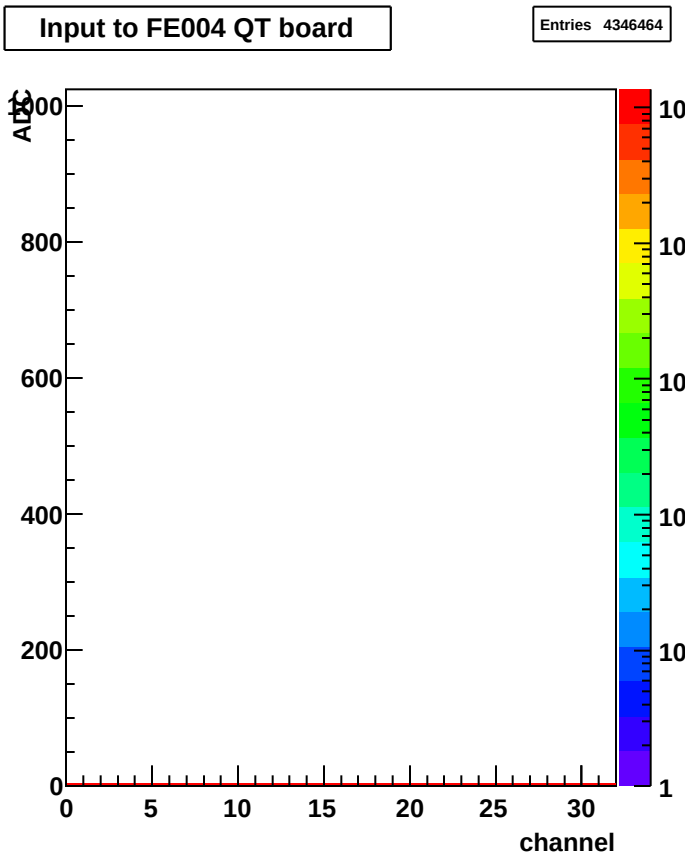

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

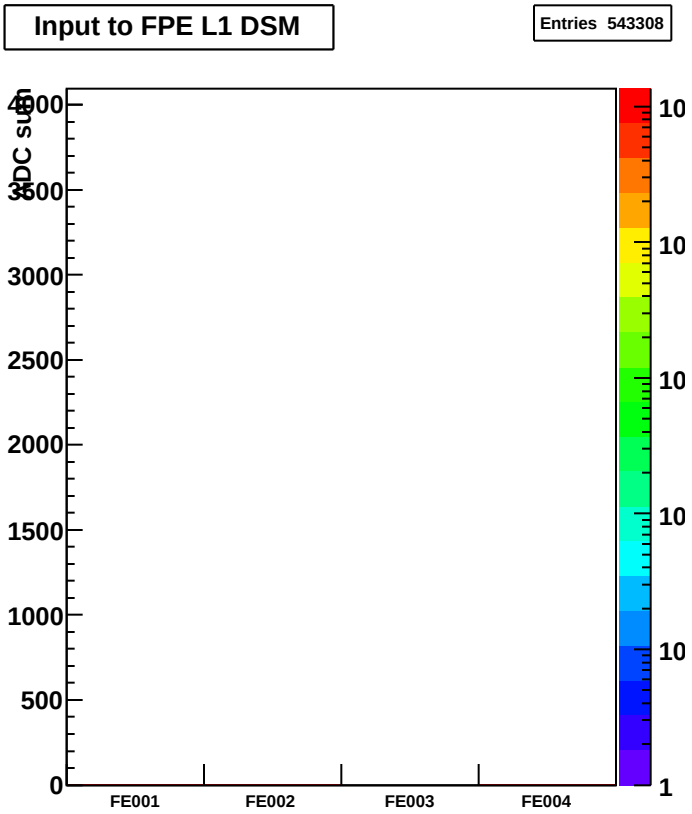

Input to FPD L2 DSM
Entries 814962
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

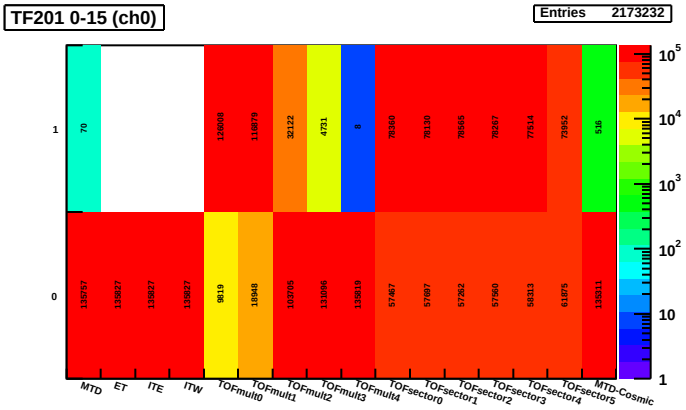

Input to FE001 QT board
Entries 4346464
ADC
channel

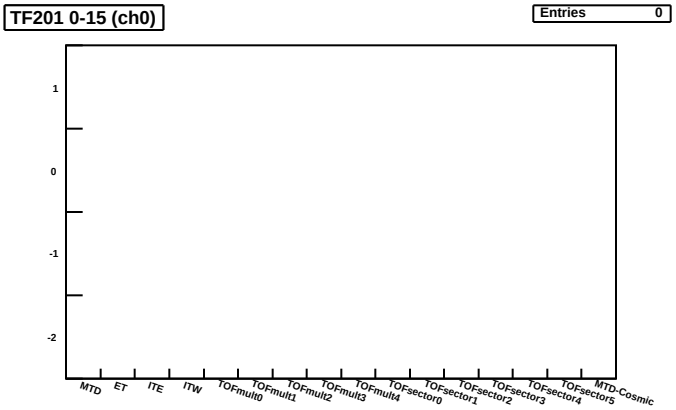

Input to FE002 QT board
Entries 4346464
ADC
channel

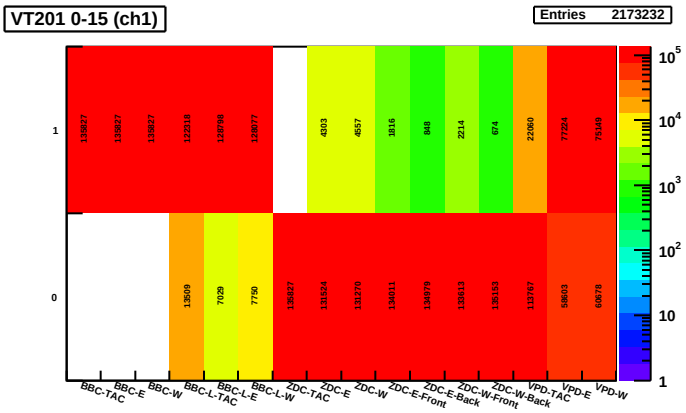

Input to FE003 QT board
Entries 4346464
ADC
channel

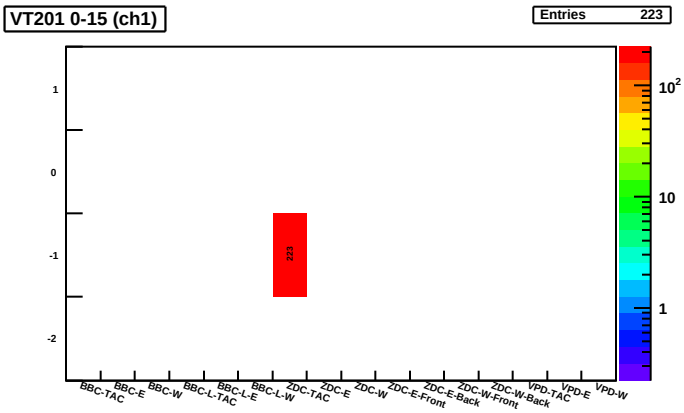

Input to FE004 QT board
Entries 4346464
ADC
channel

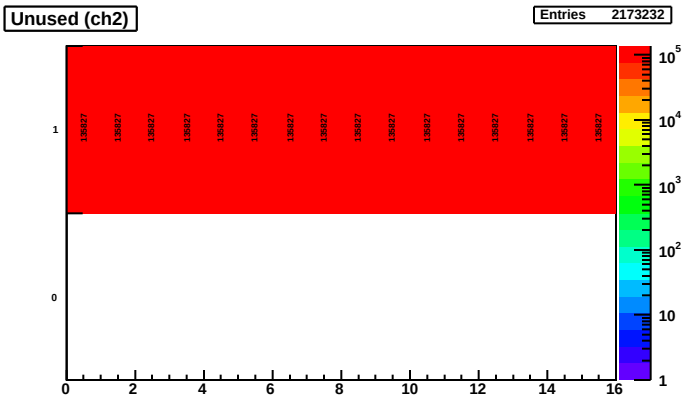

Input to FPE L1 DSM
Entries 543308
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

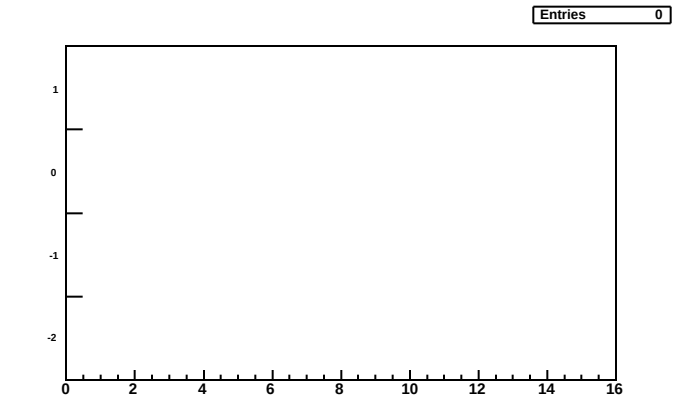

TF201 0-15 (ch0)
Entries 2173232
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

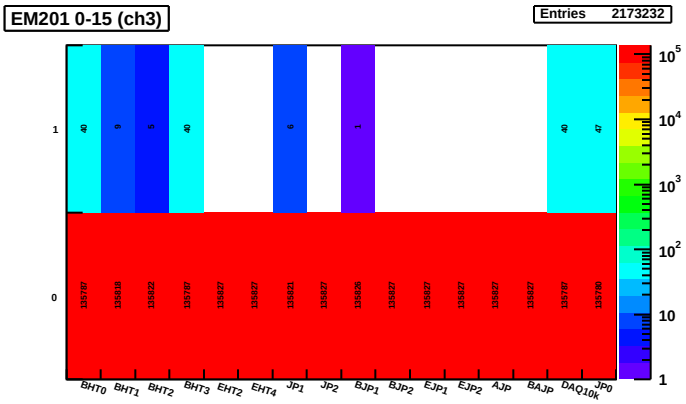

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

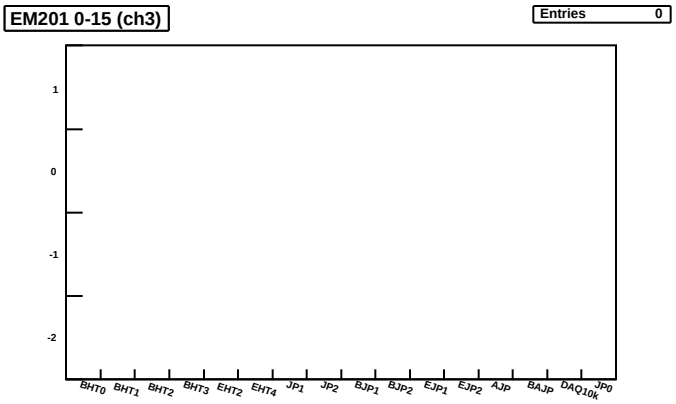

VT201 0-15 (ch1)
Entries 2173232
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 223
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 2173232


Entries 0


EM201 0-15 (ch3)
Entries 2173232
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0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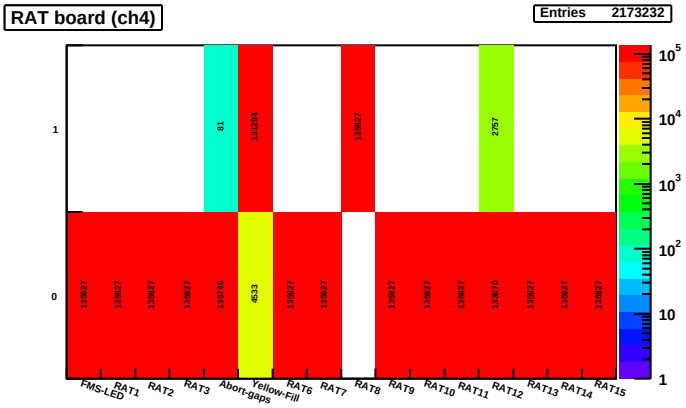

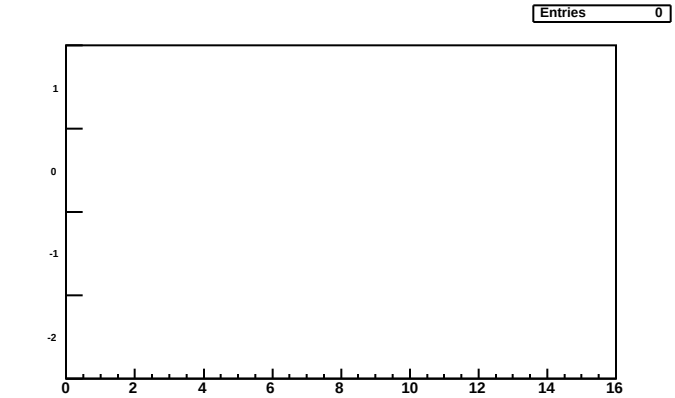

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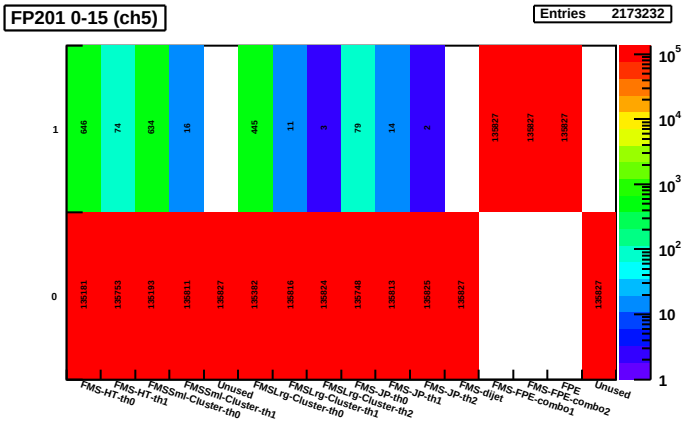

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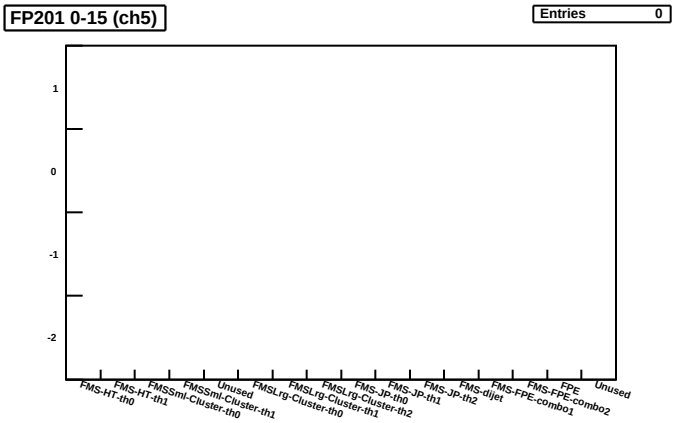

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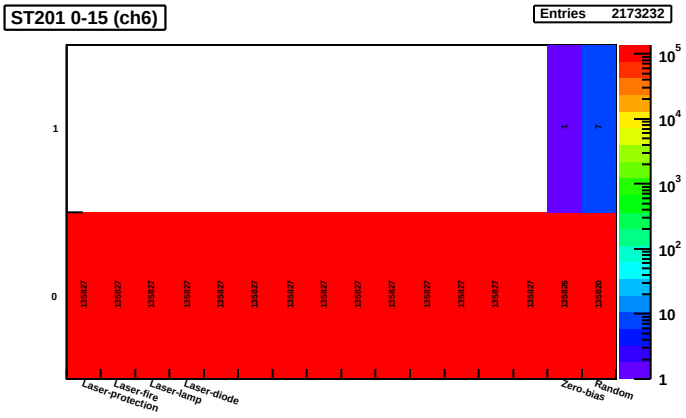

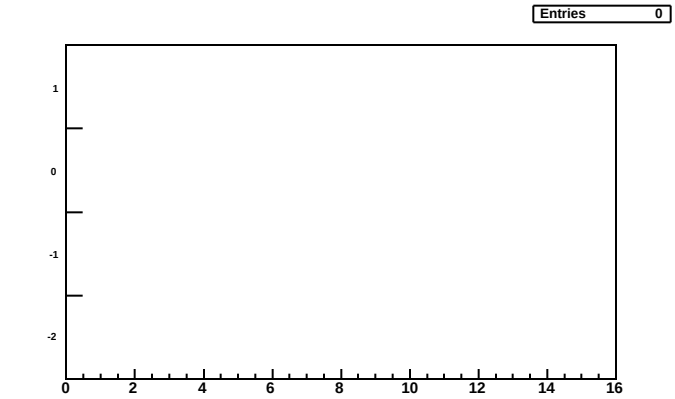
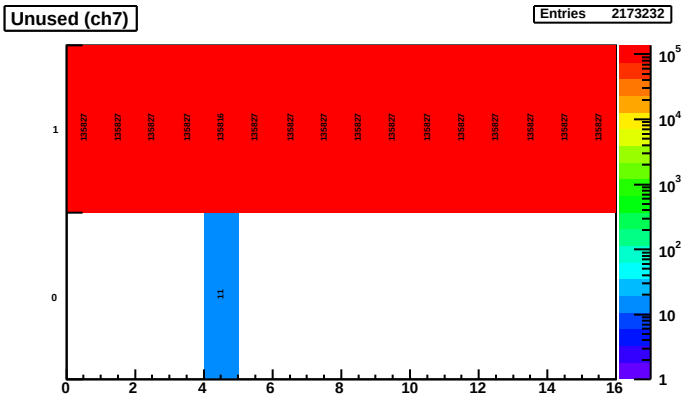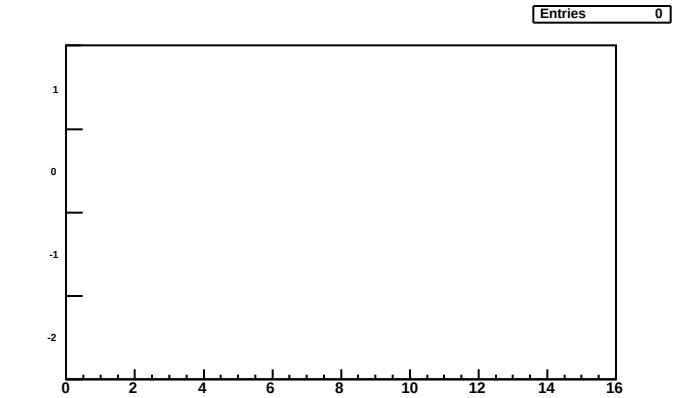
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