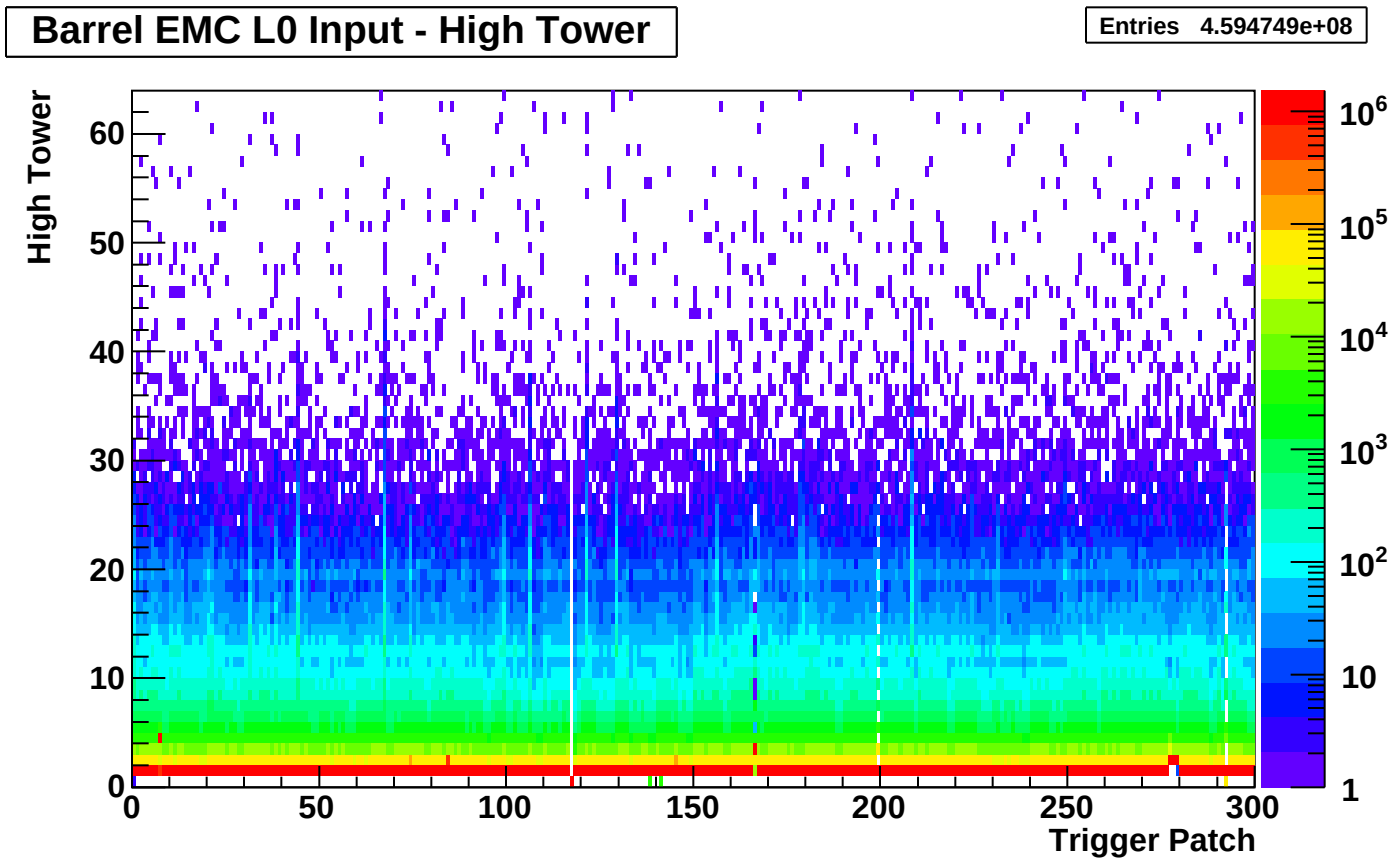
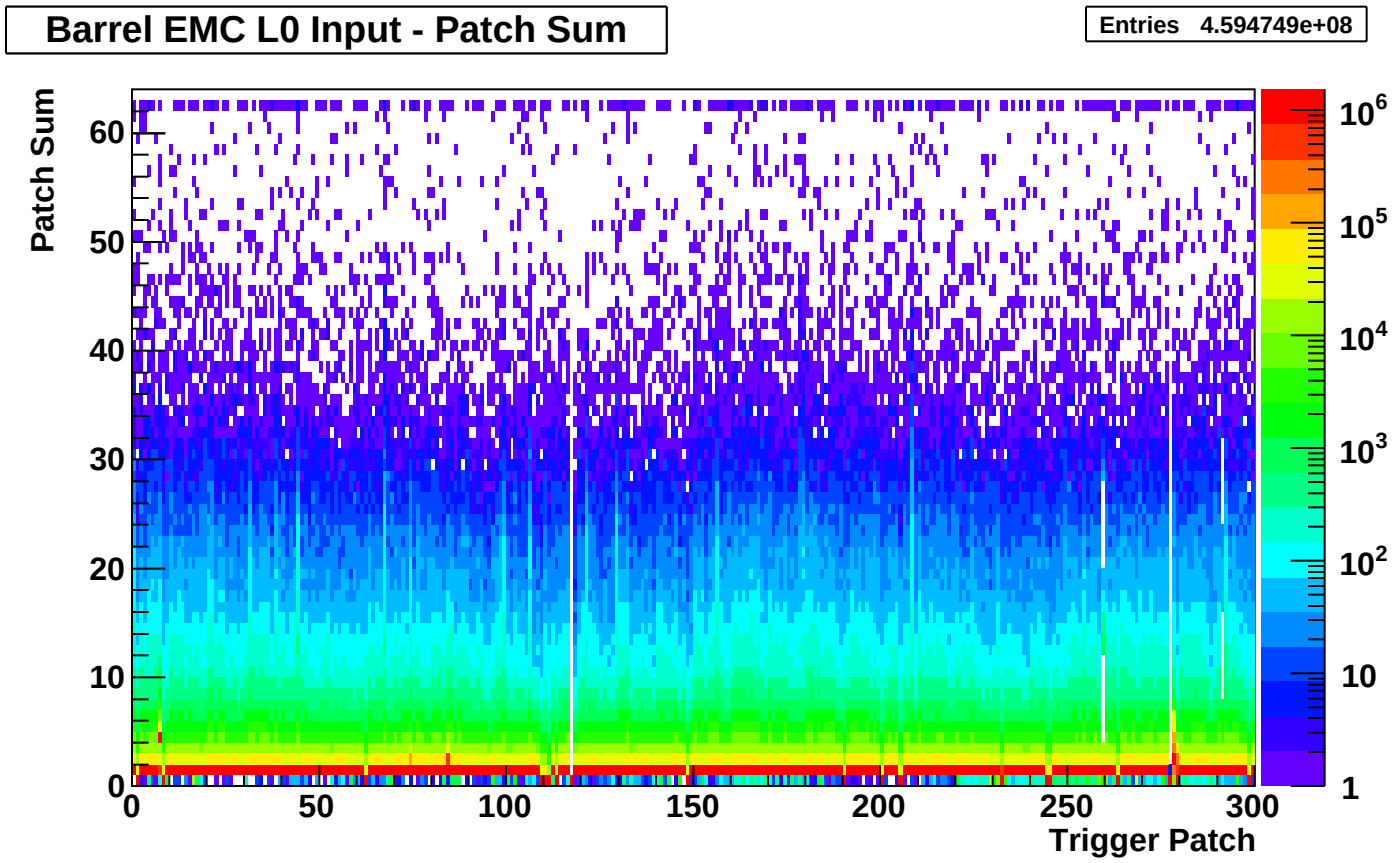
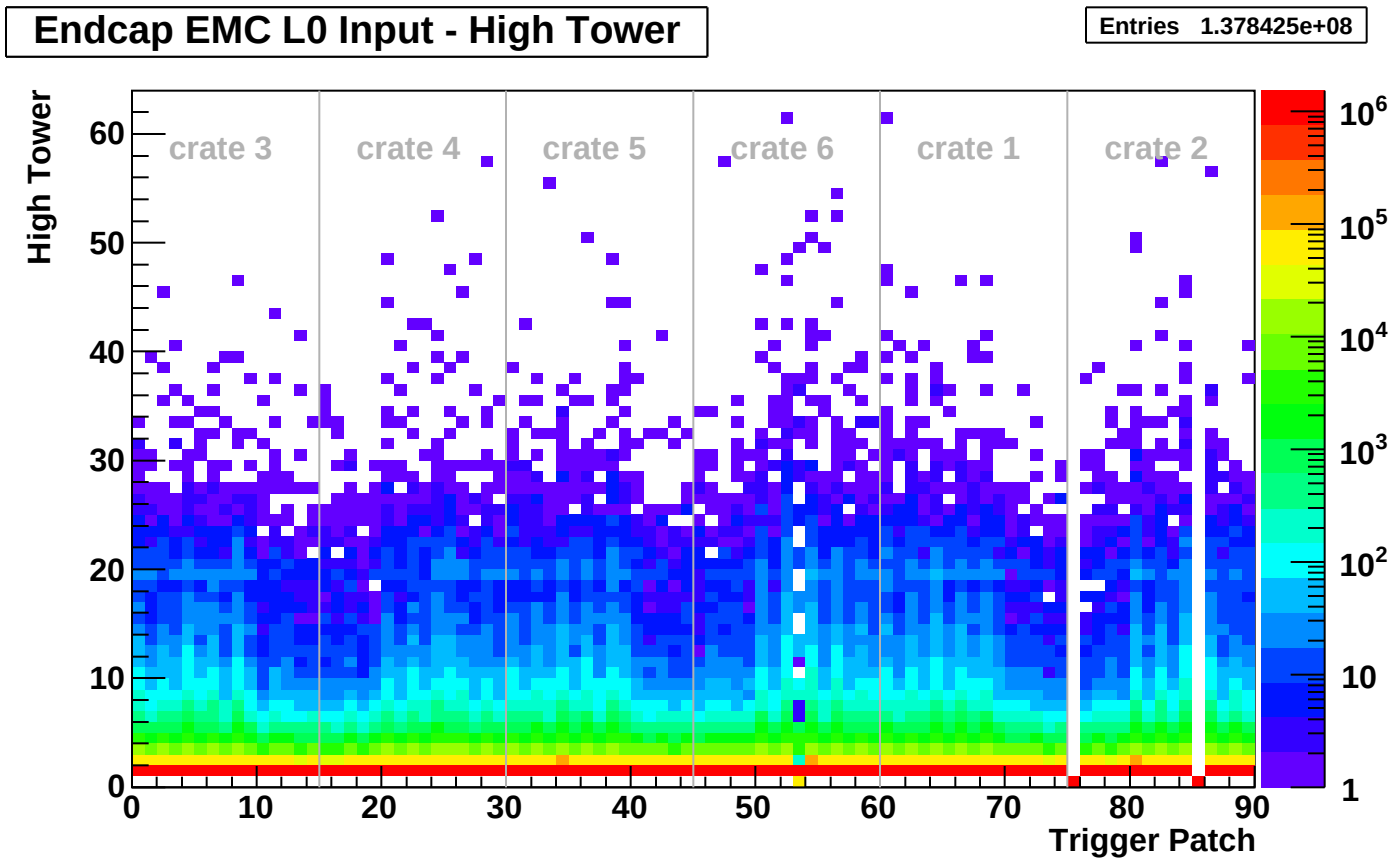
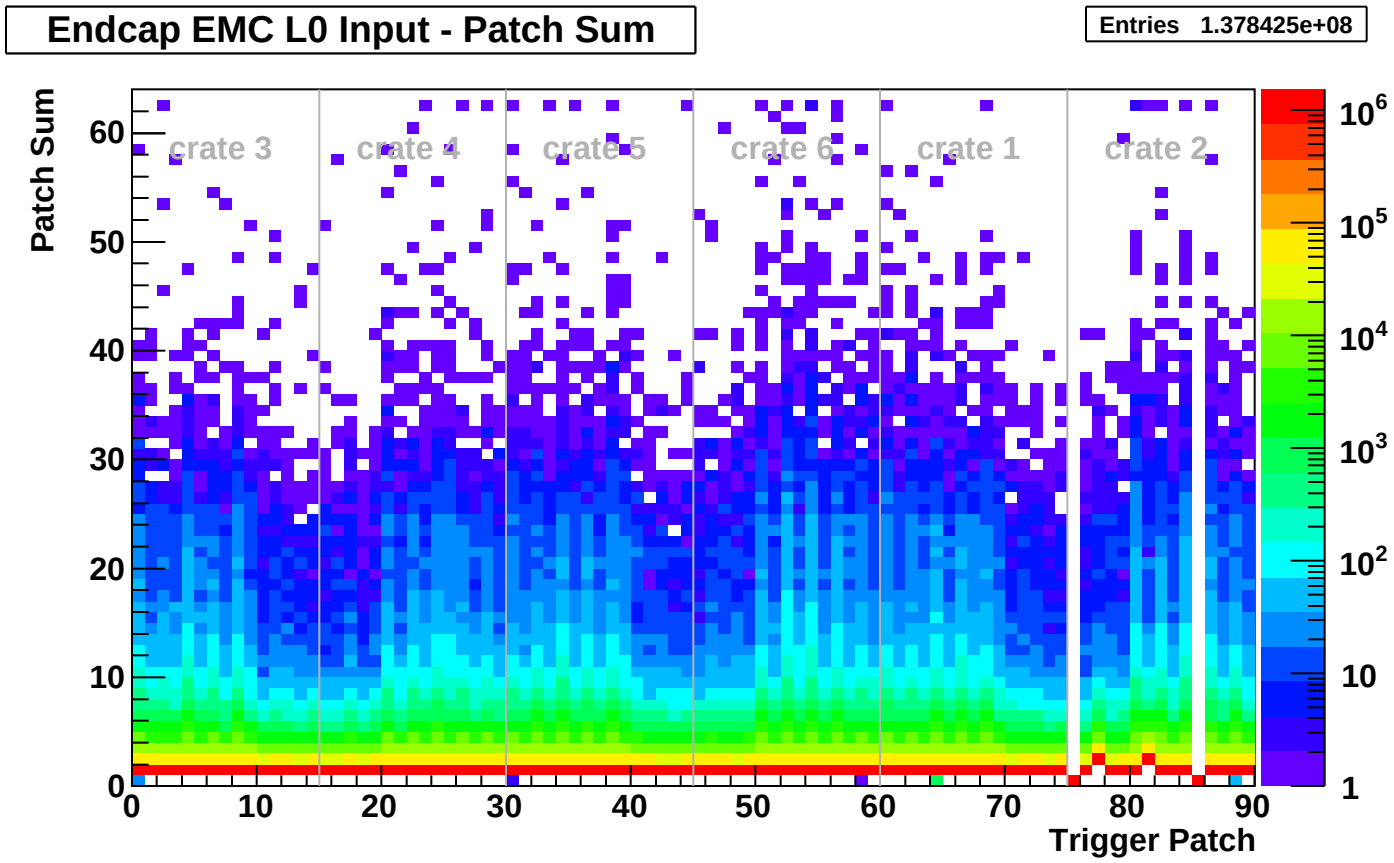
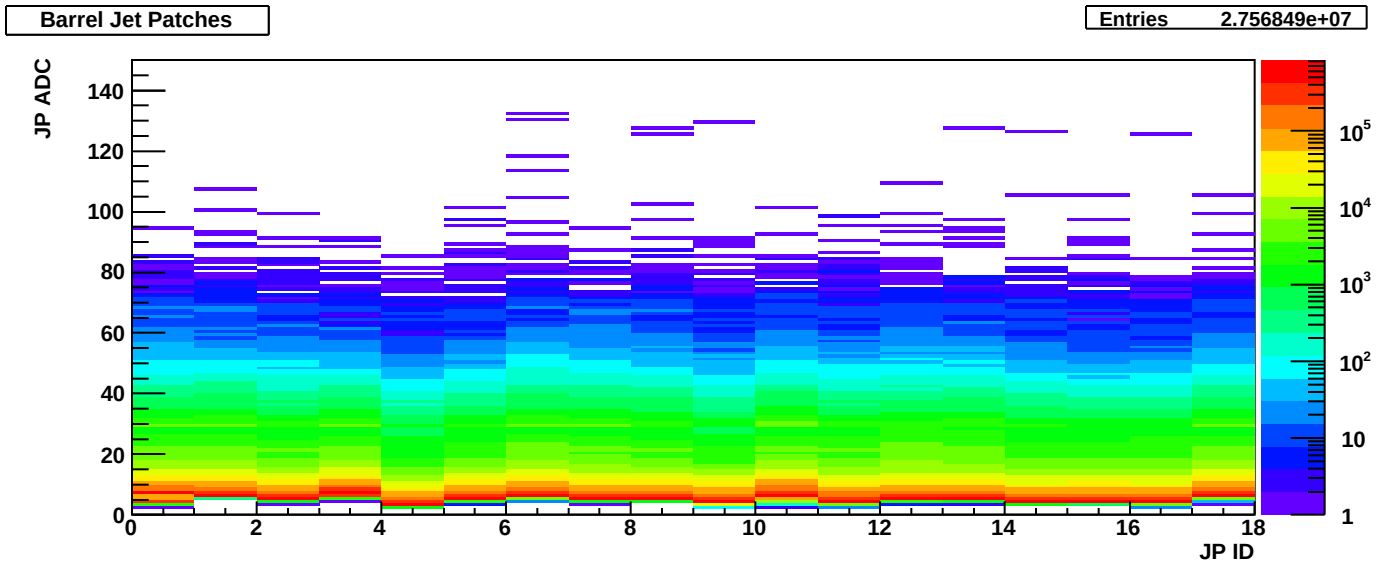
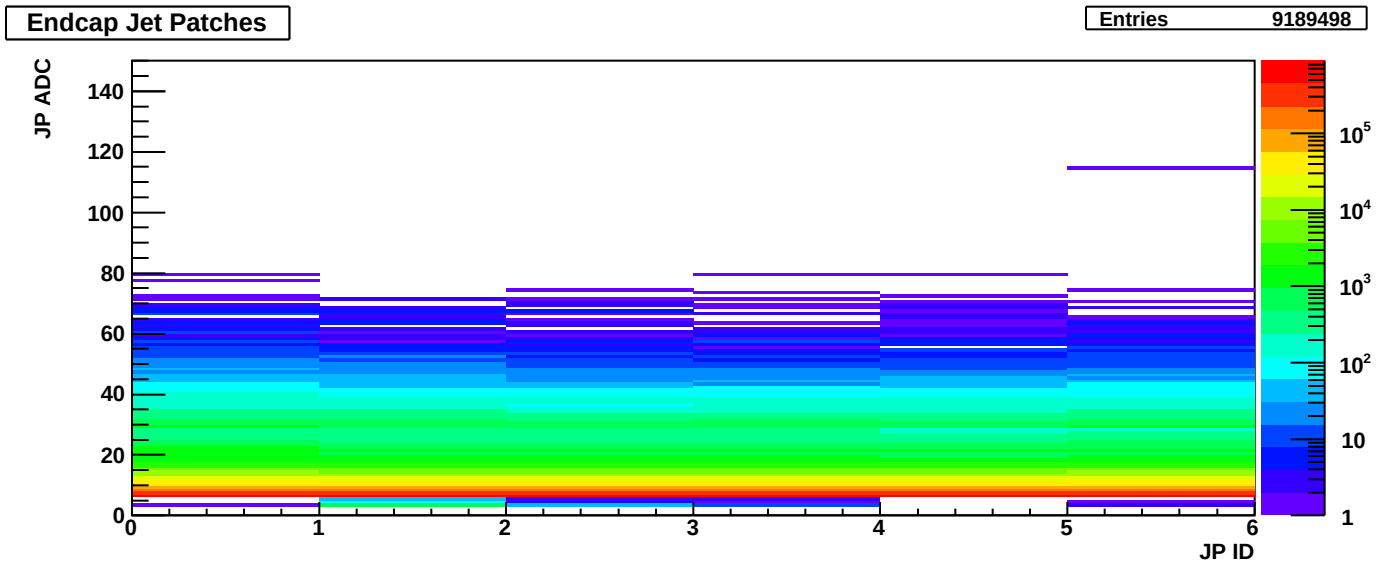

Barrel EMC L0 Input - High Tower
Entries 4.594749e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

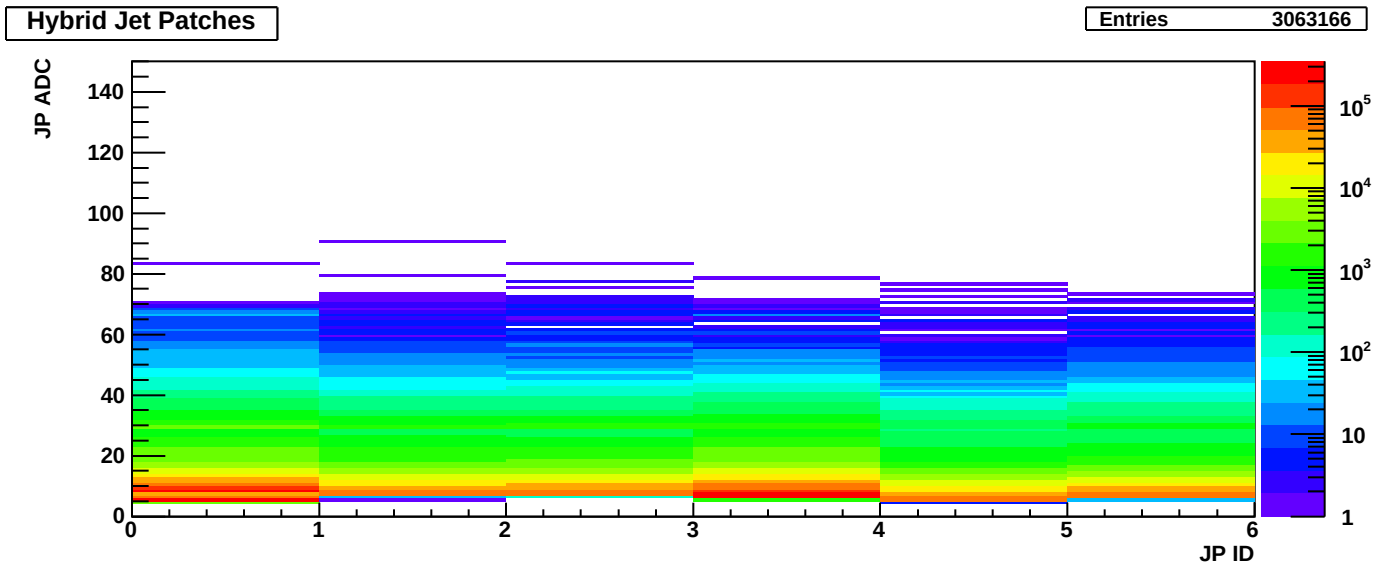

Barrel EMC L0 Input - Patch Sum
Entries 4.594749e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

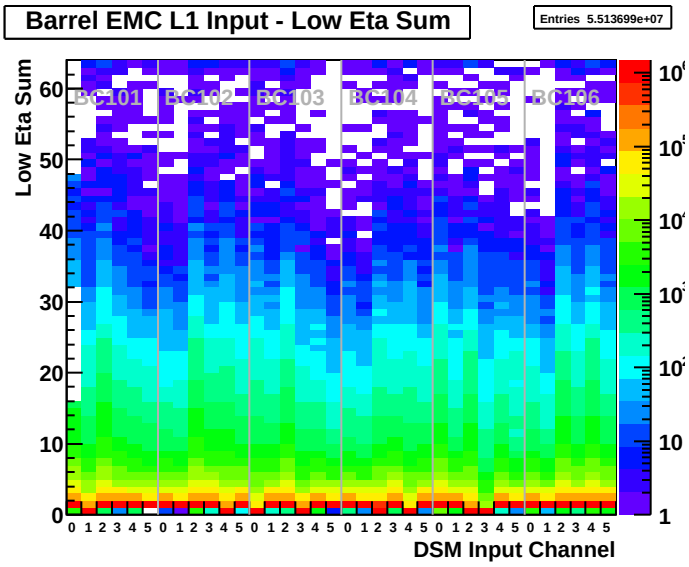

Endcap EMC L0 Input - High Tower
Entries 1.378425e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

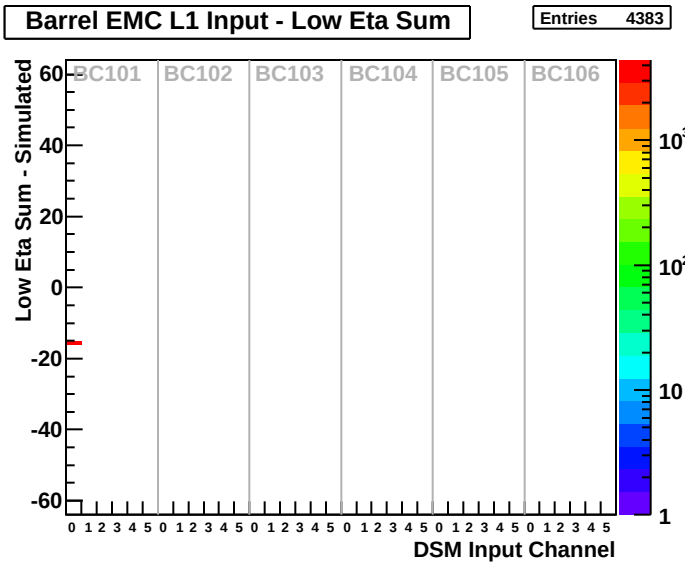

Endcap EMC L0 Input - Patch Sum
Entries 1.378425e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

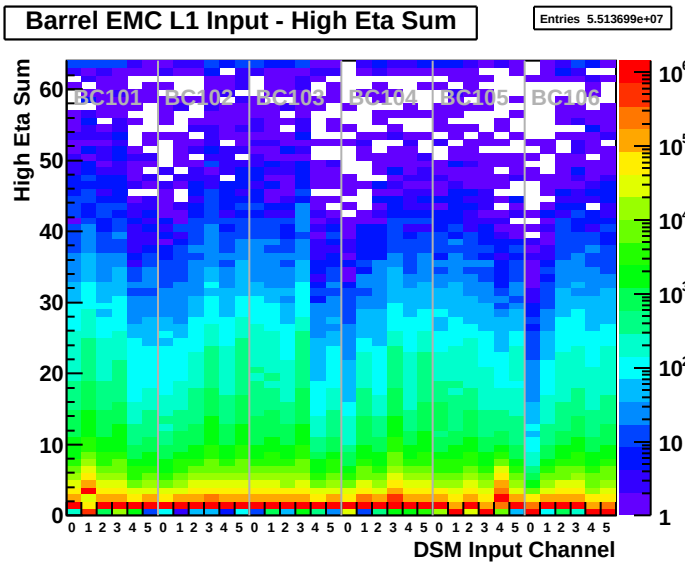

Barrel Jet Patches
Entries 2.756849e+07
JP ADC
JP ID

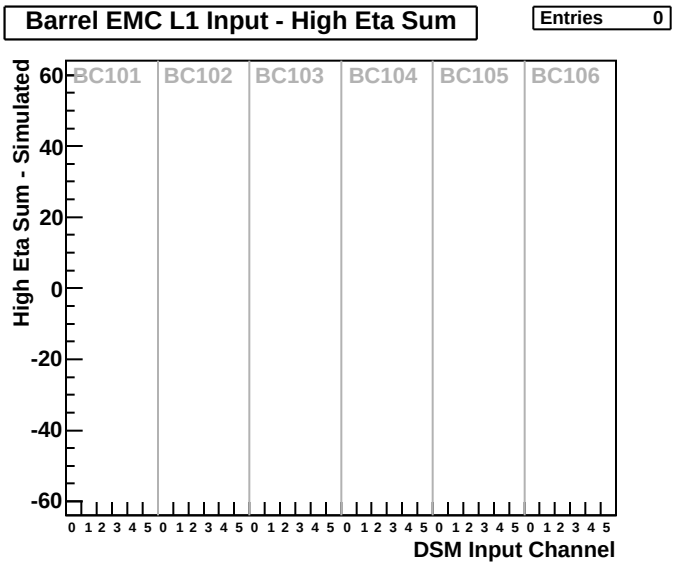

Endcap Jet Patches
Entries 9189498
JP ADC
JP ID

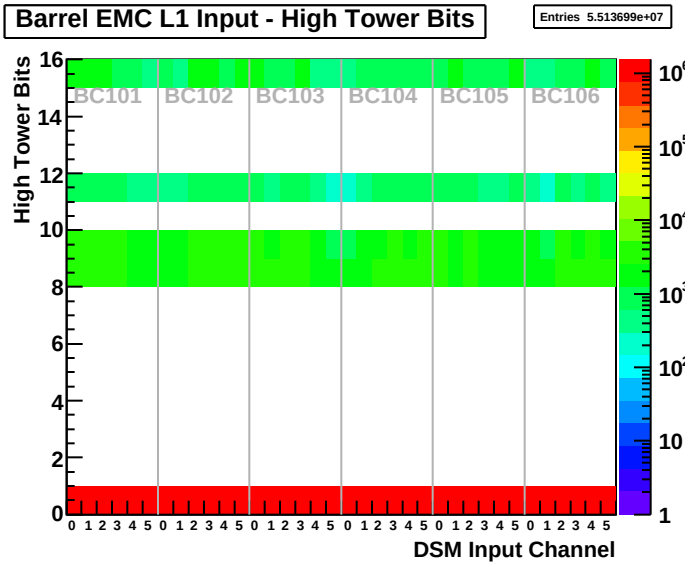

Hybrid Jet Patches
Entries 3063166
JP ADC
JP ID

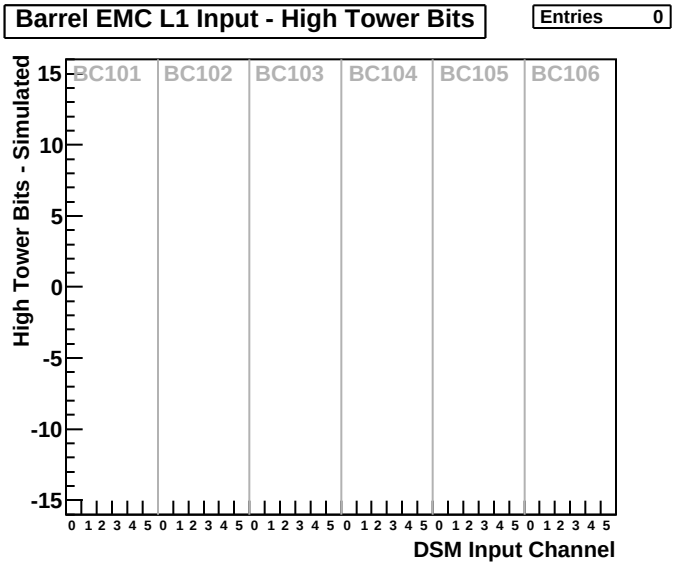

Barrel EMC L1 Input - Low Eta Sum
Entries 5.513699e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

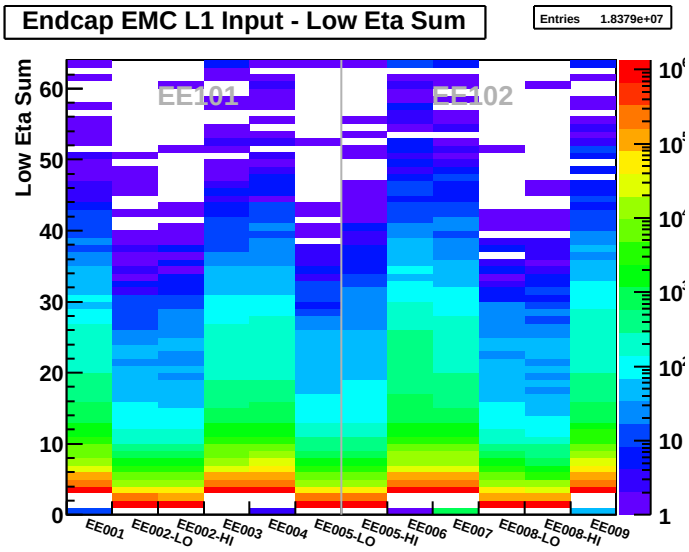

Barrel EMC L1 Input - Low Eta Sum
Entries 4383
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

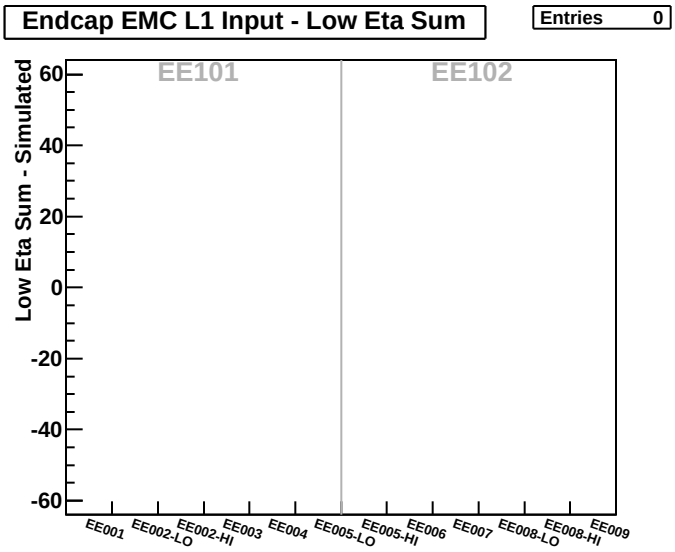

Barrel EMC L1 Input - High Eta Sum
Entries 5.513699e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

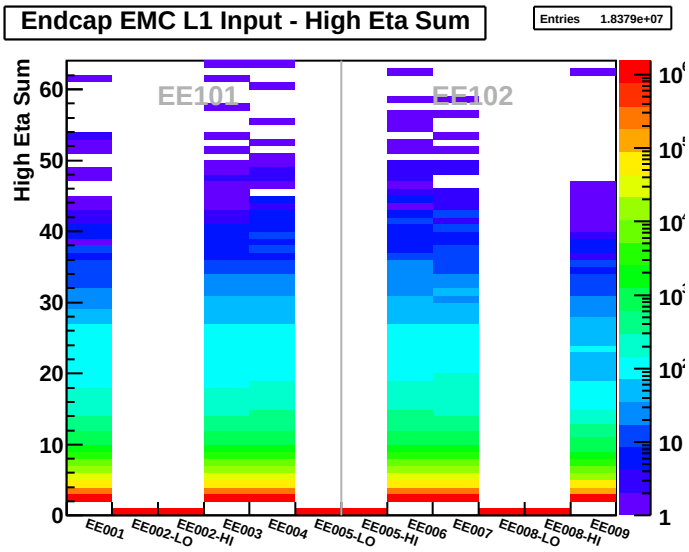

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

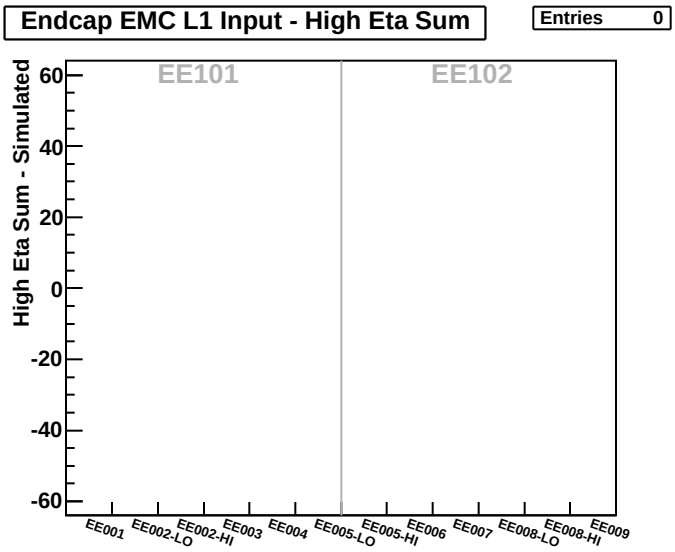

Barrel EMC L1 Input - High Tower Bits
Entries 5.513699e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

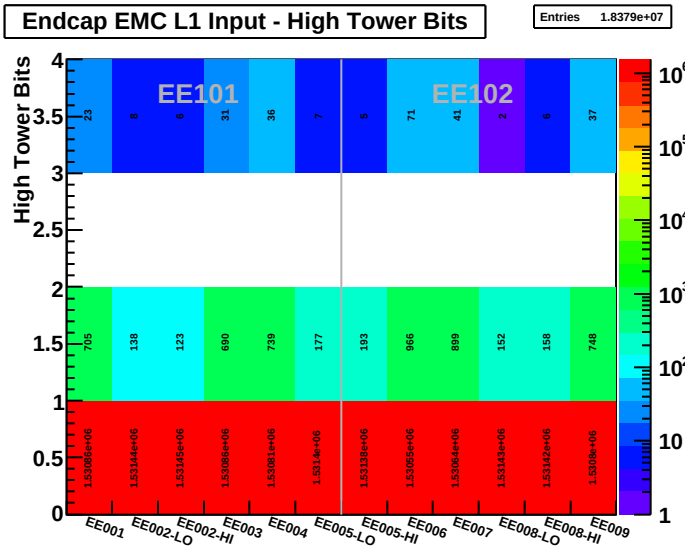

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

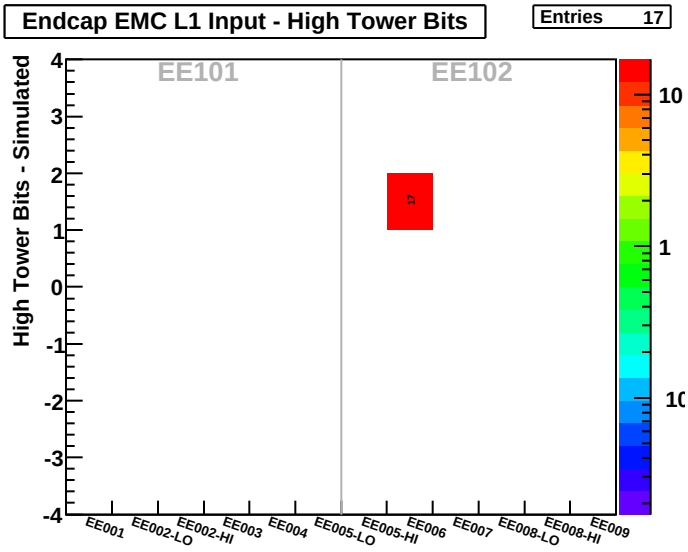

Endcap EMC L1 Input - Low Eta Sum
Entries 1.8379e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

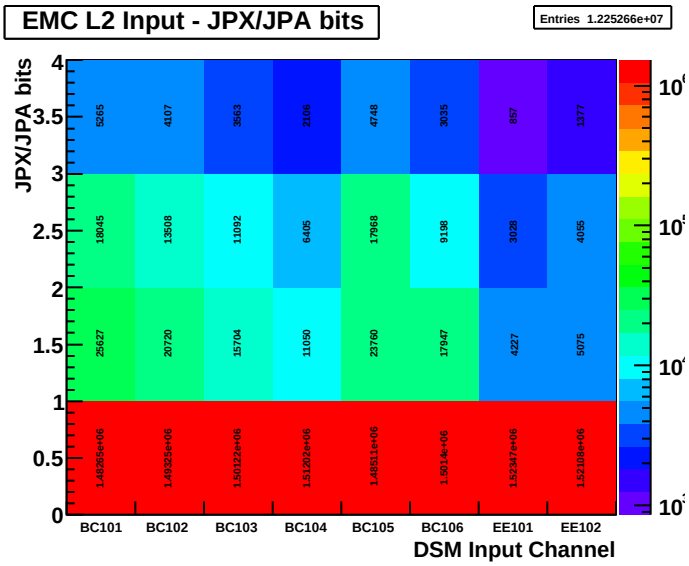

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

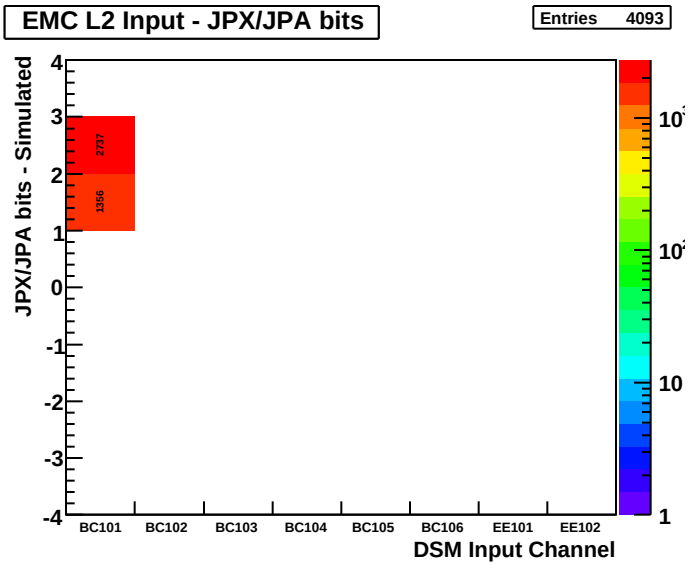

Endcap EMC L1 Input - High Eta Sum
Entries 1.8379e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

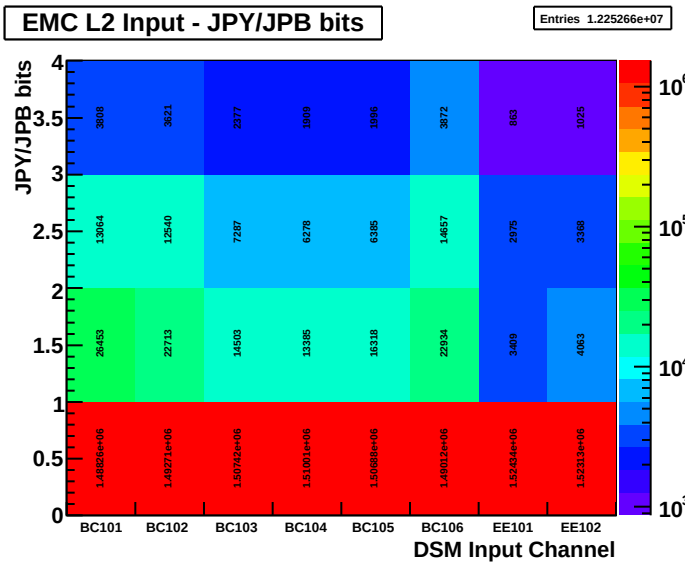

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

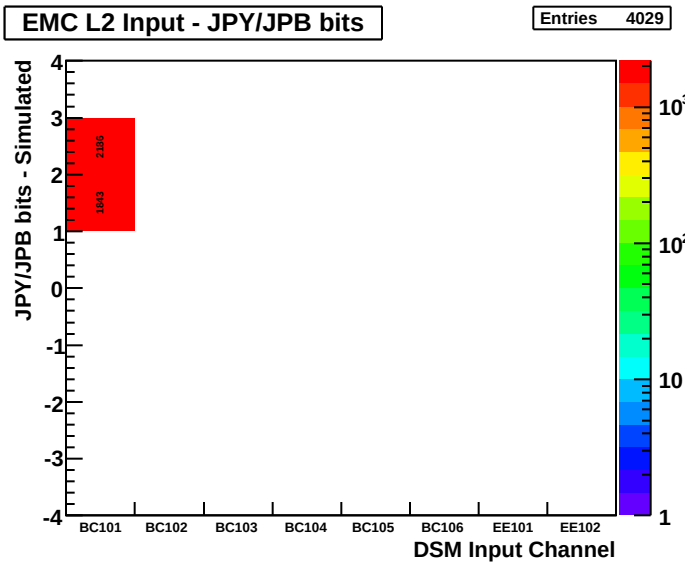

Endcap EMC L1 Input - High Tower Bits
Entries 1.8379e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

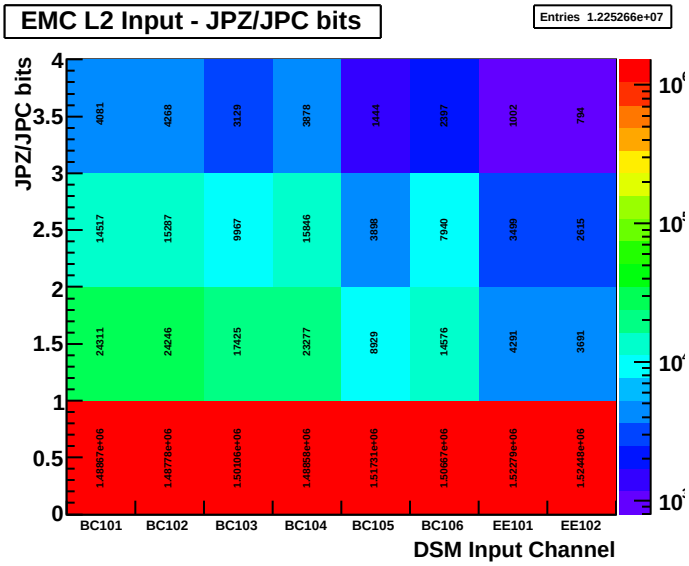

Endcap EMC L1 Input - High Tower Bits
Entries 17
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

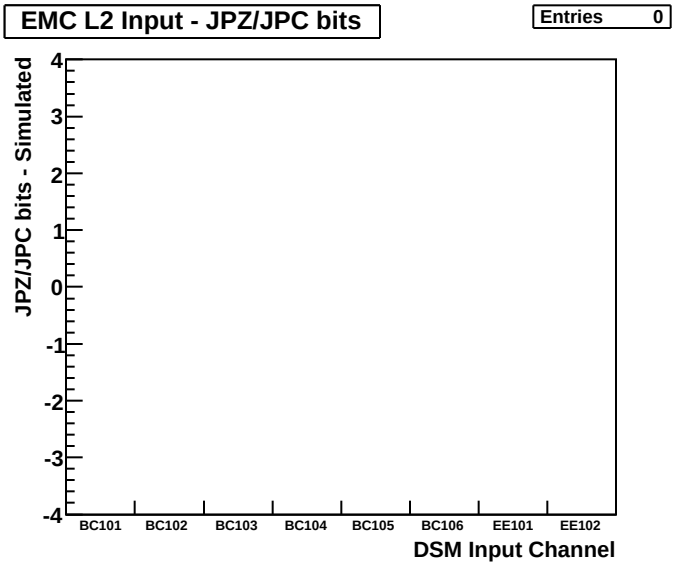

EMC L2 Input - JPX/JPA bits
Entries 1.225266e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5265 4107 3563 2106 4748 3035 857 1377
18045 13508 11092 6405 17968 9198 3028 4055
25627 20720 15704 11050 23760 17947 4227 5075
1.48255e+06 1.49325e+06 1.50122e+06 1.51202e+06 1.49511e+06 1.5014e+06 1.52347e+06 1.52108e+06

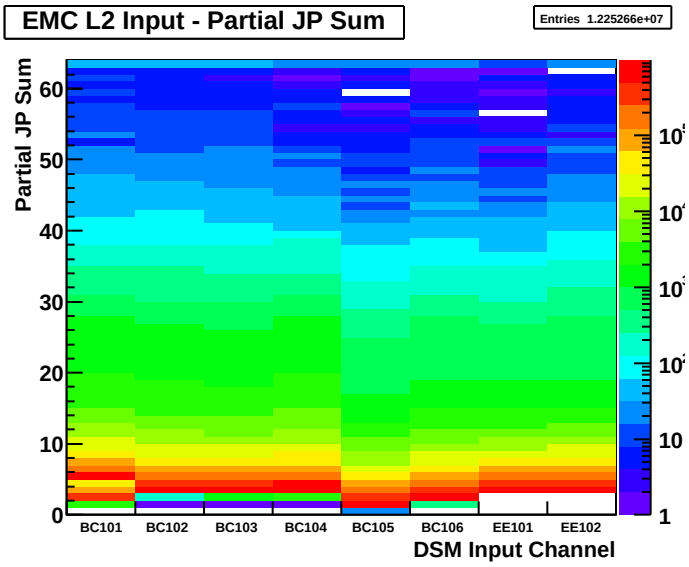

EMC L2 Input - JPX/JPA bits
Entries 4093
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2737
1356

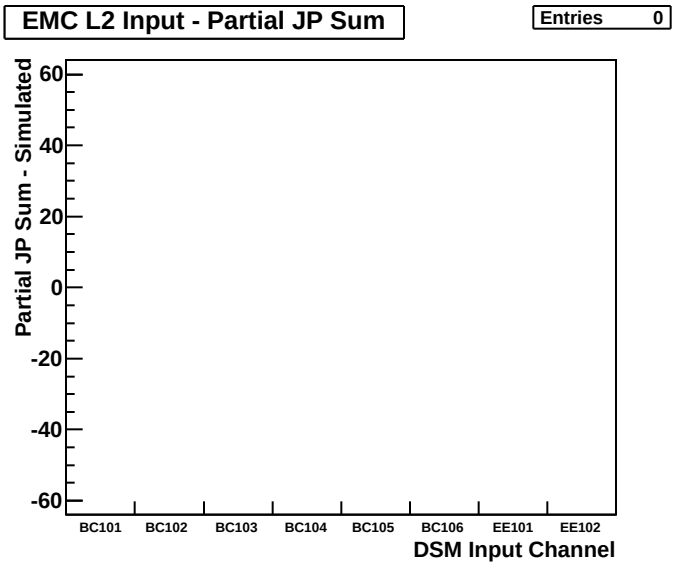

EMC L2 Input - JPY/JPB bits
Entries 1.225266e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3808 3621 2377 1909 1996 3872 863 1025
13064 12540 7287 6278 6385 14657 2975 3368
26453 22713 14503 13385 16318 22934 3409 4063
1.48826e+06 1.49271e+06 1.50742e+06 1.51001e+06 1.50698e+06 1.49012e+06 1.52434e+06 1.52313e+06

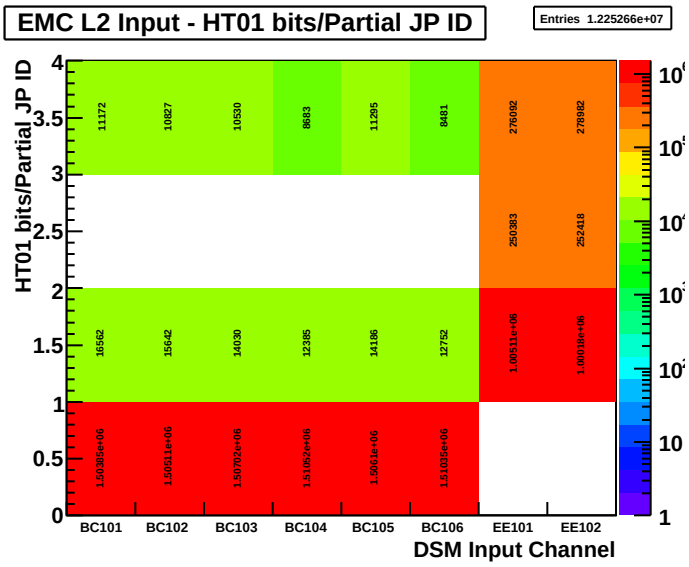

EMC L2 Input - JPY/JPB bits
Entries 4029
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2186
1843

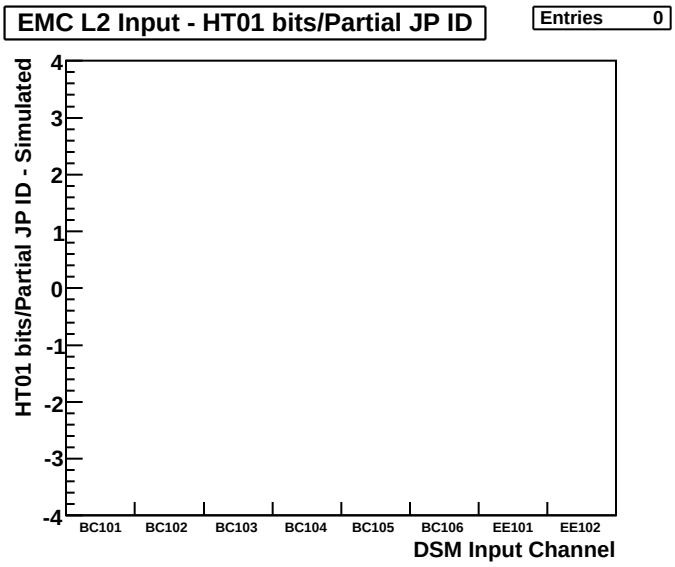

EMC L2 Input - JPZ/JPC bits
Entries 1.225266e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4081 4268 3129 3878 1444 2397 1002 794
14517 15287 9967 15846 3898 7940 3499 2615
24311 24246 17425 23277 8929 14676 4291 3691
1.48867e+06 1.48778e+06 1.50108e+06 1.48858e+06 1.51731e+06 1.50667e+06 1.52279e+06 1.52448e+06

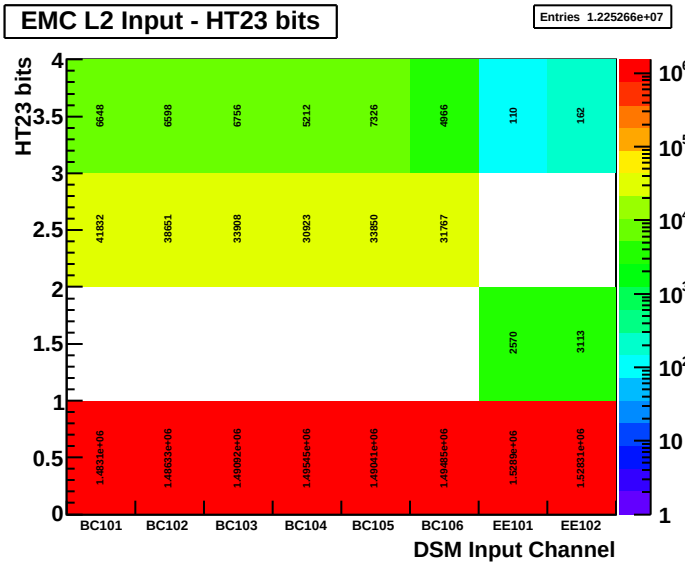

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

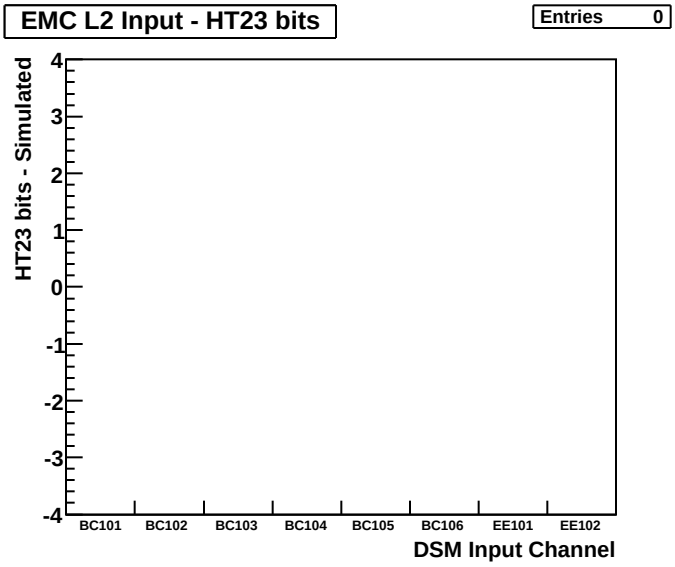

EMC L2 Input - Partial JP Sum
Entries 1.225266e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

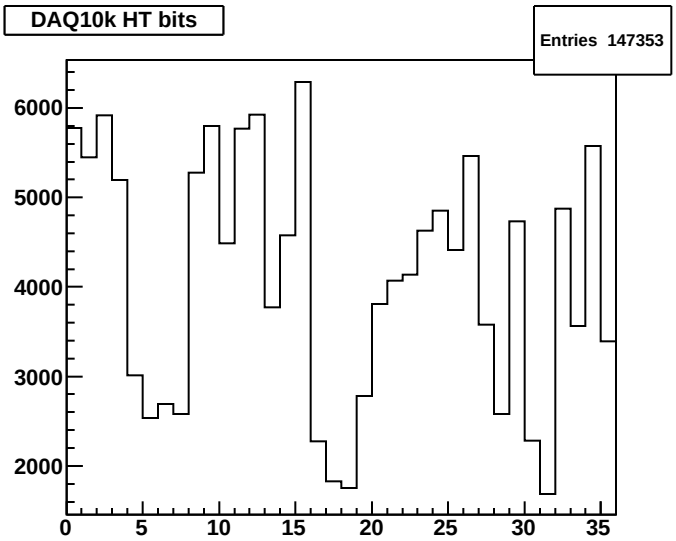

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

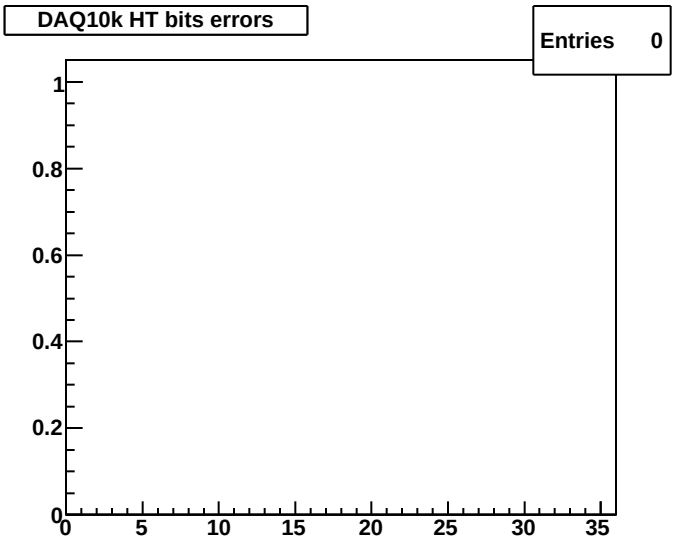

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.225266e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

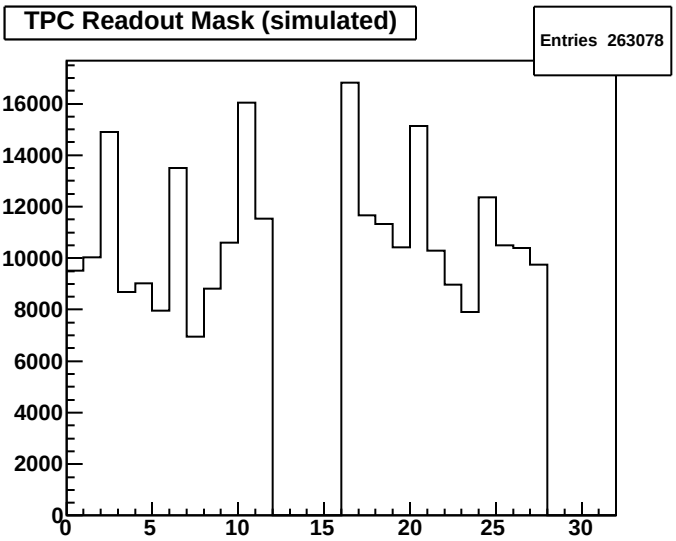

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

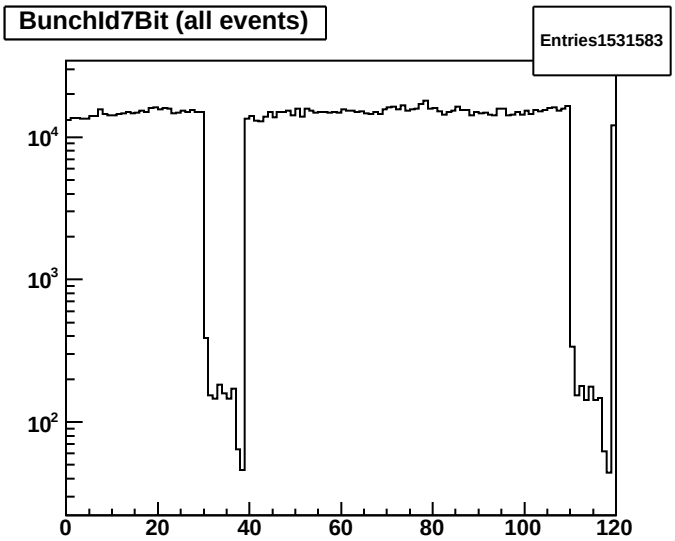

EMC L2 Input - HT23 bits
Entries 1.225266e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

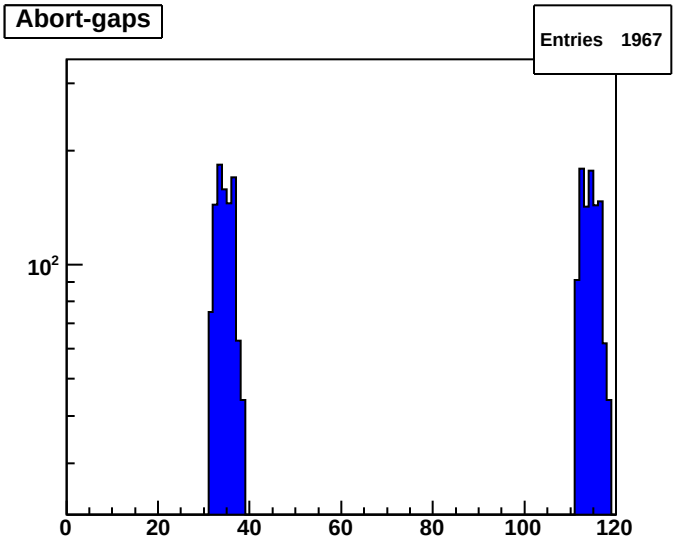

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

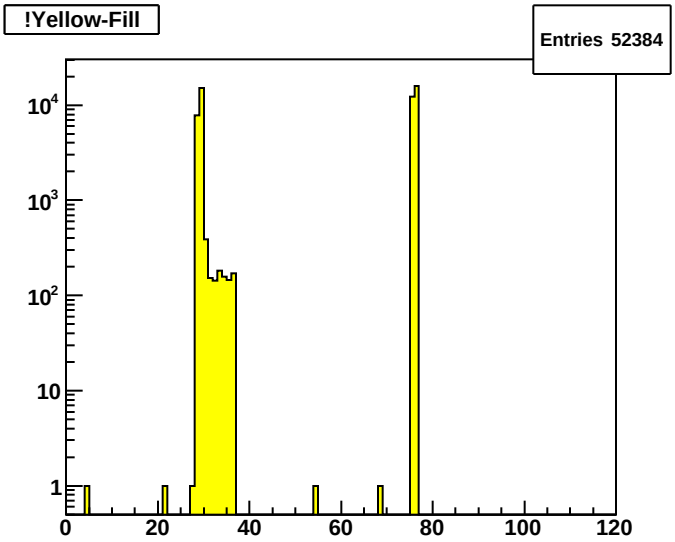

DAQ10k HT bits
Entries 147353

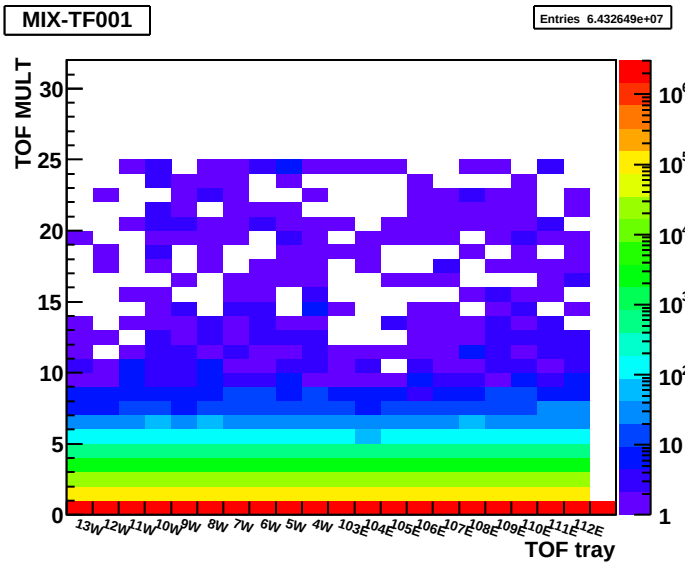

DAQ10k HT bits errors
Entries 0

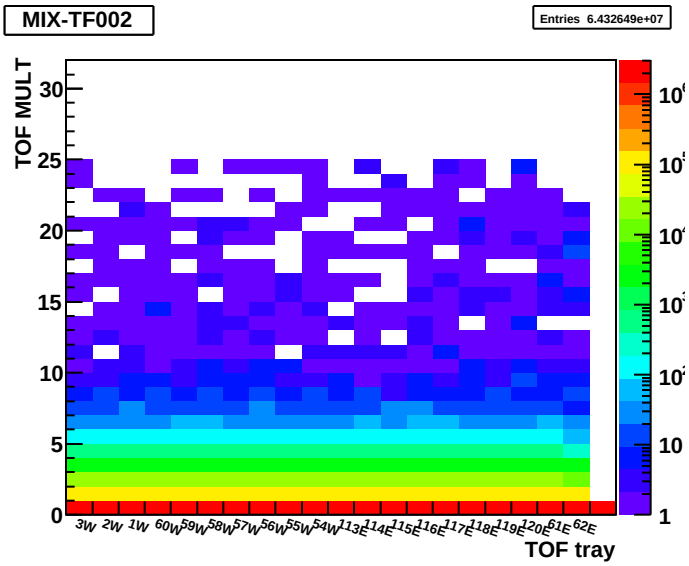

TPC Readout Mask (simulated)
Entries 263078

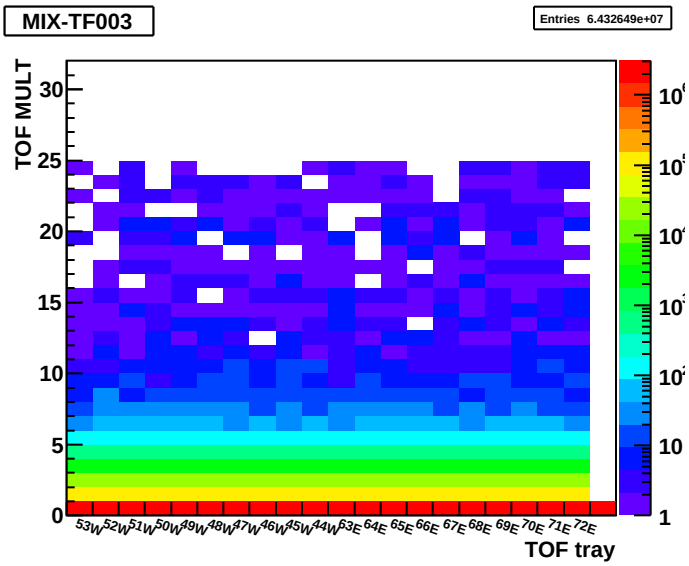

BunchId7Bit (all events)
Entries1531583

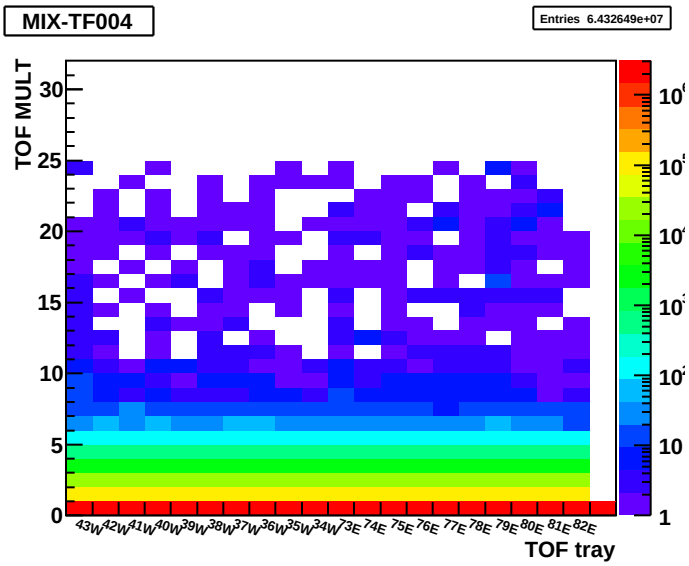

Abort-gaps
Entries 1967

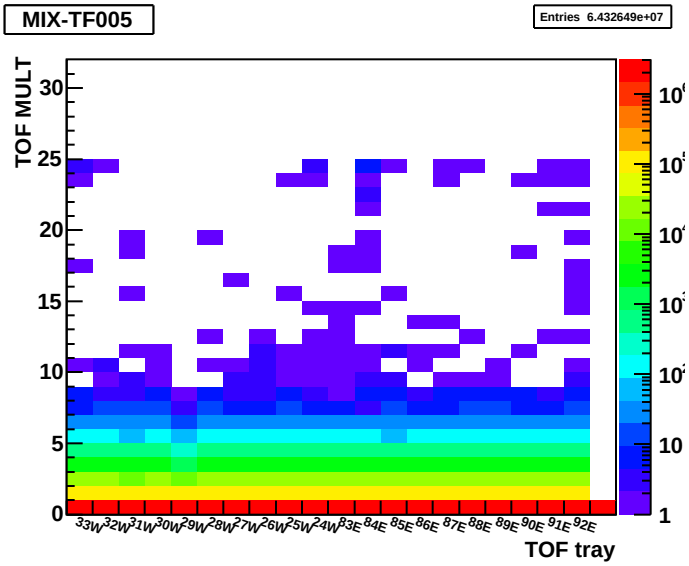

!Yellow-Fill
Entries 52384

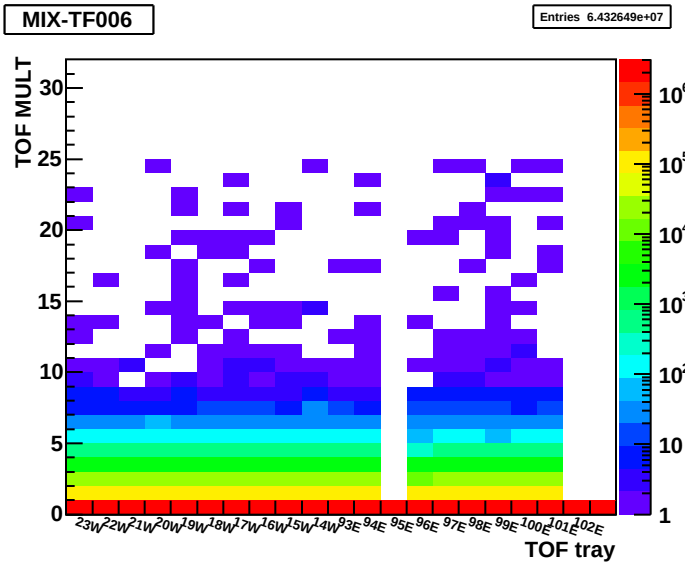

MIX-TF001
Entries 6.432649e+07
TOF MULT
TOF tray

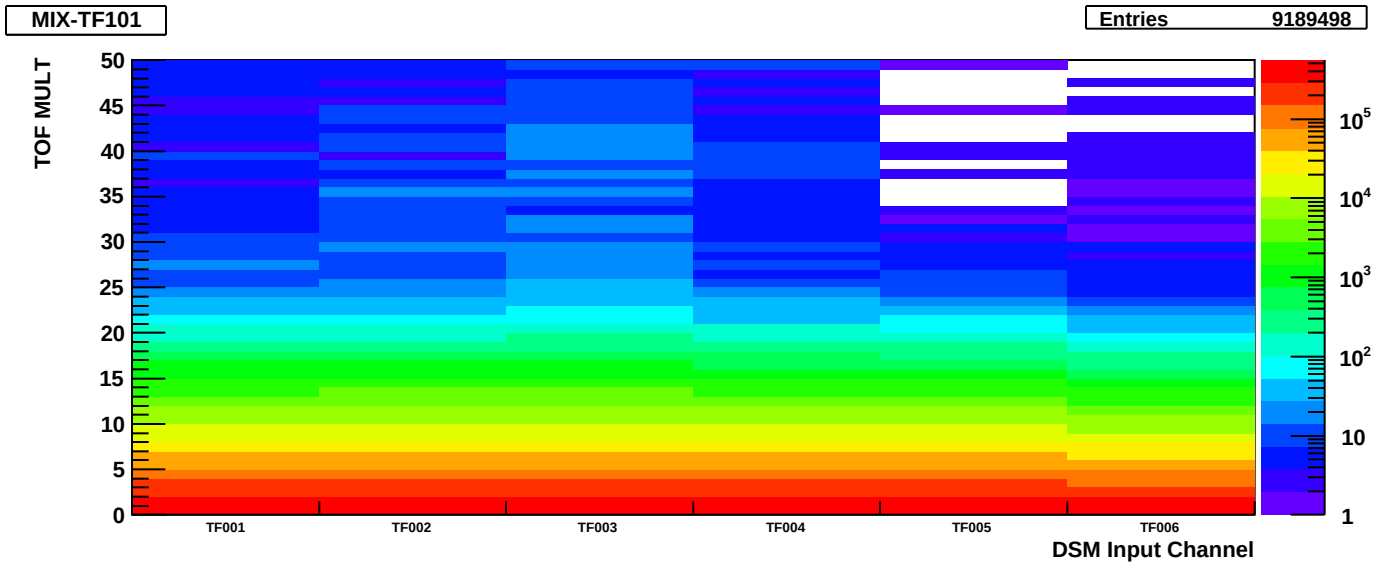

MIX-TF002
Entries 6.432649e+07
TOF MULT
TOF tray

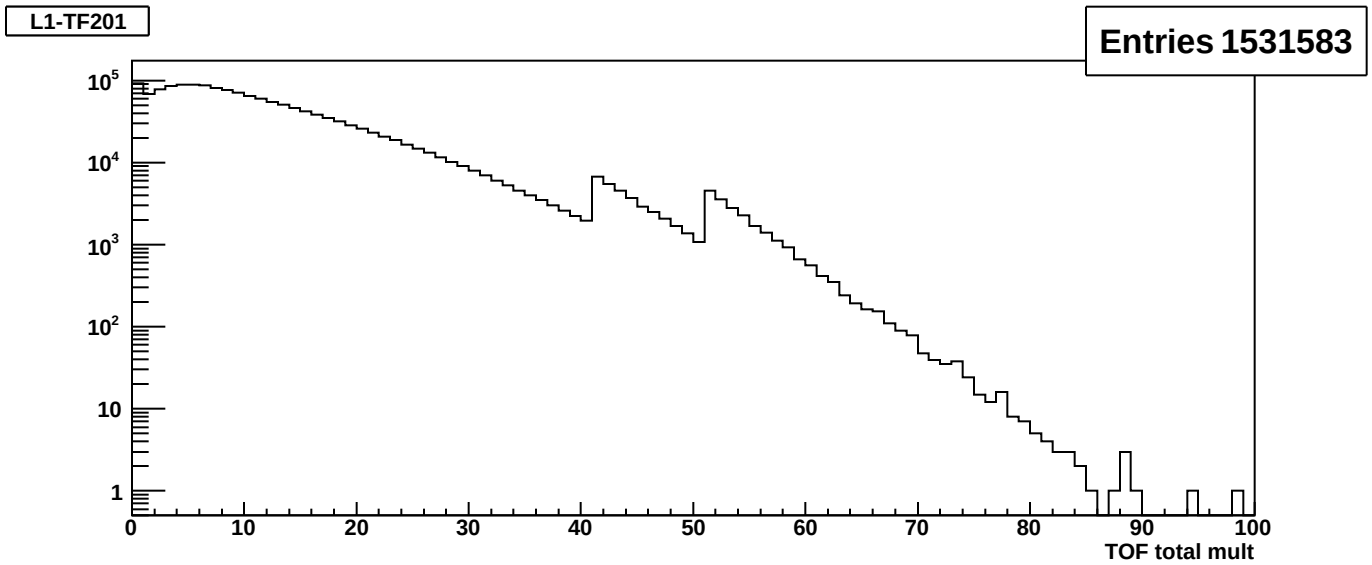

MIX-TF003
Entries 6.432649e+07
TOF MULT
TOF tray

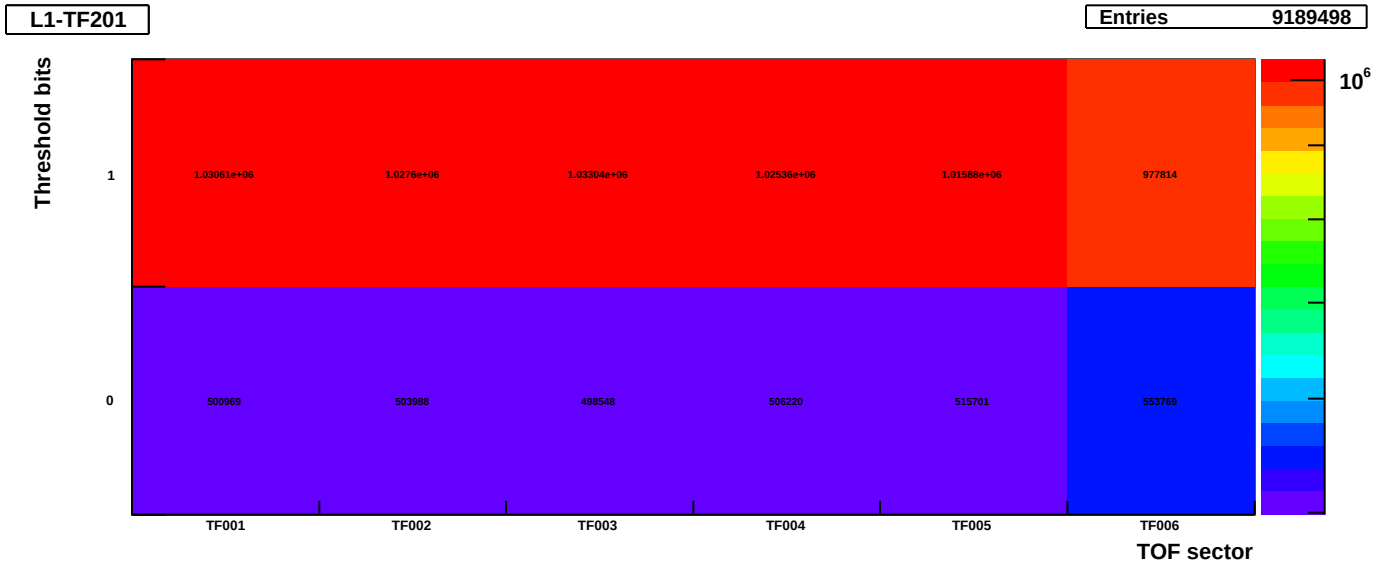

MIX-TF004
Entries 6.432649e+07
TOF MULT
TOF tray

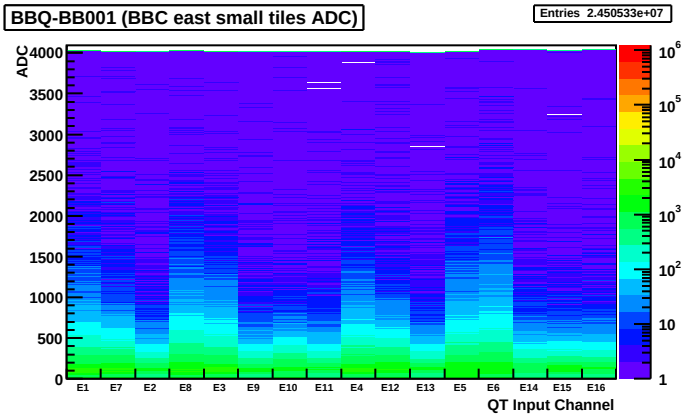

MIX-TF005
Entries 6.432649e+07
TOF MULT
TOF tray

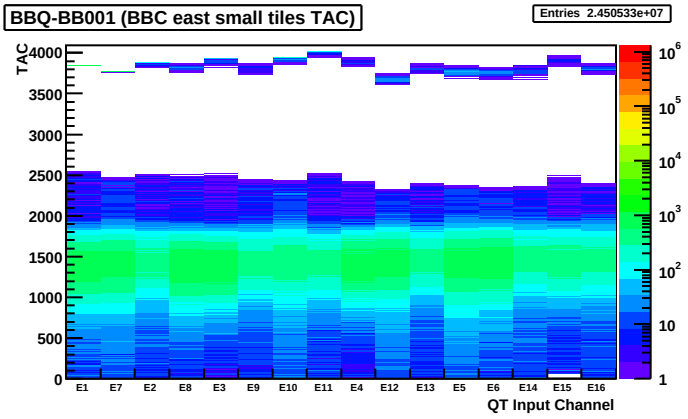

MIX-TF006
Entries 6.432649e+07
TOF MULT
TOF tray

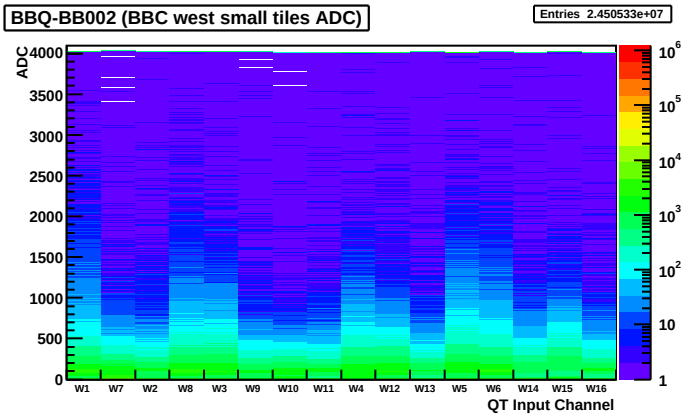

MIX-TF101
Entries 9189498
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

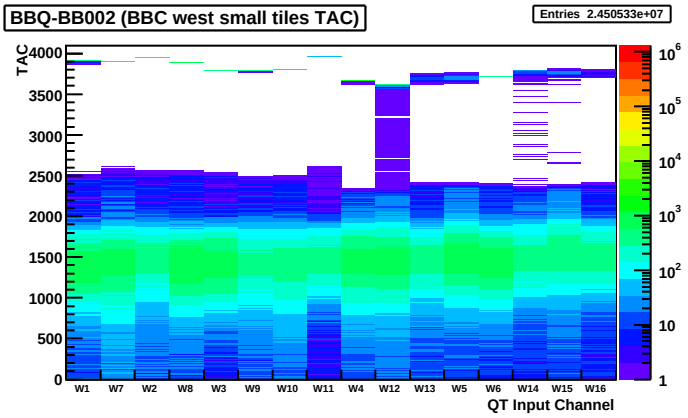

L1-TF201
Entries 1531583
TOF total mult

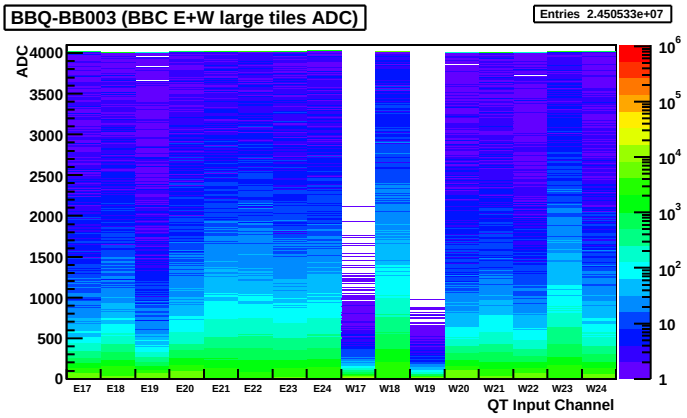

L1-TF201
Entries 9189498
Threshold bits
1.03061e+06
1.0276e+06
1.03304e+06
1.02536e+06
1.01588e+06
977814
500969
503988
498548
506220
515701
553769
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

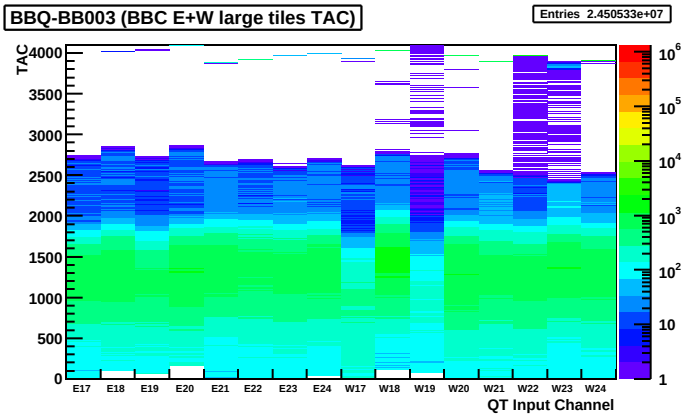

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.450533e+07
ADC
QT Input Channel

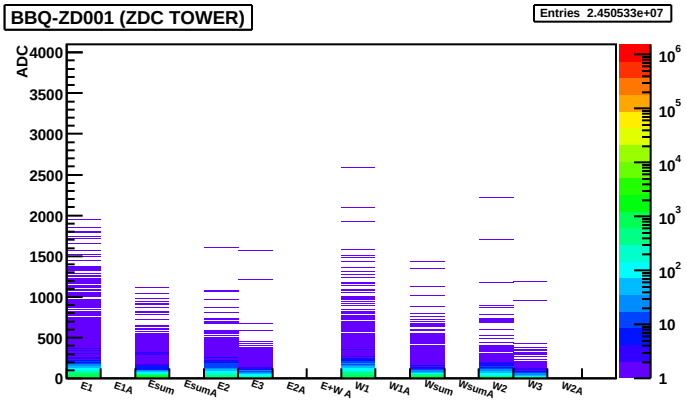

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.450533e+07
TAC
QT Input Channel

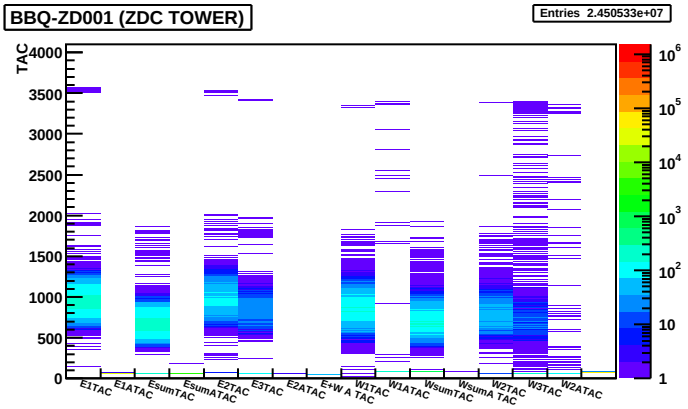

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.450533e+07
ADC
QT Input Channel

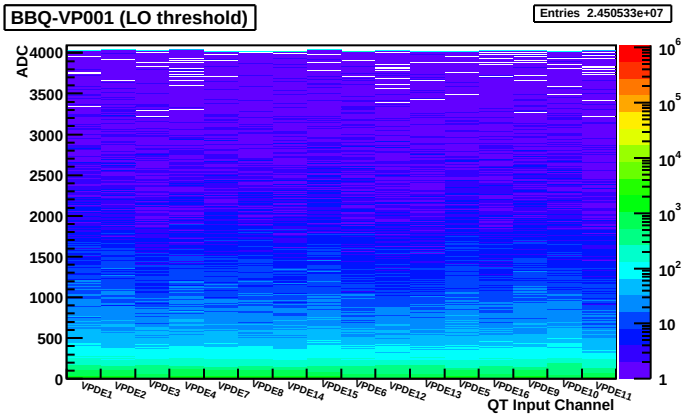

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.450533e+07
TAC
QT Input Channel

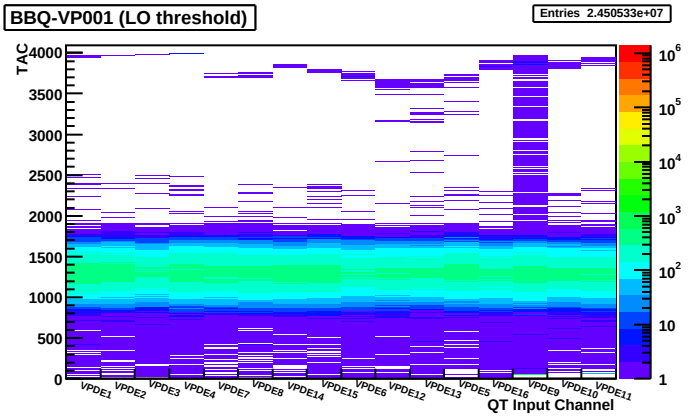

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.450533e+07
ADC
QT Input Channel

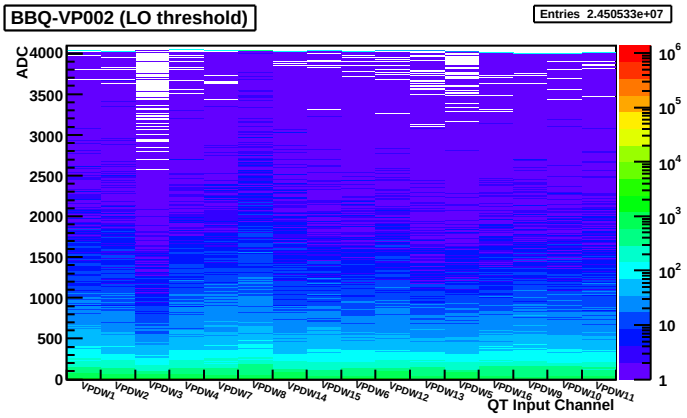

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.450533e+07
TAC
QT Input Channel

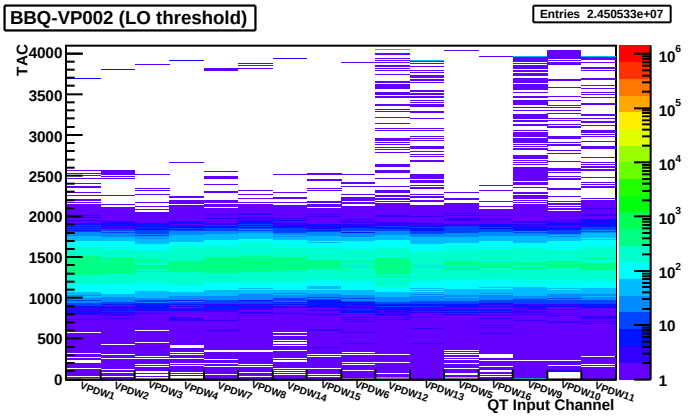

BBQ-ZD001 (ZDC TOWER)
Entries 2.450533e+07
ADC

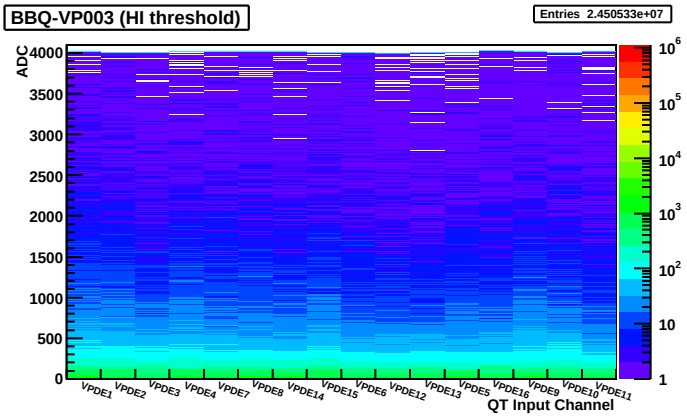

BBQ-ZD001 (ZDC TOWER)
Entries 2.450533e+07
TAC

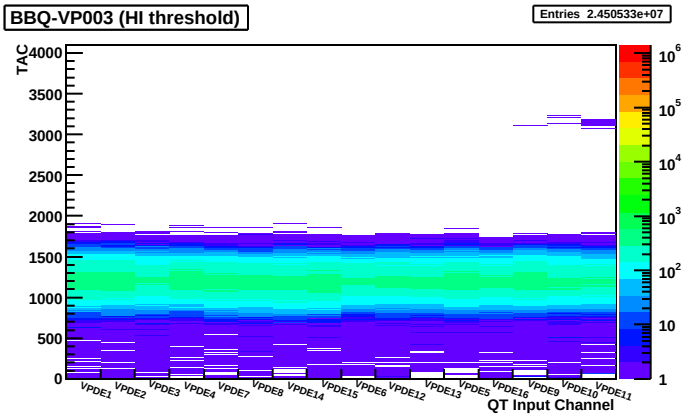

BBQ-VP001 (LO threshold)
Entries 2.450533e+07
ADC
QT Input Channel

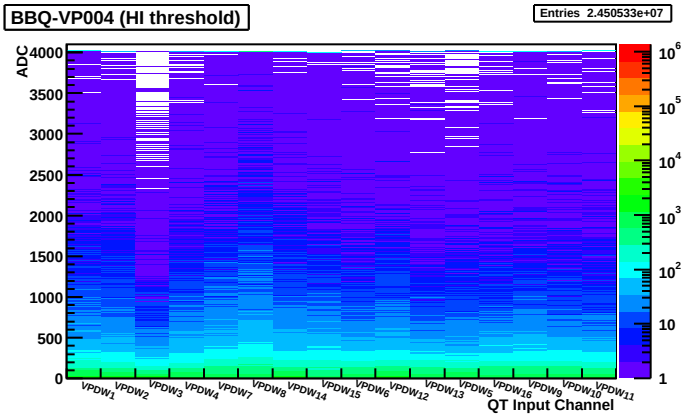

BBQ-VP001 (LO threshold)
Entries 2.450533e+07
TAC
QT Input Channel

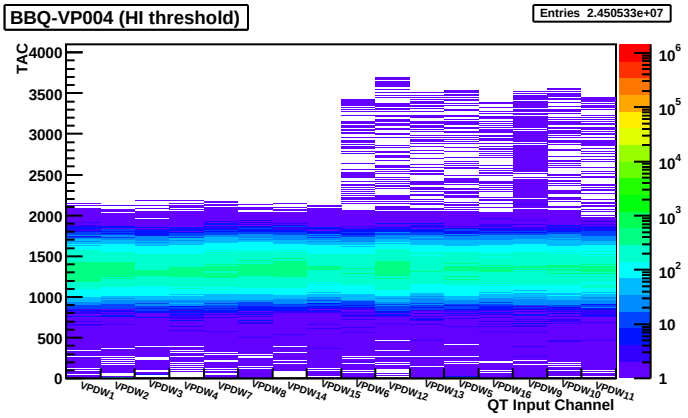

BBQ-VP002 (LO threshold)
Entries 2.450533e+07
ADC
QT Input Channel

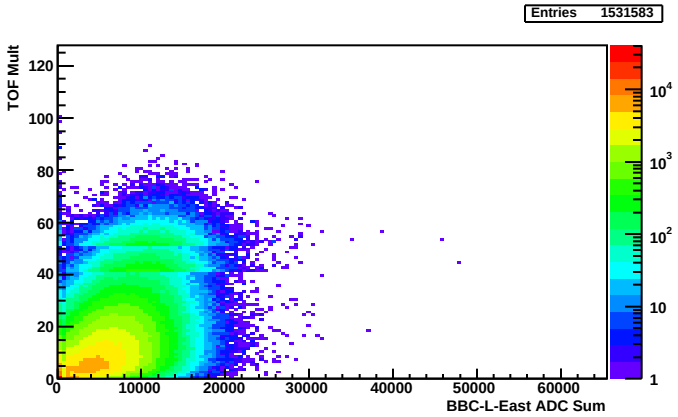

BBQ-VP002 (LO threshold)
Entries 2.450533e+07
TAC
QT Input Channel

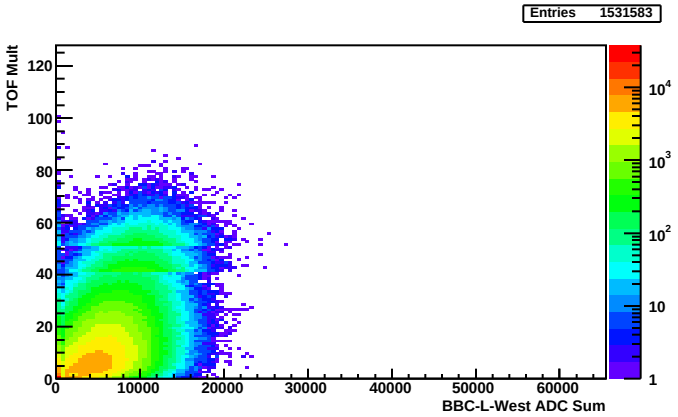

BBQ-VP003 (HI threshold)
Entries 2.450533e+07
ADC
QT Input Channel

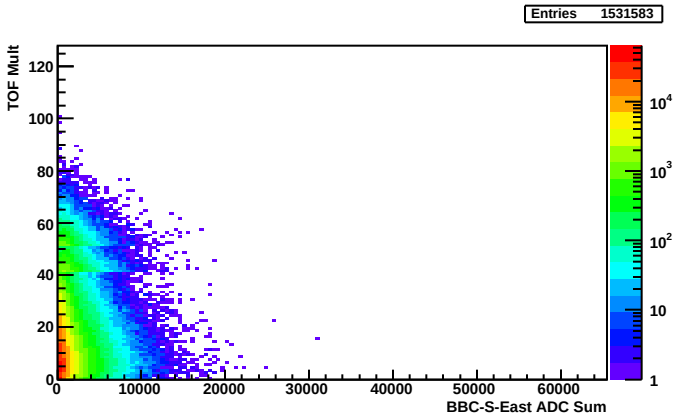

BBQ-VP003 (HI threshold)
Entries 2.450533e+07
TAC
QT Input Channel

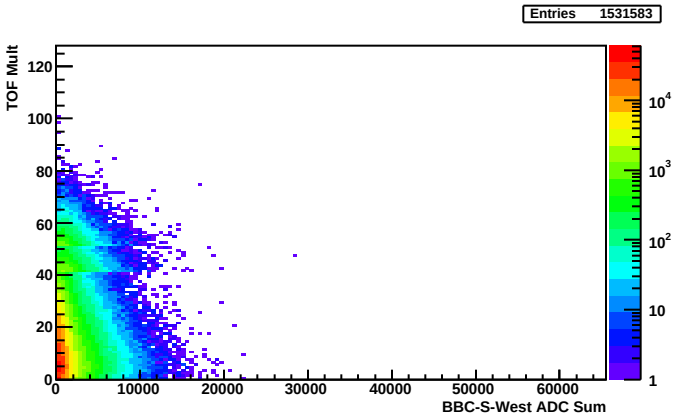

BBQ-VP004 (HI threshold)
Entries 2.450533e+07
ADC
QT Input Channel

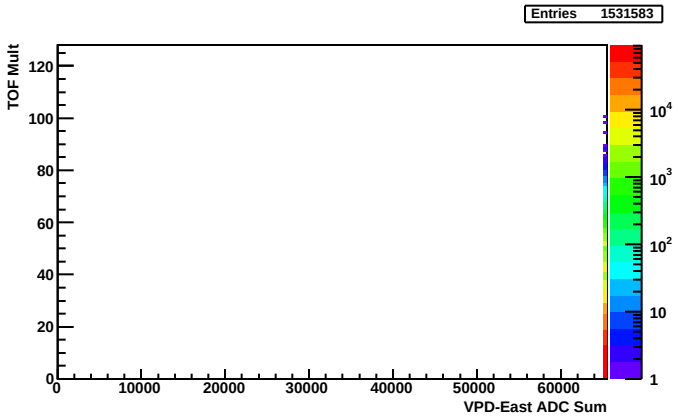

BBQ-VP004 (HI threshold)
Entries 2.450533e+07
TAC
QT Input Channel

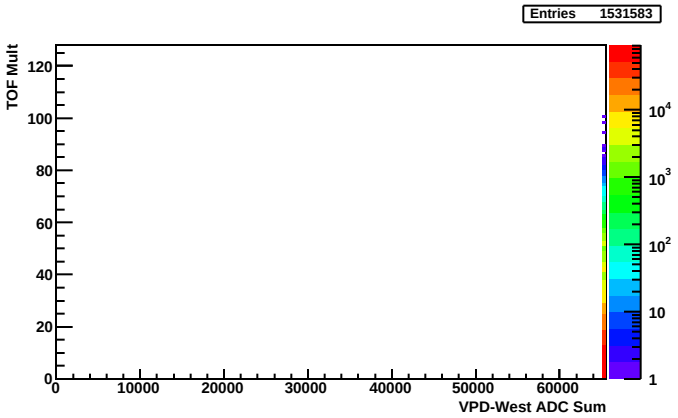

Entries 1531583
TOF Mult
BBC-L-East ADC Sum

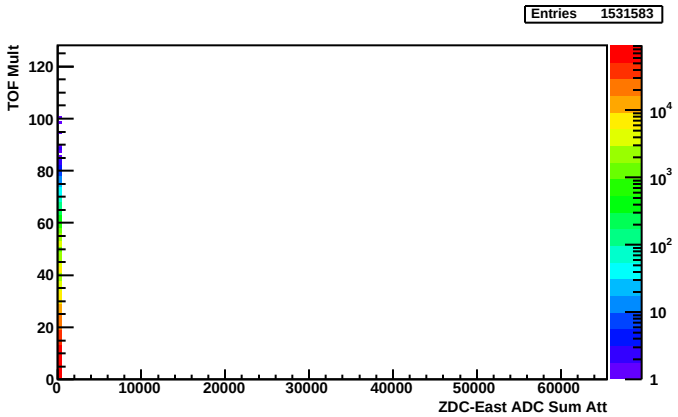

Entries 1531583
TOF Mult
BBC-L-West ADC Sum

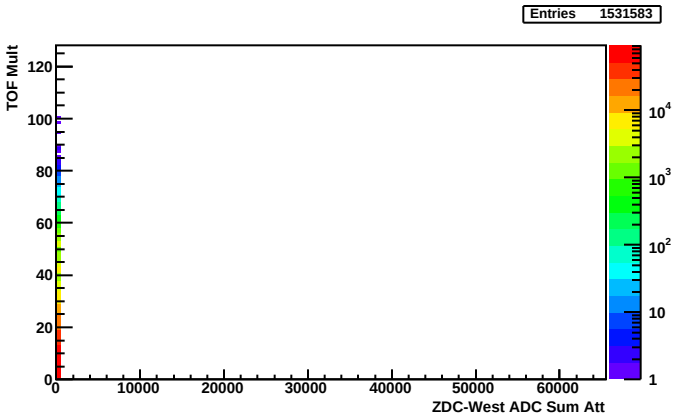

Entries 1531583
TOF Mult
BBC-S-East ADC Sum

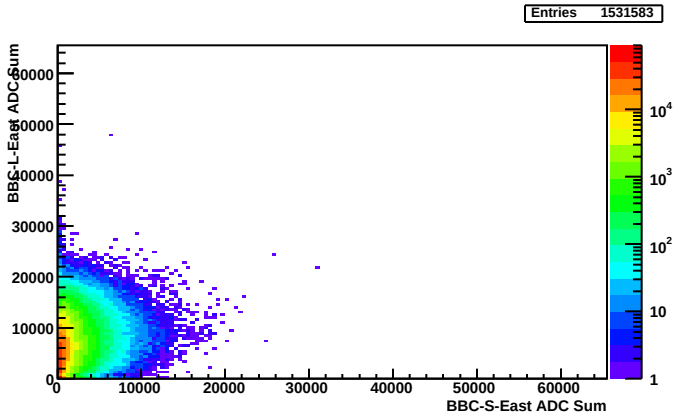

Entries 1531583
TOF Mult
BBC-S-West ADC Sum


Entries 1531583
TOF Mult
VPD-East ADC Sum


Entries 1531583
TOF Mult
VPD-West ADC Sum


Entries 1531583
TOF Mult
ZDC-East ADC Sum Att


Entries 1531583
TOF Mult
ZDC-West ADC Sum Att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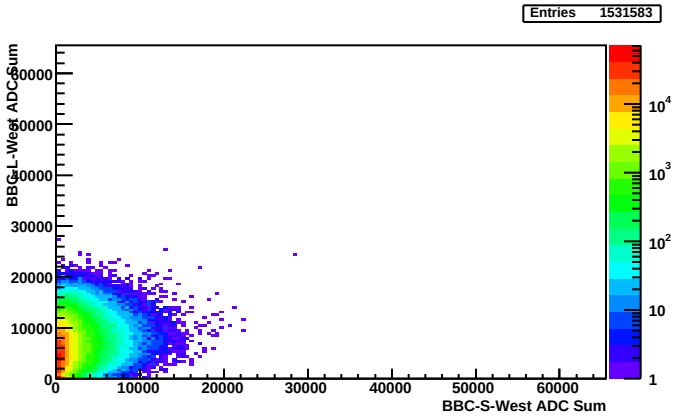
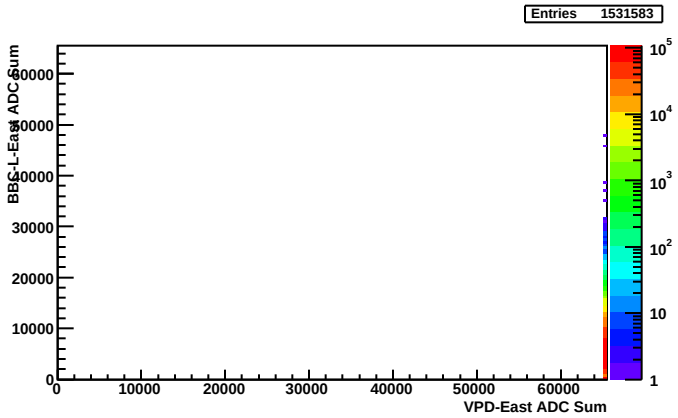
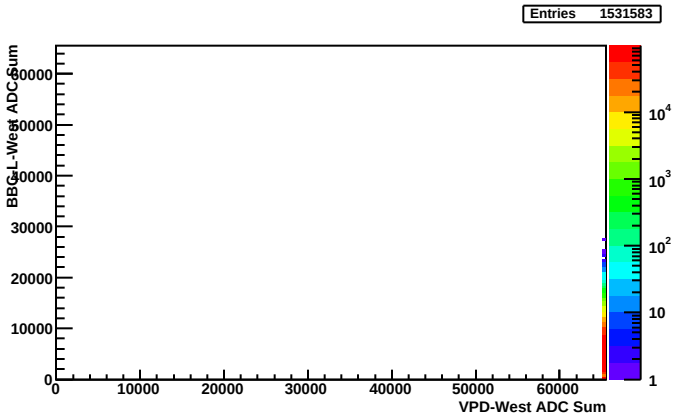
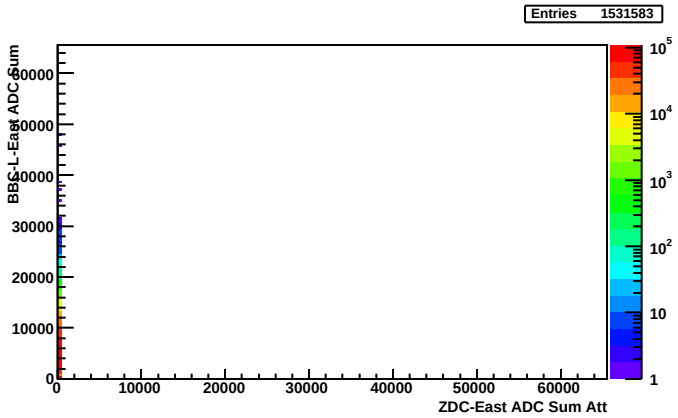
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1531583
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 1531583
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 1531583
BBC-L-West ADC Sum
VPD-West ADC Sum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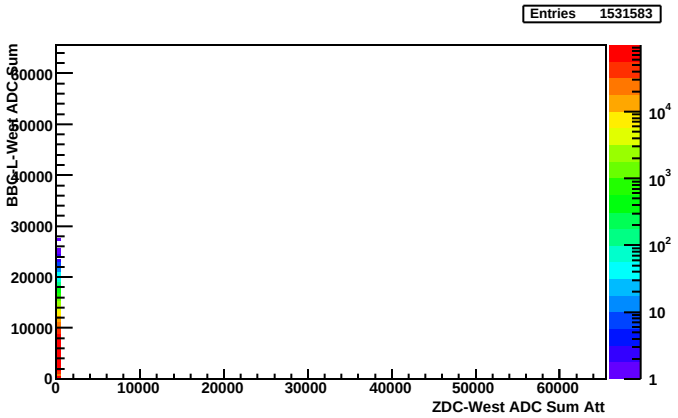
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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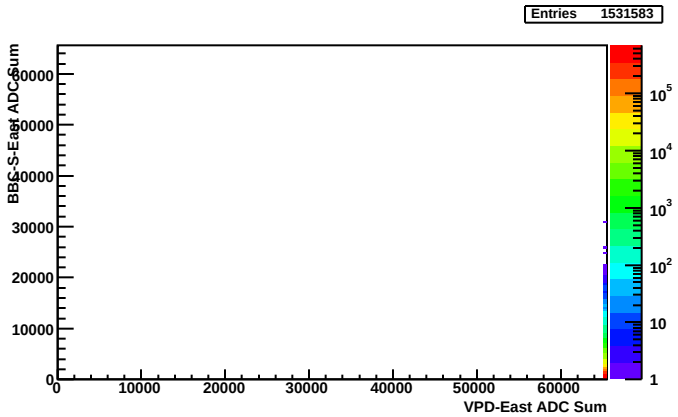
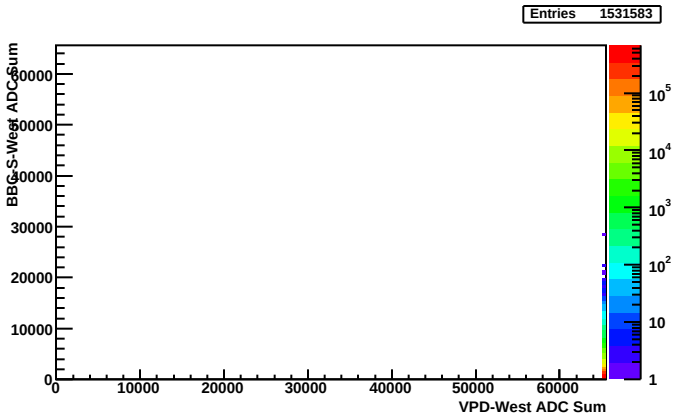
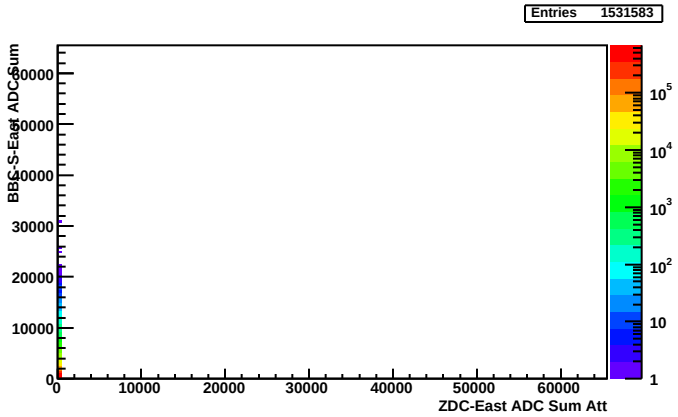
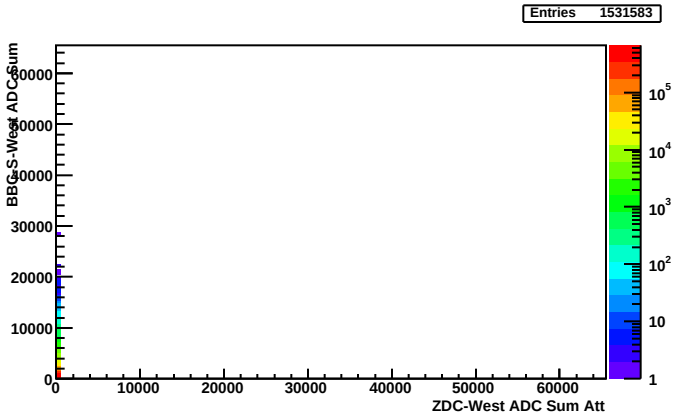
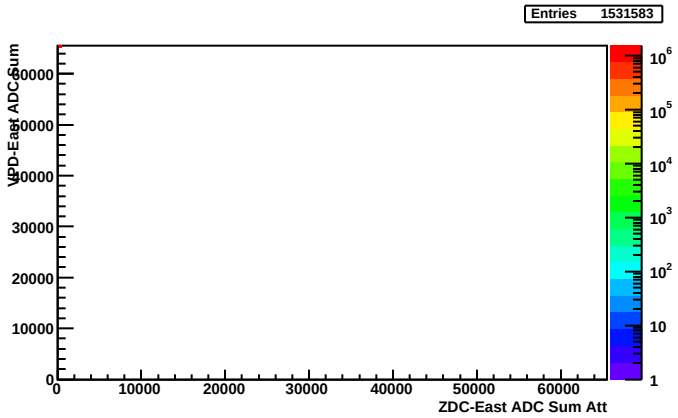
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

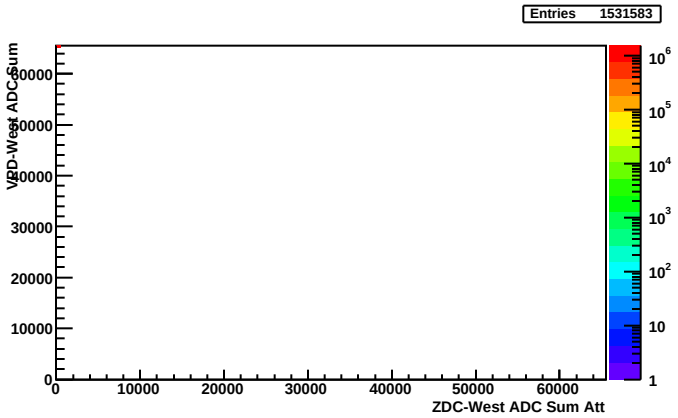

Entries 1531583
BBC-S-East ADC Sum
VPD-East ADC Sum

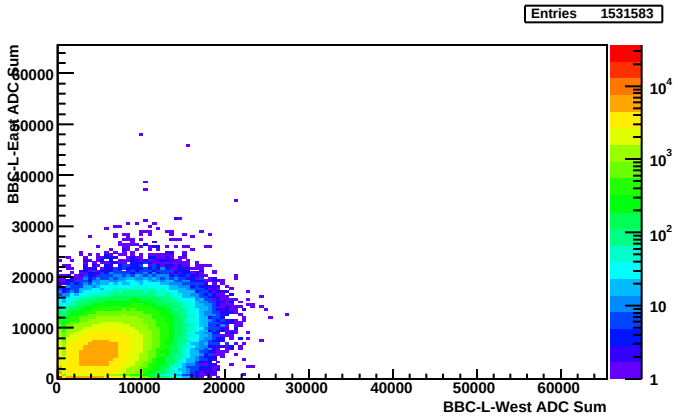

Entries 1531583
BBC-S-West ADC Sum
VPD-West ADC Sum

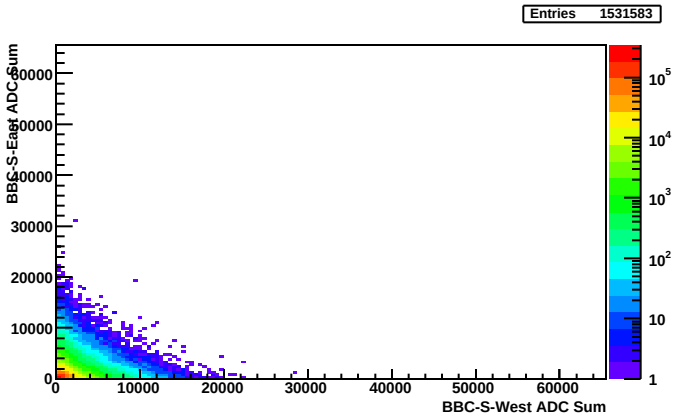

Entries 1531583
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 1531583
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1531583
VPD-East ADC Sum
ZDC-East ADC Sum Att


Entries 1531583
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 1531583
BBC-L-East ADC Sum
BBC-L-West ADC Sum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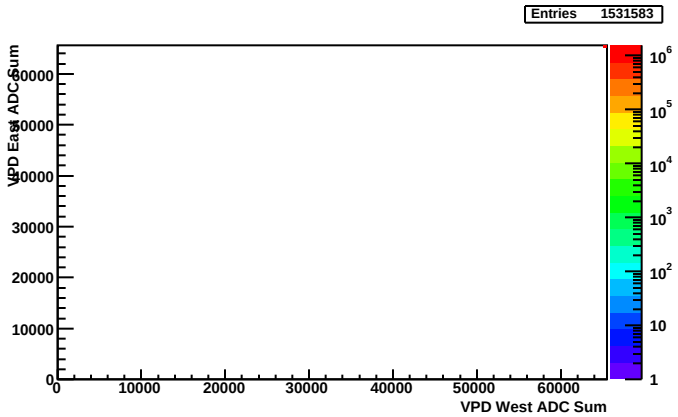
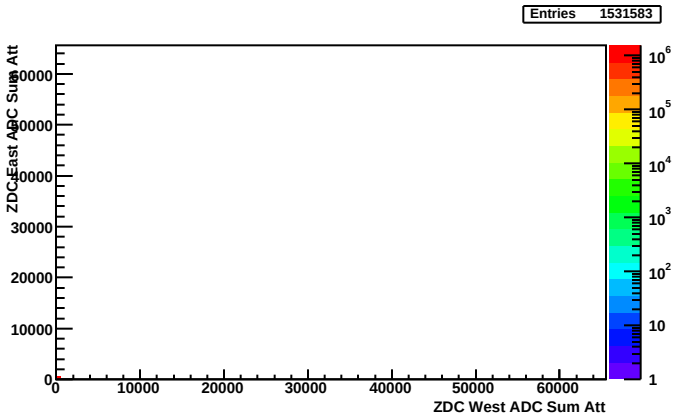
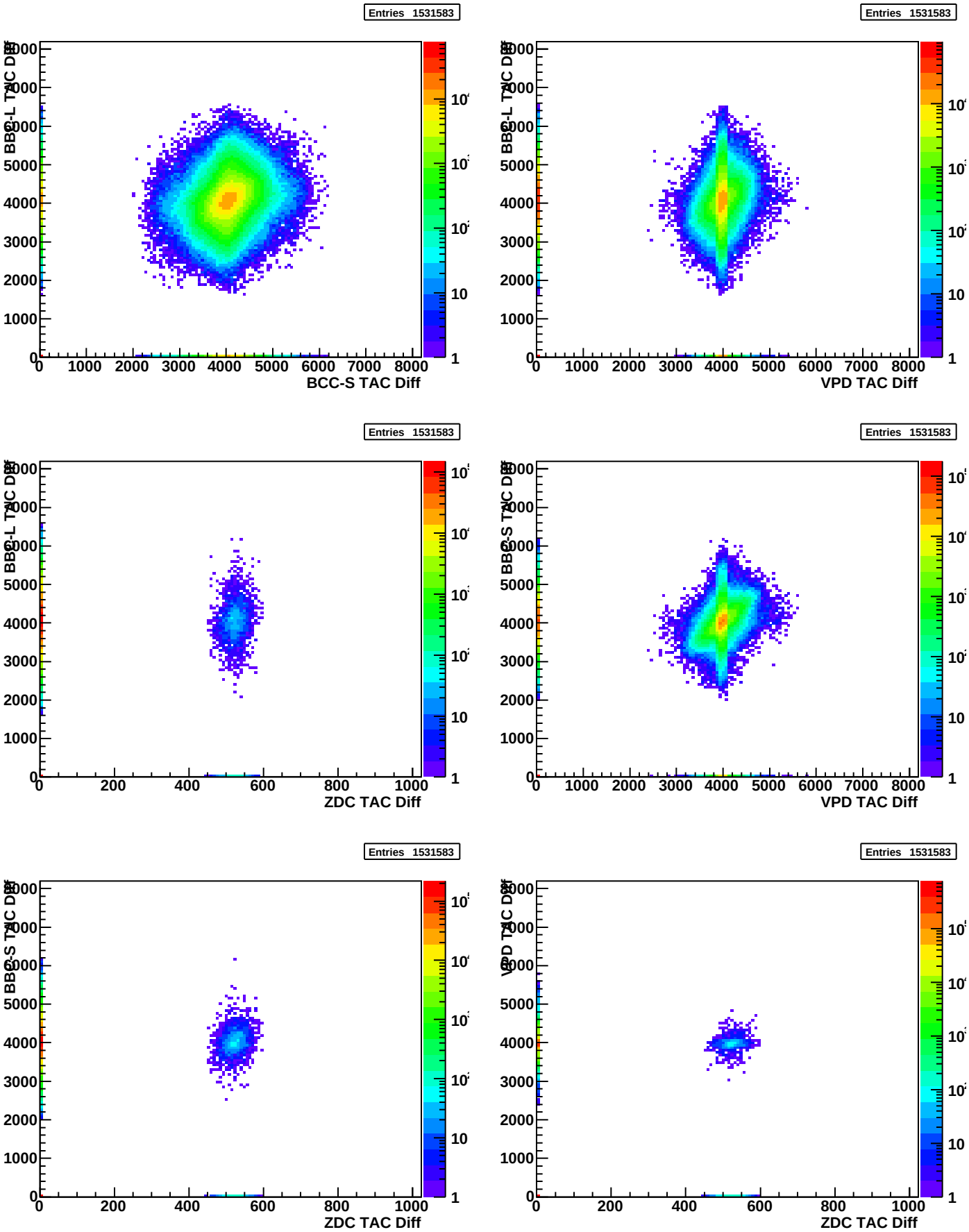
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 1531583
VPD East ADC Sum
VPD West ADC Sum


Entries 1531583
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 1531583
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1531583
BBC-L TAC Diff
VPD TAC Diff
Entries 1531583
BBC-L TAC Diff
ZDC TAC Diff
Entries 1531583
BBC-S TAC Diff
VPD TAC Diff
Entries 1531583
BBC-S TAC Diff
ZDC TAC Diff
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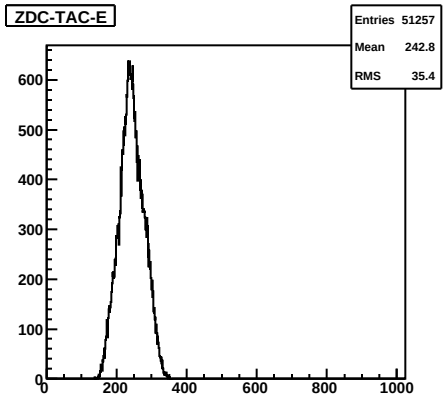
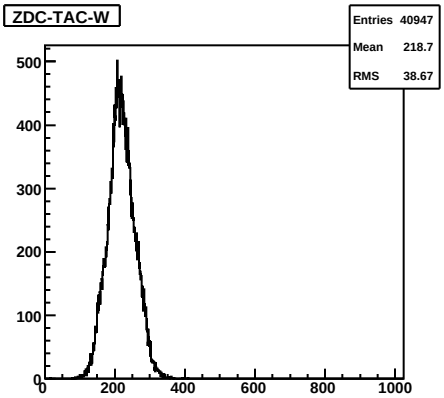
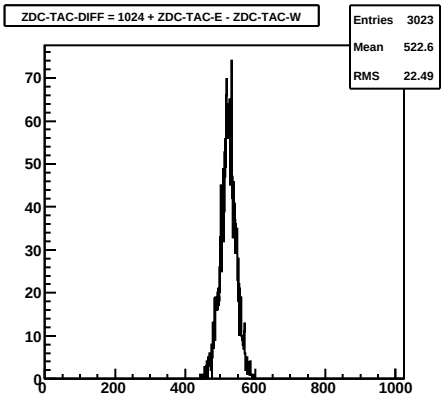
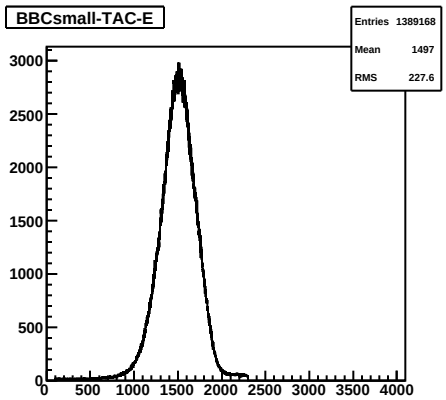
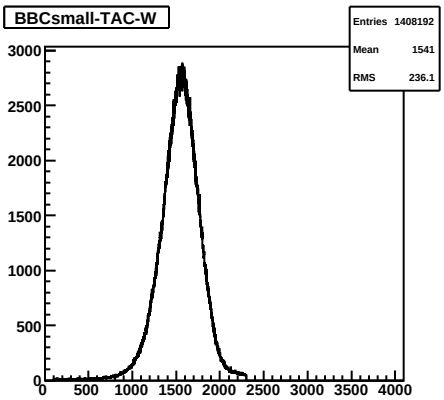
VPD TAC Diff
ZDC TAC Diff

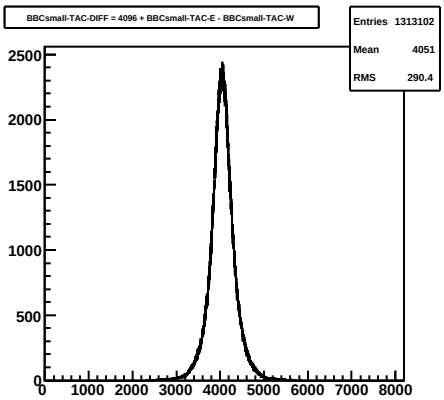

ZDC-TAC-E
Entries 51257
Mean 242.8
RMS 35.4

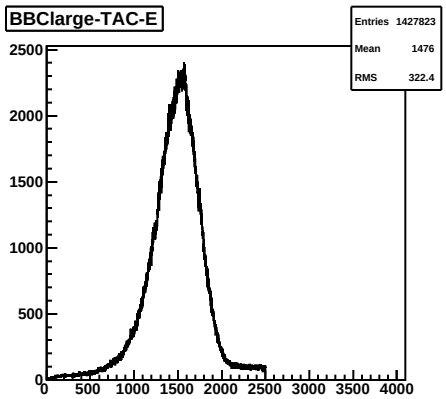

ZDC-TAC-W
Entries 40947
Mean 218.7
RMS 38.67

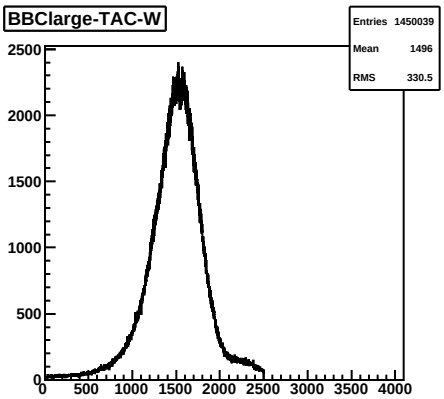

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3023
Mean 522.6
RMS 22.49

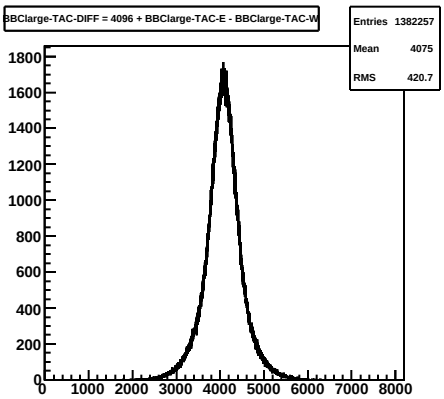

BBCsmall-TAC-E
Entries 1389168
Mean 1497
RMS 227.6

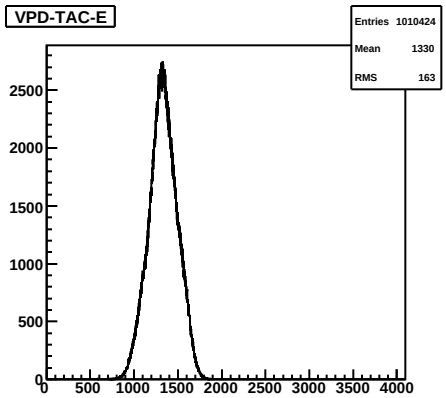

BBCsmall-TAC-W
Entries 1408192
Mean 1541
RMS 236.1

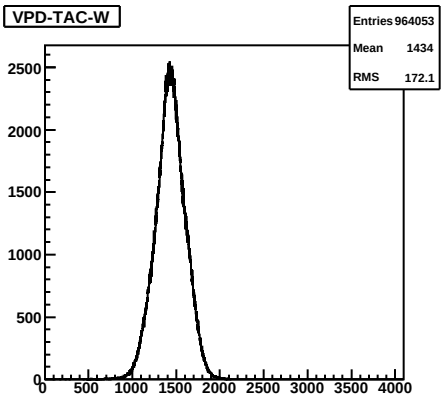

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1313102
Mean 4051
RMS 290.4

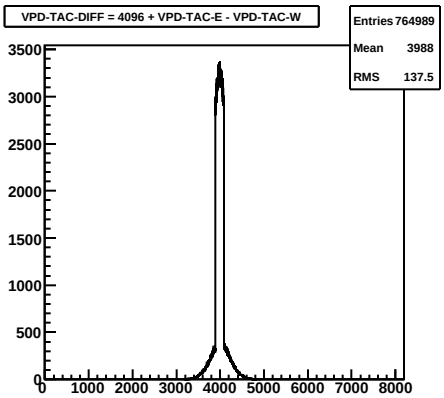

BBClarge-TAC-E
Entries 1427823
Mean 1476
RMS 322.4

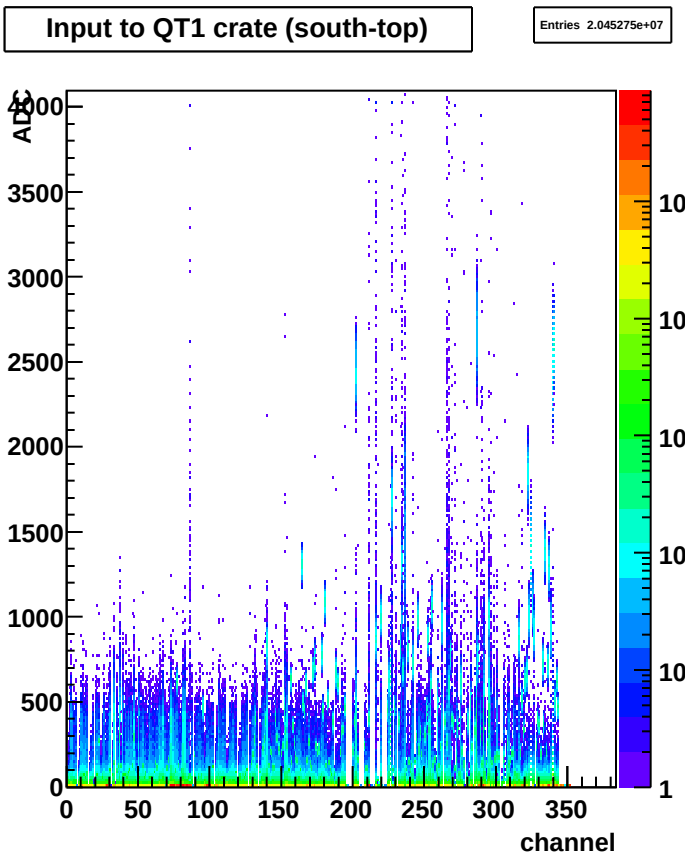

BBClarge-TAC-W
Entries 1450039
Mean 1496
RMS 330.5

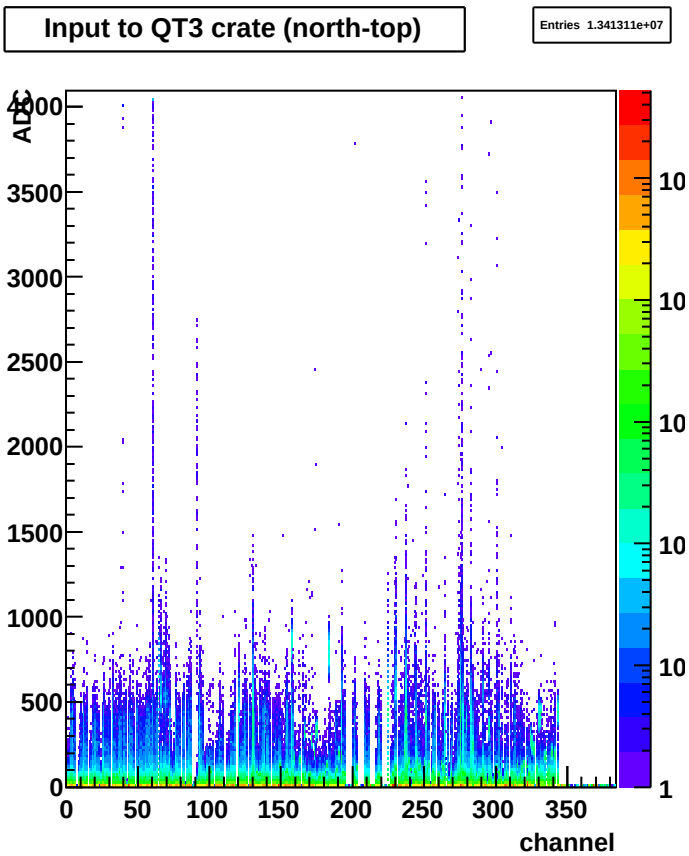

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1382257
Mean 4075
RMS 420.7

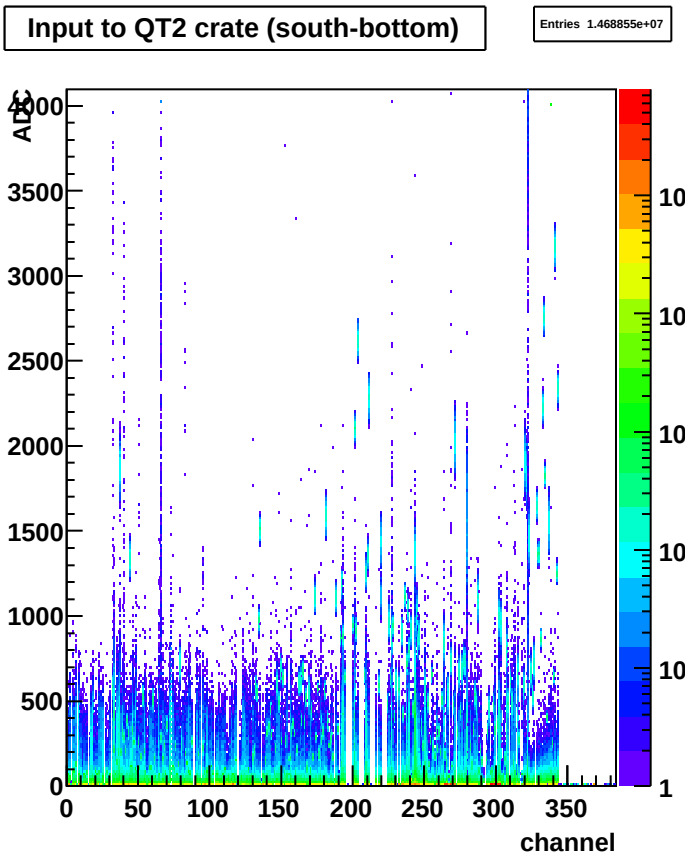

VPD-TAC-E
Entries 1010424
Mean 1330
RMS 163

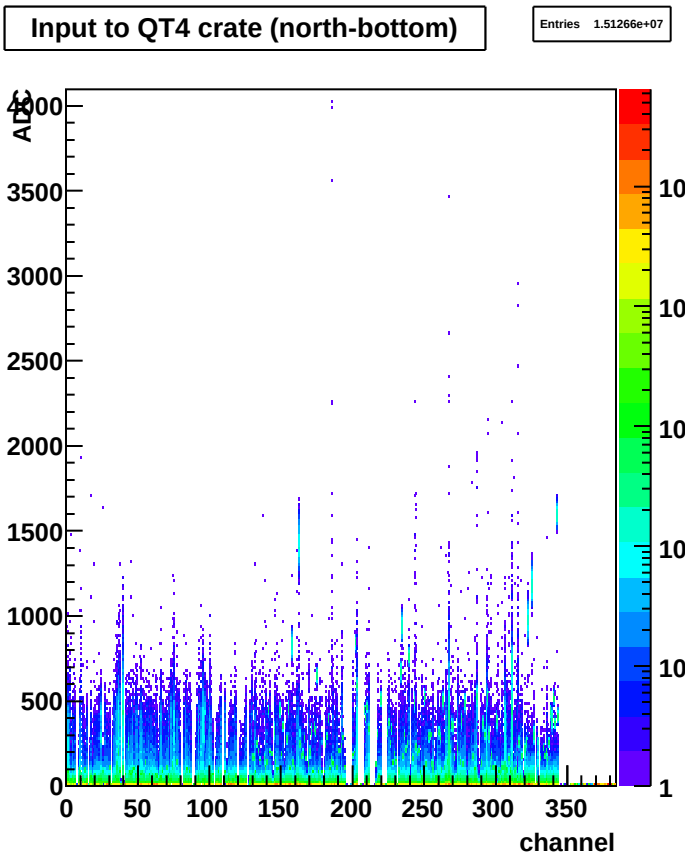

VPD-TAC-W
Entries 964053
Mean 1434
RMS 172.1

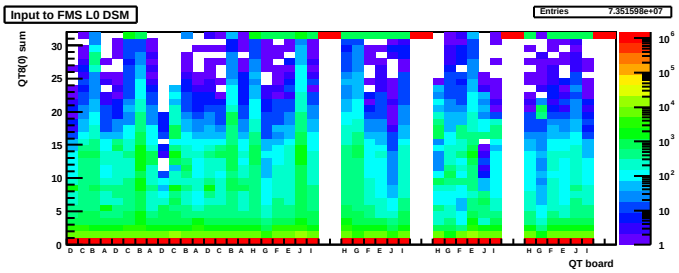

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 764989
Mean 3988
RMS 137.5

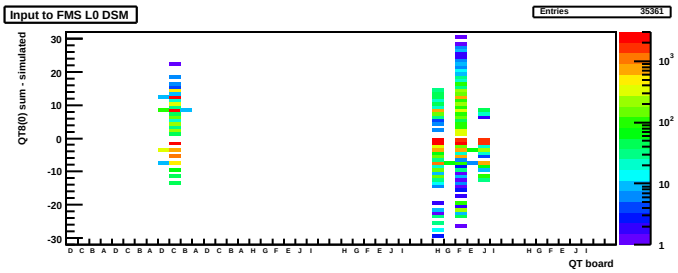

Input to QT1 crate (south-top)
Entries 2.045275e+07
ADC
channel

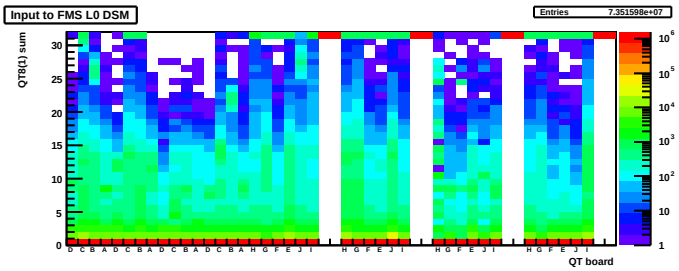

Input to QT3 crate (north-top)
Entries 1.341311e+07
ADC
channel

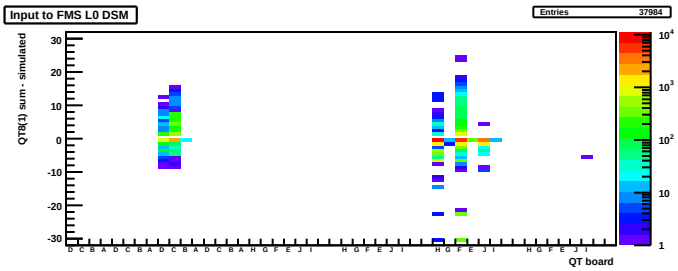

Input to QT2 crate (south-bottom)
Entries 1.468855e+07
ADC
channel

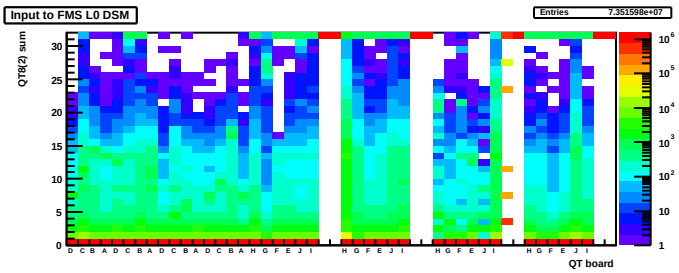

Input to QT4 crate (north-bottom)
Entries 1.51266e+07
ADC
channel

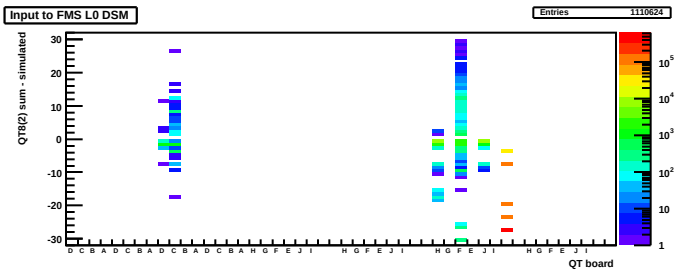

Input to FMS L0 DSM
Entries 7.351598e+07
QT8(0) sum
QT board

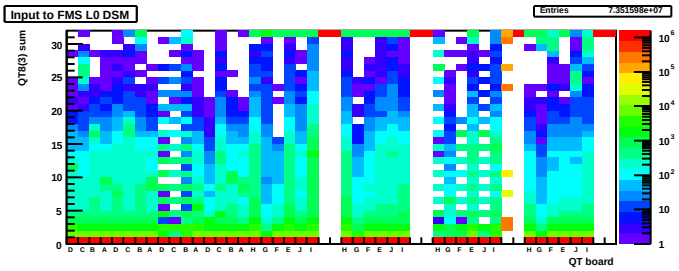

Input to FMS L0 DSM
Entries 35361
QT8(0) sum - simulated
QT board

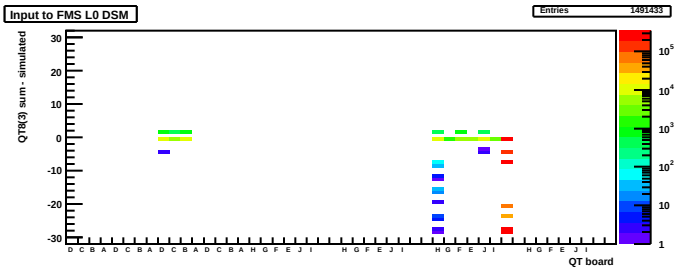

Input to FMS L0 DSM
Entries 7.351598e+07
QT8(1) sum
QT board

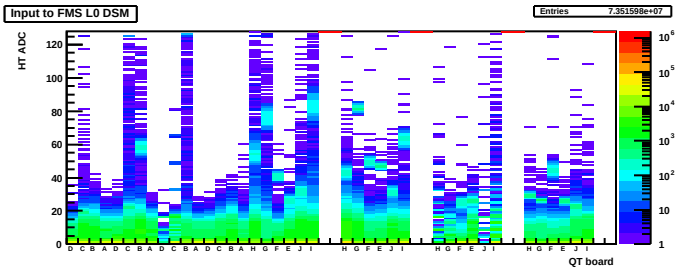

Input to FMS L0 DSM
Entries 37984
QT8(1) sum - simulated
QT board

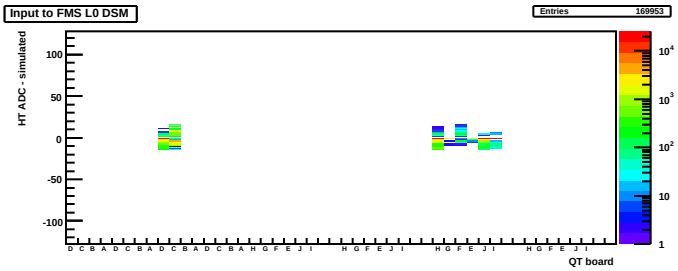

Input to FMS L0 DSM
Entries 7.351598e+07
QT8(2) sum
QT board

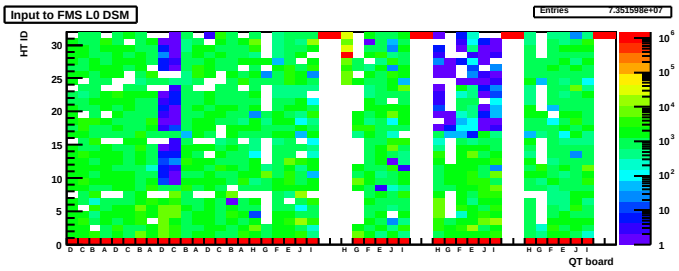

Input to FMS L0 DSM
Entries 1110624
QT8(2) sum - simulated
QT board

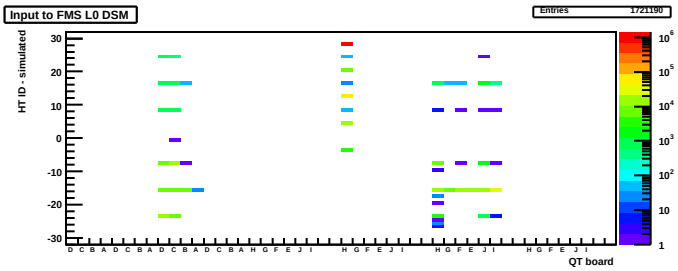

Input to FMS L0 DSM
Entries 7.351598e+07
QT8(3) sum
QT board

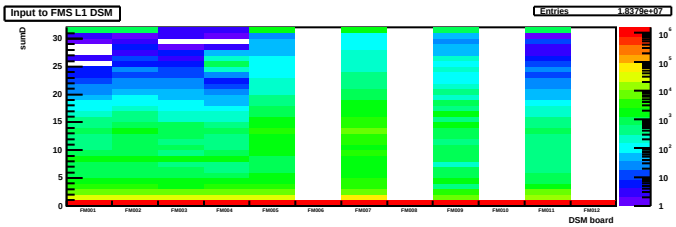

Input to FMS L0 DSM
Entries 1491433
QT8(3) sum - simulated
QT board

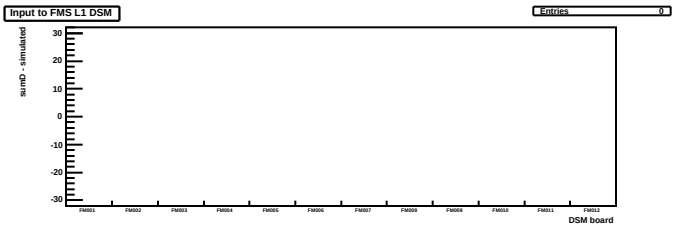

Input to FMS L0 DSM
Entries 7.351598e+07
HT ADC
QT board

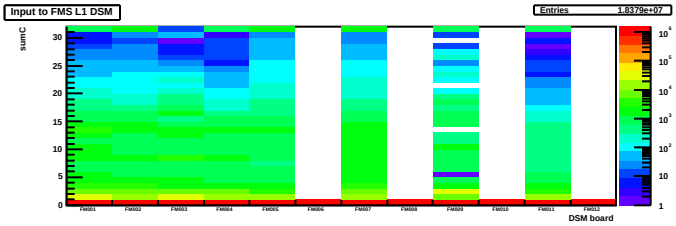

Input to FMS L0 DSM
Entries 169953
HT ADC - simulated
QT board

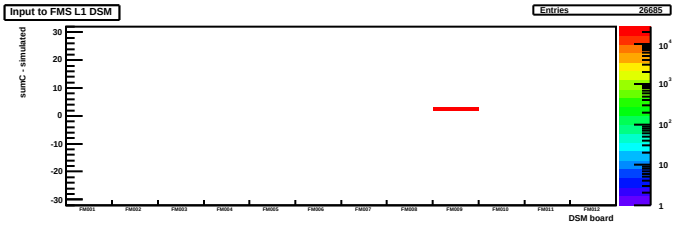

Input to FMS L0 DSM
Entries 7.351598e+07
HT ID
QT board

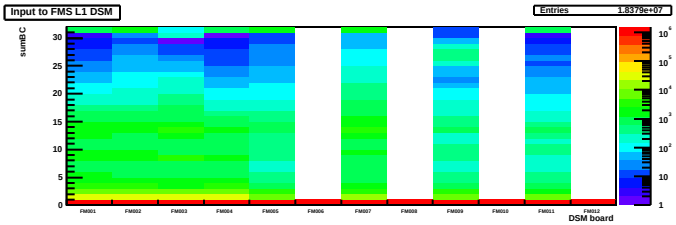

Input to FMS L0 DSM
Entries 1721190
HT ID - simulated
QT board

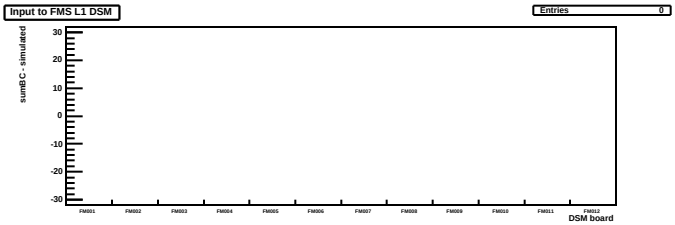

Input to FMS L1 DSM
Entries 1.8379e+07
sumD
DSM board

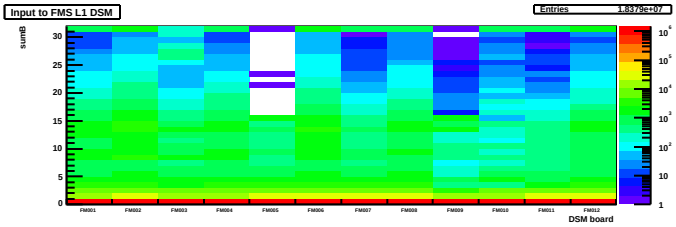

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

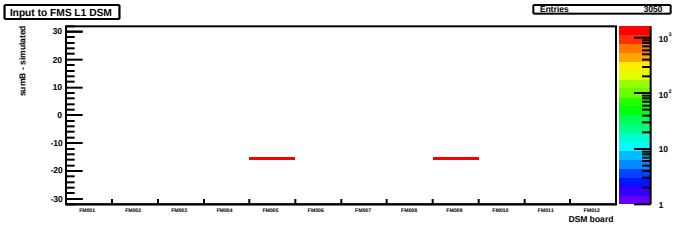

Input to FMS L1 DSM
Entries 1.8379e+07
sumC
DSM board

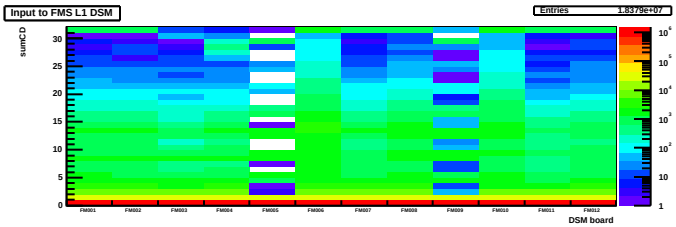

Input to FMS L1 DSM
Entries 26685
sumC - simulated
DSM board

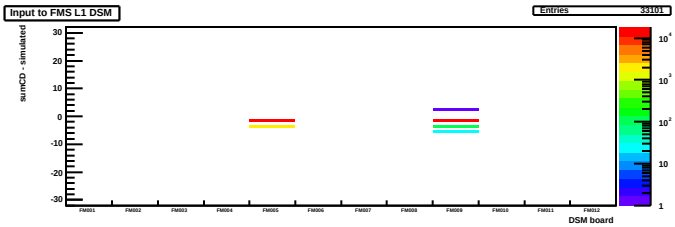

Input to FMS L1 DSM
Entries 1.8379e+07
sumBC
DSM board

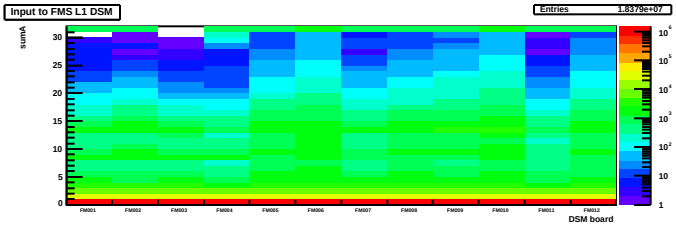

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

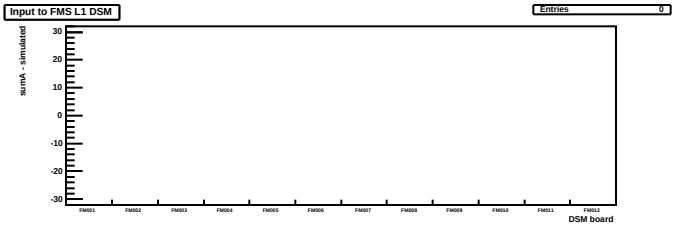

Input to FMS L1 DSM
Entries 1.8379e+07
sumB
DSM board

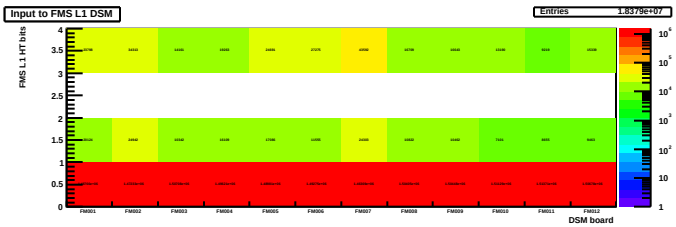

Input to FMS L1 DSM
Entries 3050
sumB - simulated
DSM board

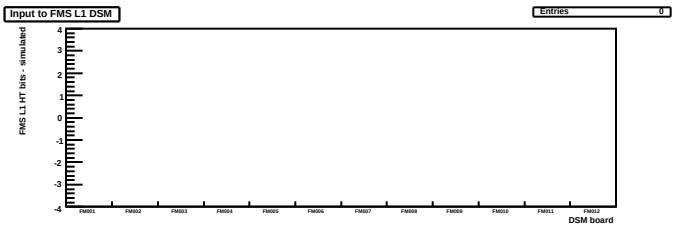

Input to FMS L1 DSM
Entries 1.8379e+07
sumCD
DSM board

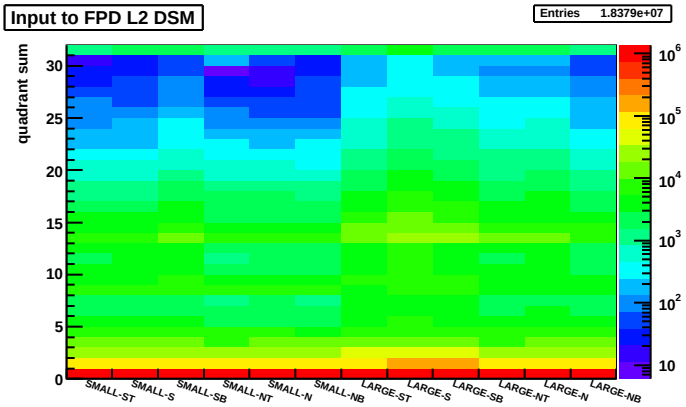

Input to FMS L1 DSM
Entries 33101
sumCD - simulated
DSM board

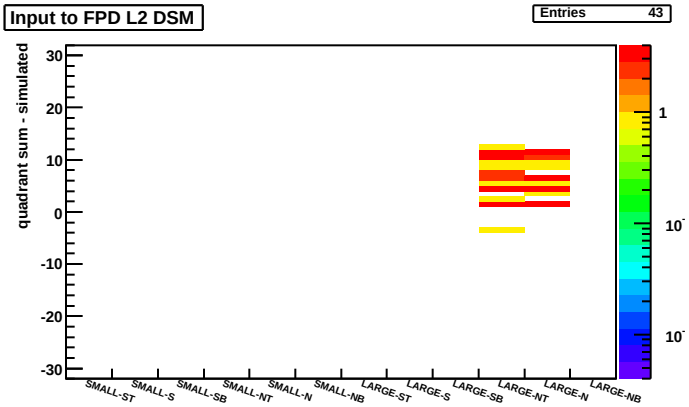

Input to FMS L1 DSM
Entries 1.8379e+07
sumA
DSM board

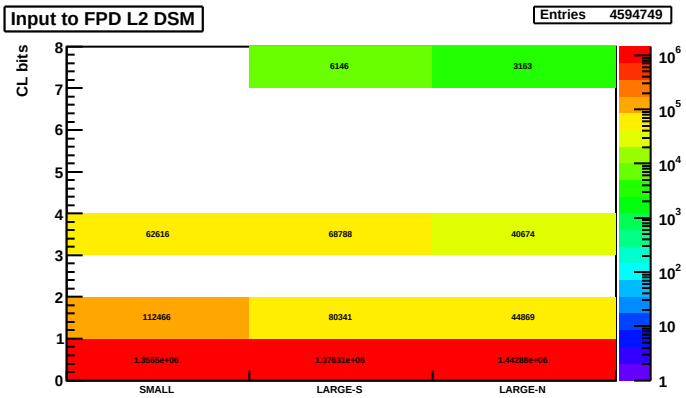

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

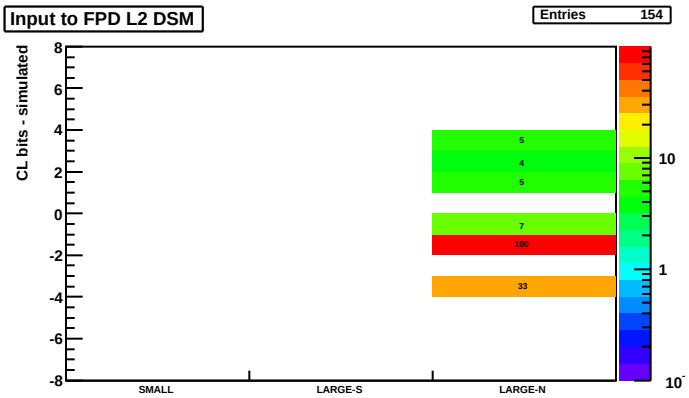

Input to FMS L1 DSM
Entries 1.8379e+07
FMS L1 HT bits
DSM board

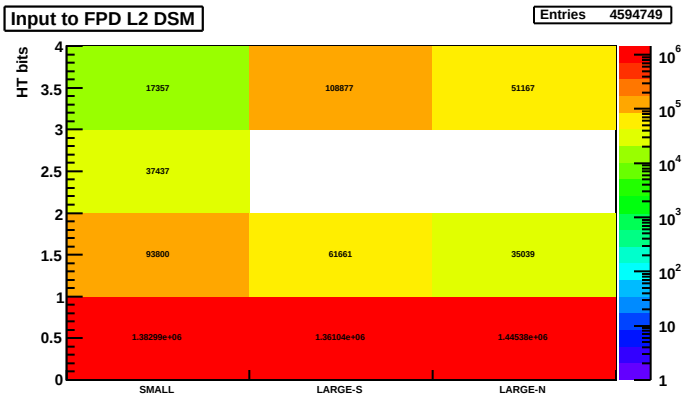

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

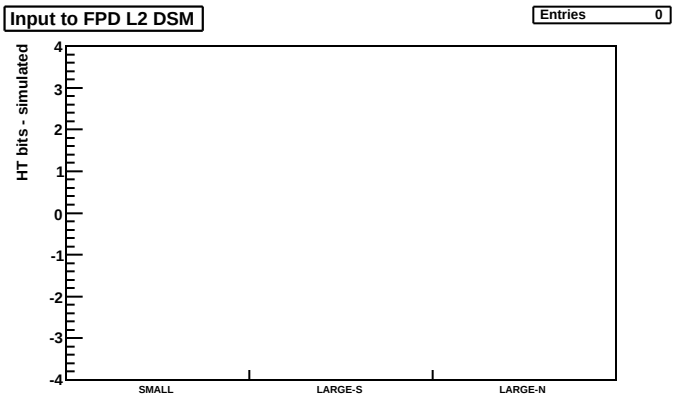

Input to FPD L2 DSM
Entries 1.8379e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

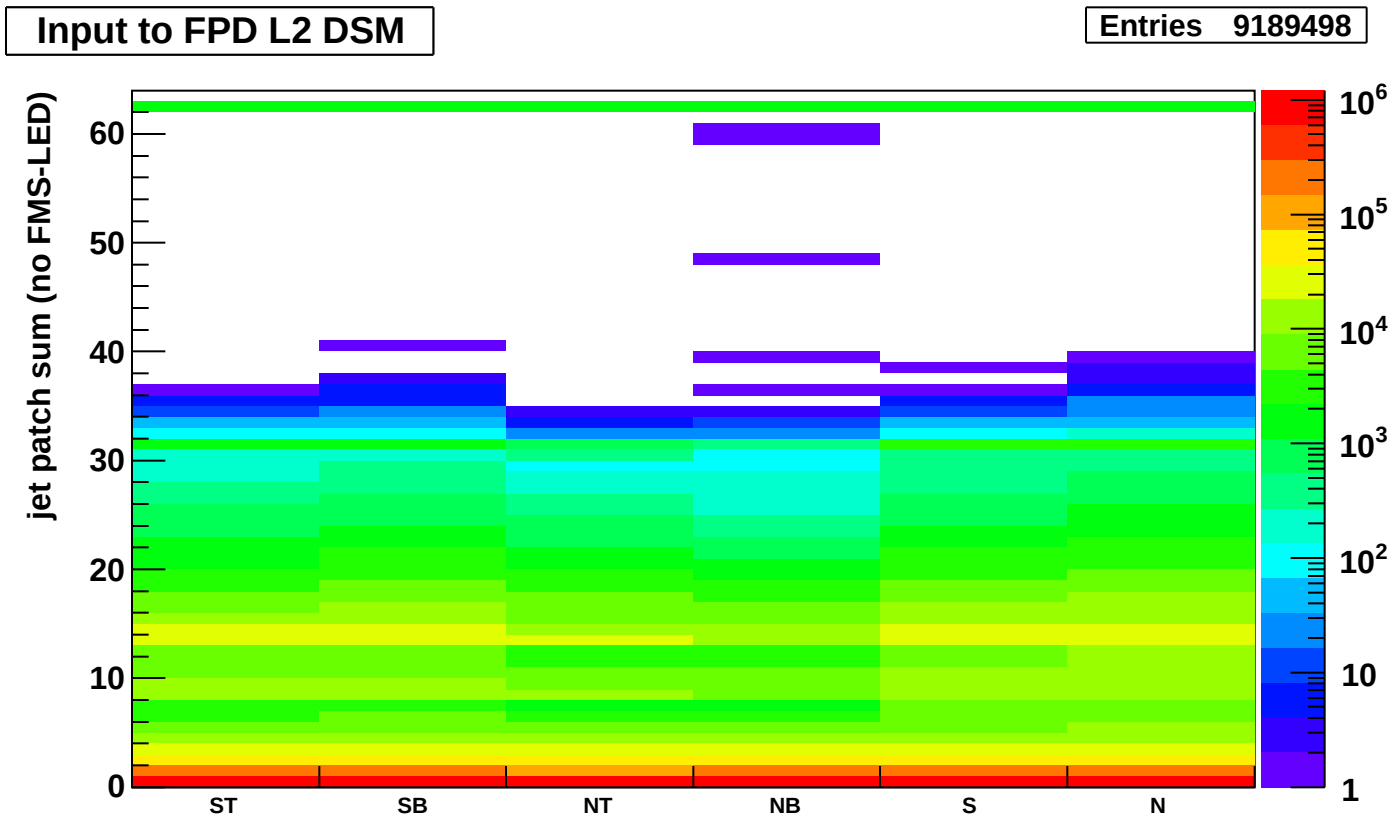

Input to FPD L2 DSM
Entries 43
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

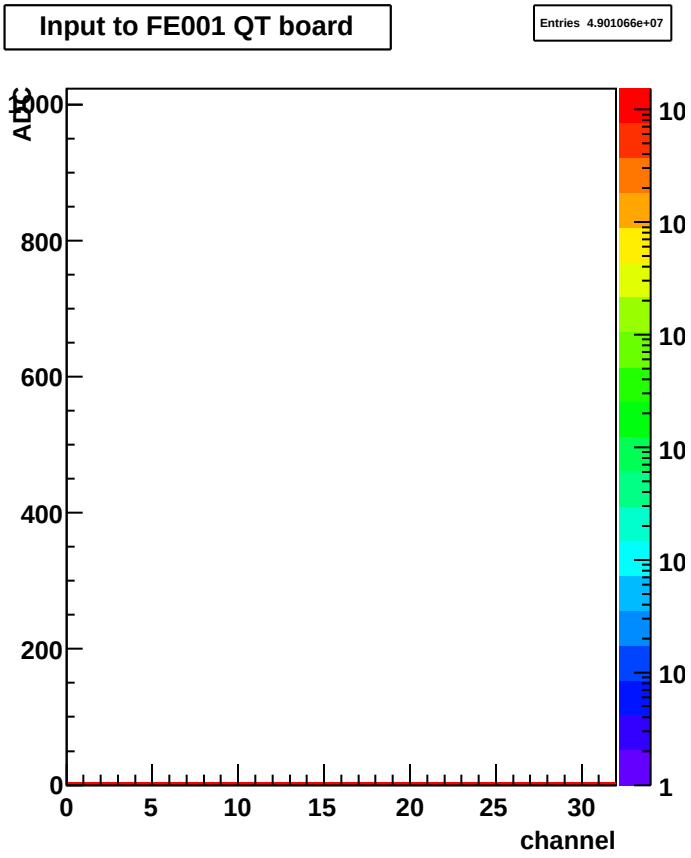

Input to FPD L2 DSM
Entries 4594749
CL bits
6146
3163
62616
68788
40674
112466
80341
44869
1.3565e+06
1.37631e+06
1.44288e+06
SMALL
LARGE-S
LARGE-N

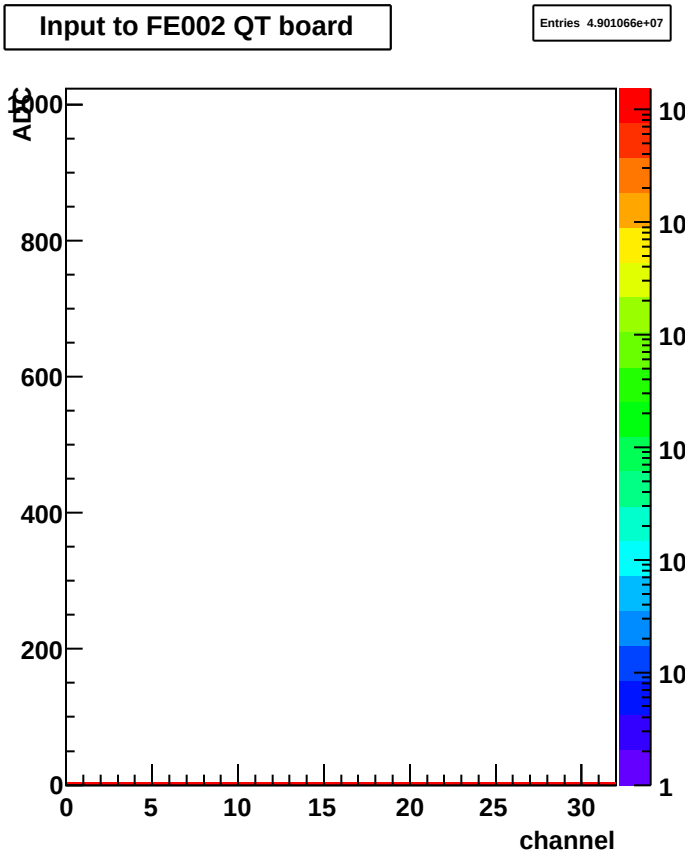

Input to FPD L2 DSM
Entries 154
CL bits - simulated
5
4
5
7
100
33
SMALL
LARGE-S
LARGE-N

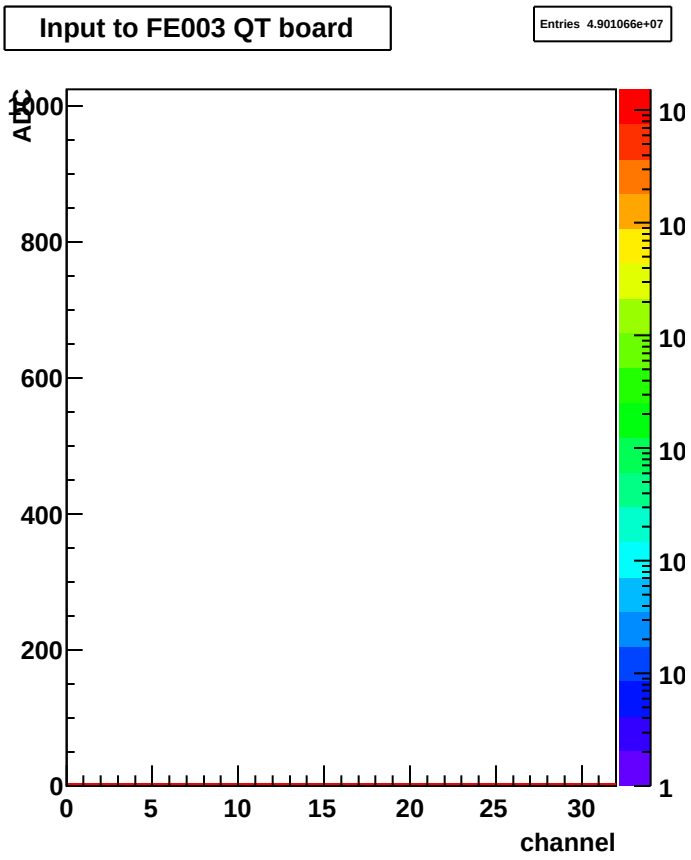

Input to FPD L2 DSM
Entries 4594749
HT bits
17357
108877
51167
37437
93800
61661
35039
1.38299e+06
1.36104e+06
1.44538e+06
SMALL
LARGE-S
LARGE-N

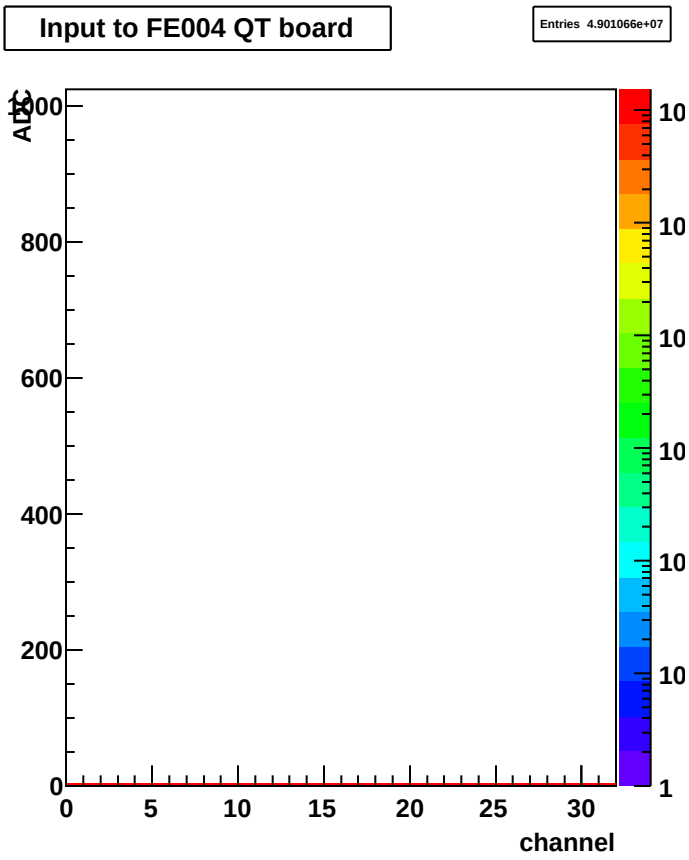

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

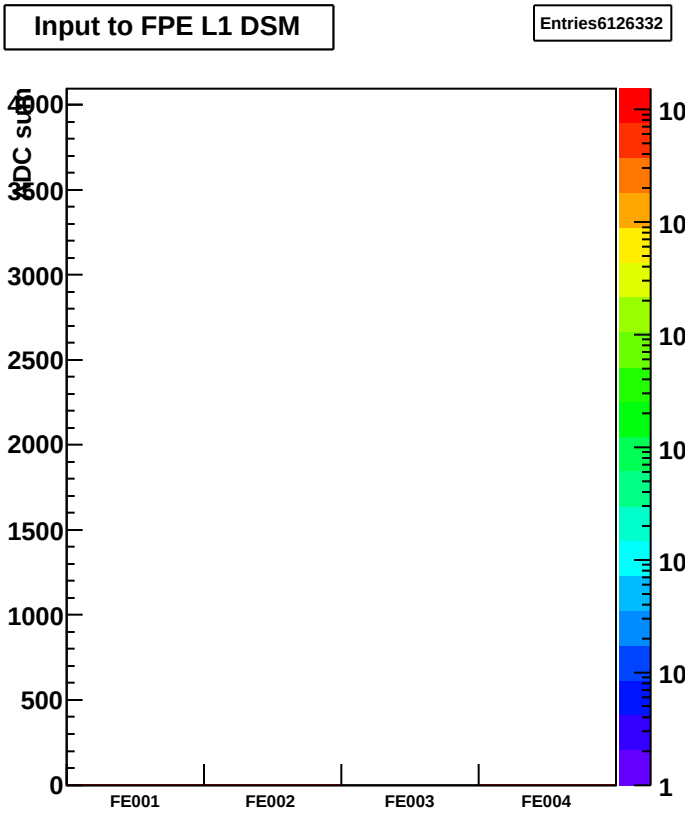

Input to FPD L2 DSM
Entries 9189498
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 4.901066e+07
ADC
channel

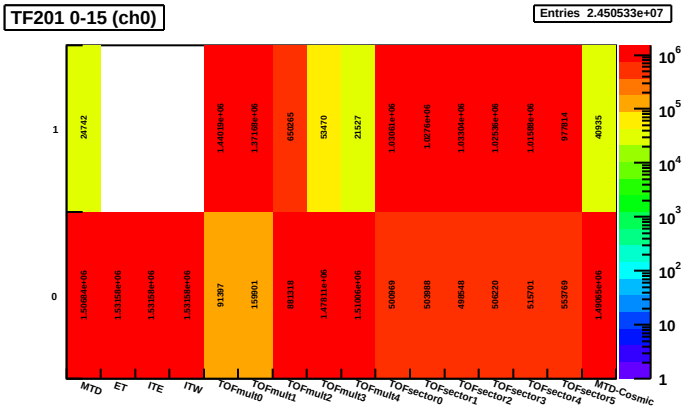

Input to FE002 QT board
Entries 4.901066e+07
ADC
channel


Input to FE003 QT board
Entries 4.901066e+07
ADC
channel

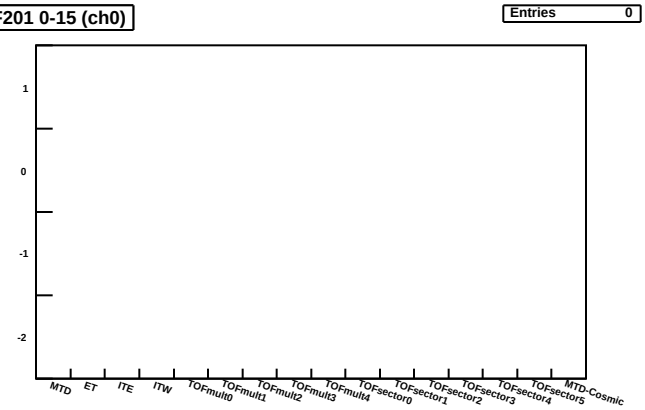

Input to FE004 QT board
Entries 4.901066e+07
ADC
channel


Input to FPE L1 DSM
Entries6126332
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

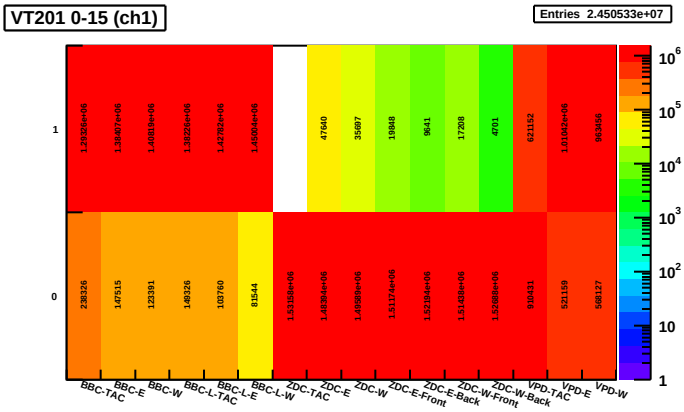
TF201 0-15 (ch0)



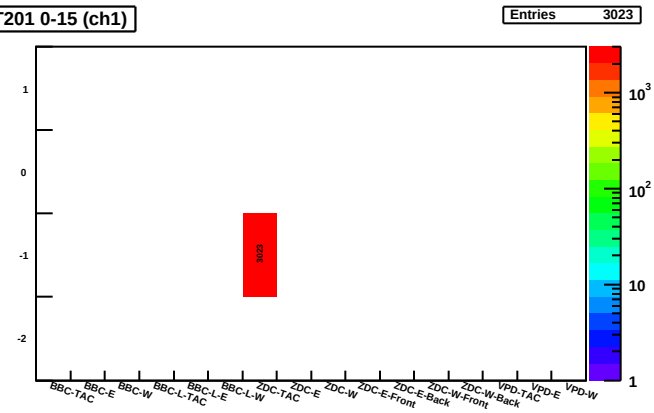
TF201 0-15 (ch0)



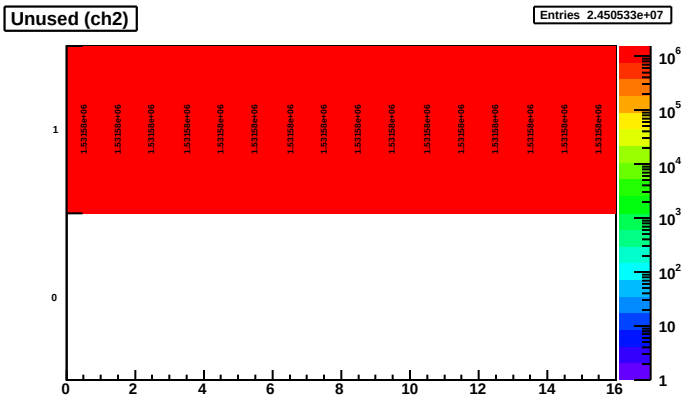
VT201 0-15 (ch1)

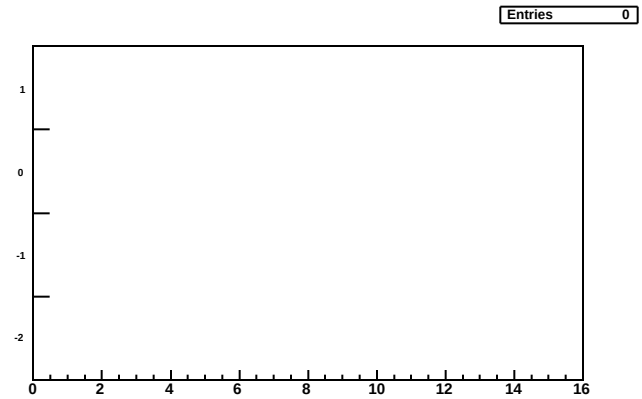


VT201 0-15 (ch1)

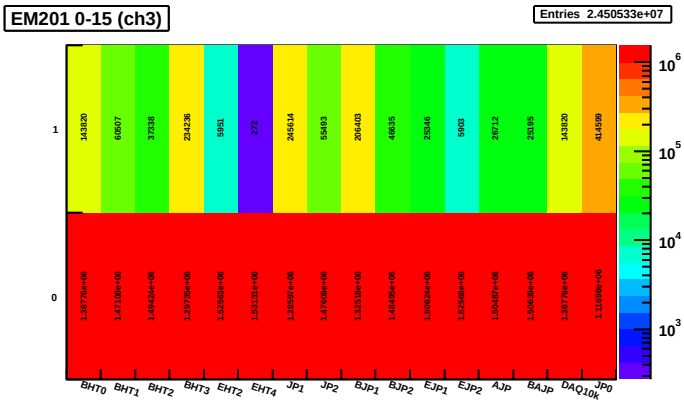


Unused (ch2)

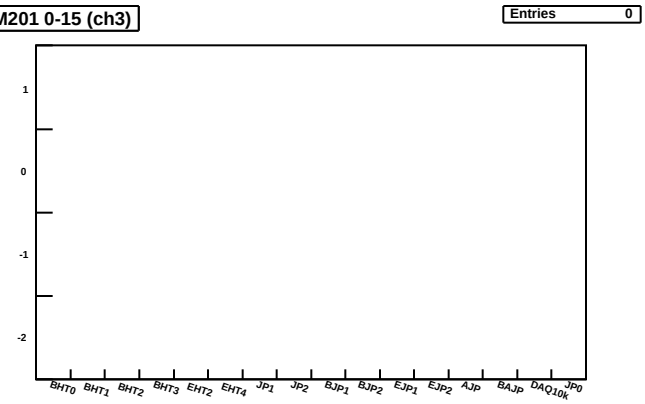




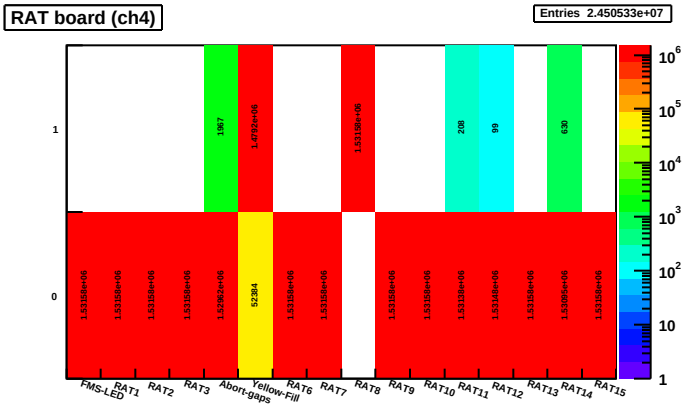
EM201 0-15 (ch3)

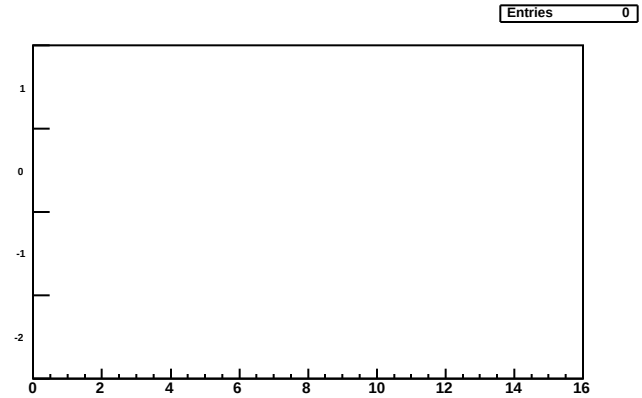


EM201 0-15 (ch3)

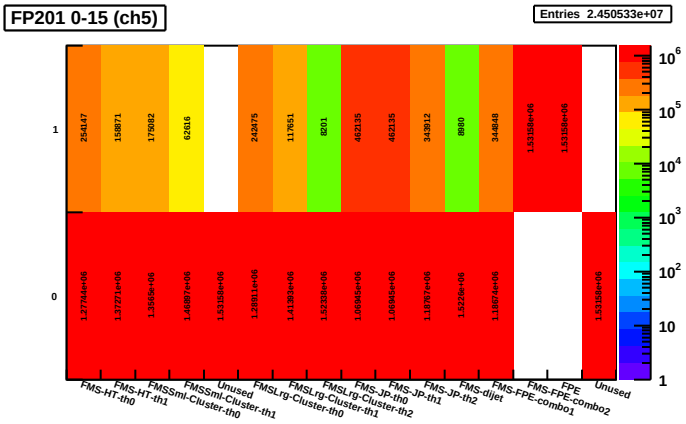


RAT board (ch4)

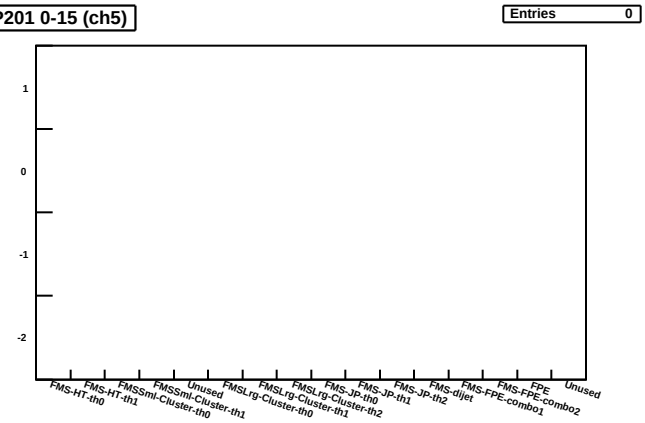




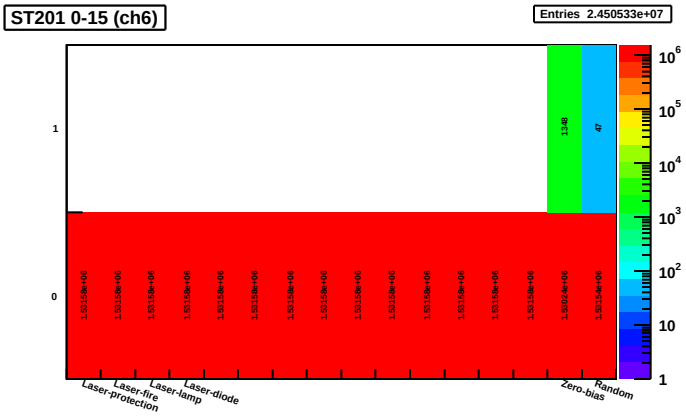
FP201 0-15 (ch5)

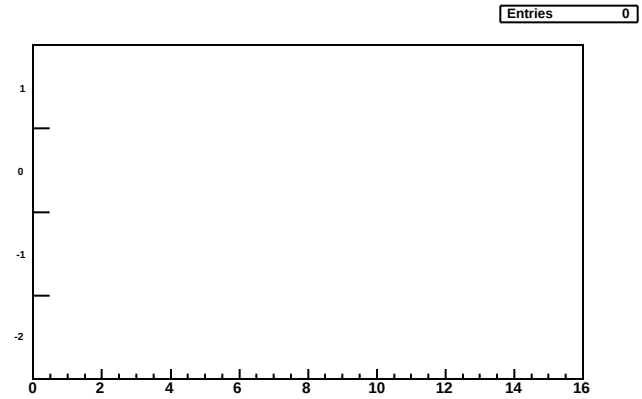
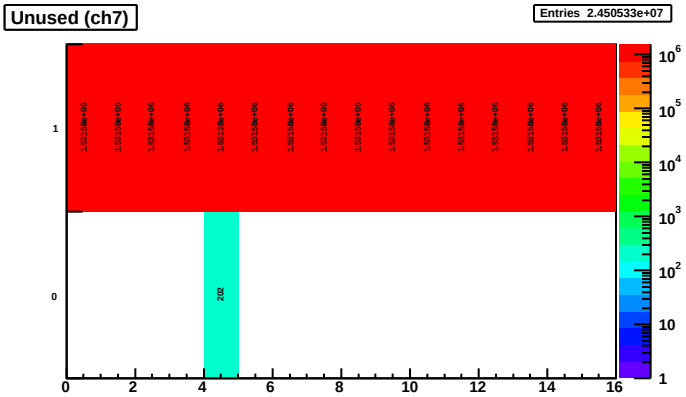
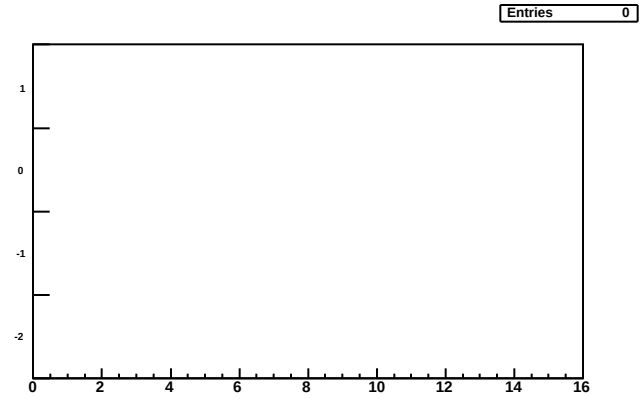


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)