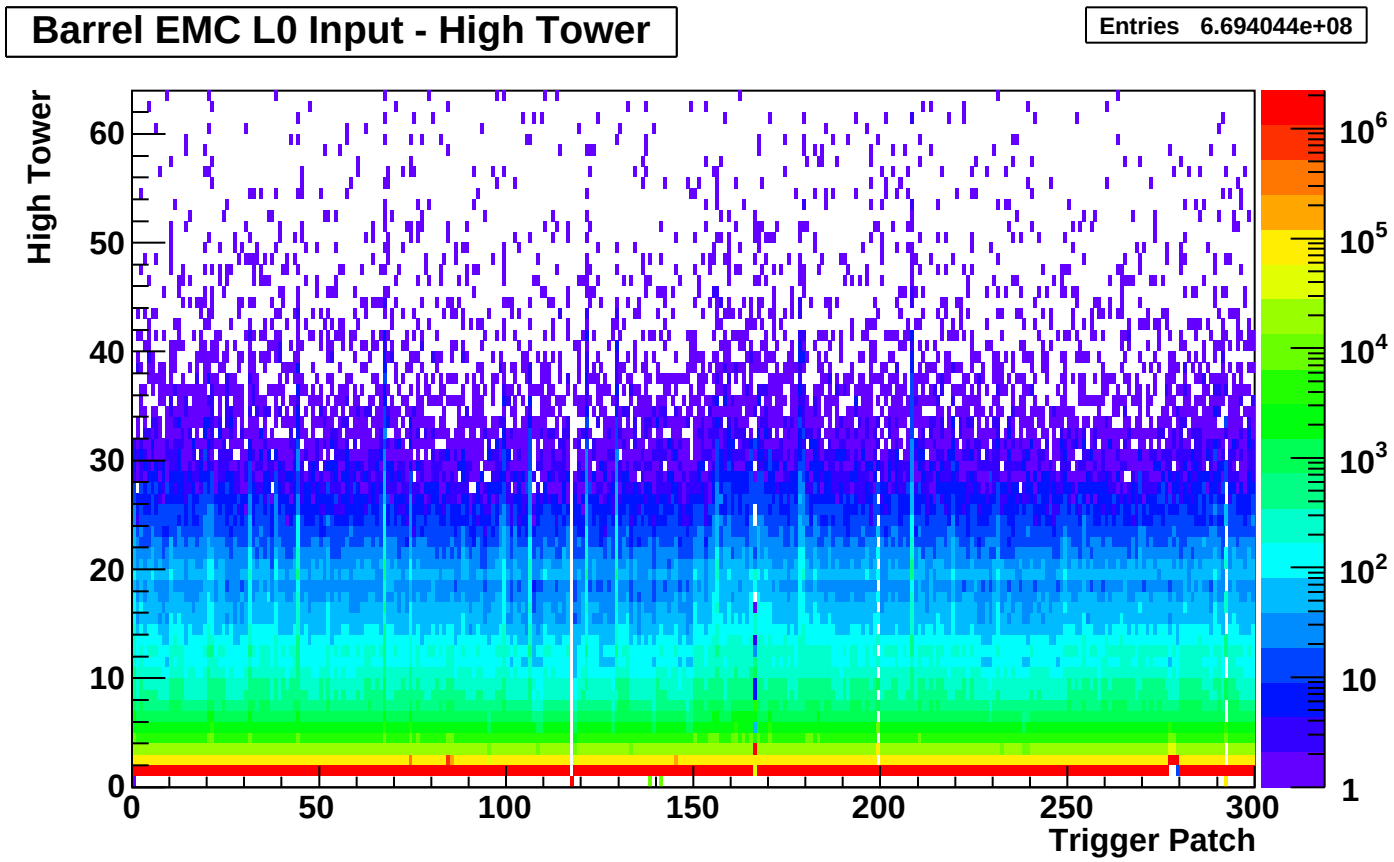
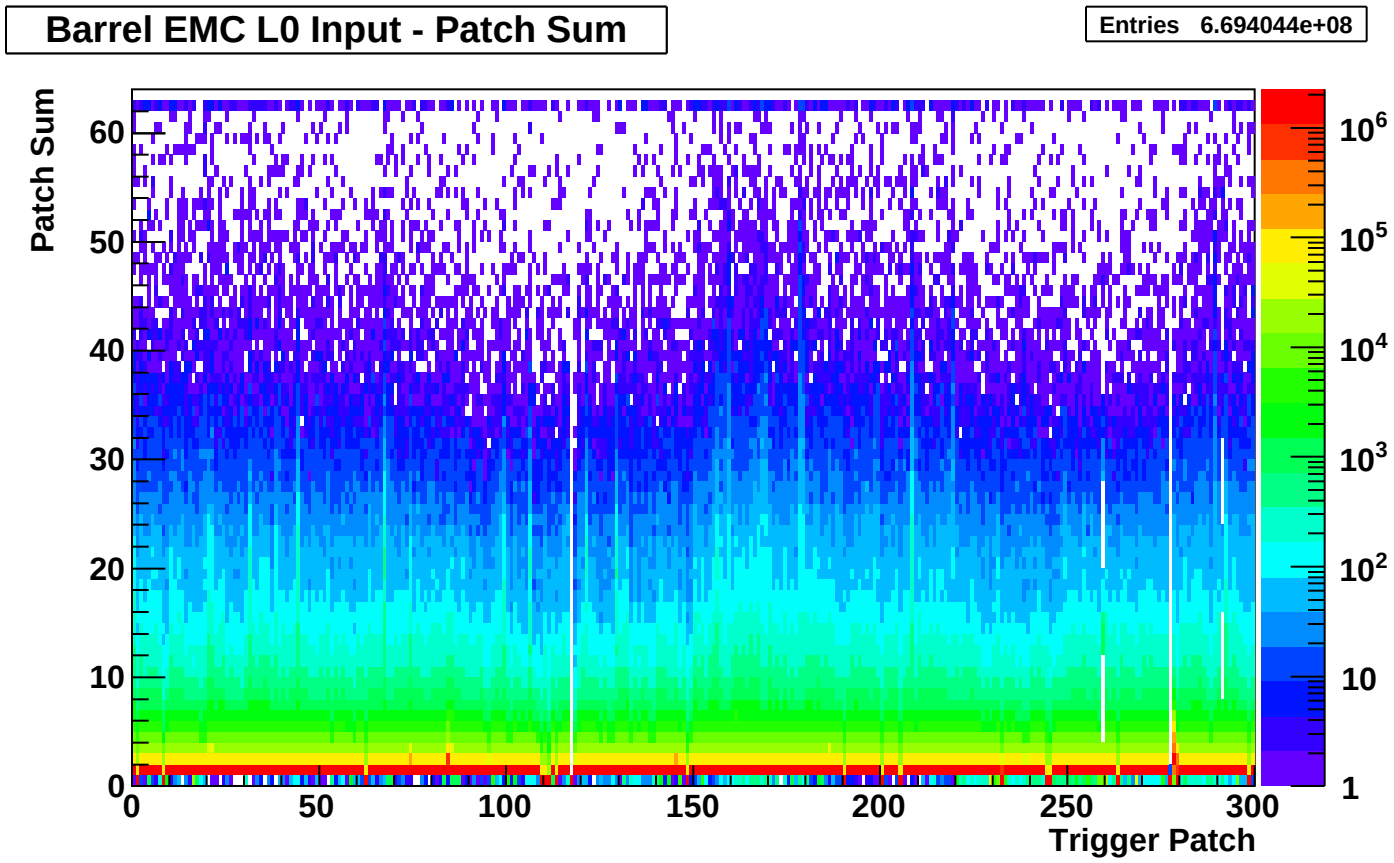
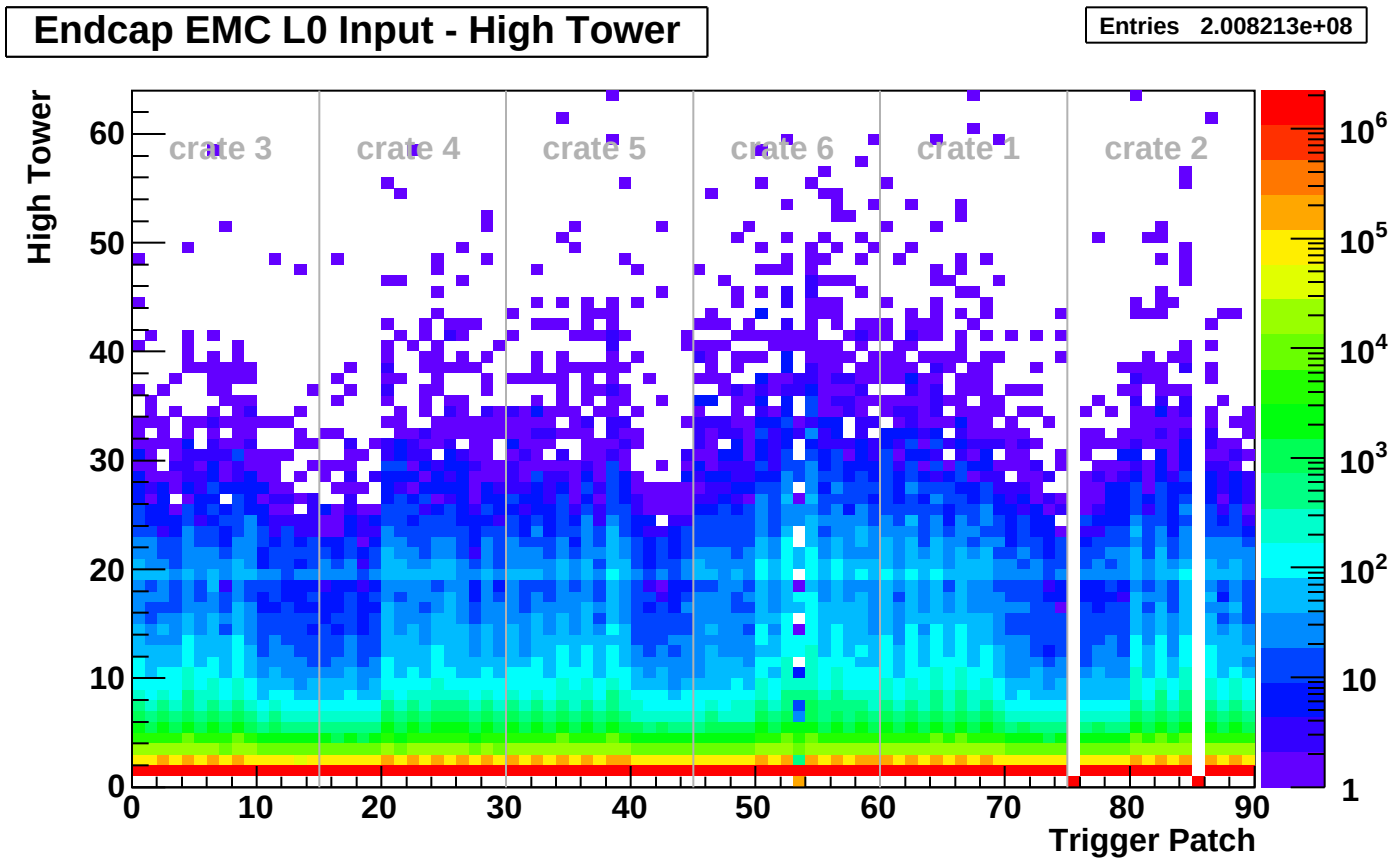
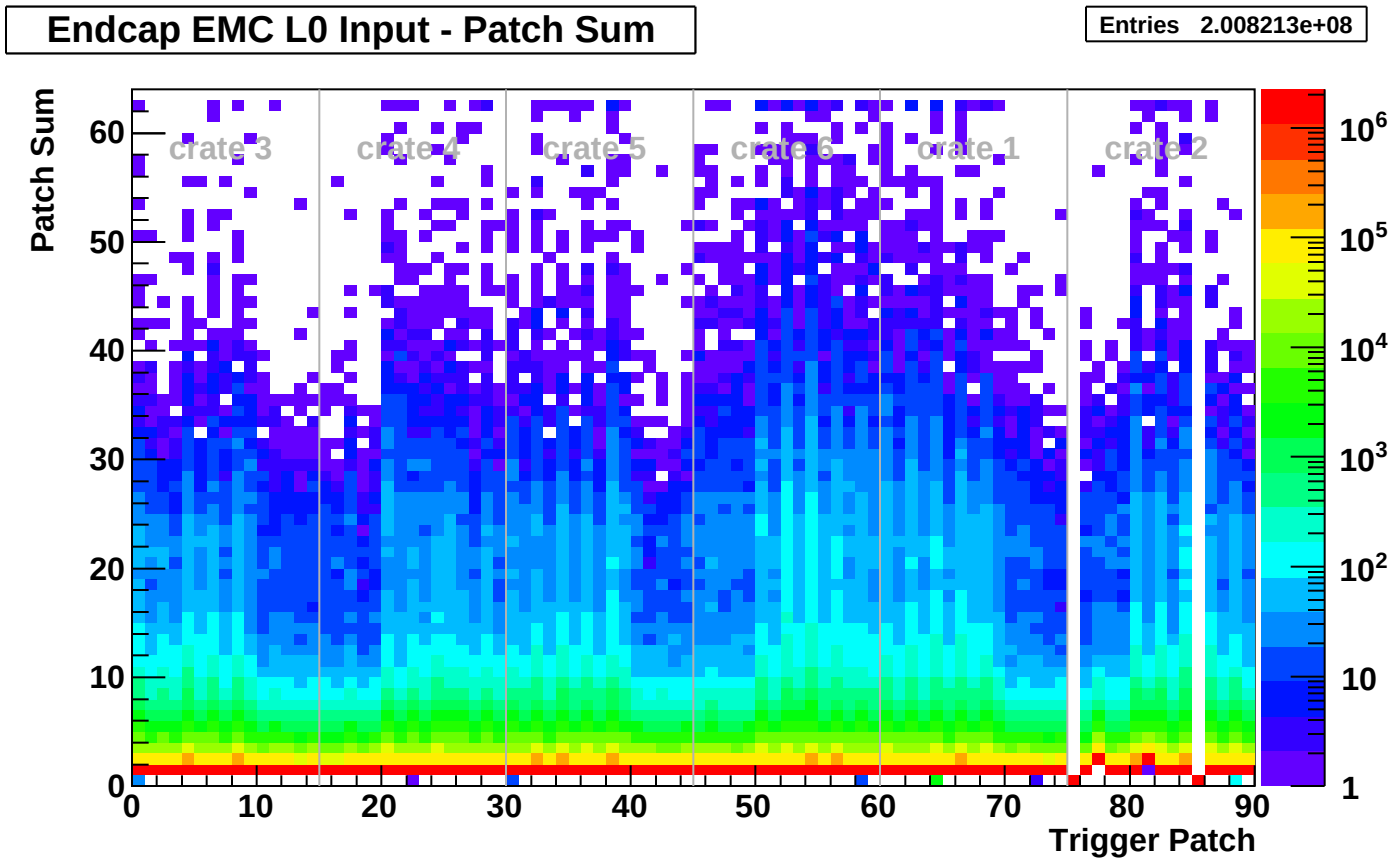
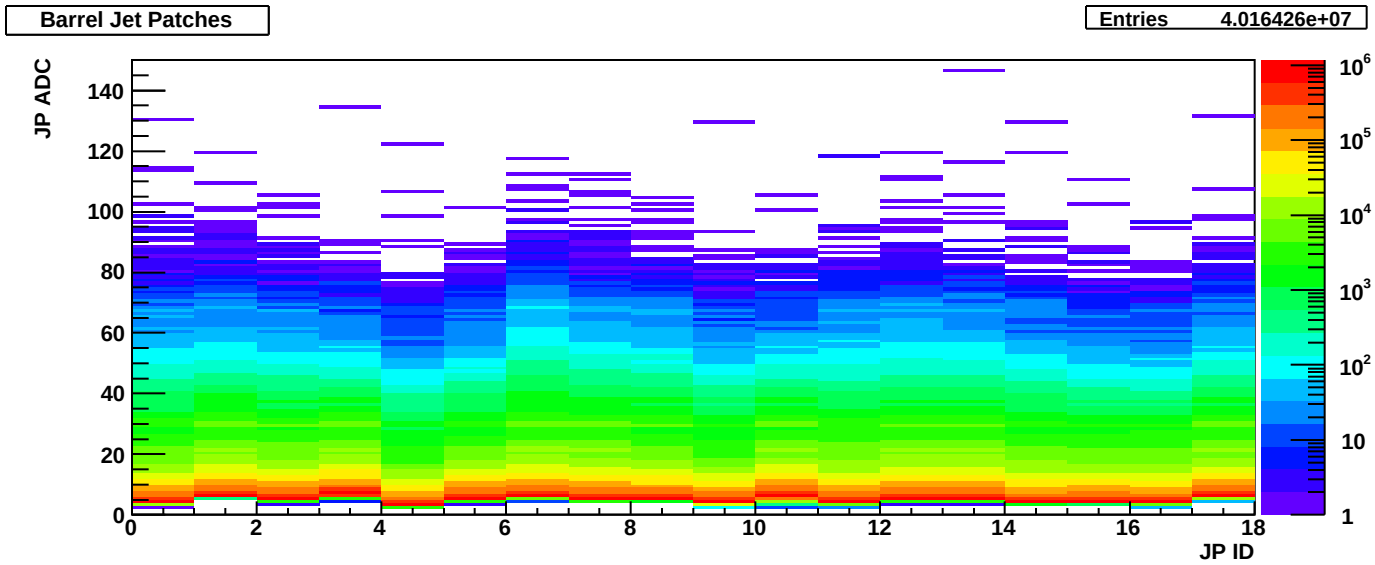
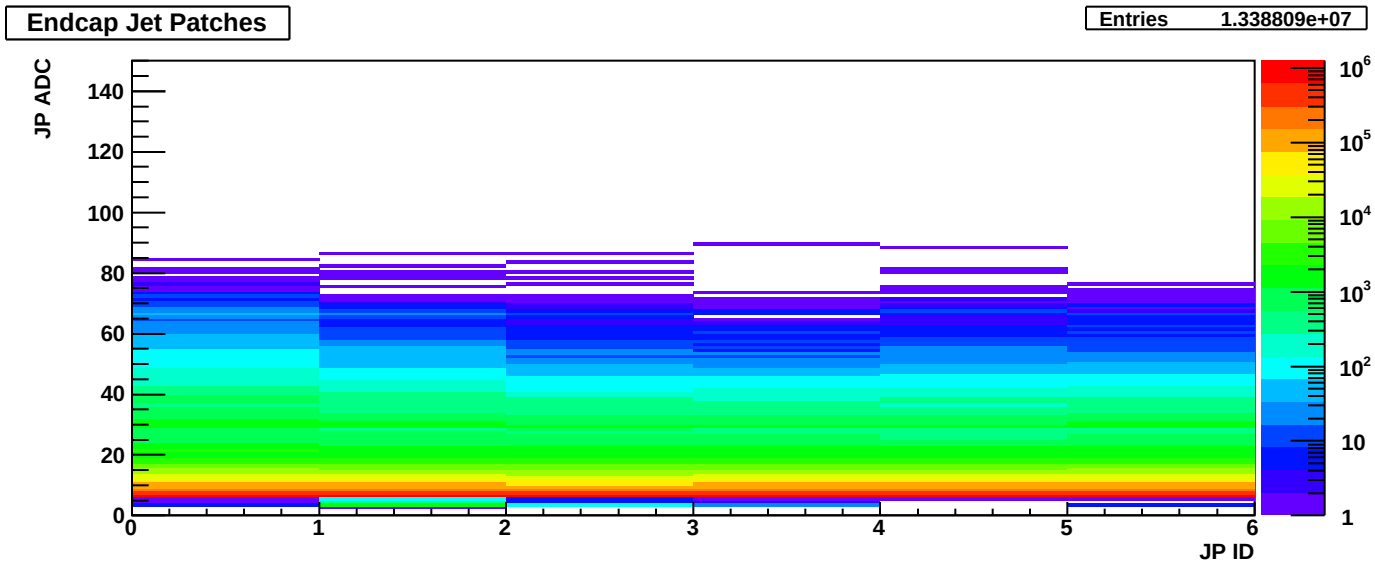

Barrel EMC L0 Input - High Tower
Entries 6.694044e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

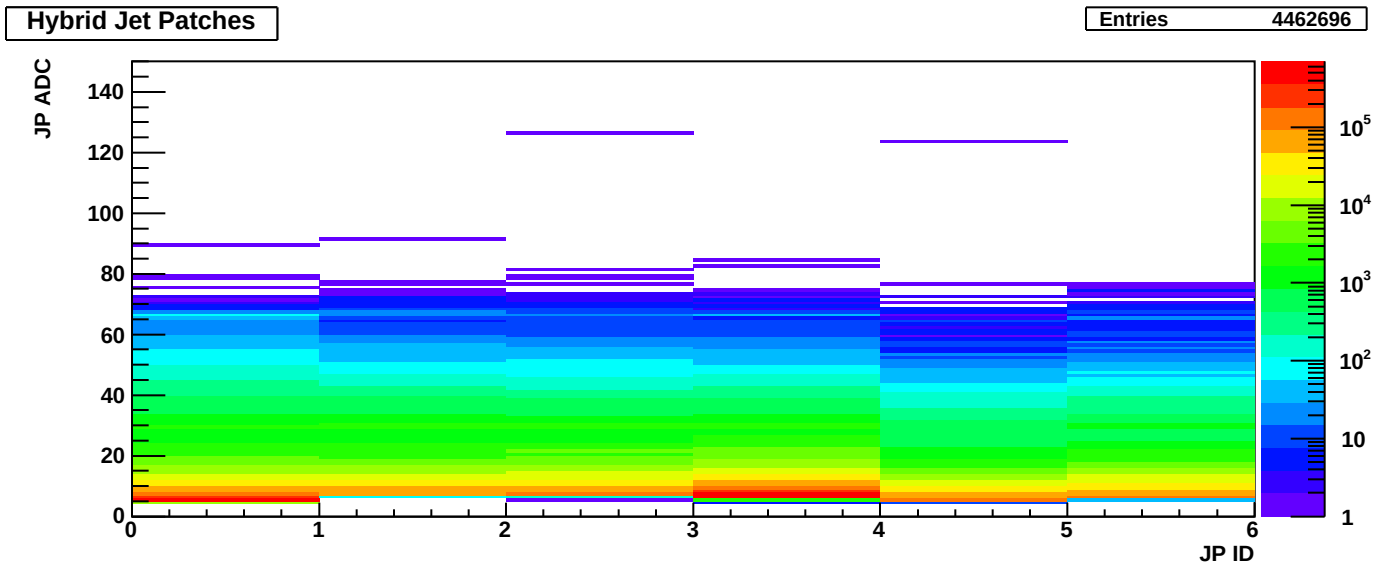

Barrel EMC L0 Input - Patch Sum
Entries 6.694044e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

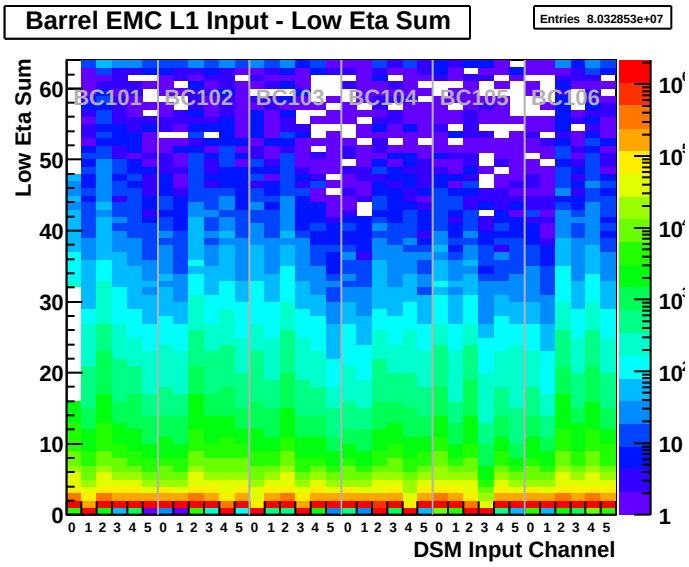

Endcap EMC L0 Input - High Tower
Entries 2.008213e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

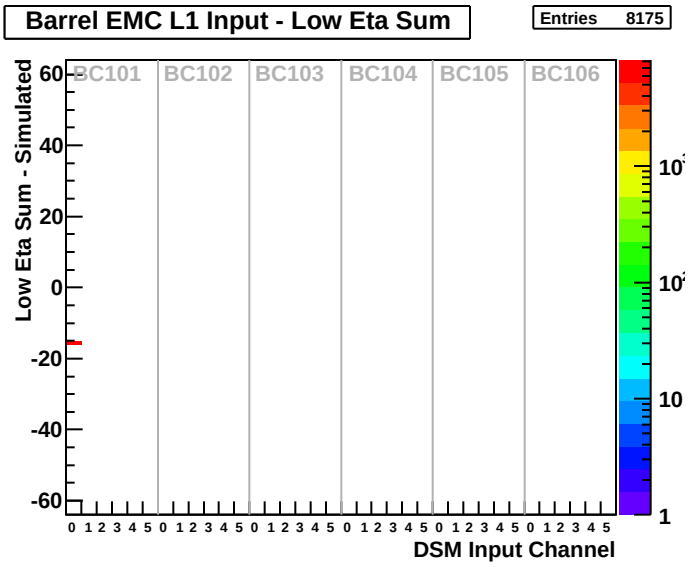

Endcap EMC L0 Input - Patch Sum
Entries 2.008213e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

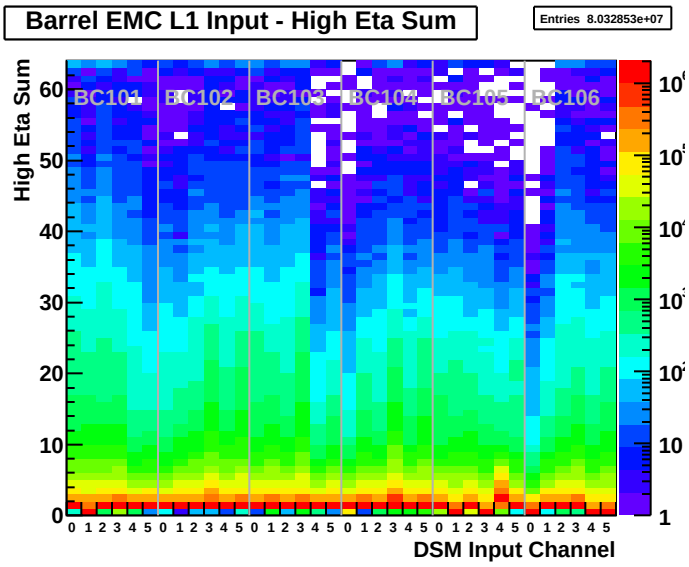

Barrel Jet Patches
Entries 4.016426e+07
JP ADC
JP ID

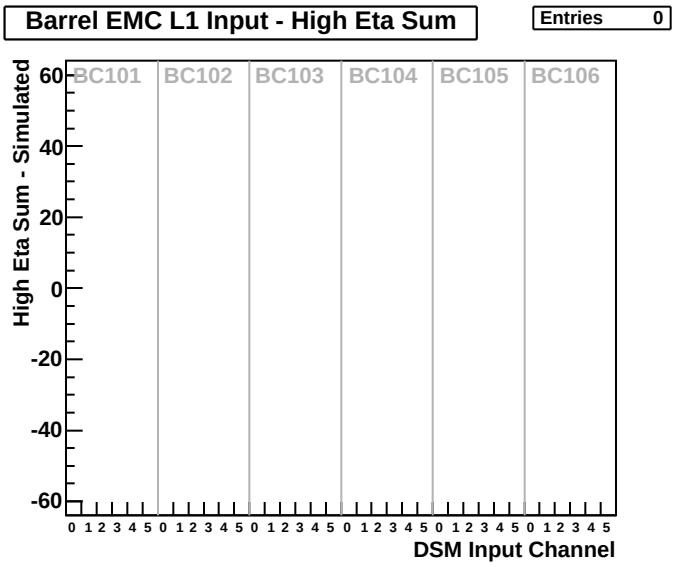

Endcap Jet Patches
Entries 1.338809e+07
JP ADC
JP ID

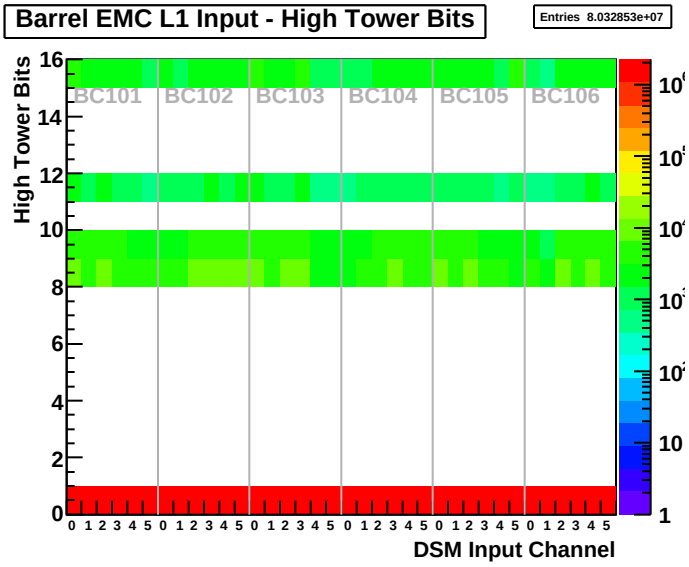

Hybrid Jet Patches
Entries 4462696
JP ADC
JP ID

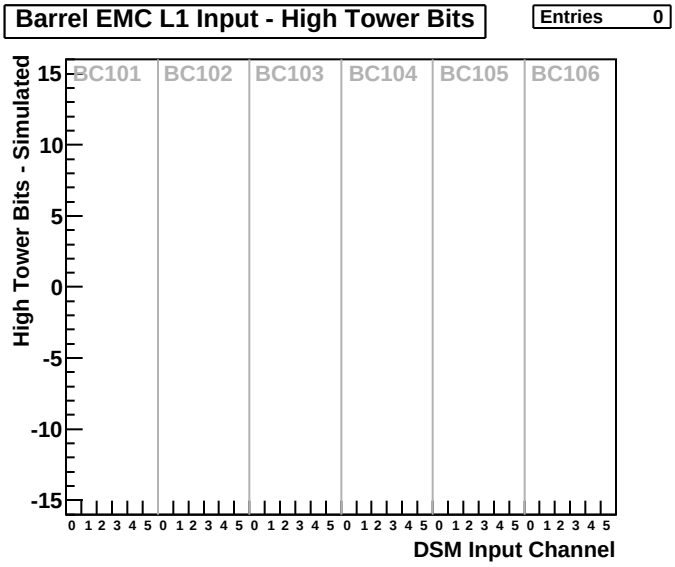

Barrel EMC L1 Input - Low Eta Sum
Entries 8.032853e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

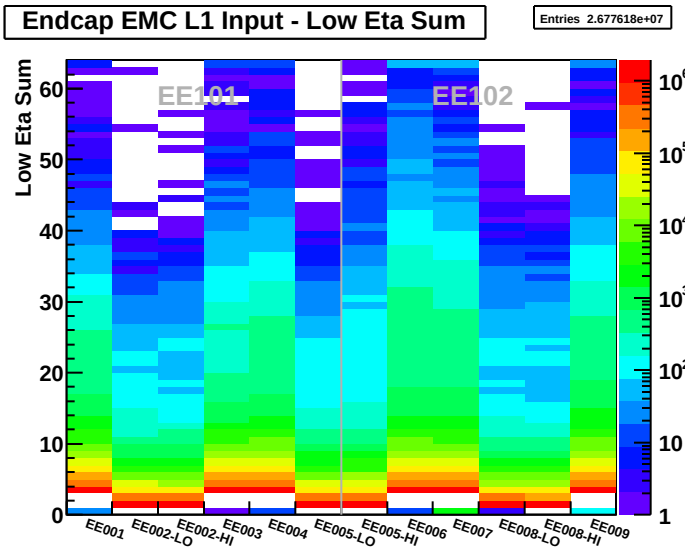

Barrel EMC L1 Input - Low Eta Sum
Entries 8175
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

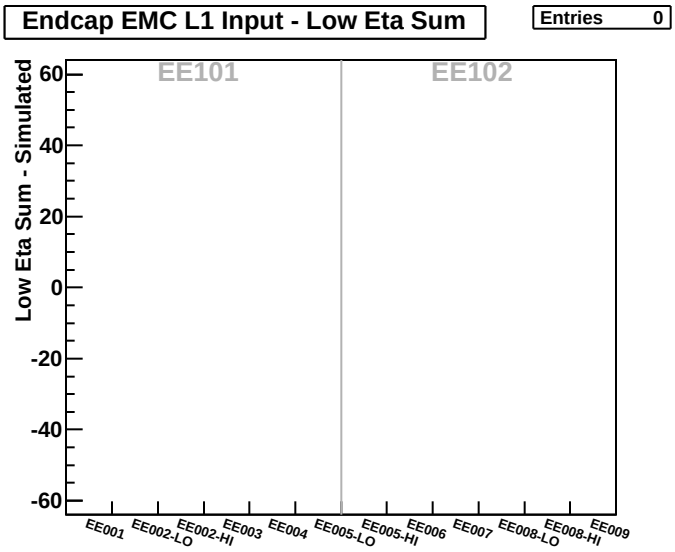

Barrel EMC L1 Input - High Eta Sum
Entries 8.032853e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

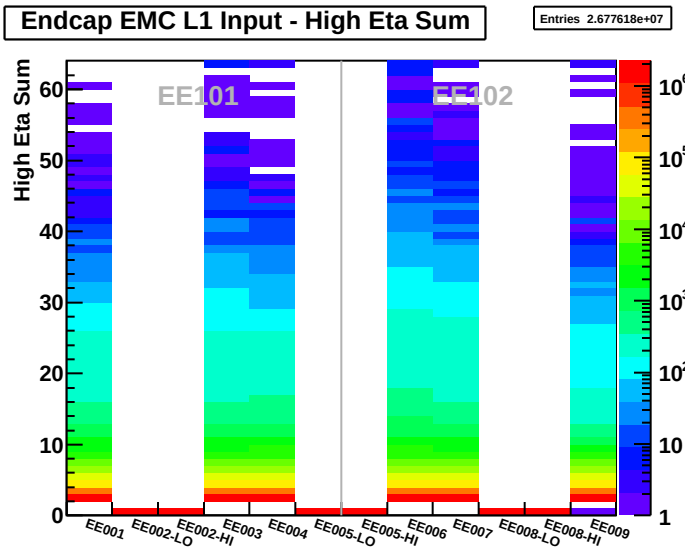

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

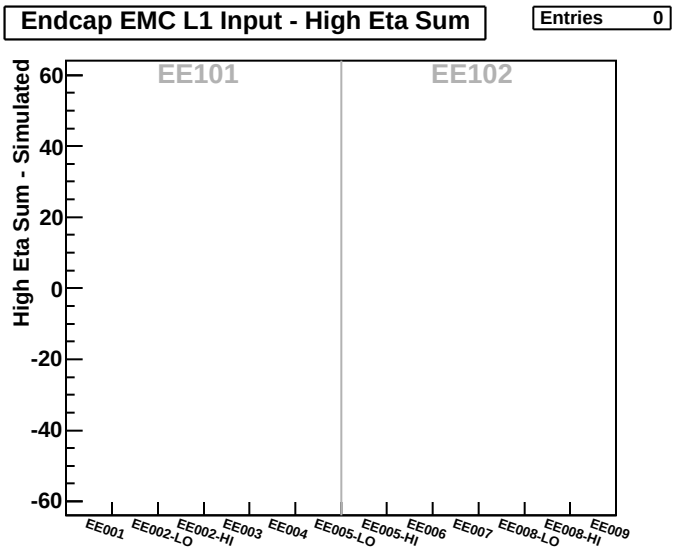

Barrel EMC L1 Input - High Tower Bits
Entries 8.032853e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

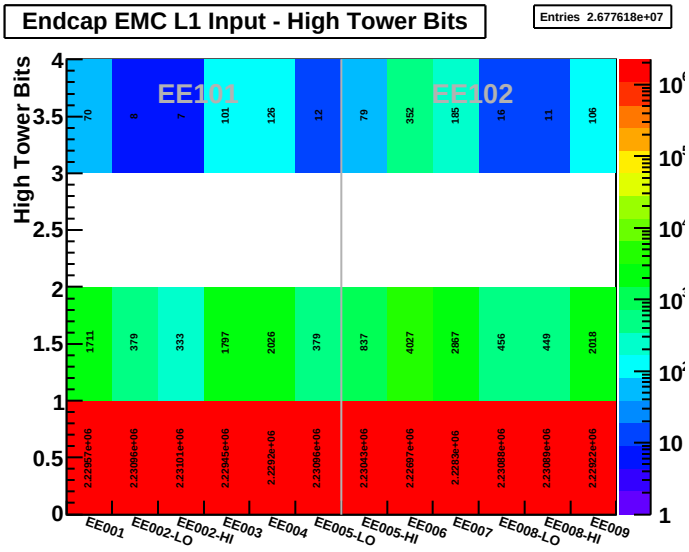

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

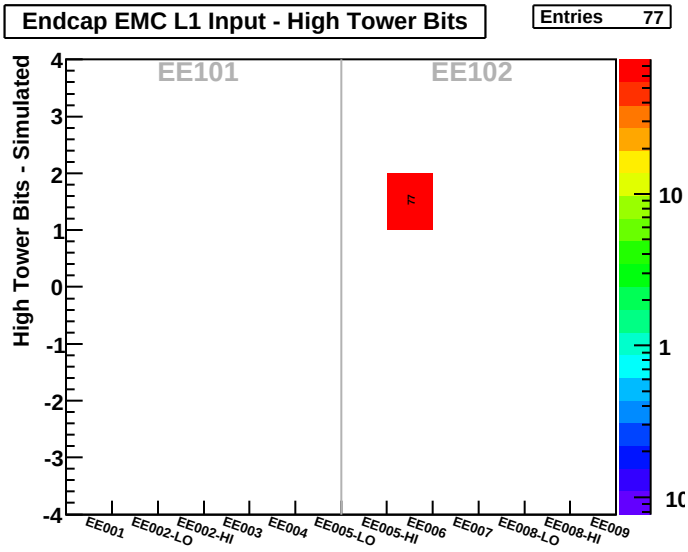

Endcap EMC L1 Input - Low Eta Sum
Entries 2.677618e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

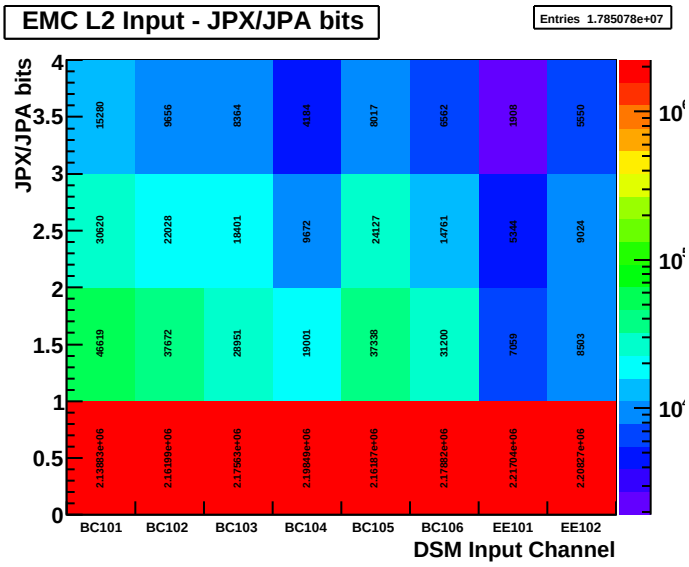

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

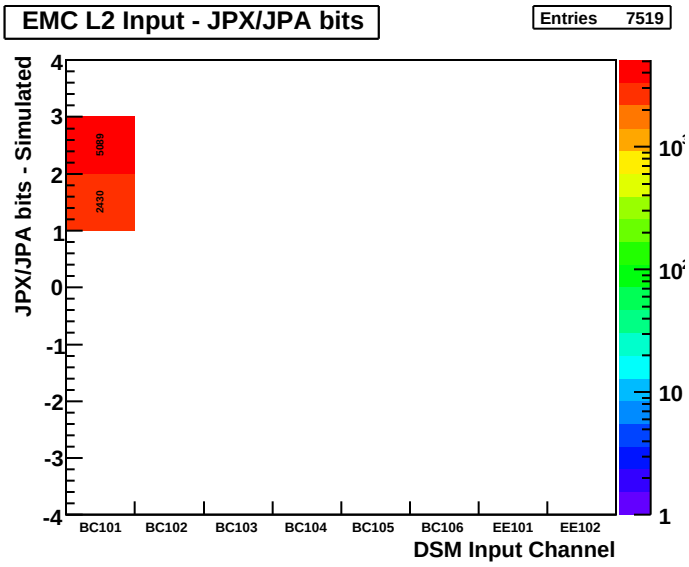

Endcap EMC L1 Input - High Eta Sum
Entries 2.677618e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

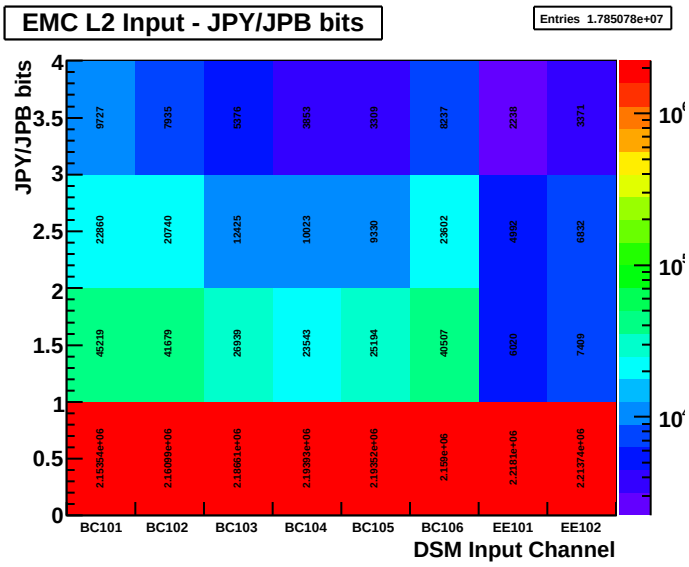

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

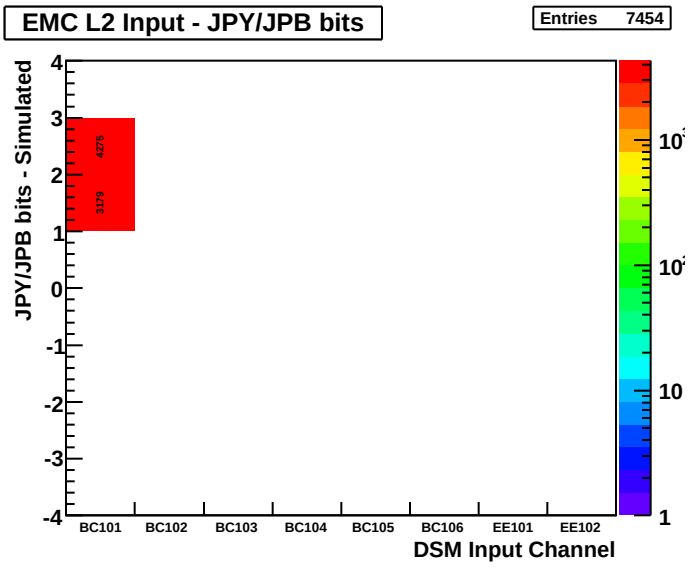

Endcap EMC L1 Input - High Tower Bits
Entries 2.677618e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

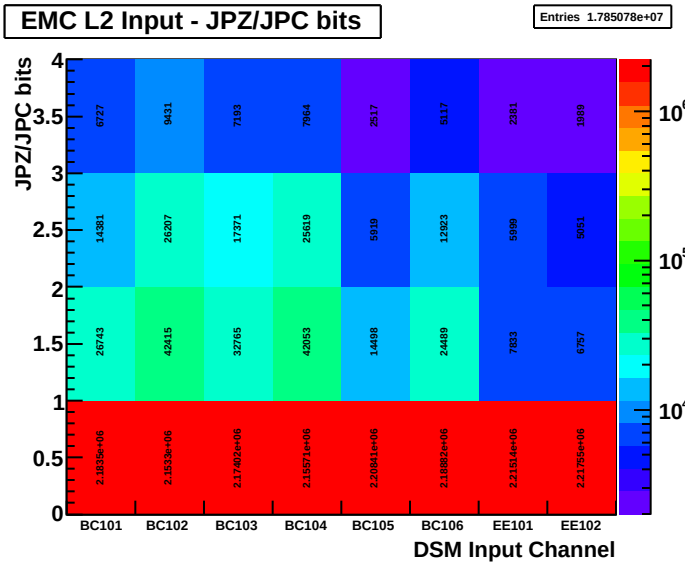

Endcap EMC L1 Input - High Tower Bits
Entries 77
High Tower Bits - Simulated
EE101
EE102
77
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

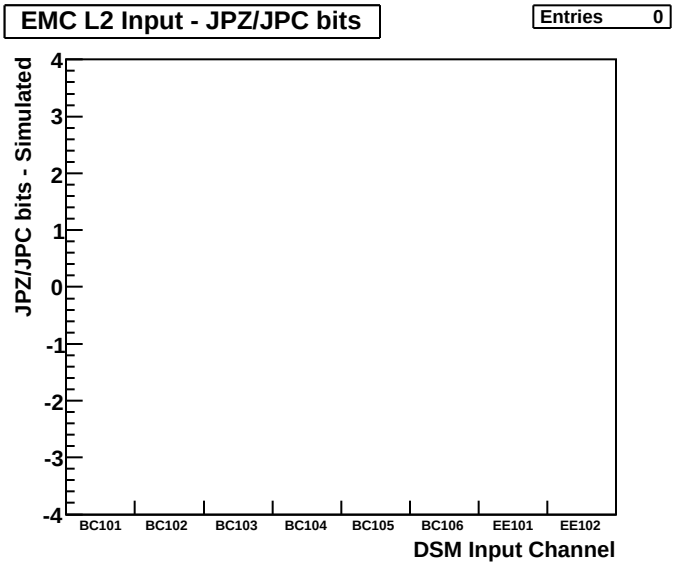

EMC L2 Input - JPX/JPA bits
Entries 1.785078e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
15280 9656 8364 4184 8017 6562 1908 5550
30620 22028 18401 9672 24127 14761 5344 9024
46619 37672 28951 19001 37338 31200 7059 8503
2.13883e+06 2.16199e+06 2.17953e+06 2.19849e+06 2.16187e+06 2.17882e+06 2.21704e+06 2.20827e+06

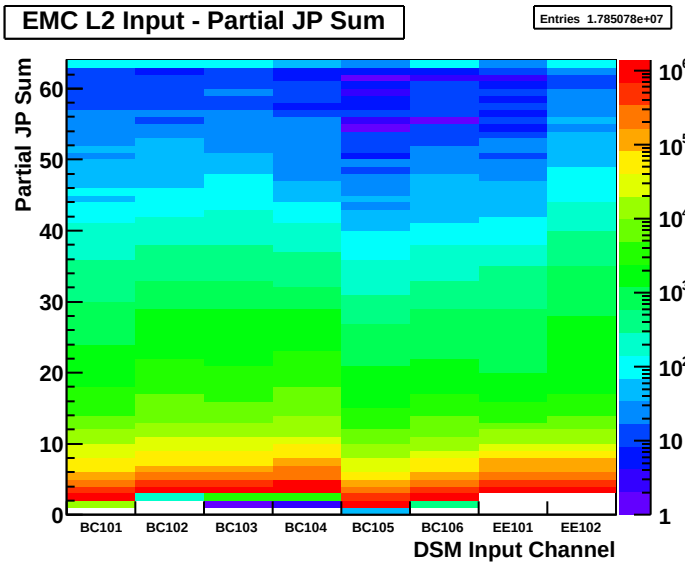

EMC L2 Input - JPX/JPA bits
Entries 7519
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5089
2430

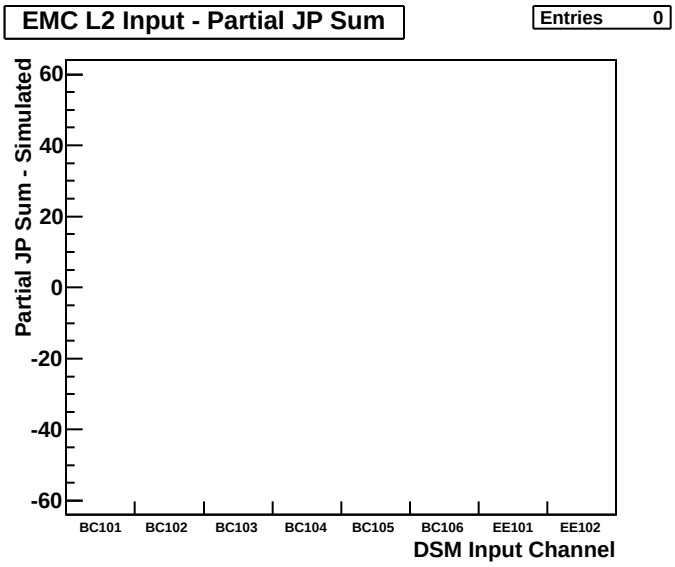

EMC L2 Input - JPY/JPB bits
Entries 1.785078e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
9727 7935 5376 3853 3309 8237 2238 3371
22860 20740 12425 10023 9330 23602 4992 6832
45219 41679 26939 23543 25194 40507 6020 7409
2.15534e+06 2.16099e+06 2.18661e+06 2.19393e+06 2.19352e+06 2.159e+06 2.2181e+06 2.21374e+06

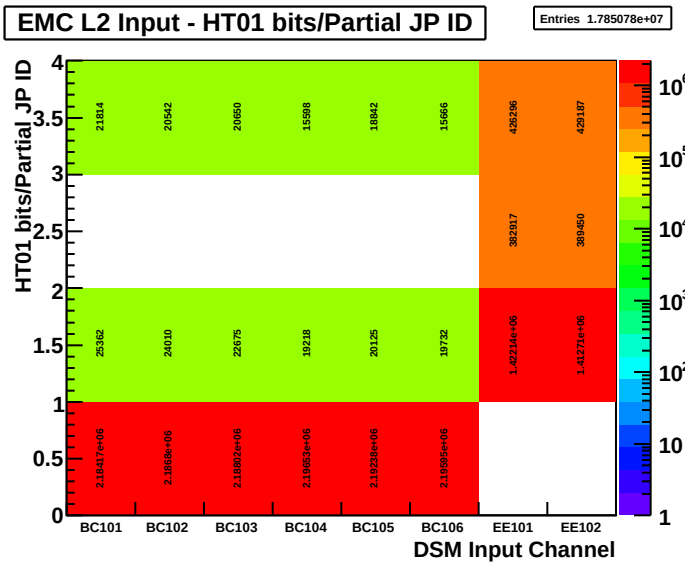

EMC L2 Input - JPY/JPB bits
Entries 7454
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4275
3179

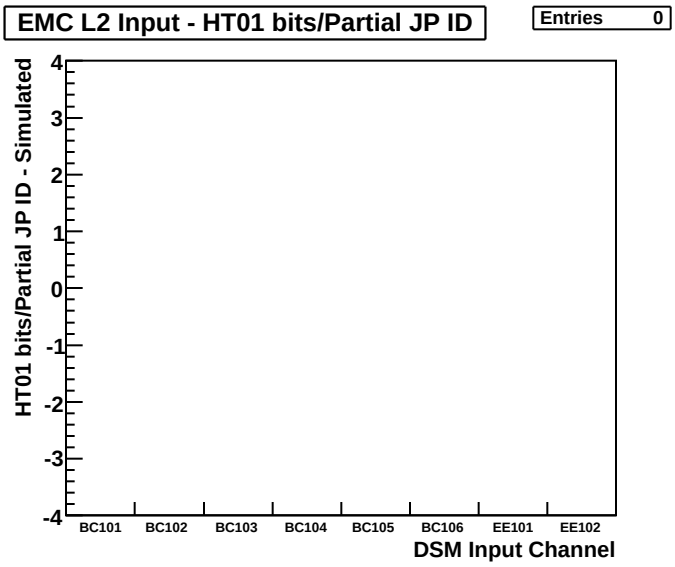

EMC L2 Input - JPZ/JPC bits
Entries 1.785078e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6727 9431 7193 7964 2517 5117 2381 1989
14381 26207 17371 25619 5919 12923 5999 5051
26743 42415 32765 42053 14498 24489 7833 6757
2.1839e+06 2.1532e+06 2.17402e+06 2.15571e+06 2.20841e+06 2.18882e+06 2.2151e+06 2.21755e+06

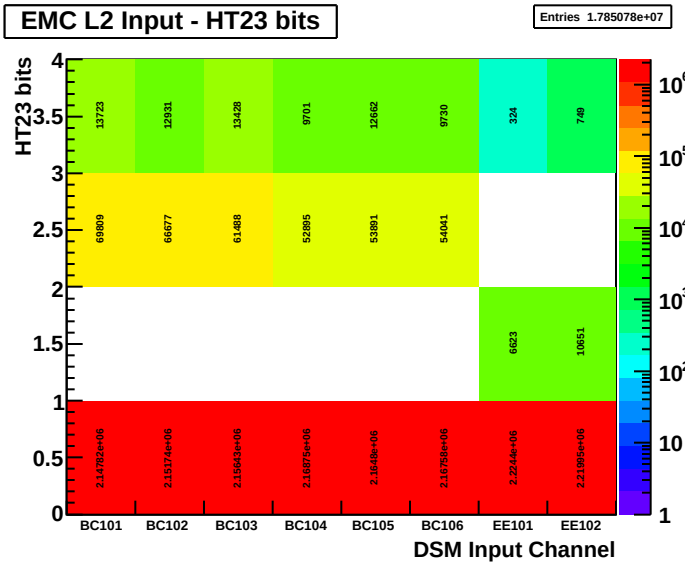

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

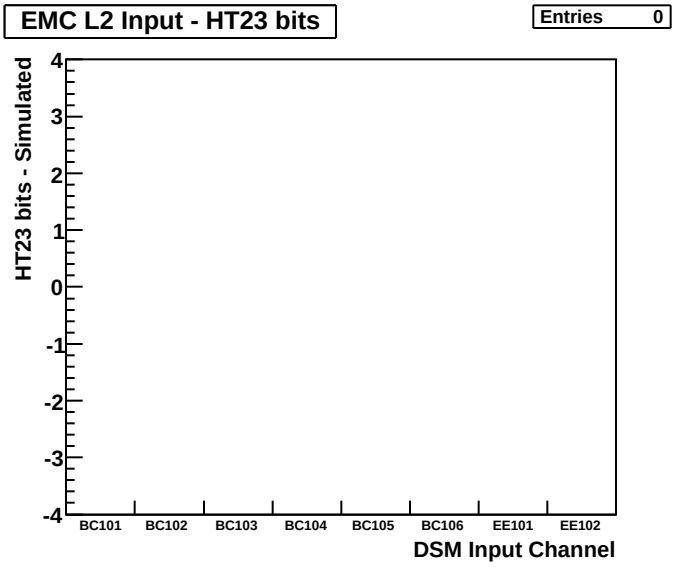

EMC L2 Input - Partial JP Sum
Entries 1.785078e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

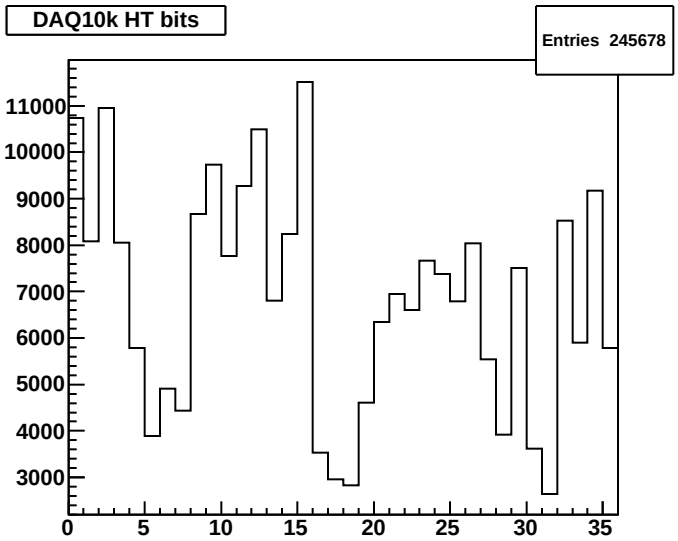

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

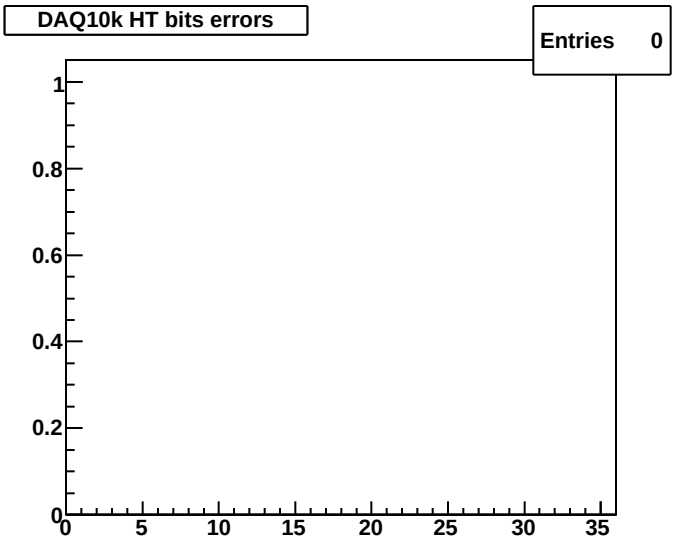

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.785078e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

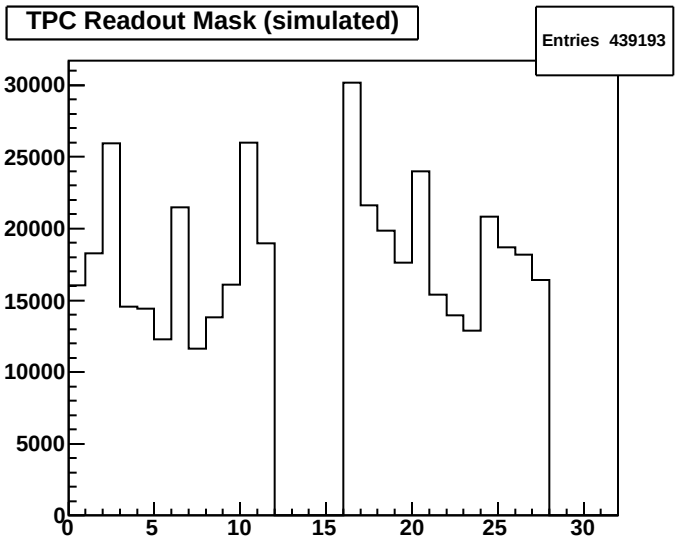

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

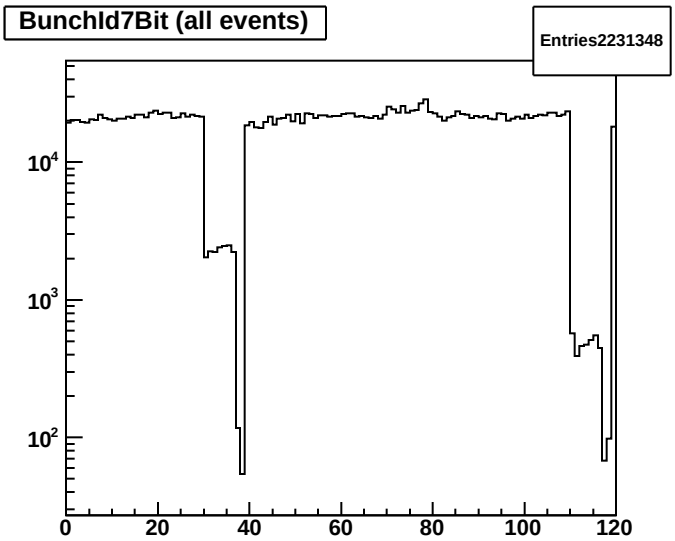

EMC L2 Input - HT23 bits
Entries 1.785078e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

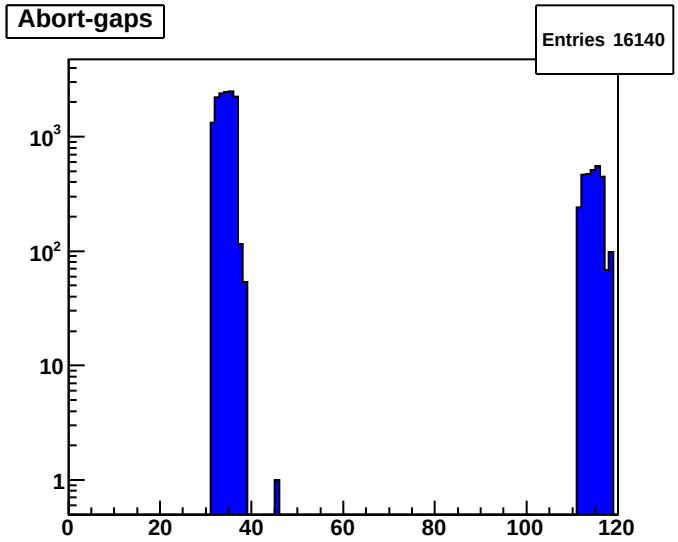

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

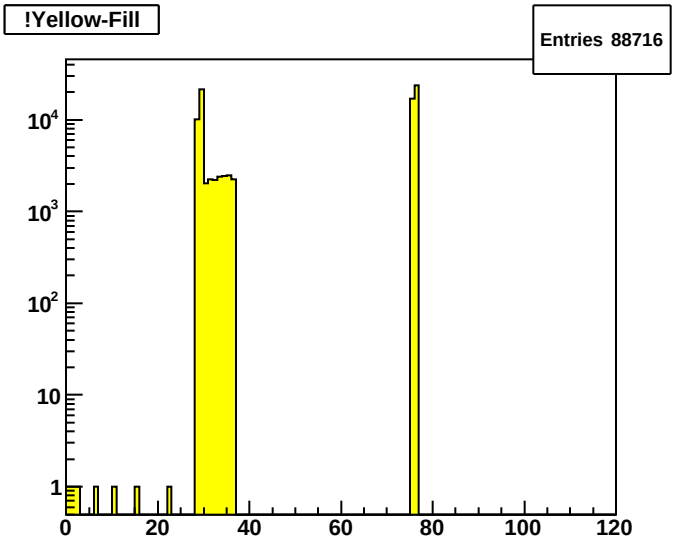

DAQ10k HT bits
Entries 245678

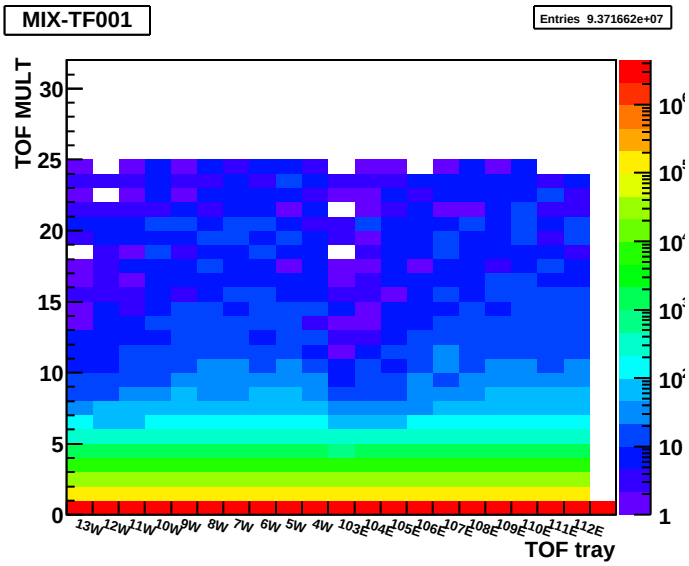

DAQ10k HT bits errors
Entries 0

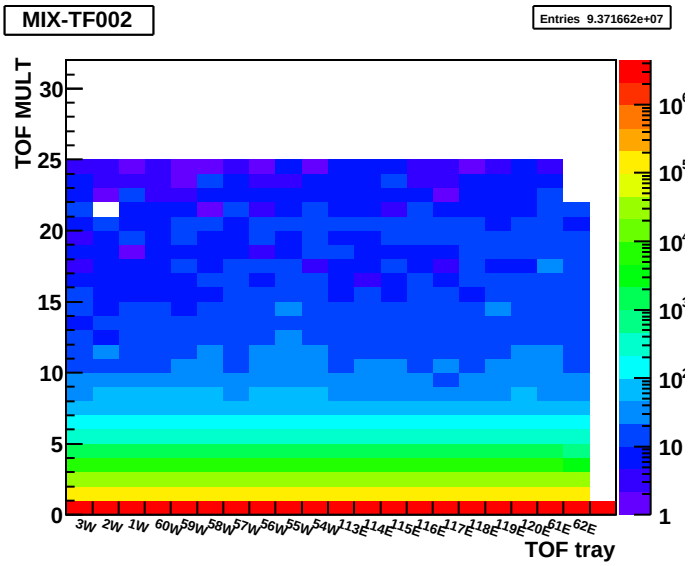

TPC Readout Mask (simulated)
Entries 439193

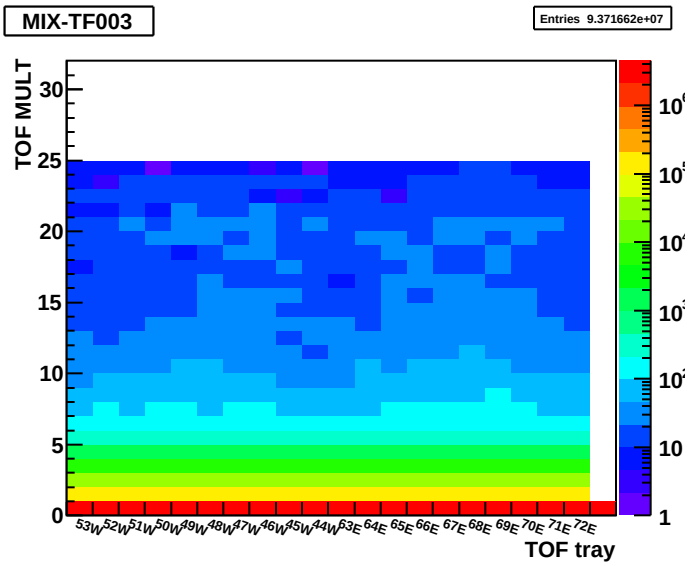

BunchId7Bit (all events)
Entries2231348

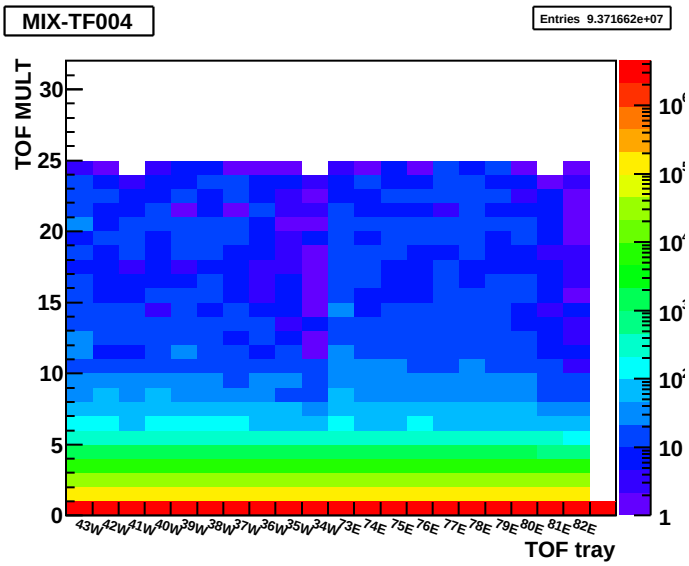

Abort-gaps
Entries 16140

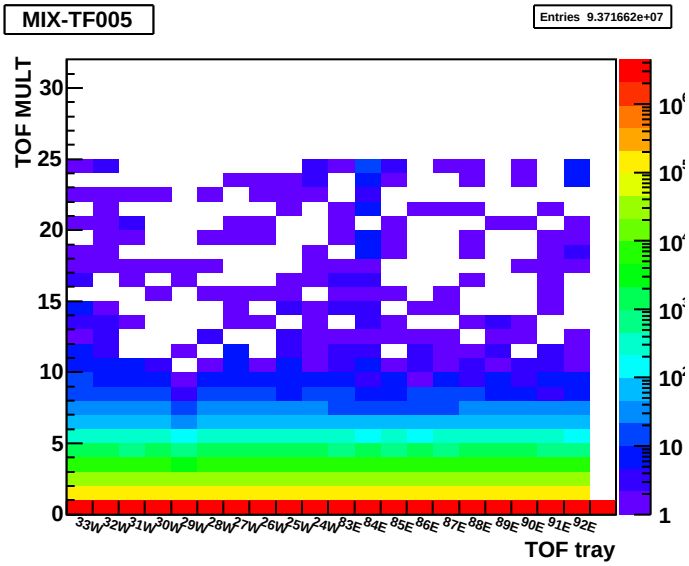

!Yellow-Fill
Entries 88716

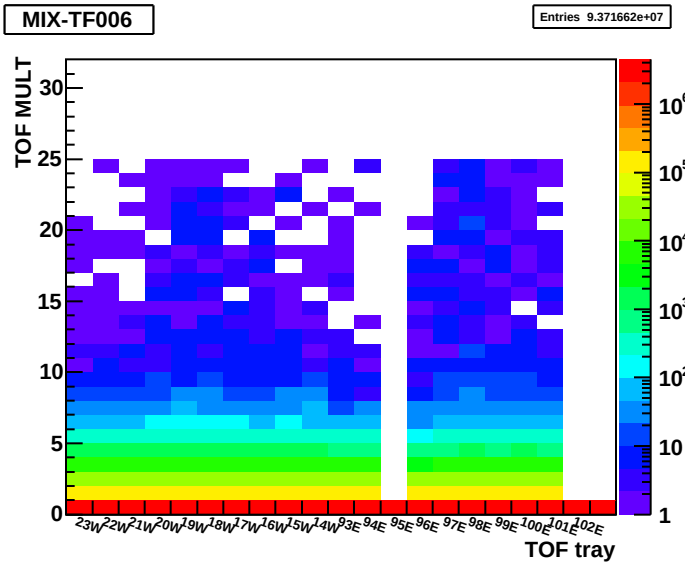

MIX-TF001
Entries 9.371662e+07
TOF MULT
TOF tray

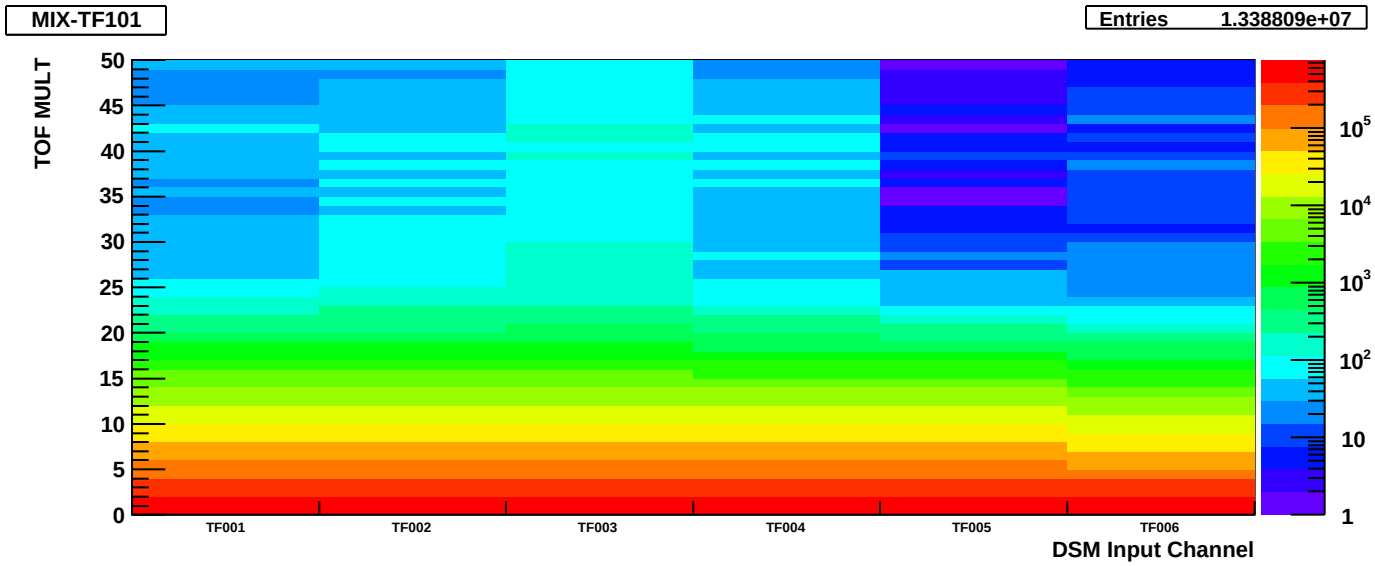

MIX-TF002
Entries 9.371662e+07
TOF MULT
TOF tray

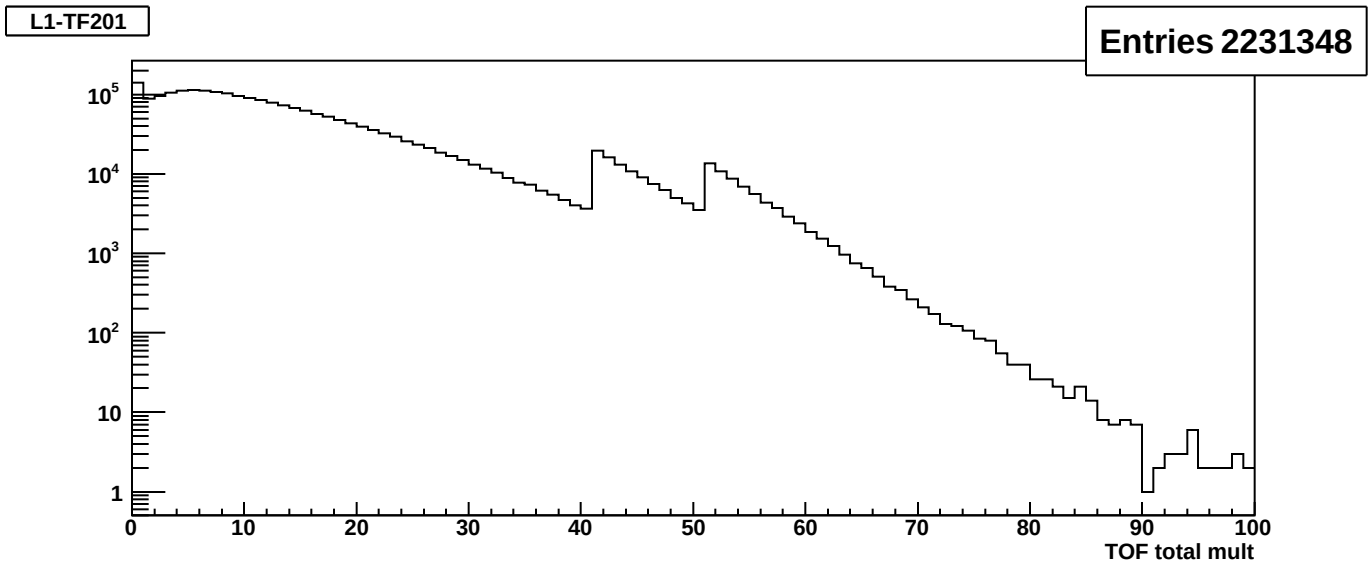

MIX-TF003
Entries 9.371662e+07
TOF MULT
TOF tray

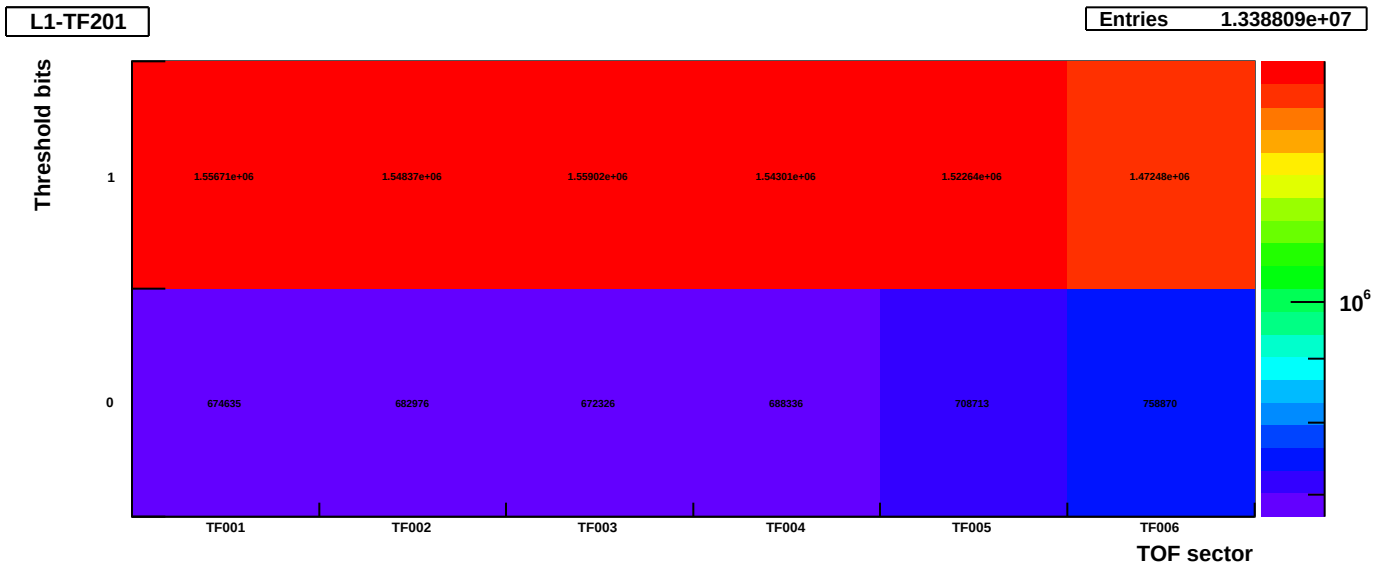

MIX-TF004
Entries 9.371662e+07
TOF MULT
TOF tray

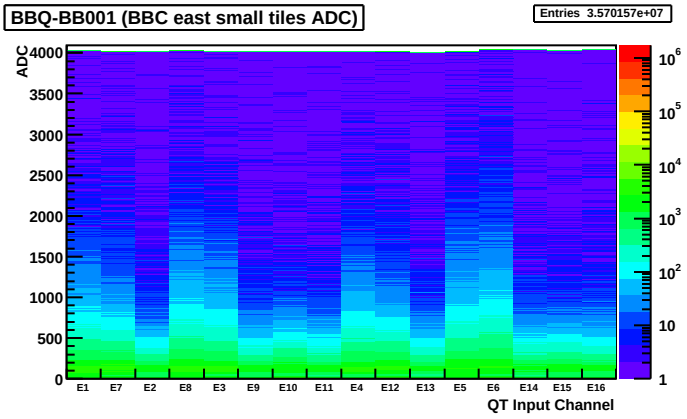

MIX-TF005
Entries 9.371662e+07
TOF MULT
TOF tray

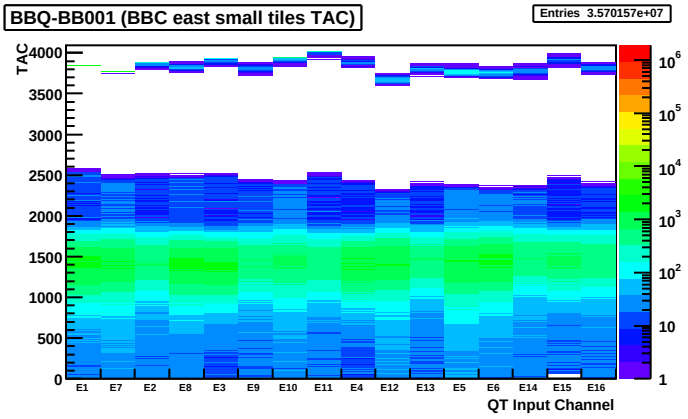

MIX-TF006
Entries 9.371662e+07
TOF MULT
TOF tray

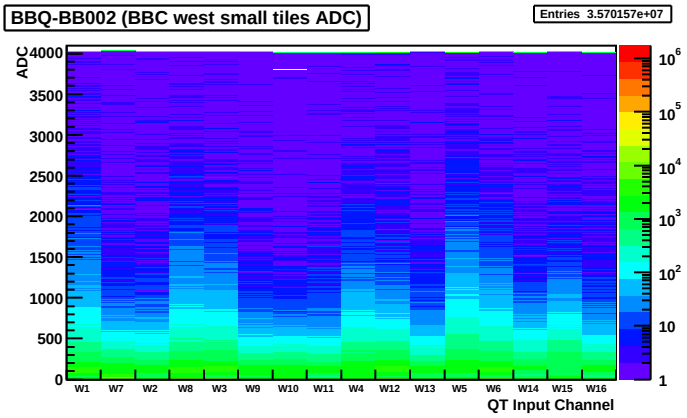

MIX-TF101
Entries 1.338809e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

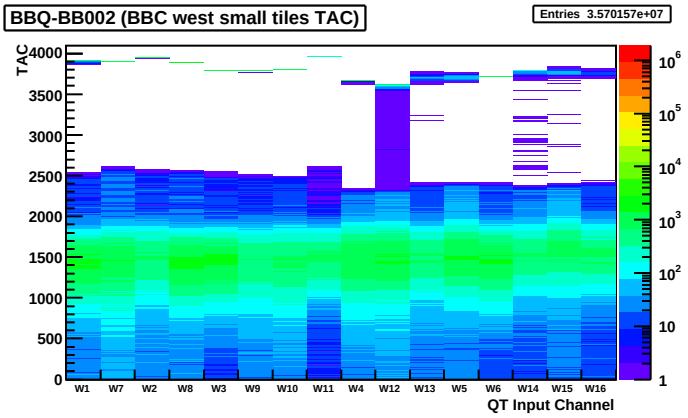

L1-TF201
Entries 2231348
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

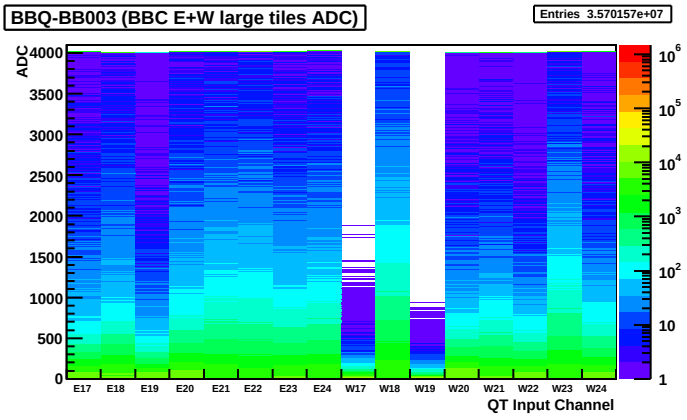

L1-TF201
Entries 1.338809e+07
Threshold bits
1
0
1.55671e+06
1.54837e+06
1.55902e+06
1.54301e+06
1.52264e+06
1.47248e+06
674635
682976
672326
688336
708713
758870
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

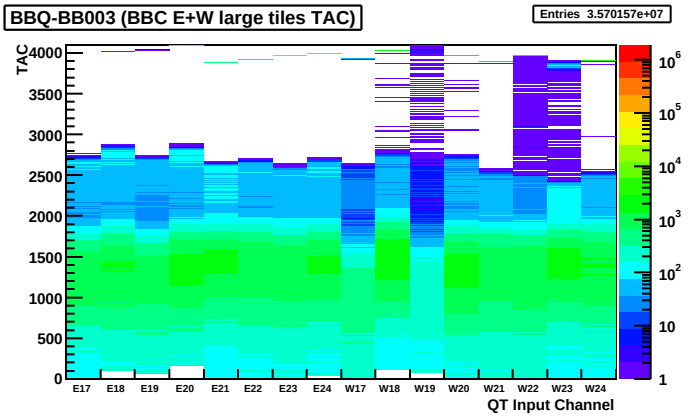

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.570157e+07
ADC
QT Input Channel

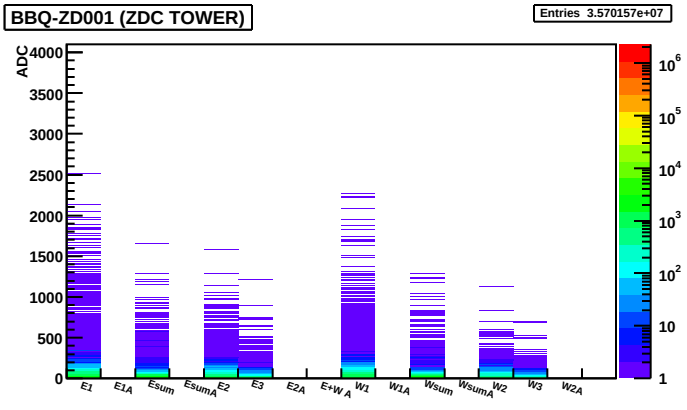

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.570157e+07
TAC
QT Input Channel

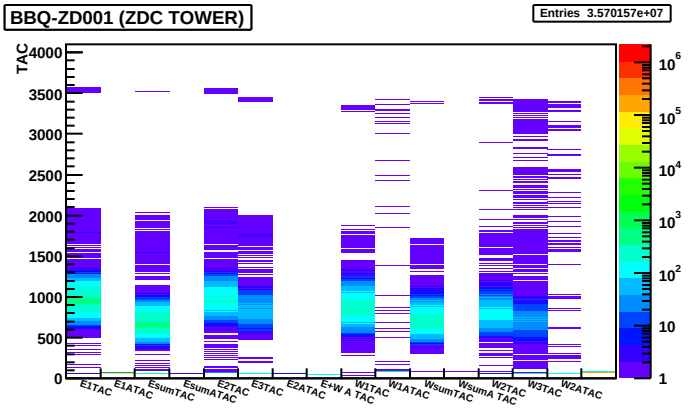

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.570157e+07
ADC
QT Input Channel

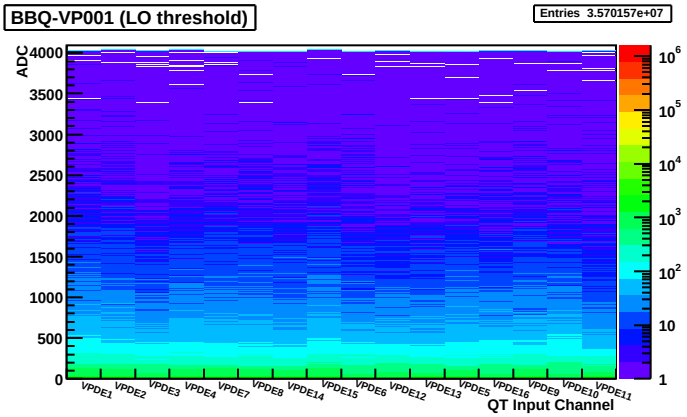

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.570157e+07
TAC
QT Input Channel

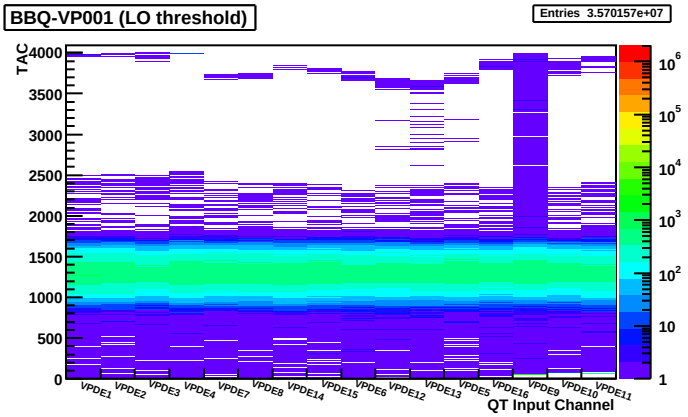

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.570157e+07
ADC
QT Input Channel

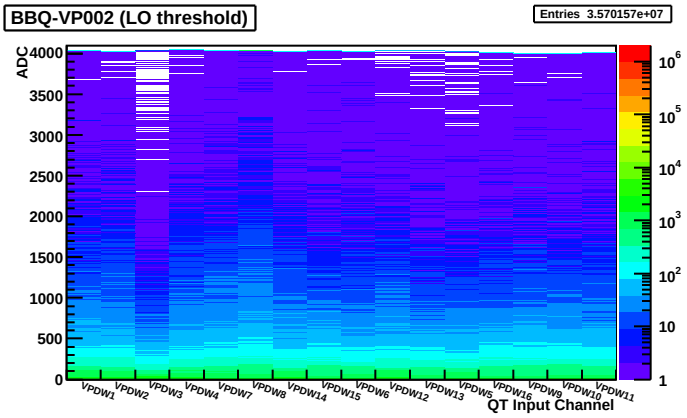

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.570157e+07
TAC
QT Input Channel

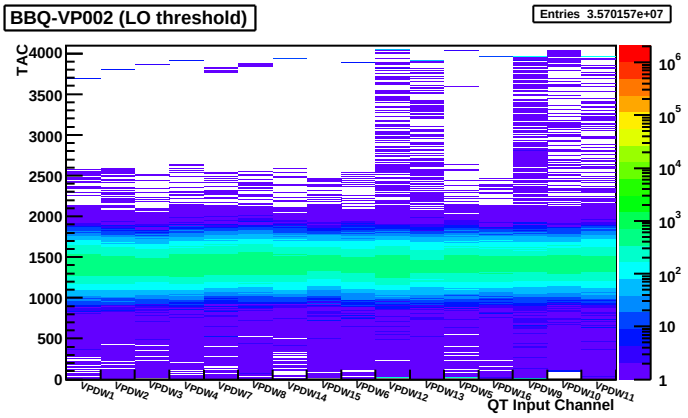

BBQ-ZD001 (ZDC TOWER)
Entries 3.570157e+07
ADC

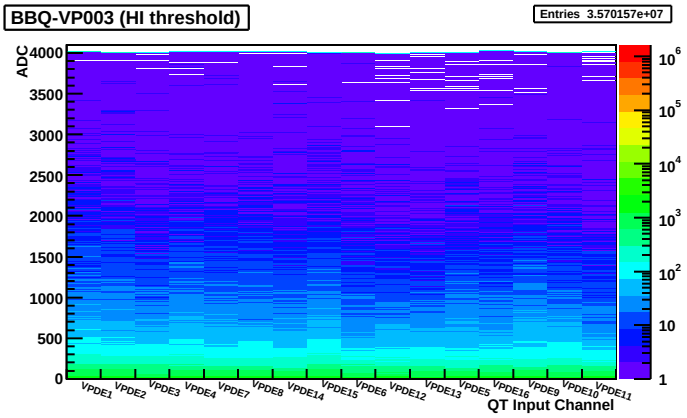

BBQ-ZD001 (ZDC TOWER)
Entries 3.570157e+07
TAC

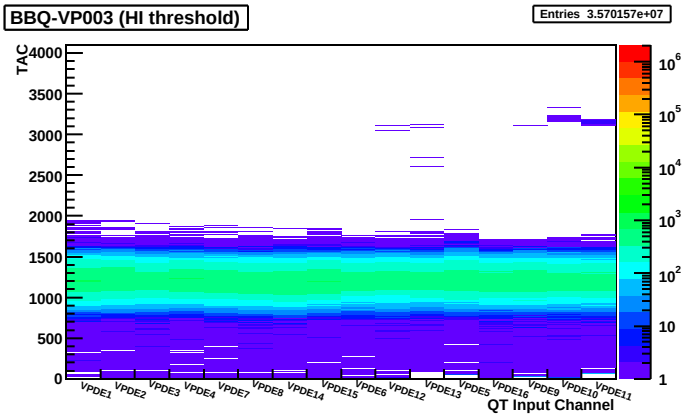

BBQ-VP001 (LO threshold)
Entries 3.570157e+07
ADC
QT Input Channel

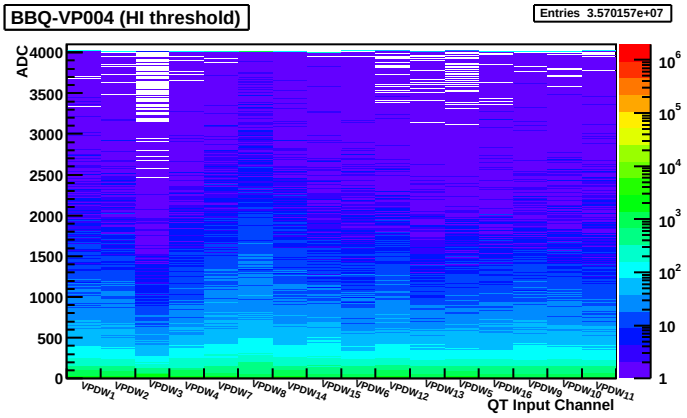

BBQ-VP001 (LO threshold)
Entries 3.570157e+07
TAC
QT Input Channel

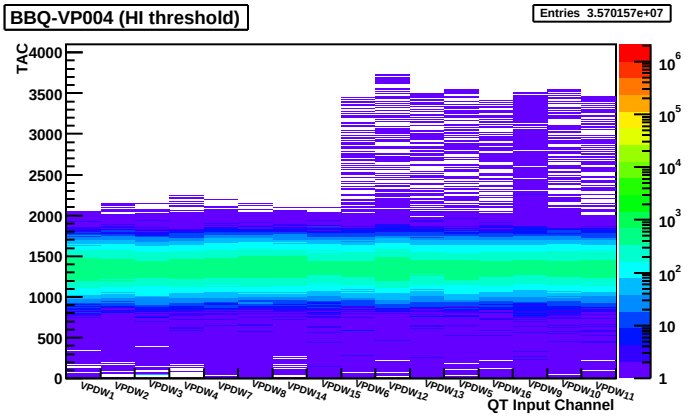

BBQ-VP002 (LO threshold)
Entries 3.570157e+07
ADC
QT Input Channel

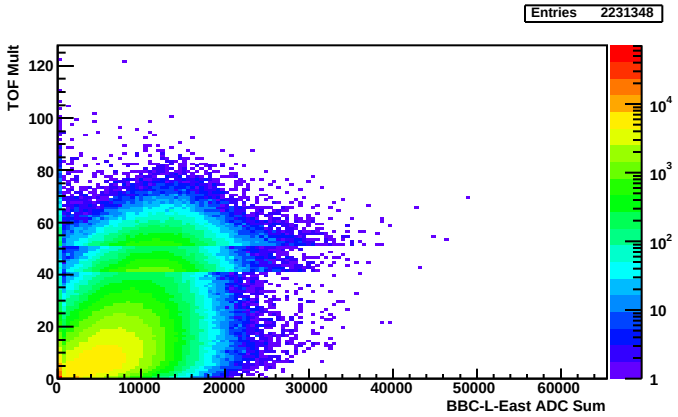

BBQ-VP002 (LO threshold)
Entries 3.570157e+07
TAC
QT Input Channel

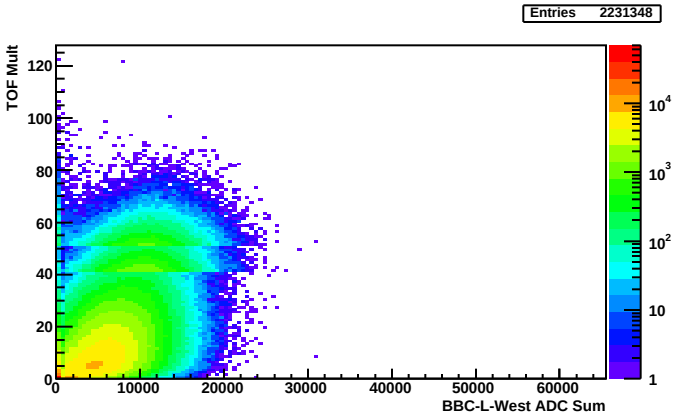

BBQ-VP003 (HI threshold)
Entries 3.570157e+07
ADC
QT Input Channel

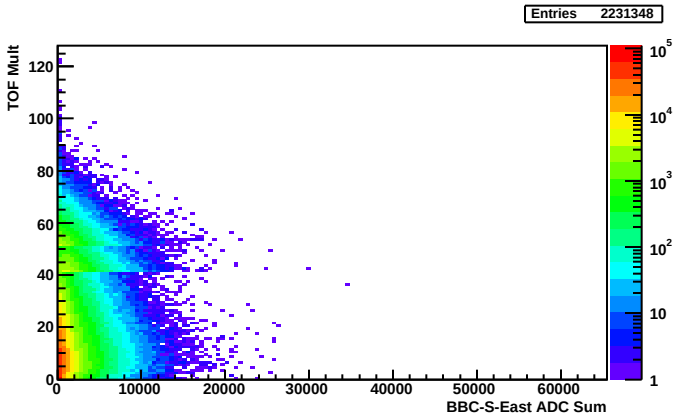

BBQ-VP003 (HI threshold)
Entries 3.570157e+07
TAC
QT Input Channel

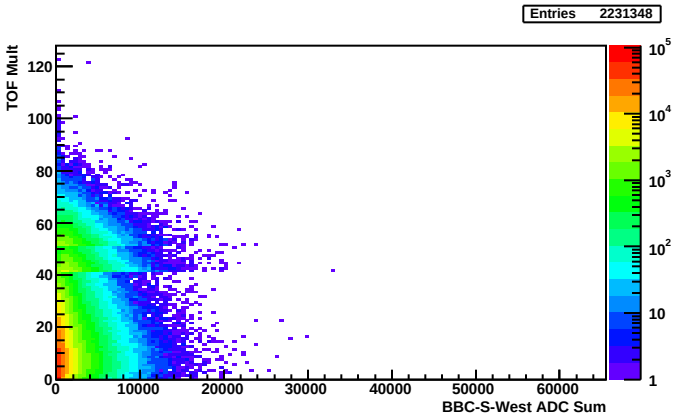

BBQ-VP004 (HI threshold)
Entries 3.570157e+07
ADC
QT Input Channel

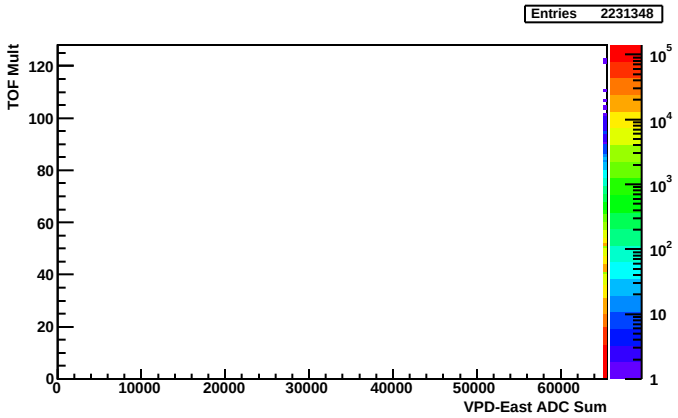

BBQ-VP004 (HI threshold)
Entries 3.570157e+07
TAC
QT Input Channel

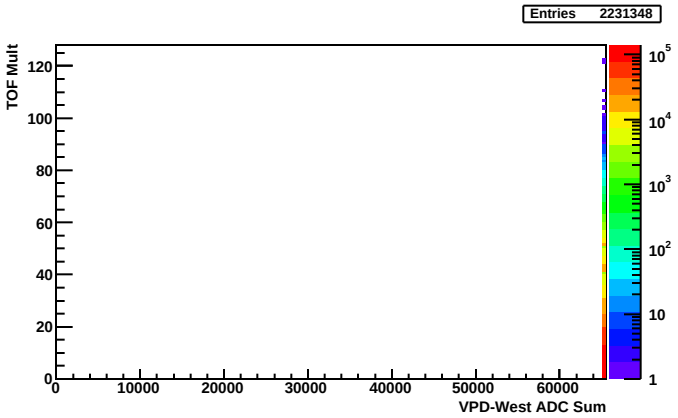

Entries 2231348
TOF Mult
BBC-L-East ADC Sum

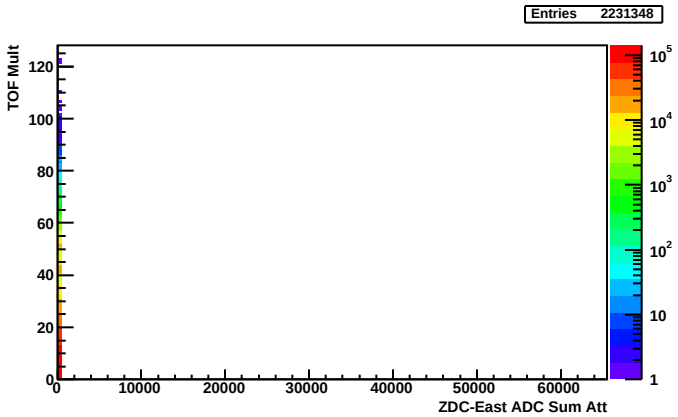

Entries 2231348
TOF Mult
BBC-L-West ADC Sum

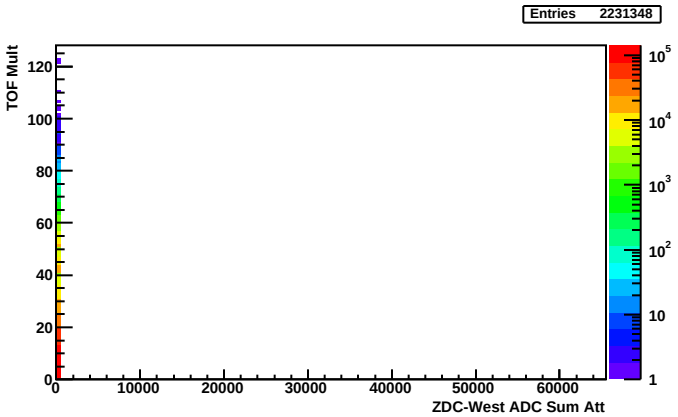

Entries 2231348
TOF Mult
BBC-S-East ADC Sum

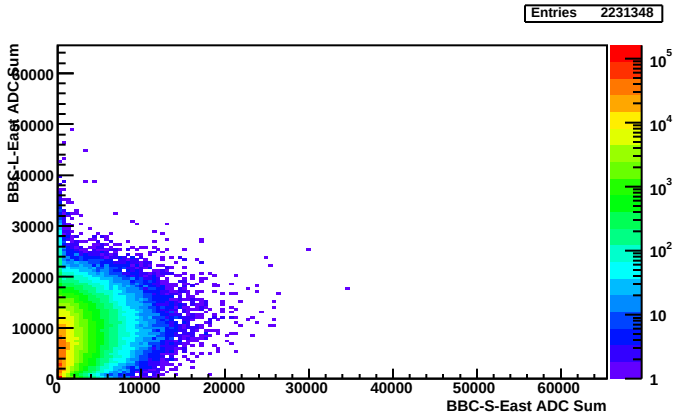

Entries 2231348
TOF Mult
BBC-S-West ADC Sum

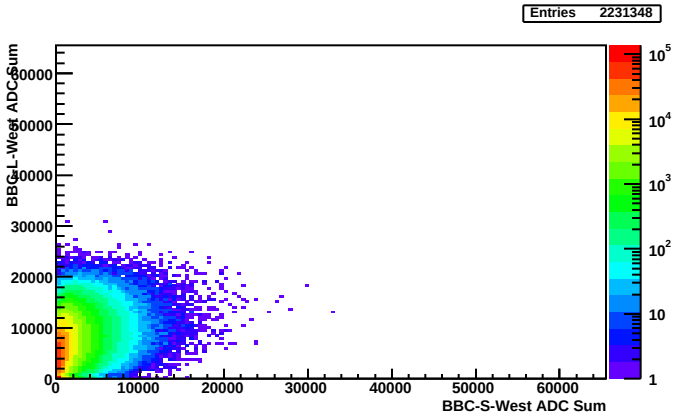

Entries 2231348
TOF Mult
VPD-East ADC Sum

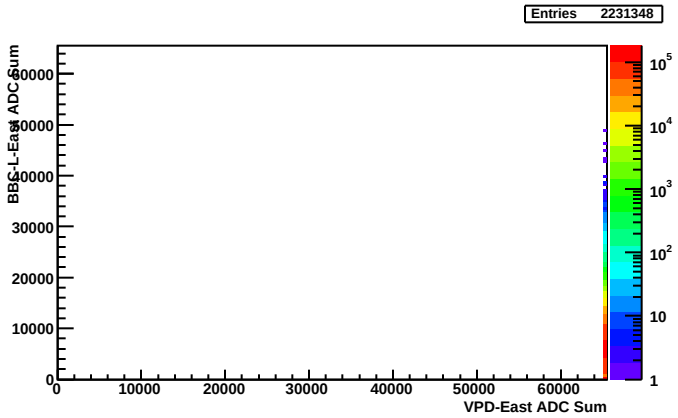

Entries 2231348
TOF Mult
VPD-West ADC Sum

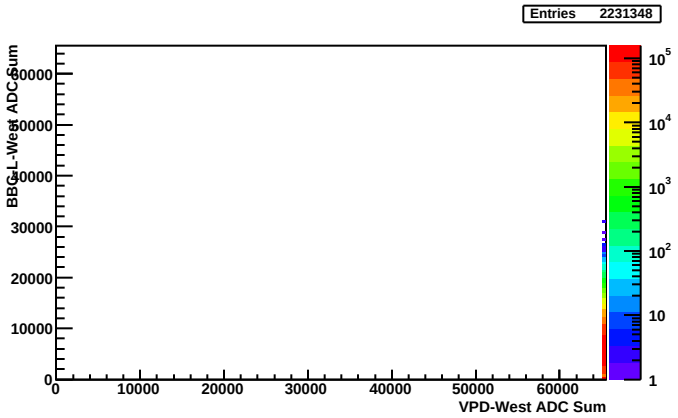

Entries 2231348
TOF Mult
ZDC-East ADC Sum Att

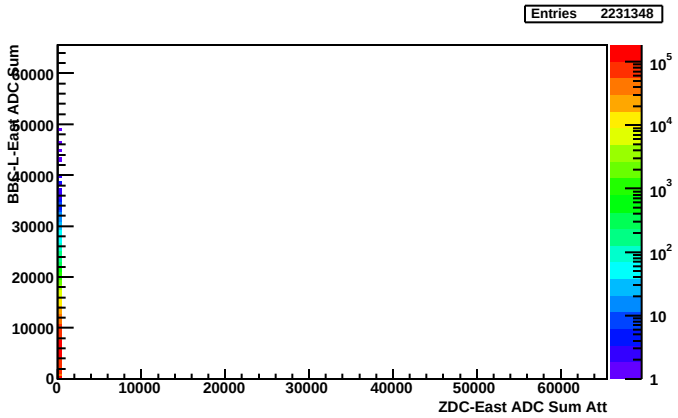

Entries 2231348
TOF Mult
ZDC-West ADC Sum Att

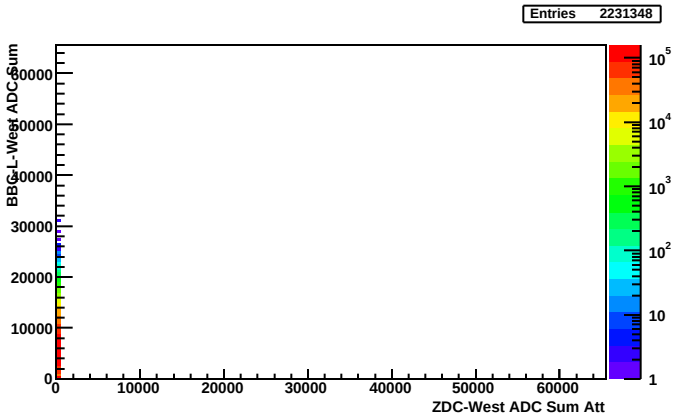

Entries 2231348
BBC-L-East ADC Sum
BBC-S-East ADC Sum

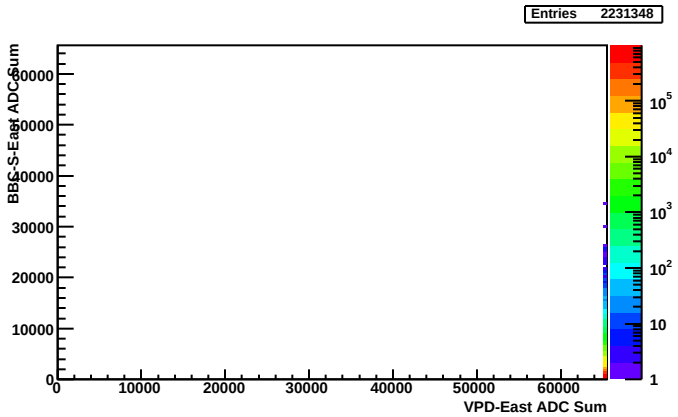

Entries 2231348
BBC-L-West ADC Sum
BBC-S-West ADC Sum

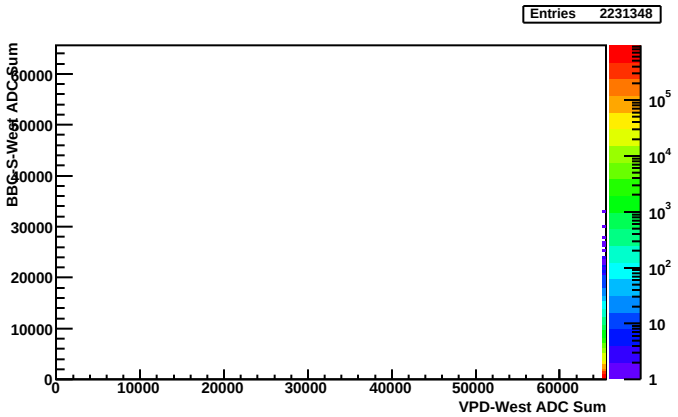

Entries 2231348
BBC-L-East ADC Sum
VPD-East ADC Sum

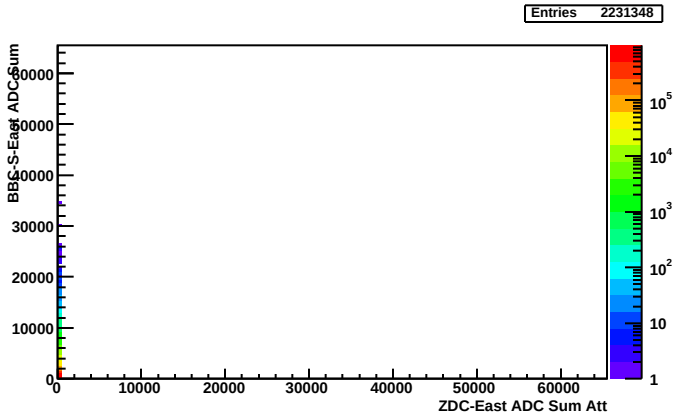

Entries 2231348
BBC-L-West ADC Sum
VPD-West ADC Sum

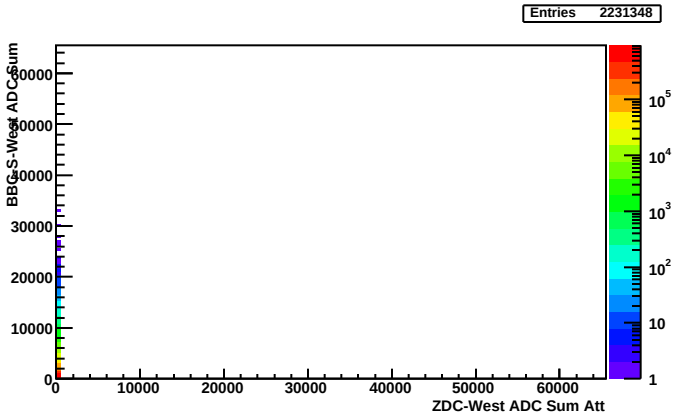

Entries 2231348
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

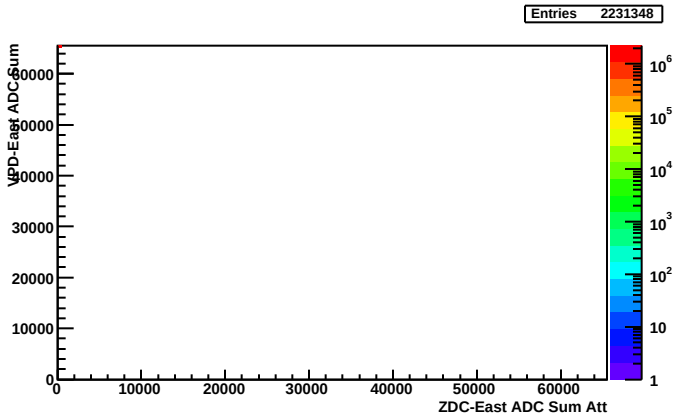

Entries 2231348
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

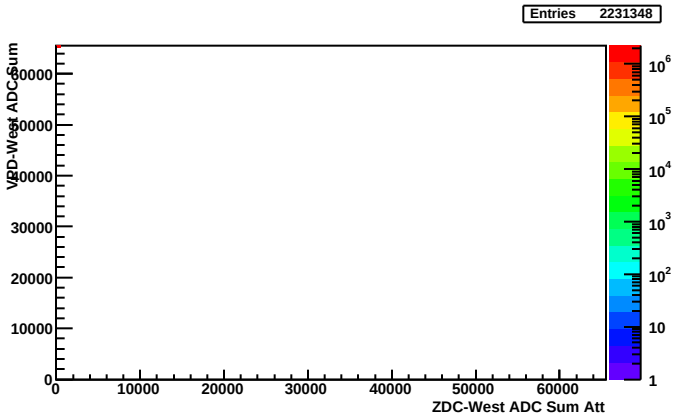

Entries 2231348
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 2231348
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 2231348
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 2231348
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 2231348
VPD-East ADC Sum
ZDC-East ADC Sum Att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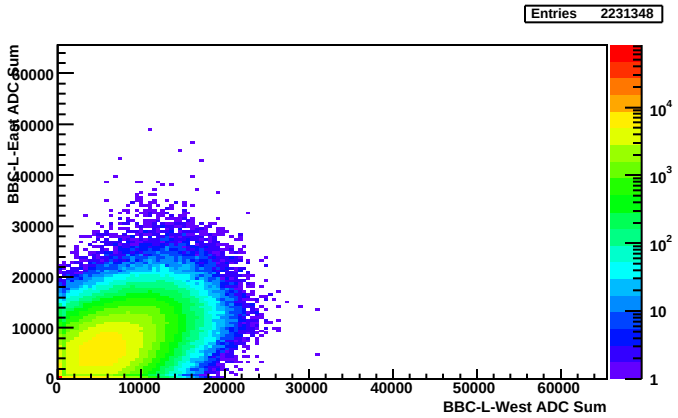
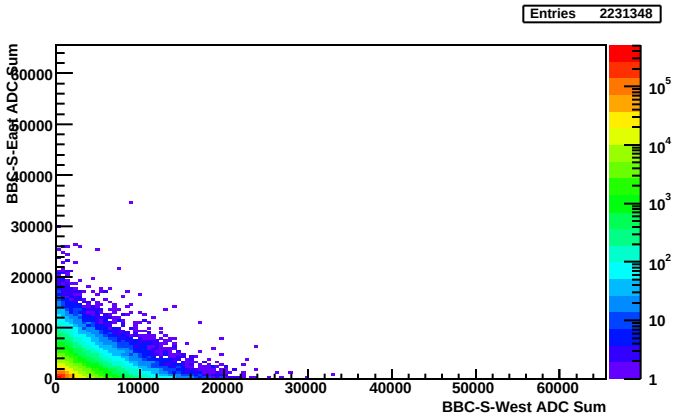
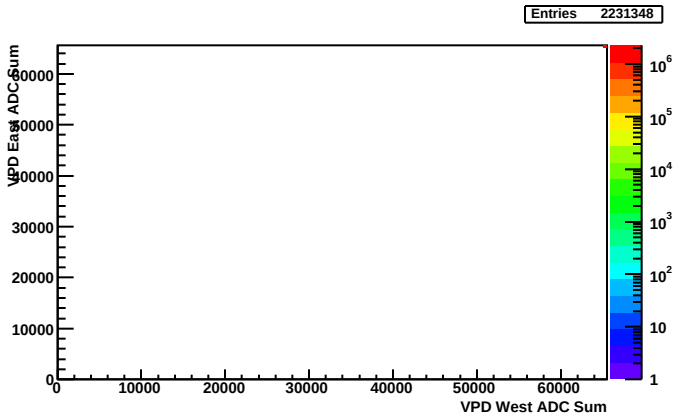
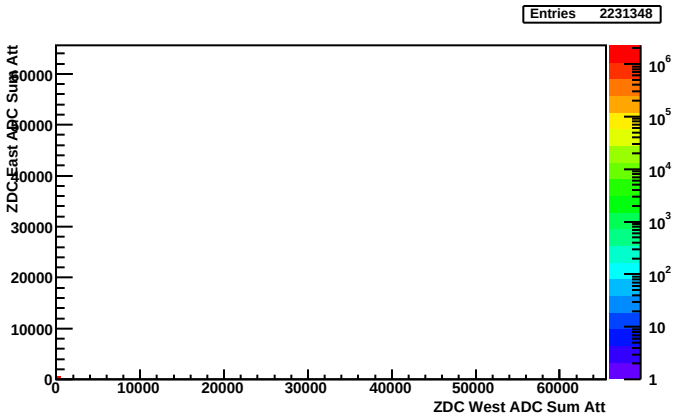
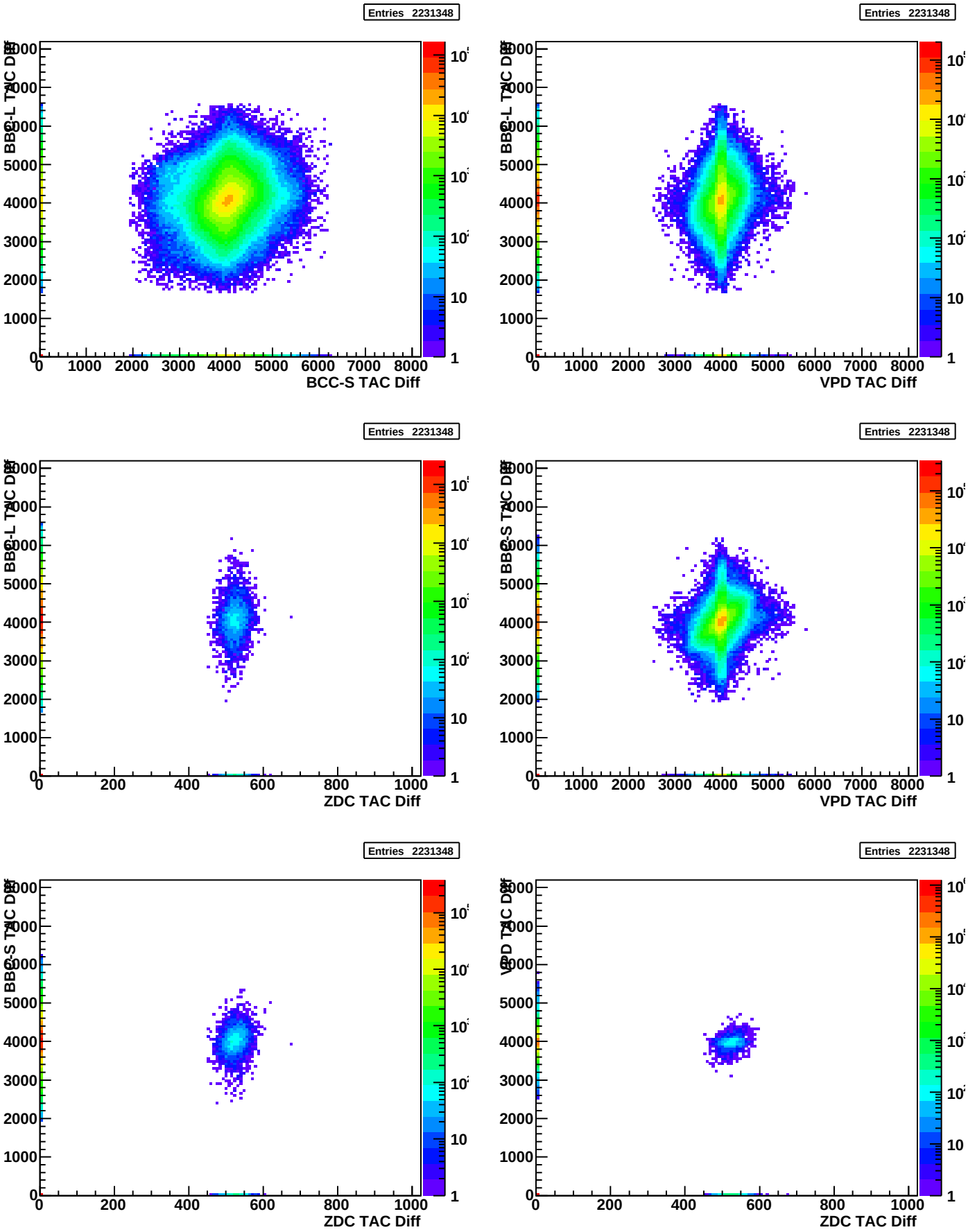
VPD-West ADC Sum
ZDC-West ADC Sum Att

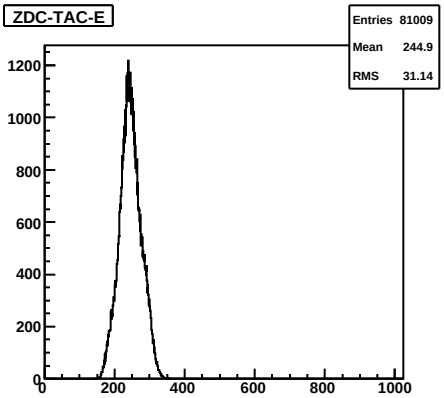

Entries 2231348
BBC-L-East ADC Sum
BBC-L-West ADC Sum

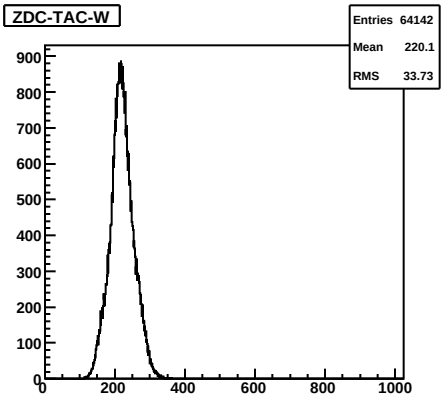

Entries 2231348
BBC-S-East ADC Sum
BBC-S-West ADC Sum

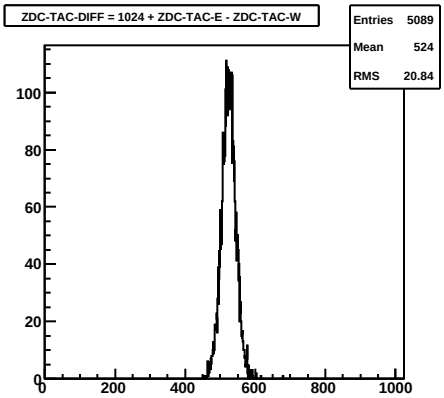

Entries 2231348
VPD East ADC Sum
VPD West ADC Sum

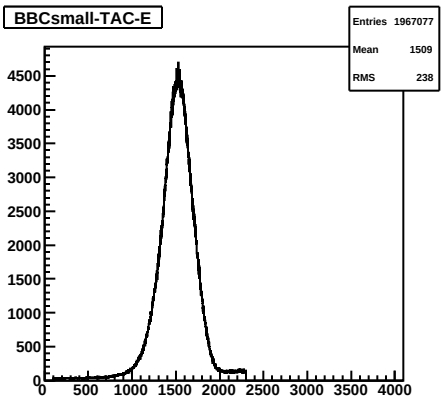

Entries 2231348
ZDC East ADC Sum Att
ZDC West ADC Sum Att

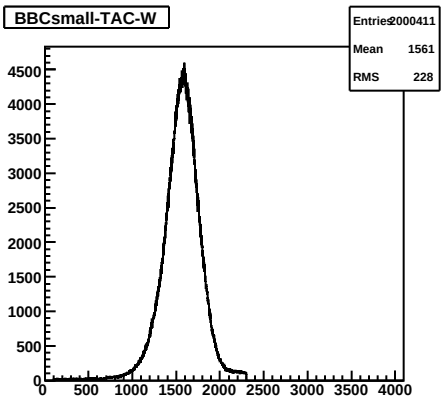

Entries 2231348
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2231348
BBC-L TAC Diff
VPD TAC Diff
Entries 2231348
BBC-L TAC Diff
ZDC TAC Diff
Entries 2231348
BBC-S TAC Diff
VPD TAC Diff
Entries 2231348
BBC-S TAC Diff
ZDC TAC Diff
Entries 2231348
VPD TAC Diff
ZDC TAC Diff

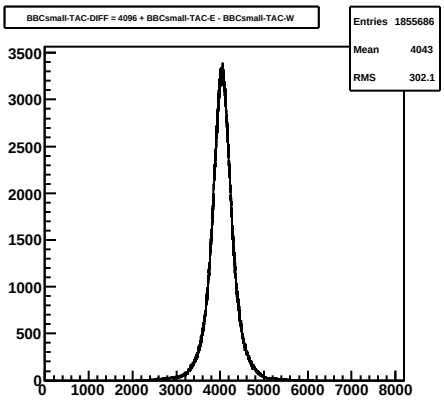

ZDC-TAC-E
Entries 81009
Mean 244.9
RMS 31.14

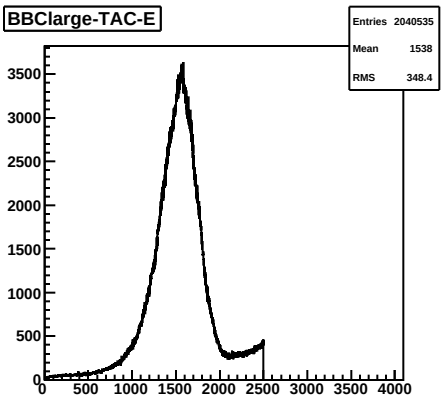

ZDC-TAC-W
Entries 64142
Mean 220.1
RMS 33.73

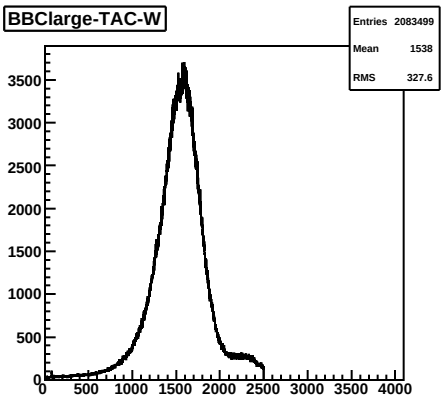

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5089
Mean 524
RMS 20.84

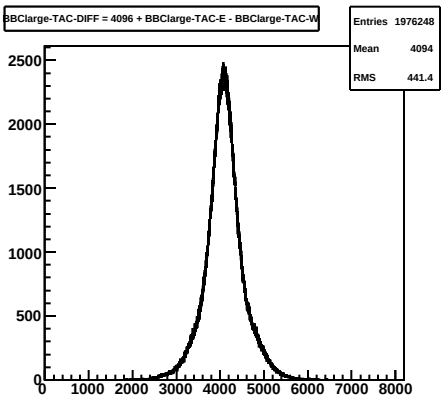

BBCsmall-TAC-E
Entries 1967077
Mean 1509
RMS 238

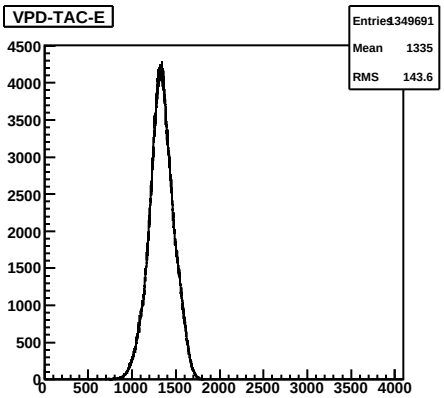

BBCsmall-TAC-W
Entries 2000411
Mean 1561
RMS 228

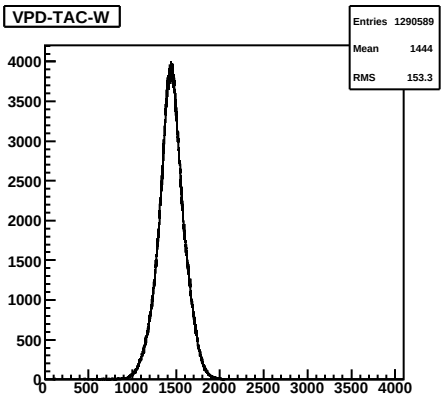

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1855686
Mean 4043
RMS 302.1

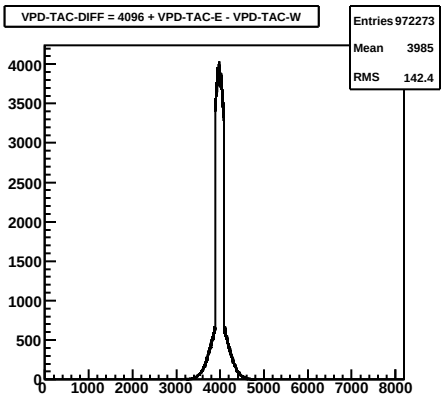

BBClarge-TAC-E
Entries 2040535
Mean 1538
RMS 348.4

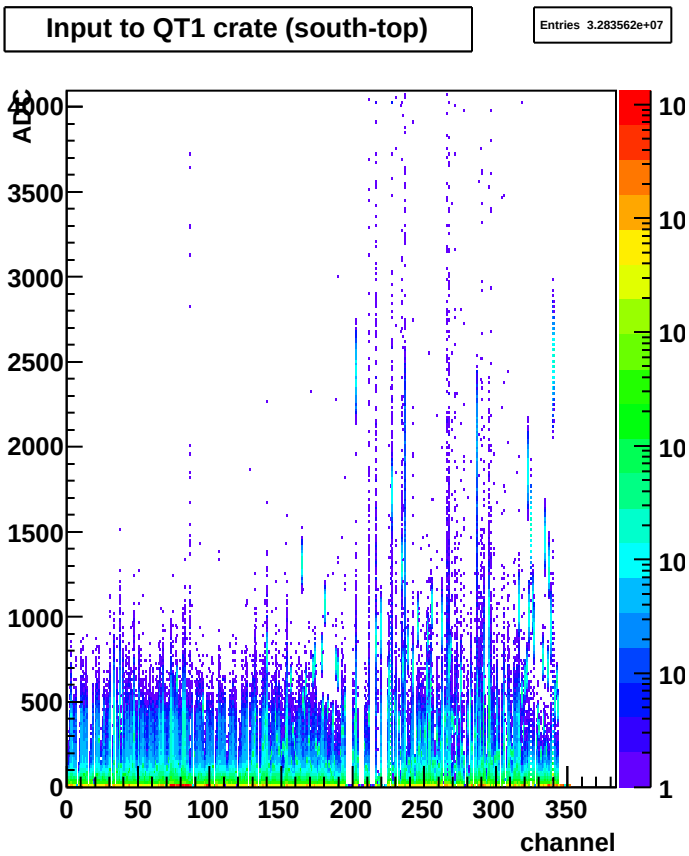

BBClarge-TAC-W
Entries 2083499
Mean 1538
RMS 327.6

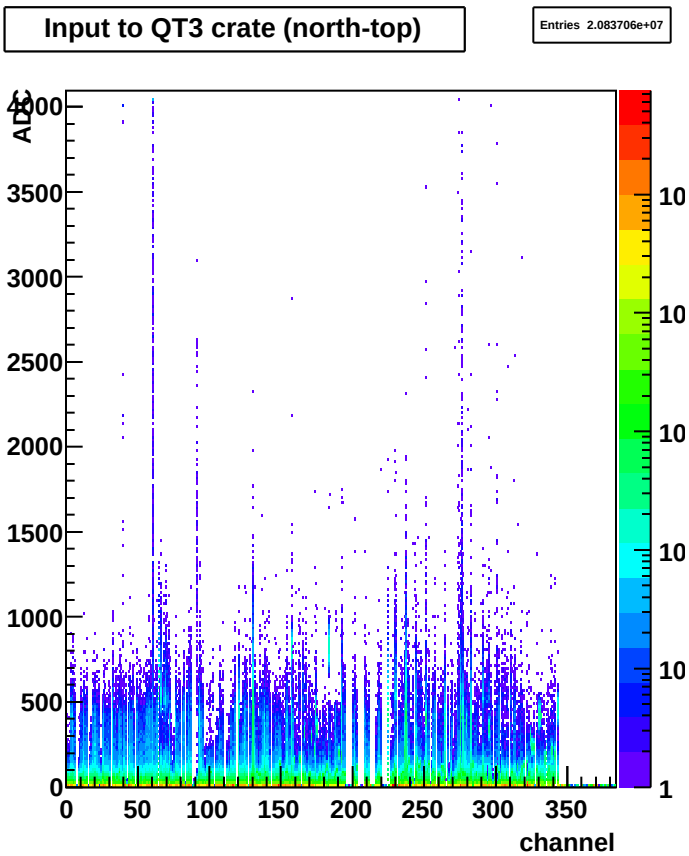

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1976248
Mean 4094
RMS 441.4

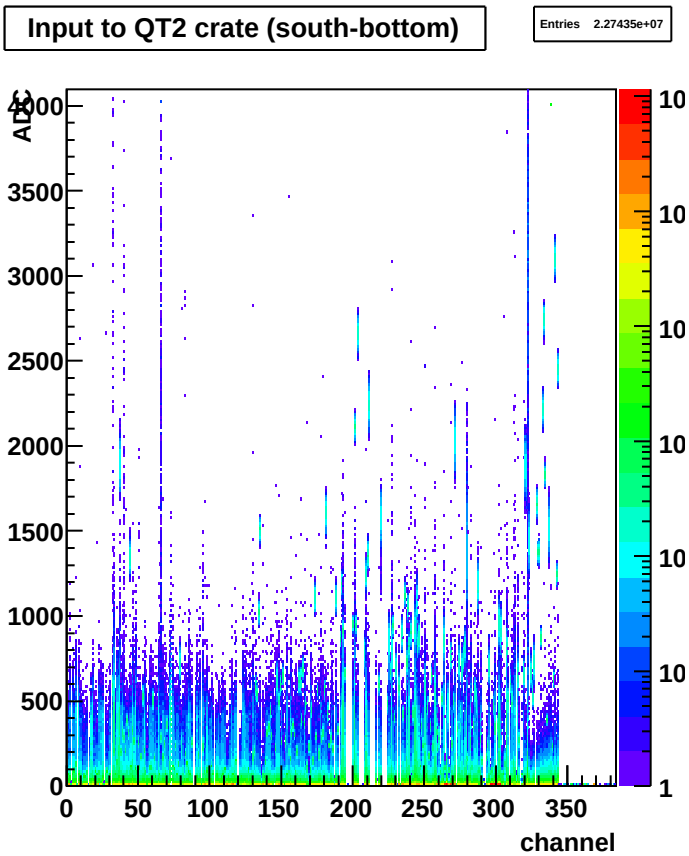

VPD-TAC-E
Entries 1349691
Mean 1335
RMS 143.6

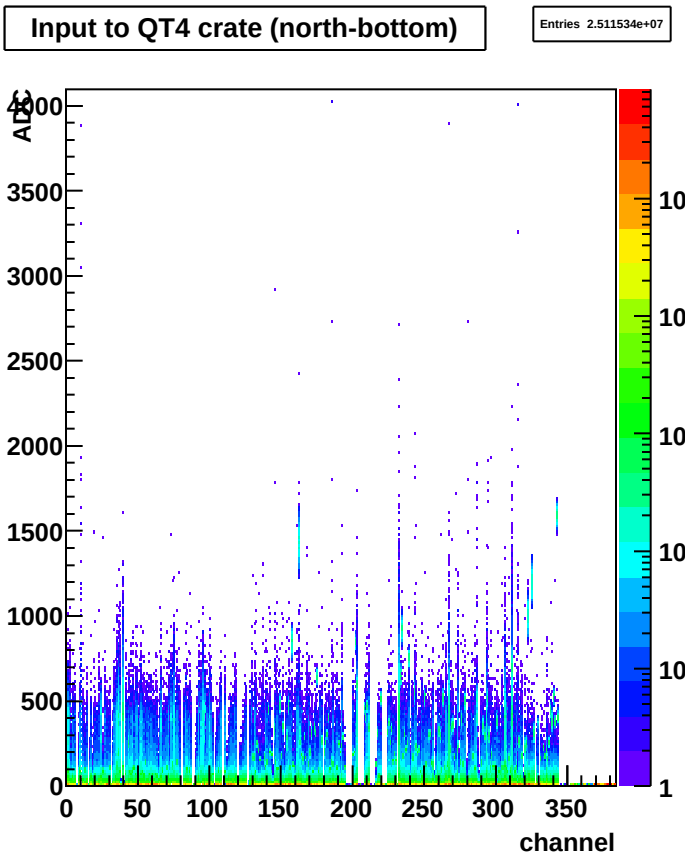

VPD-TAC-W
Entries 1290589
Mean 1444
RMS 153.3

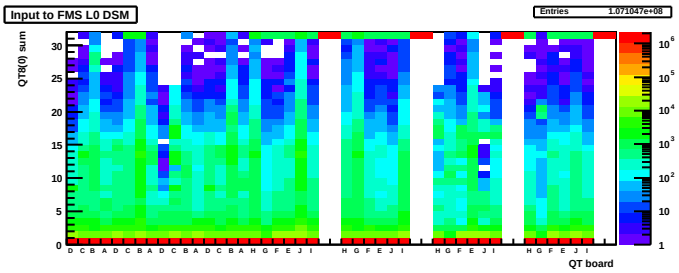

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 972273
Mean 3985
RMS 142.4

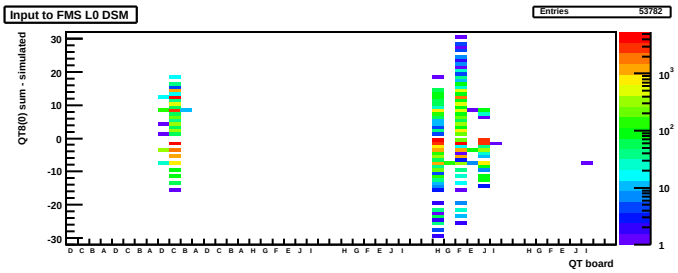

Input to QT1 crate (south-top)
Entries 3.283562e+07
ADC
channel

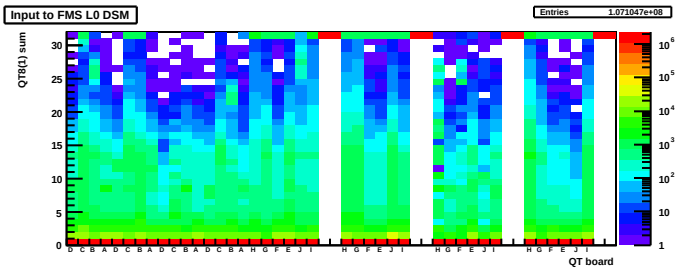

Input to QT3 crate (north-top)
Entries 2.083706e+07
ADC
channel

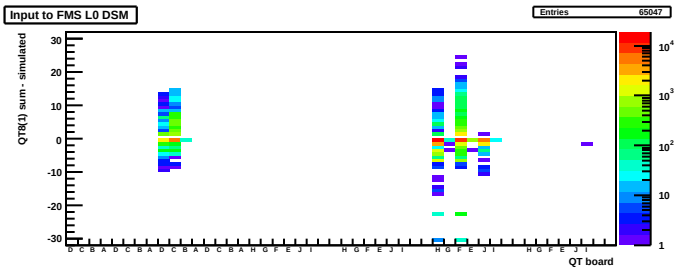

Input to QT2 crate (south-bottom)
Entries 2.27435e+07
ADC
channel

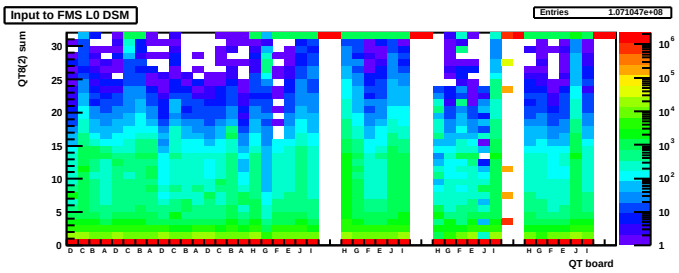

Input to QT4 crate (north-bottom)
Entries 2.511534e+07
ADC
channel

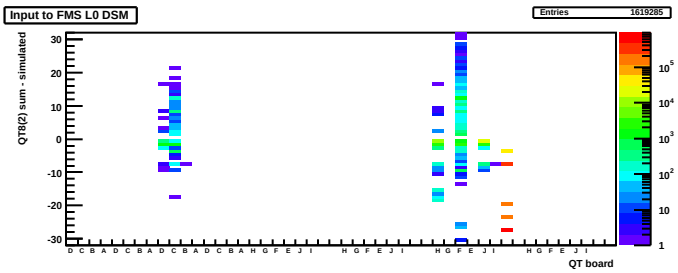

Input to FMS L0 DSM
Entries 1.071047e+08
QT8(0) sum
QT board

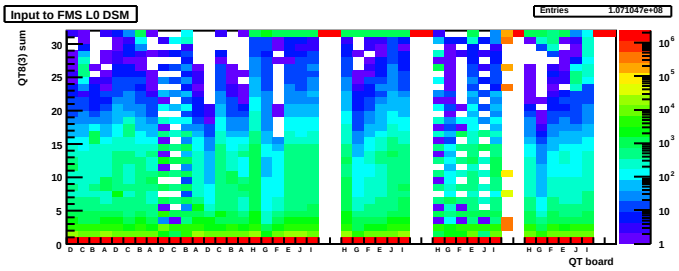

Input to FMS L0 DSM
Entries 53782
QT8(0) sum - simulated
QT board

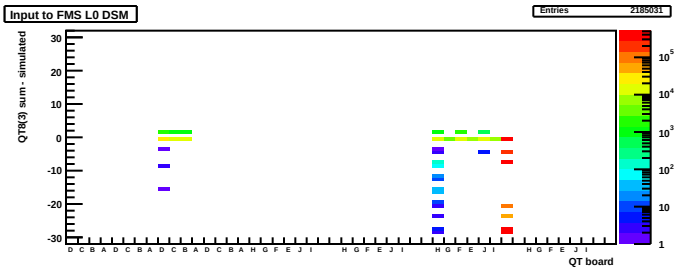

Input to FMS L0 DSM
Entries 1.071047e+08
QT8(1) sum
QT board

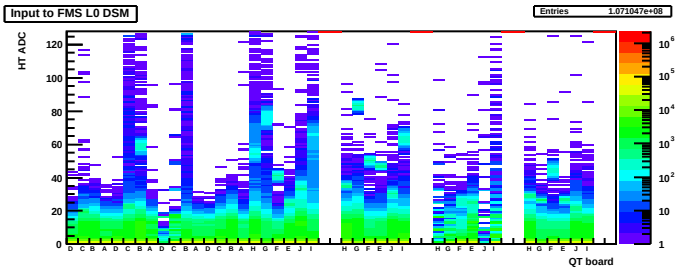

Input to FMS L0 DSM
Entries 65047
QT8(1) sum - simulated
QT board

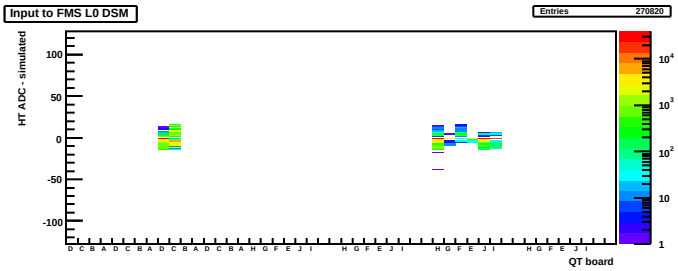

Input to FMS L0 DSM
Entries 1.071047e+08
QT8(2) sum
QT board

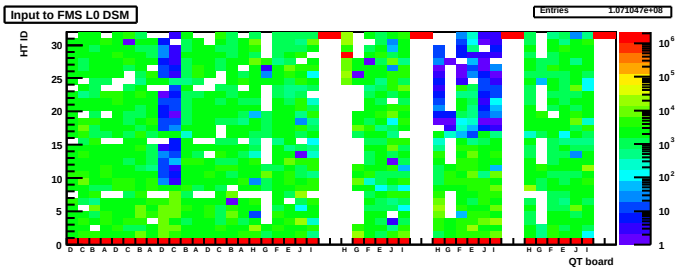

Input to FMS L0 DSM
Entries 1619285
QT8(2) sum - simulated
QT board

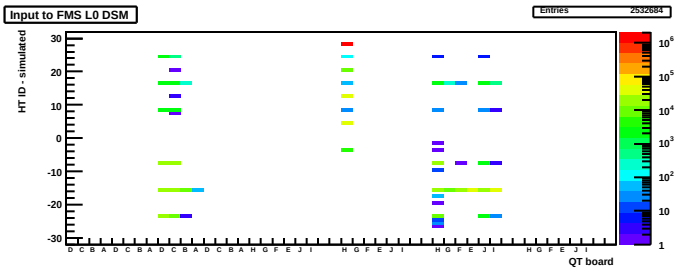

Input to FMS L0 DSM
Entries 1.071047e+08
QT8(3) sum
QT board

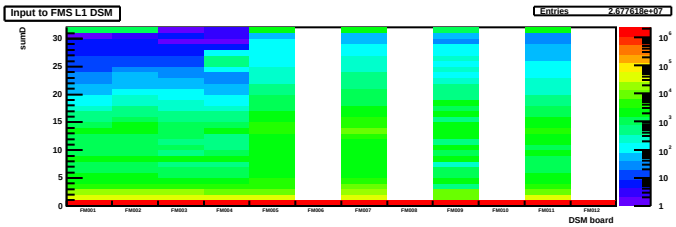

Input to FMS L0 DSM
Entries 2185031
QT8(3) sum - simulated
QT board

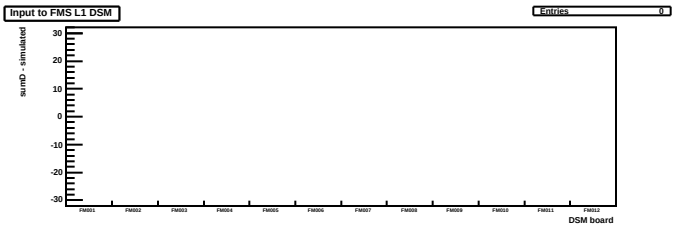

Input to FMS L0 DSM
Entries 1.071047e+08
HT ADC
QT board

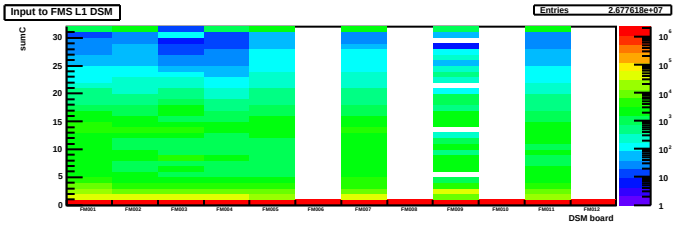

Input to FMS L0 DSM
Entries 270820
HT ADC - simulated
QT board

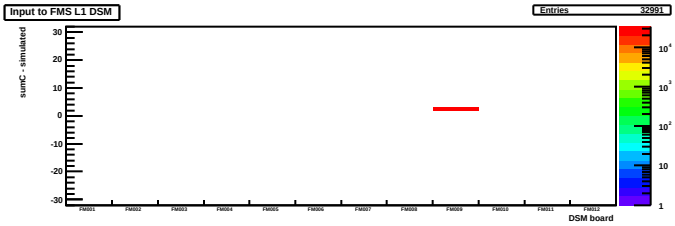

Input to FMS L0 DSM
Entries 1.071047e+08
HT ID
QT board

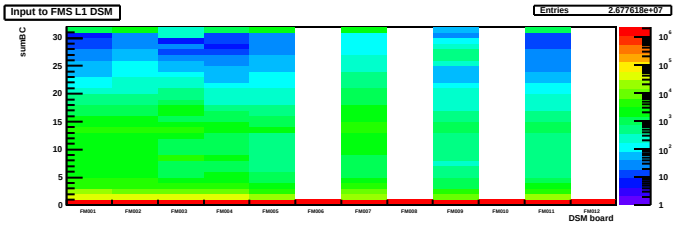

Input to FMS L0 DSM
Entries 2532684
HT ID - simulated
QT board

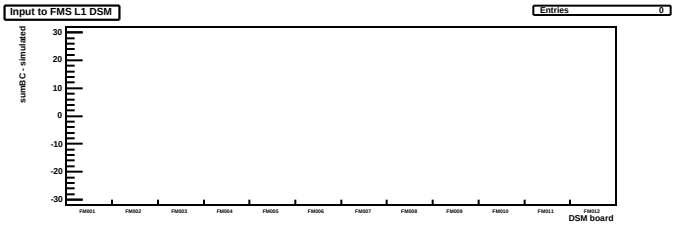

Input to FMS L1 DSM
Entries 2.677618e+07
sumD
DSM board

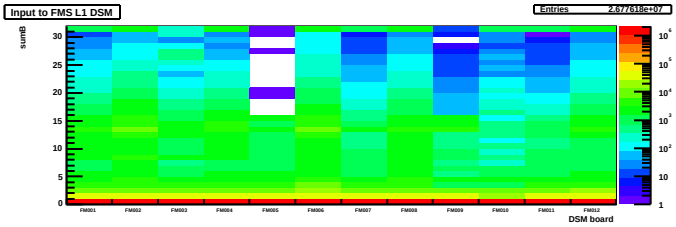

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

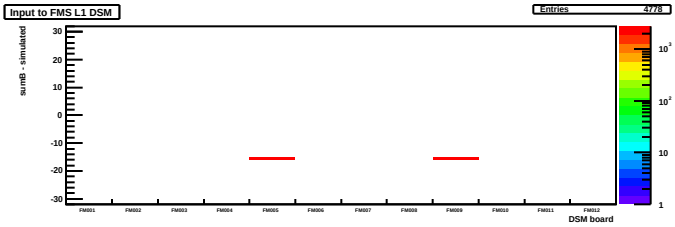

Input to FMS L1 DSM
Entries 2.677618e+07
sumC
DSM board

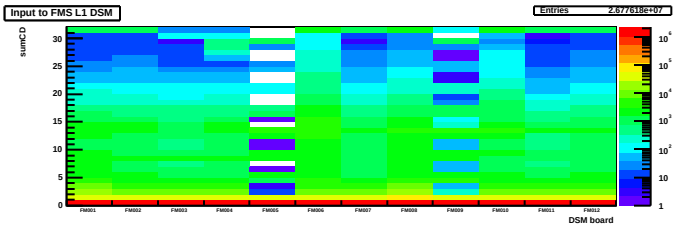

Input to FMS L1 DSM
Entries 32991
sumC - simulated
DSM board

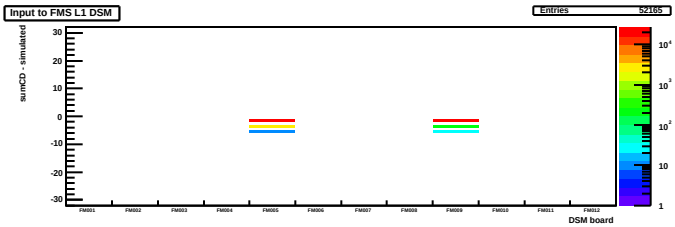

Input to FMS L1 DSM
Entries 2.677618e+07
sumBC
DSM board

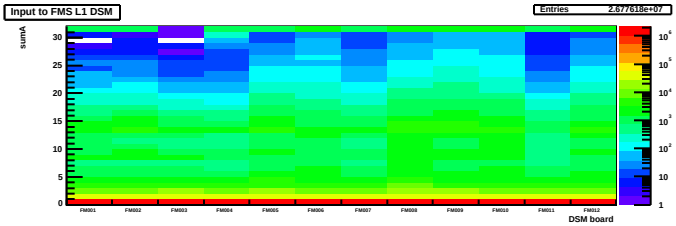

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

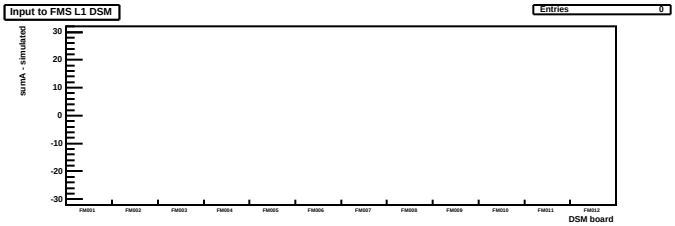

Input to FMS L1 DSM
Entries 2.677618e+07
sumB
DSM board

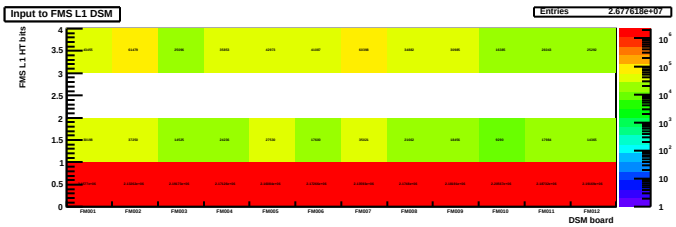

Input to FMS L1 DSM
Entries 4778
sumB - simulated
DSM board

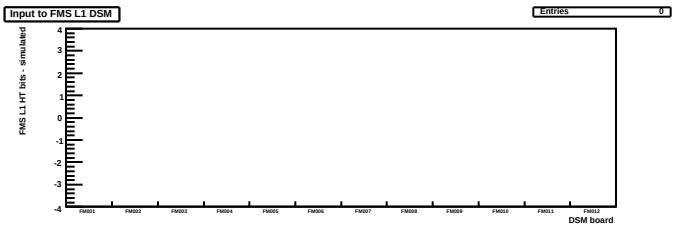

Input to FMS L1 DSM
Entries 2.677618e+07
sumCD
DSM board

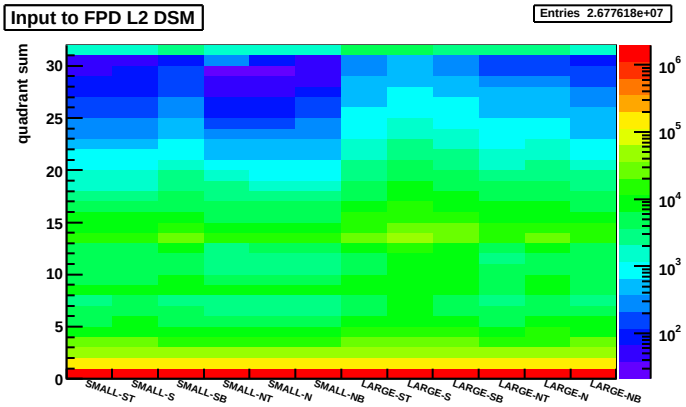

Input to FMS L1 DSM
Entries 52165
sumCD - simulated
DSM board

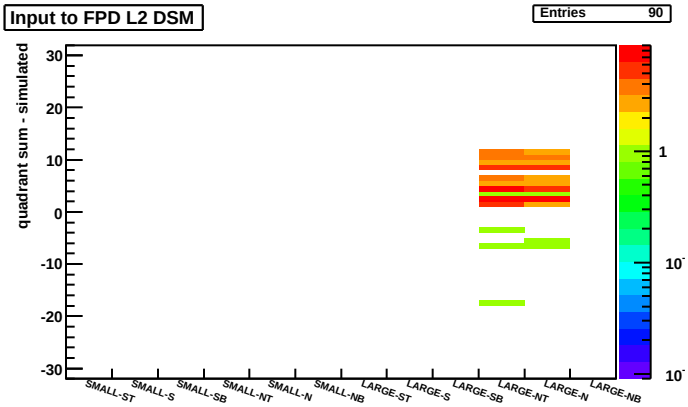

Input to FMS L1 DSM
Entries 2.677618e+07
sumA
DSM board

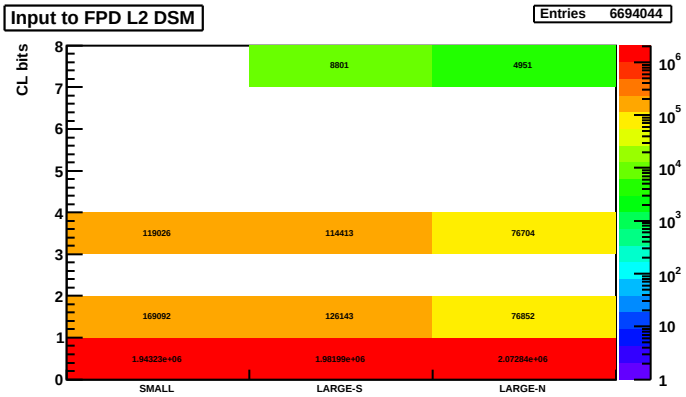

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

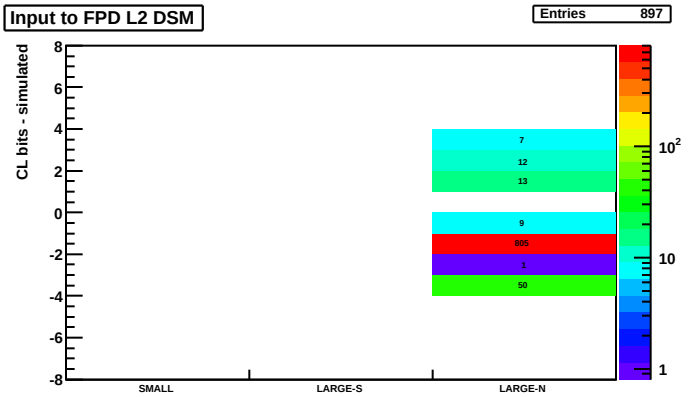

Input to FMS L1 DSM
Entries 2.677618e+07
FMS L1 HT bits
DSM board

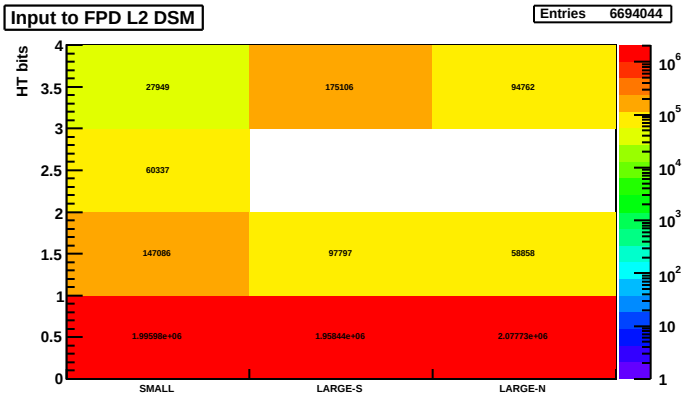

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

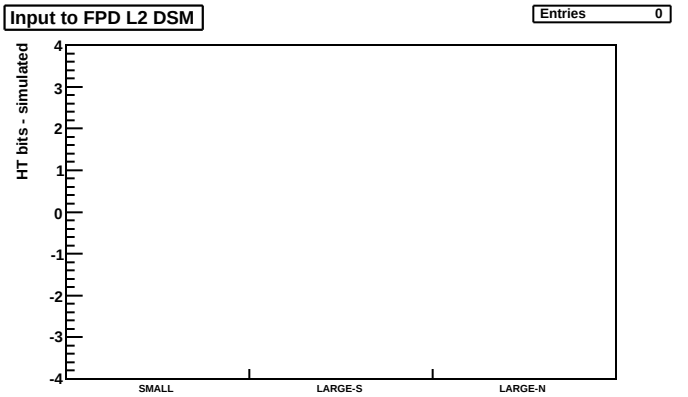

Input to FPD L2 DSM
Entries 2.677618e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

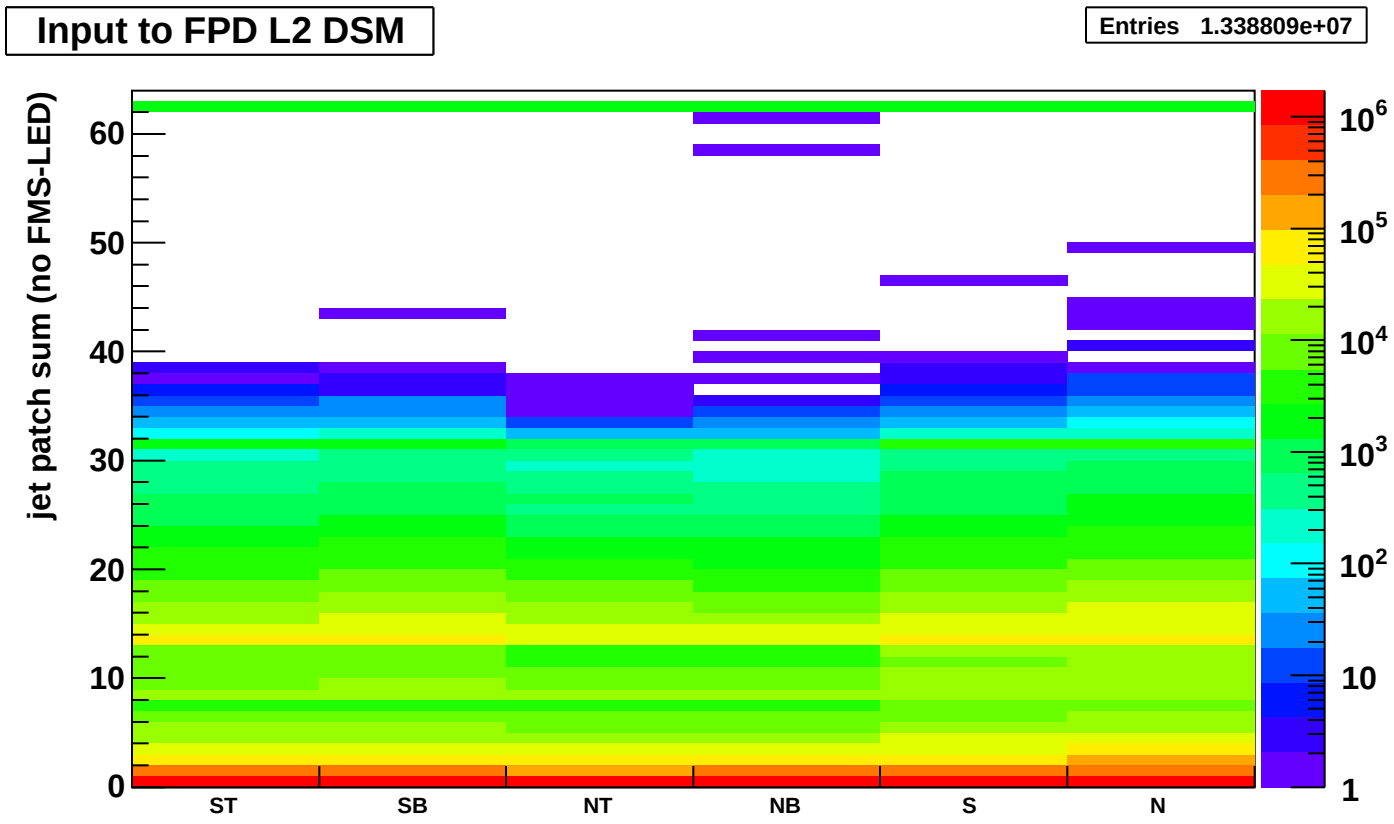

Input to FPD L2 DSM
Entries 90
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

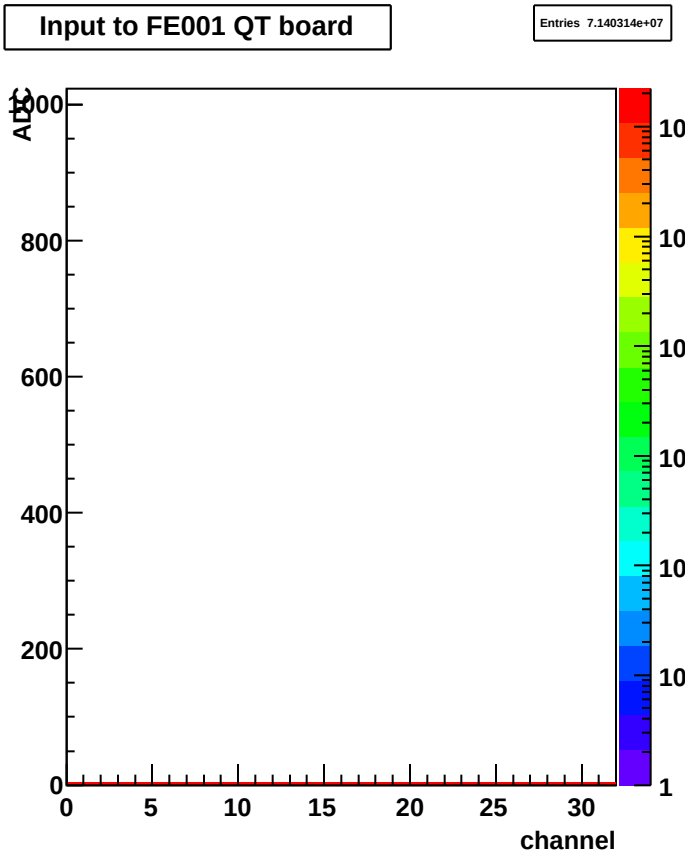

Input to FPD L2 DSM
Entries 6694044
CL bits
8801
4951
119026
114413
76704
169092
126143
76852
1.94323e+06
1.98199e+06
2.07284e+06
SMALL
LARGE-S
LARGE-N

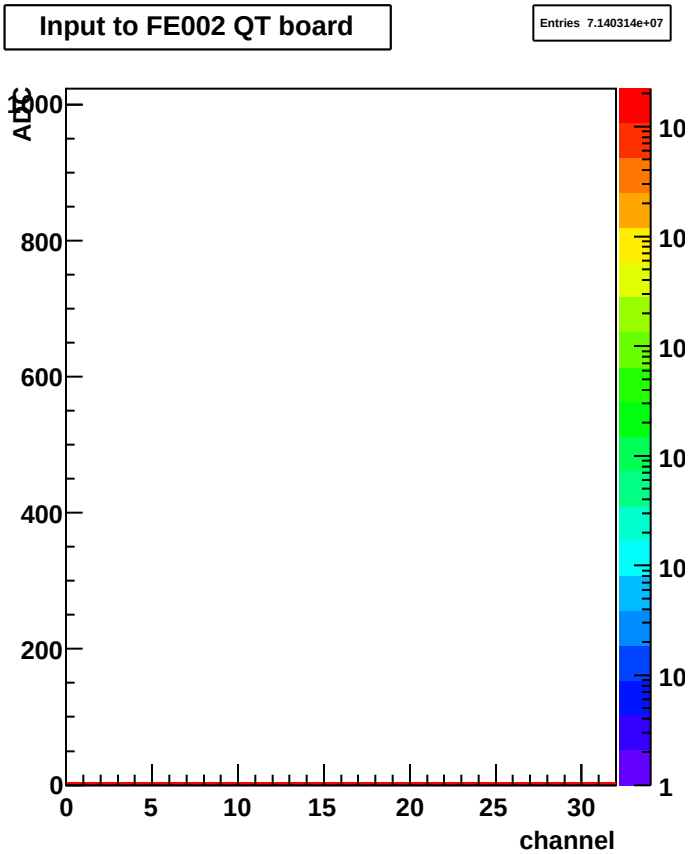

Input to FPD L2 DSM
Entries 897
CL bits - simulated
7
12
13
9
905
1
50
SMALL
LARGE-S
LARGE-N

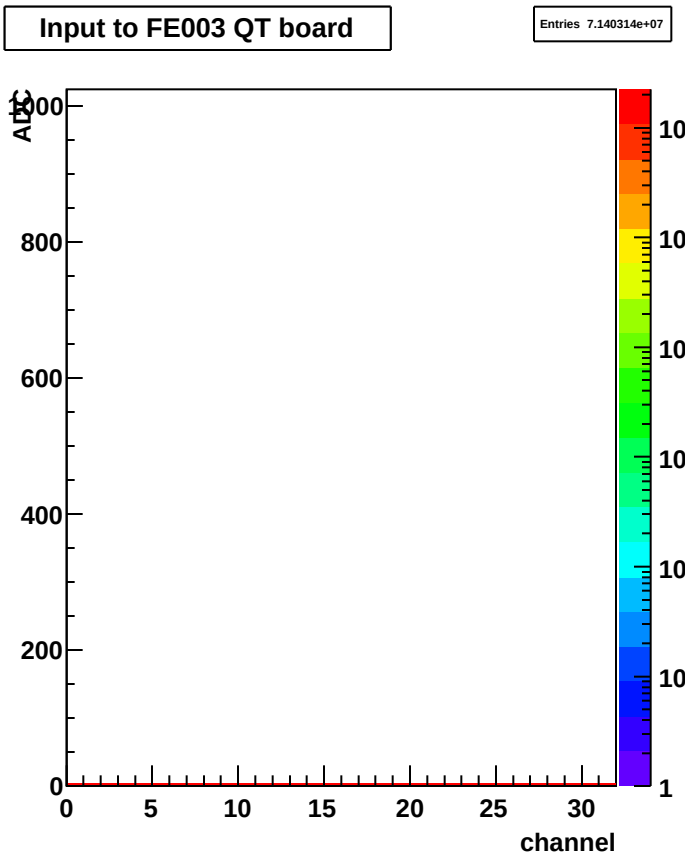

Input to FPD L2 DSM
Entries 6694044
HT bits
27949
175106
94762
60337
147086
97797
58858
1.99598e+06
1.95844e+06
2.07773e+06
SMALL
LARGE-S
LARGE-N

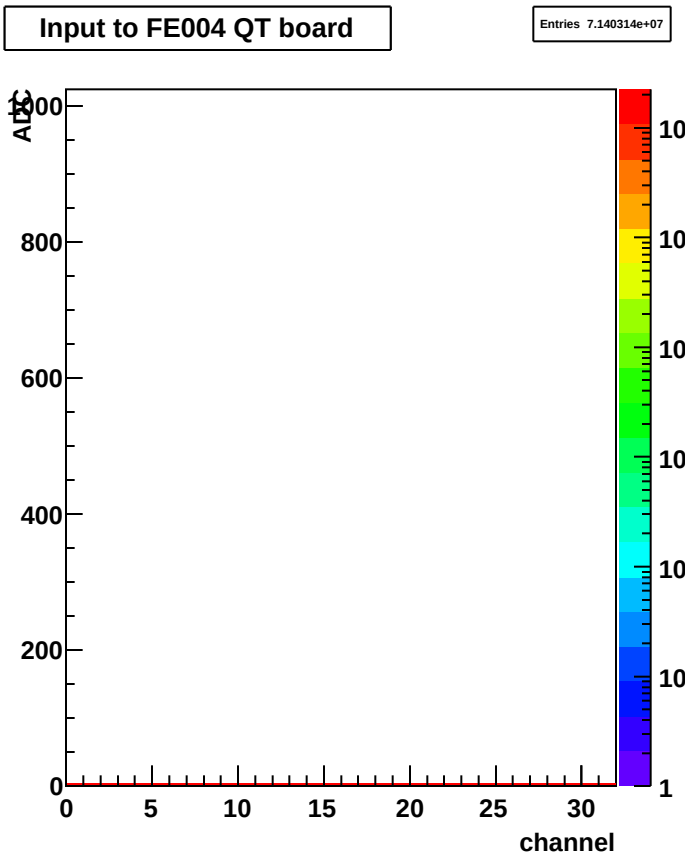

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

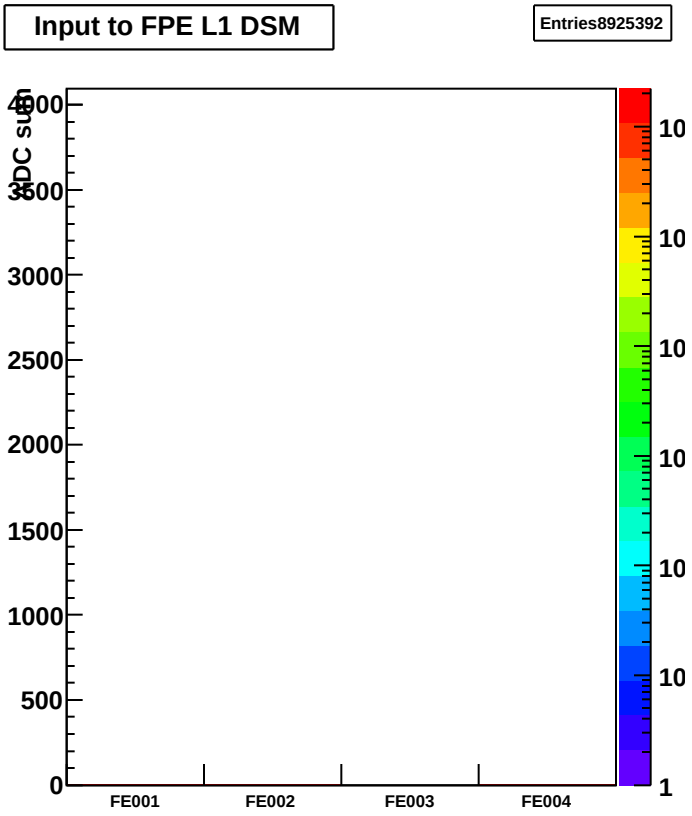

Input to FPD L2 DSM
Entries 1.338809e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

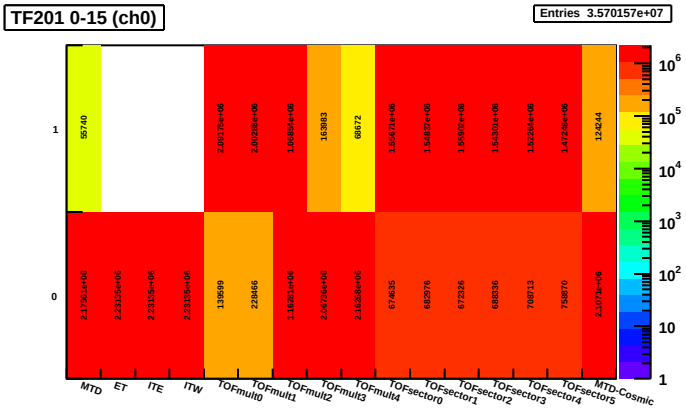

Input to FE001 QT board
Entries 7.140314e+07
ADC
channel

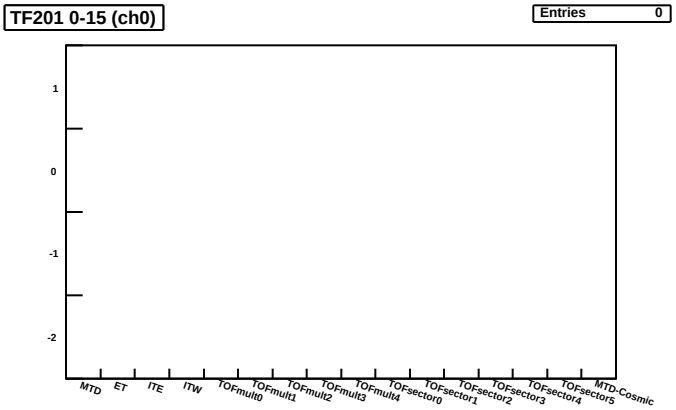

Input to FE002 QT board
Entries 7.140314e+07
ADC
channel


Input to FE003 QT board
Entries 7.140314e+07
ADC
channel


Input to FE004 QT board
Entries 7.140314e+07
ADC
channel


Input to FPE L1 DSM
Entries8925392
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 3.570157e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
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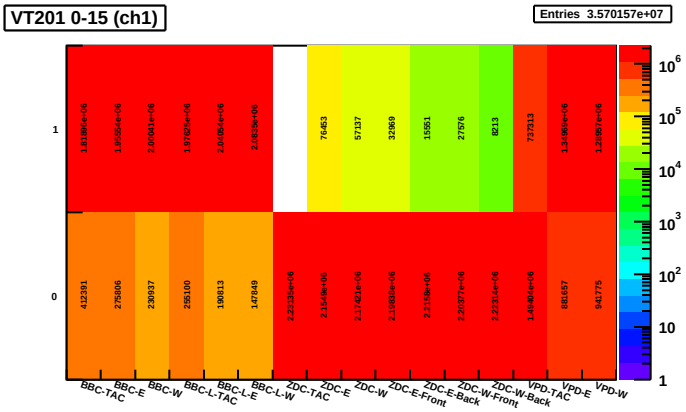
VT201 0-15 (ch1)
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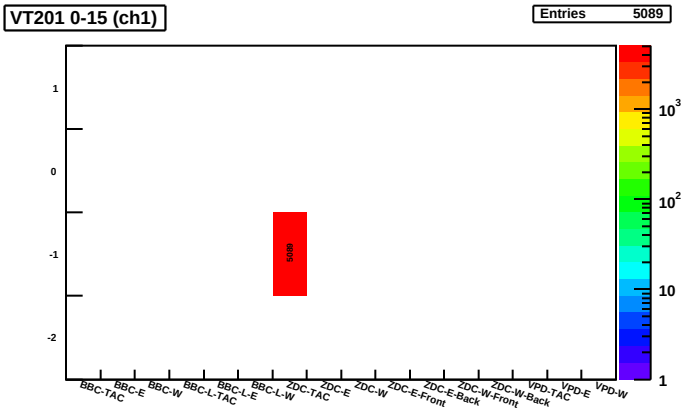
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W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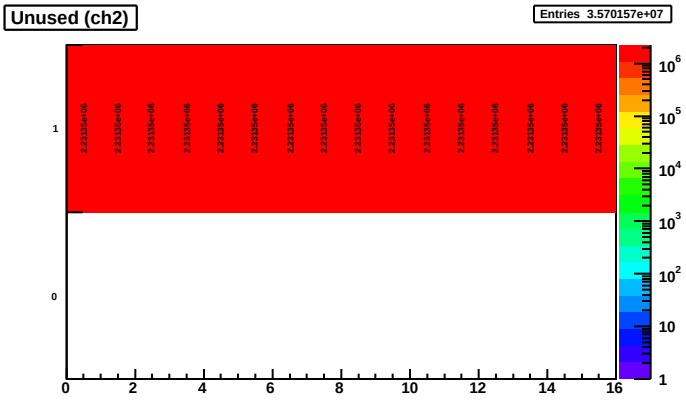
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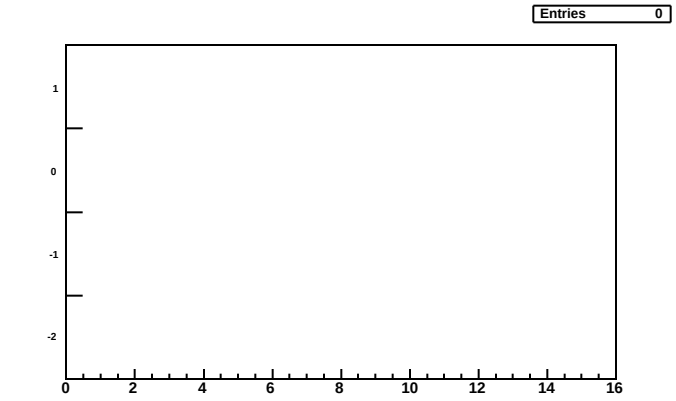
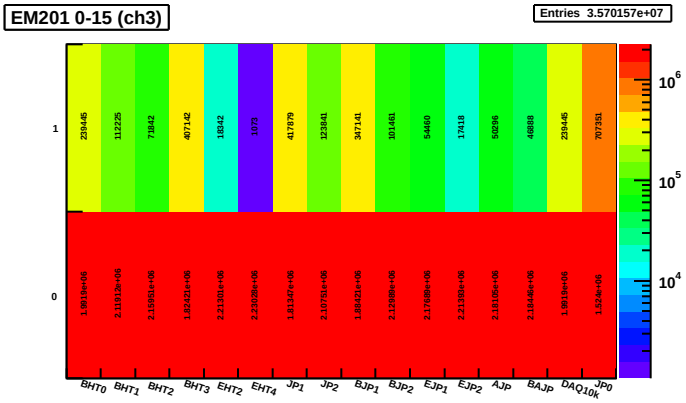

Entries 0


EM201 0-15 (ch3)
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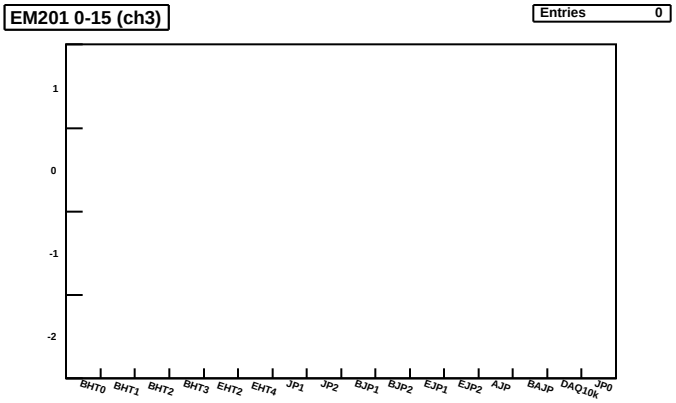
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0


EM201 0-15 (ch3)
Entries 0
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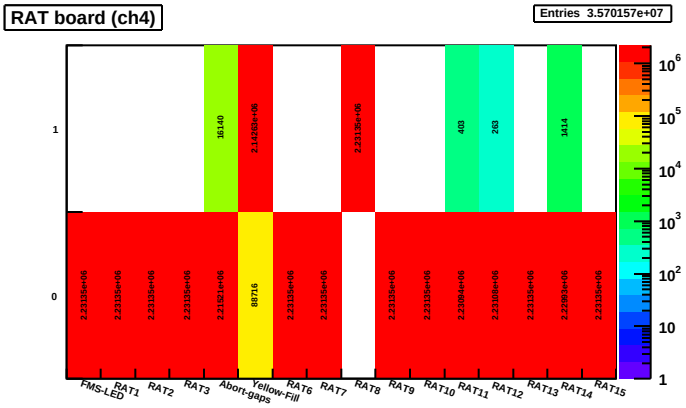
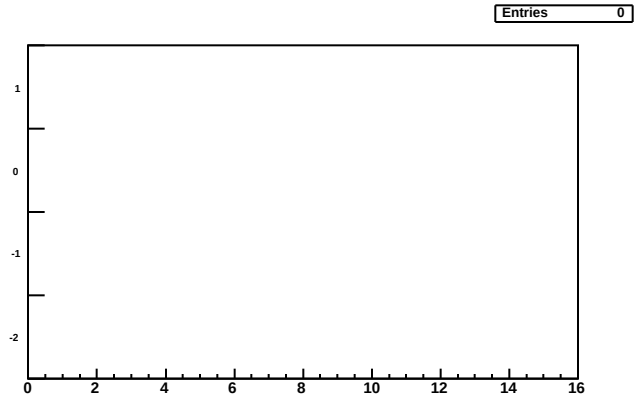
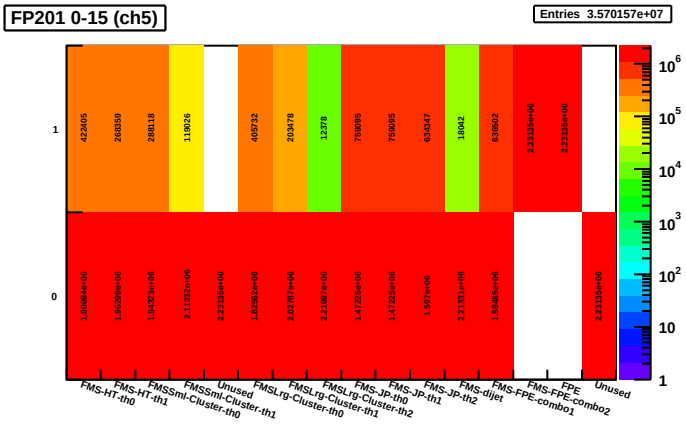
RAT board (ch4)





FP201 0-15 (ch5)



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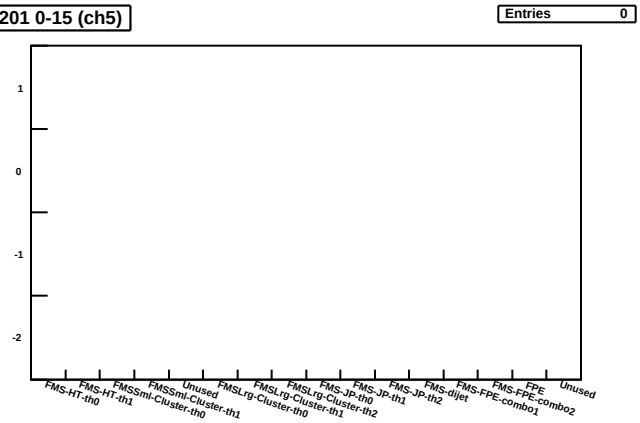
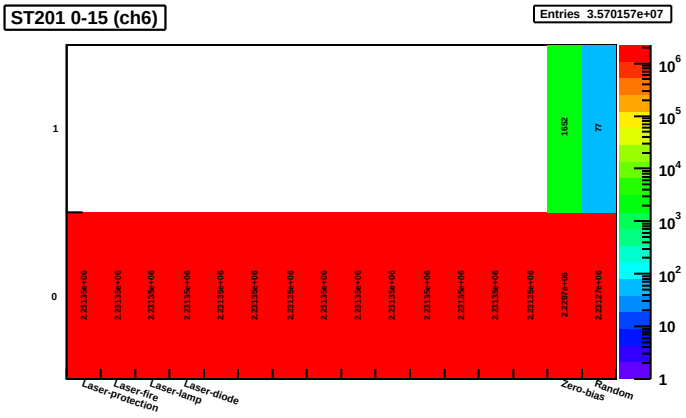
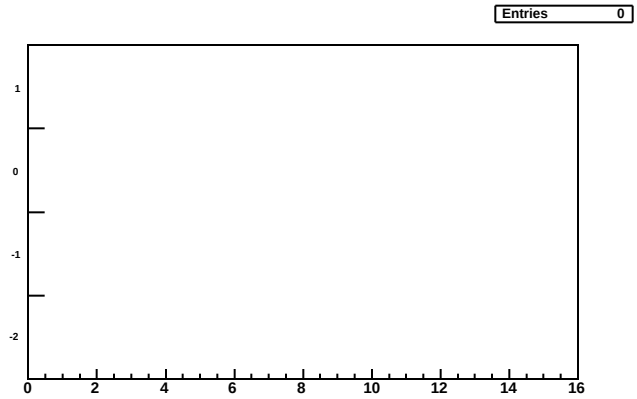
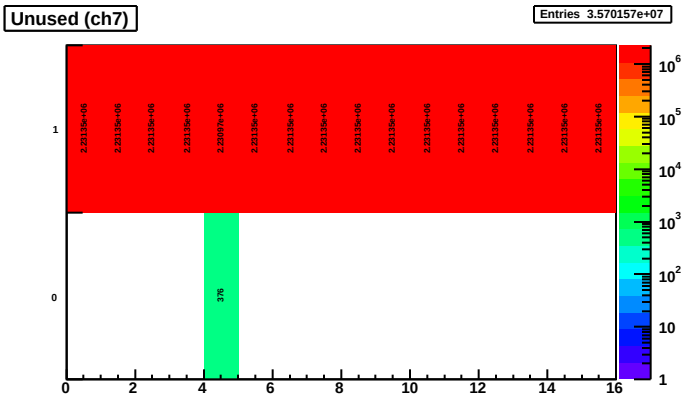
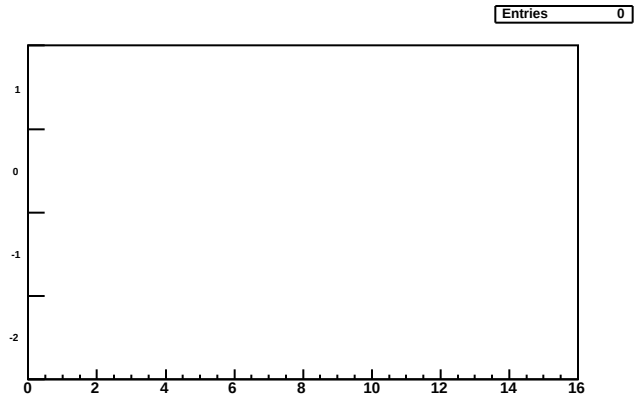


ST201 0-15 (ch6)





Unused (ch7)