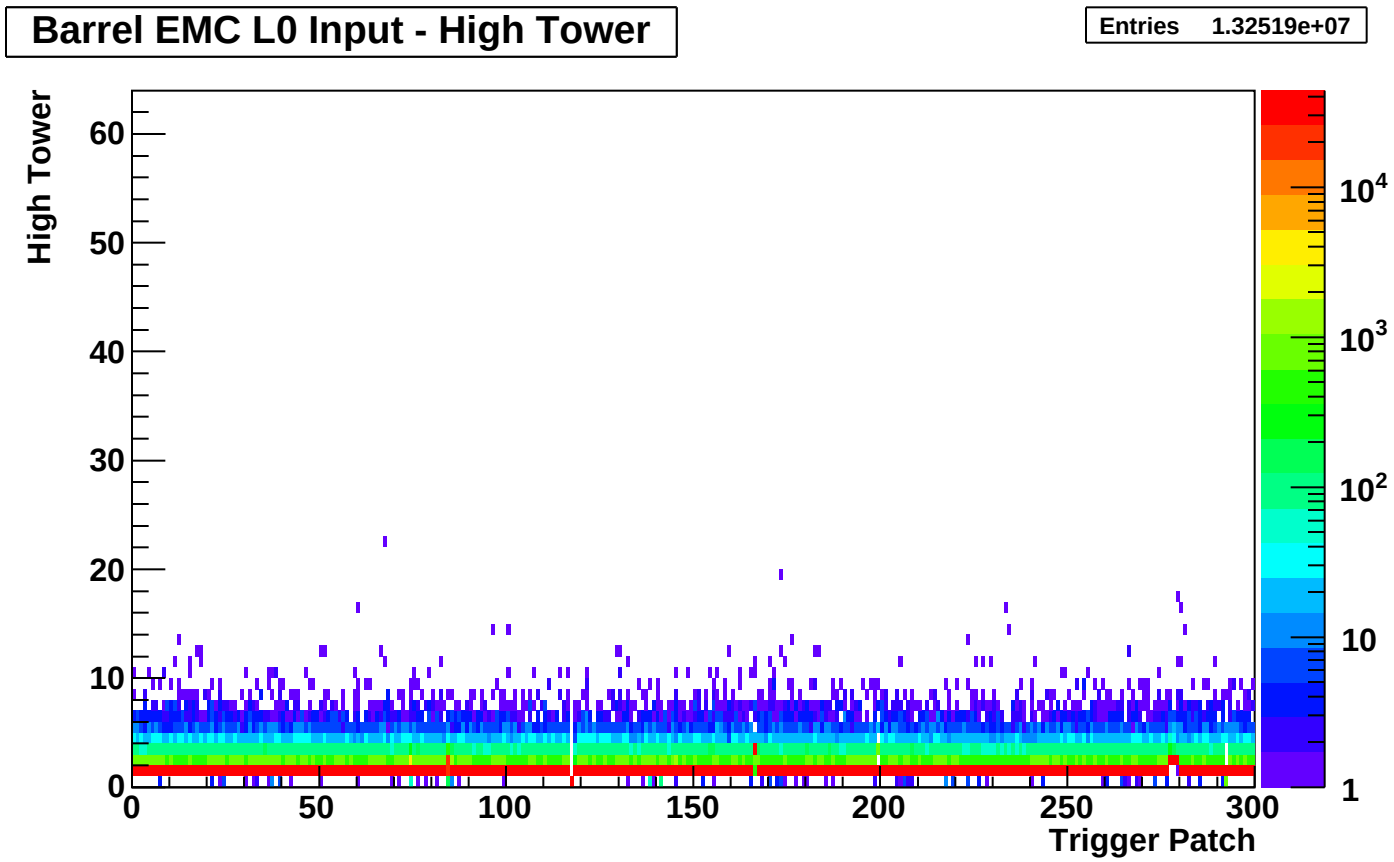
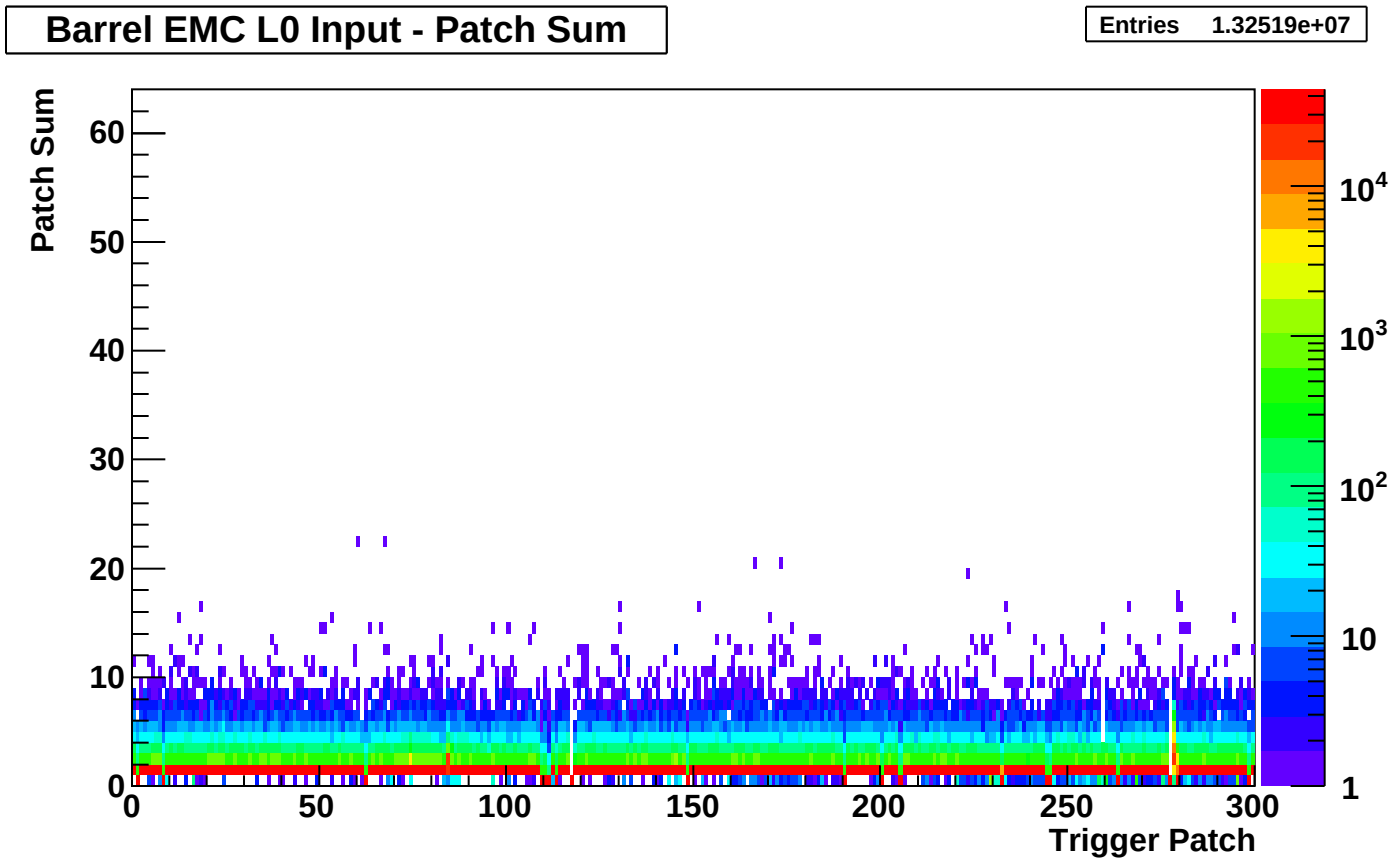
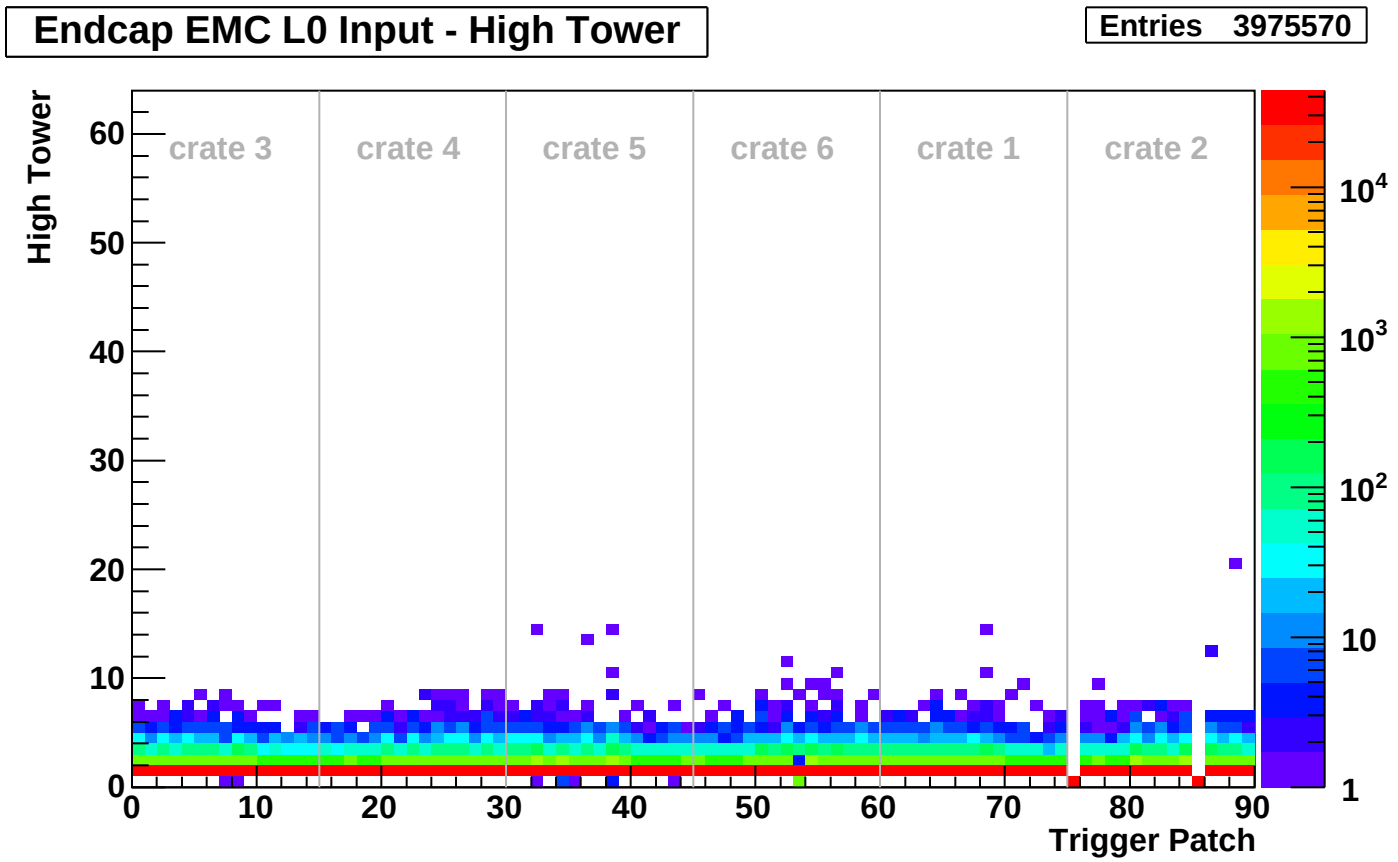
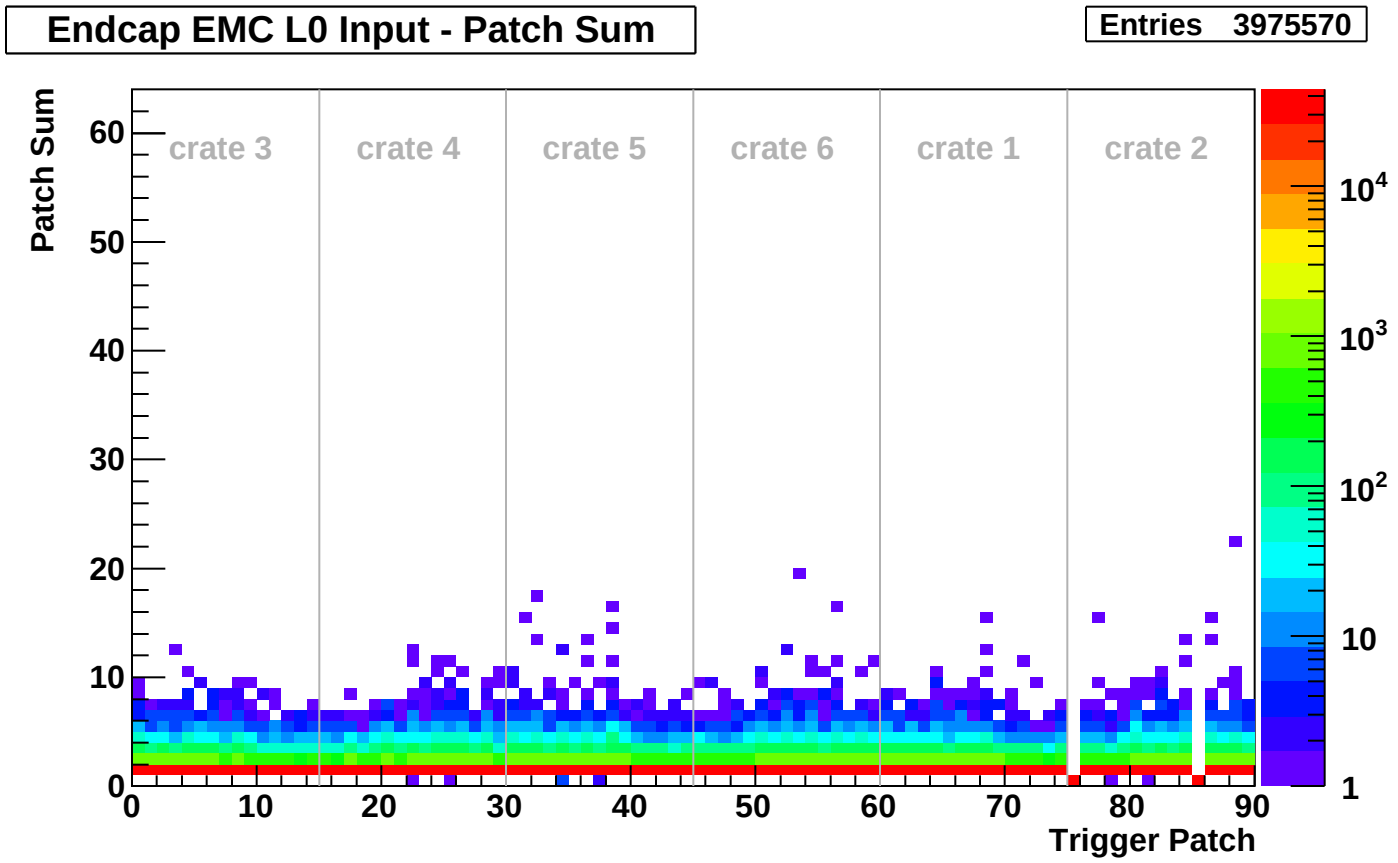
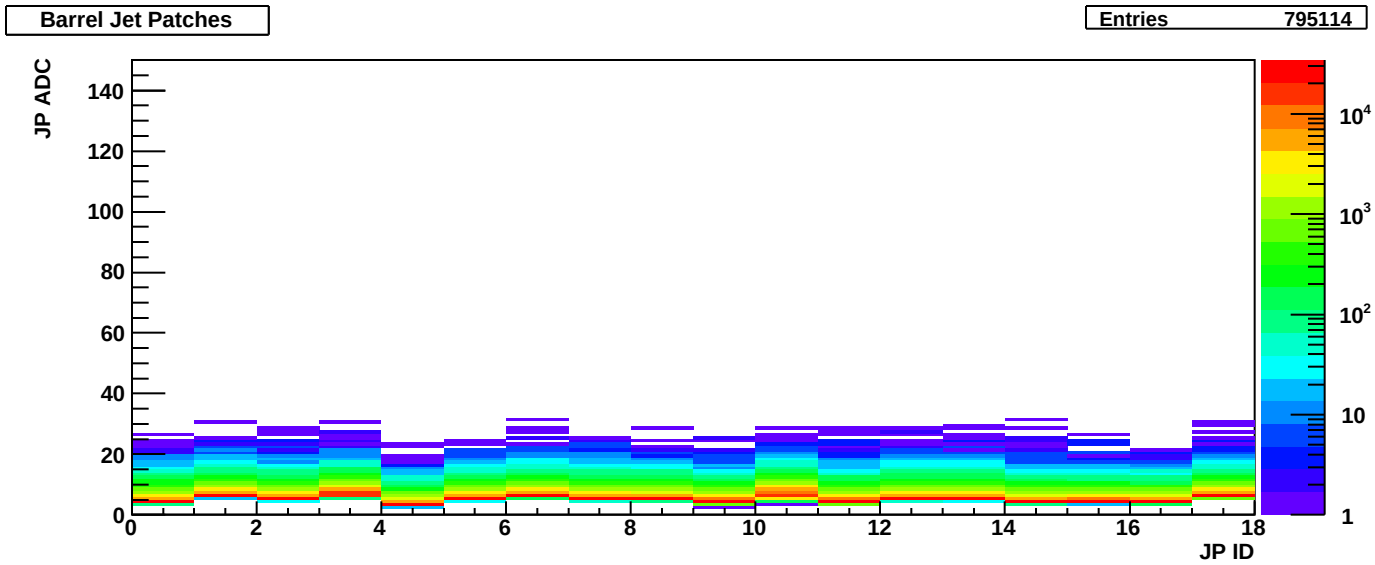
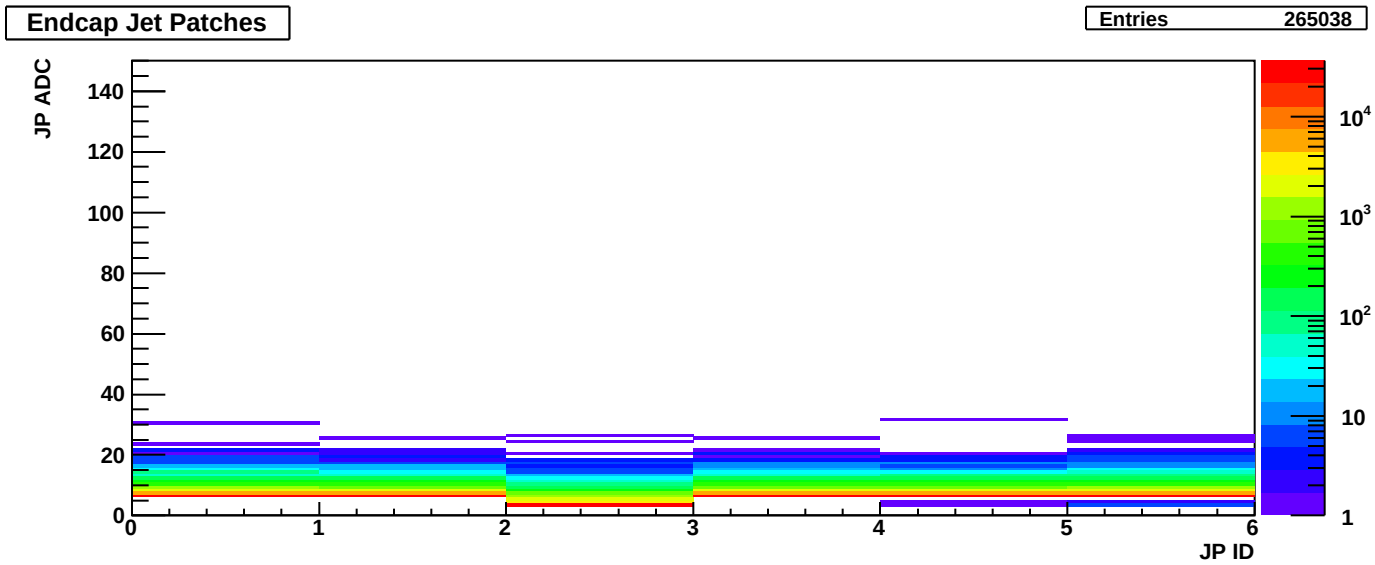

Barrel EMC L0 Input - High Tower
Entries 1.32519e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

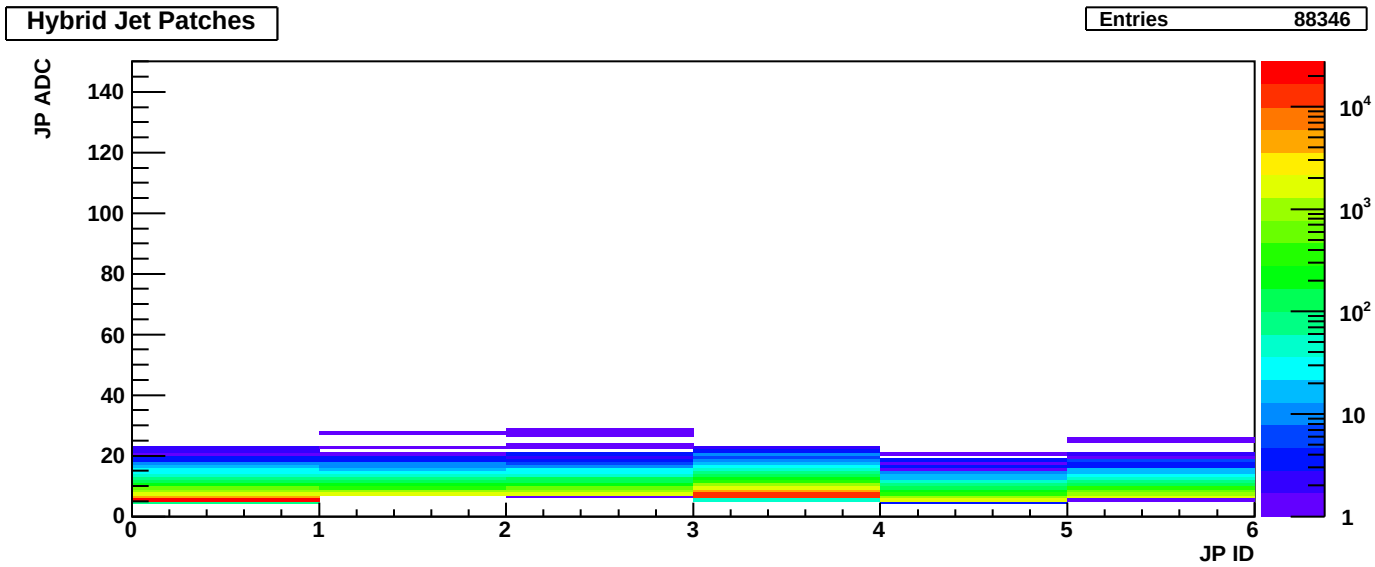

Barrel EMC L0 Input - Patch Sum
Entries 1.32519e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

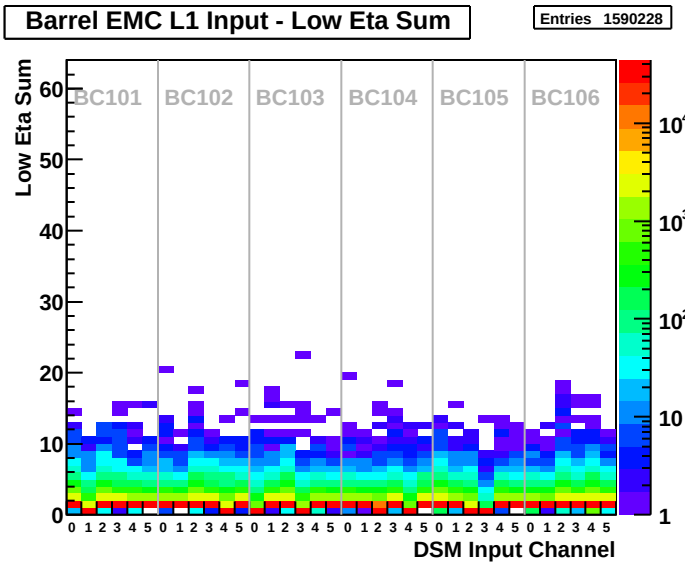

Endcap EMC L0 Input - High Tower
Entries 3975570
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

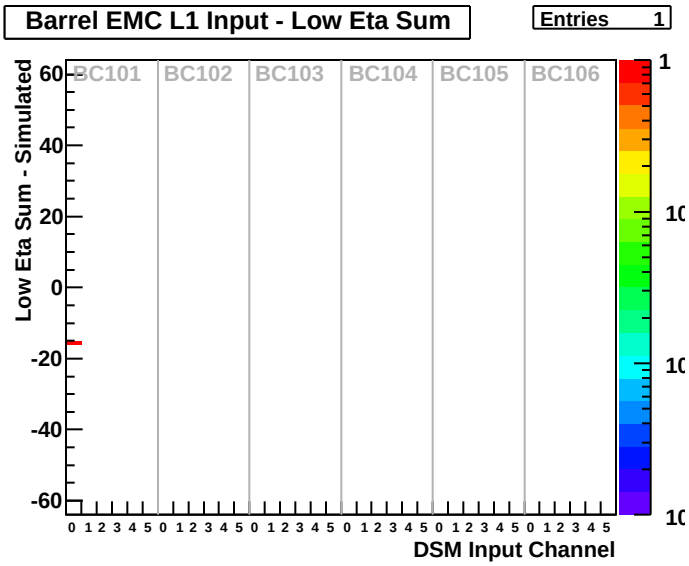

Endcap EMC L0 Input - Patch Sum
Entries 3975570
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

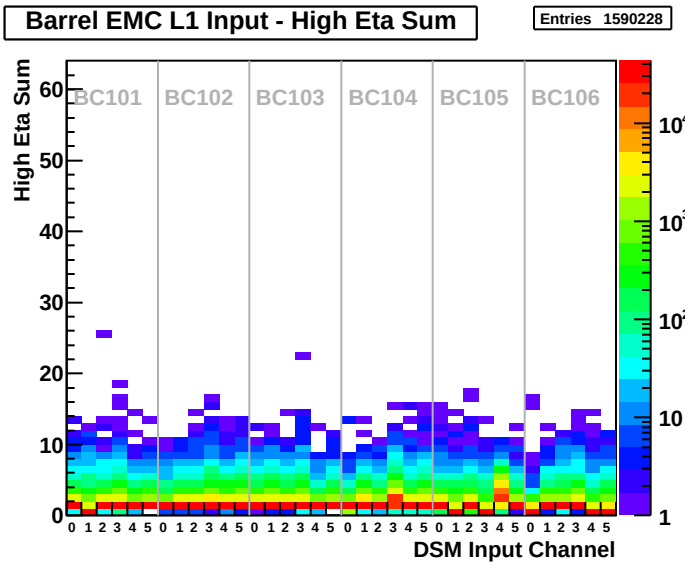

Barrel Jet Patches
Entries 795114
JP ADC
JP ID

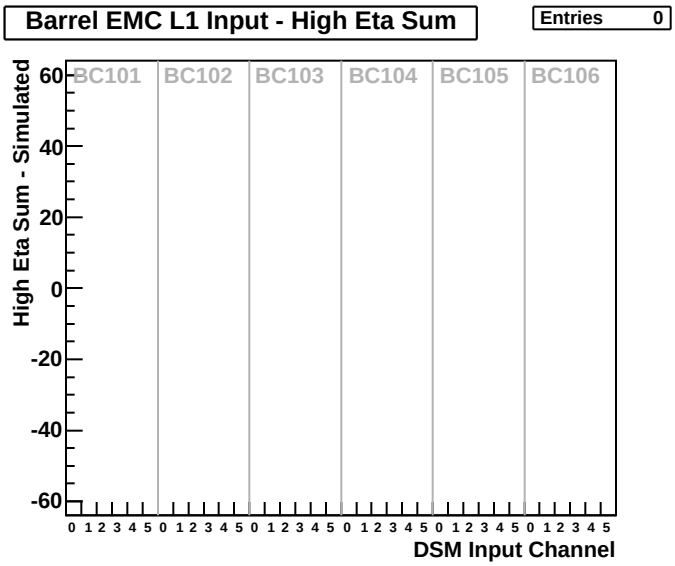

Endcap Jet Patches
Entries 265038
JP ADC
JP ID

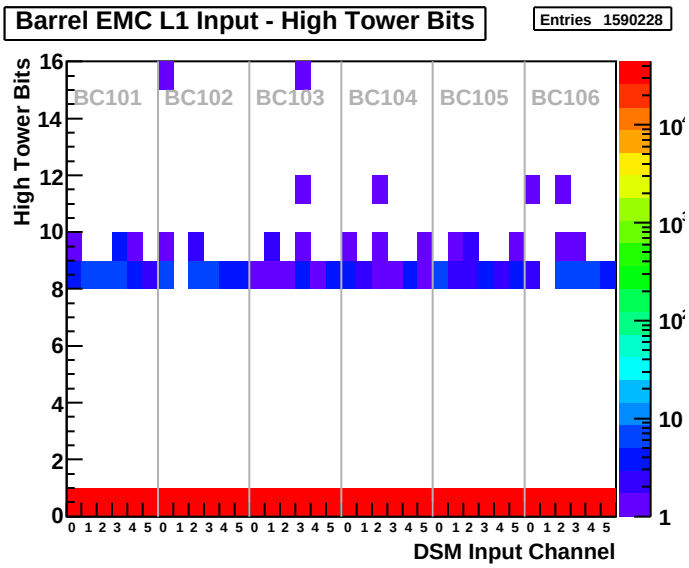

Hybrid Jet Patches
Entries 88346
JP ADC
JP ID

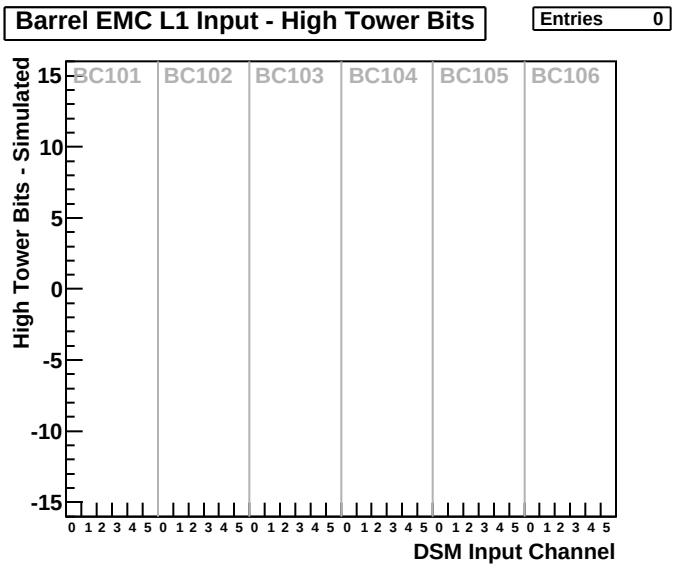

Barrel EMC L1 Input - Low Eta Sum
Entries 1590228
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

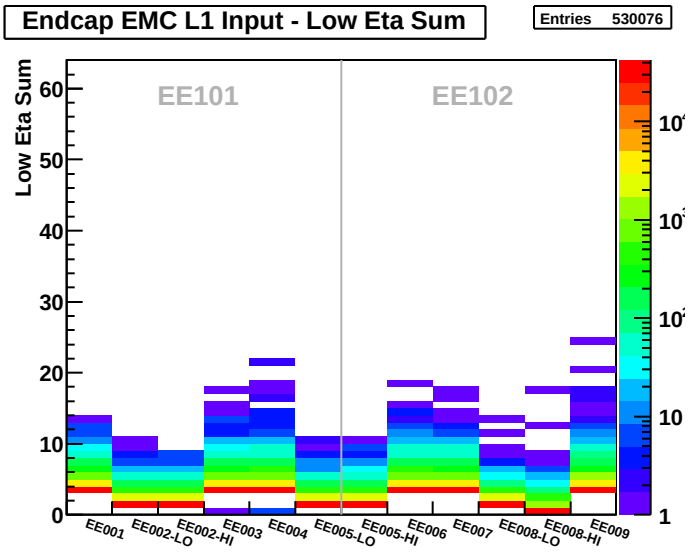

Barrel EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

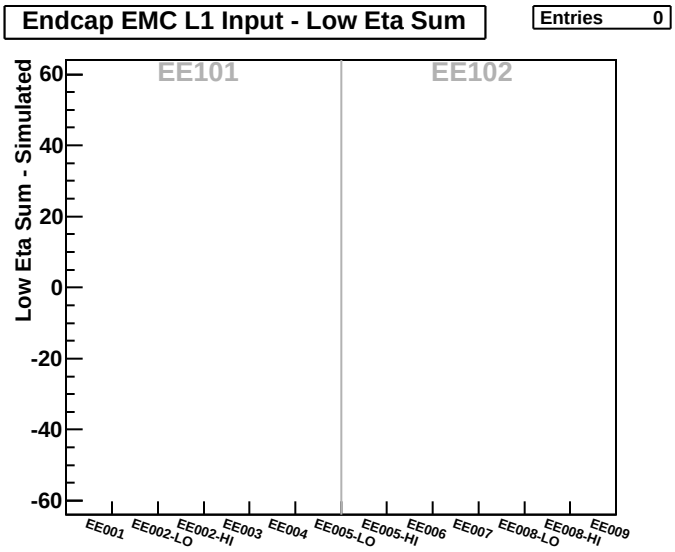

Barrel EMC L1 Input - High Eta Sum
Entries 1590228
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

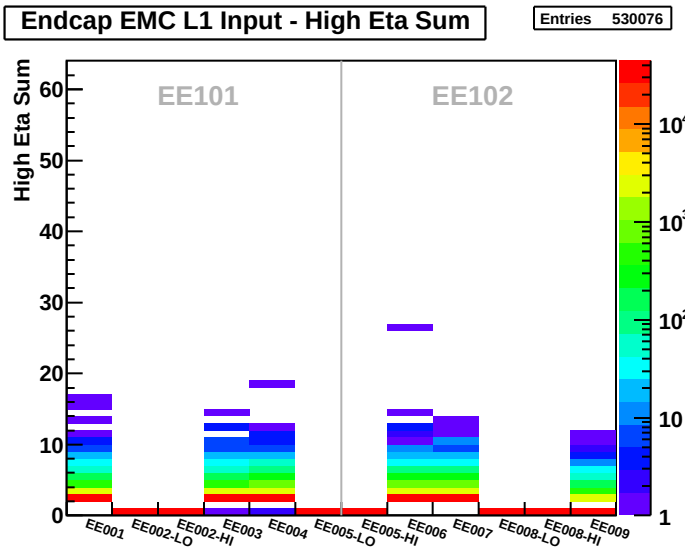

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

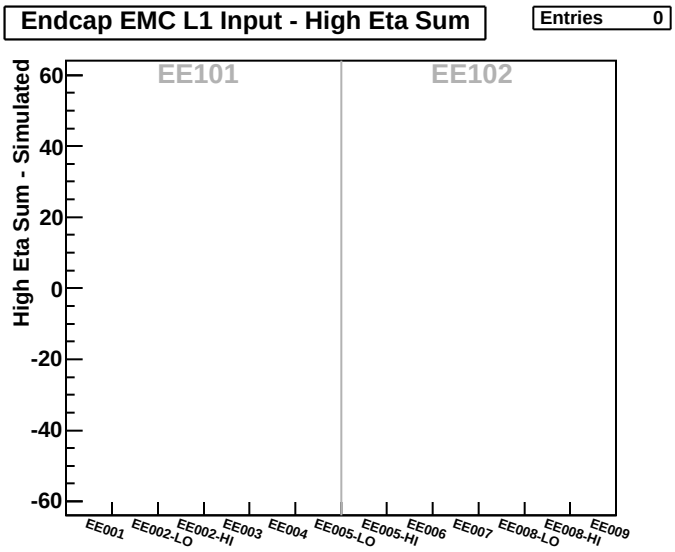

Barrel EMC L1 Input - High Tower Bits
Entries 1590228
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

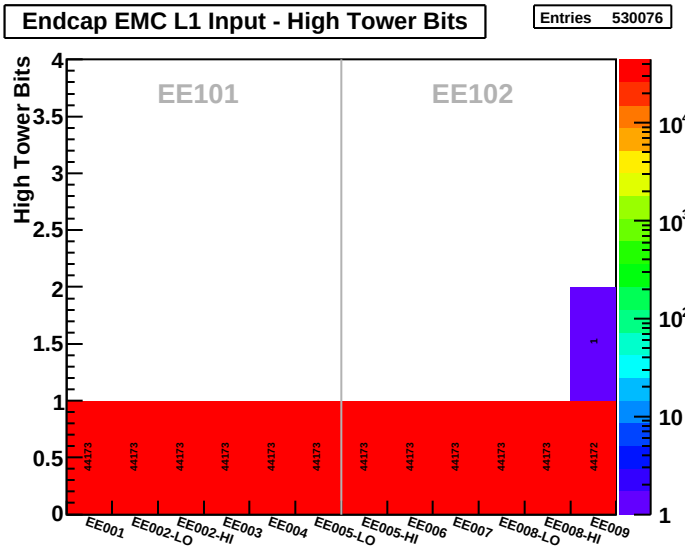

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

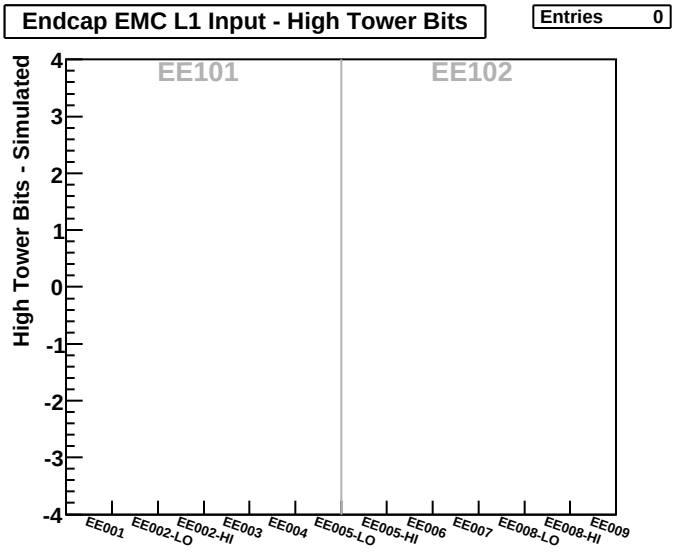

Endcap EMC L1 Input - Low Eta Sum
Entries 530076
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

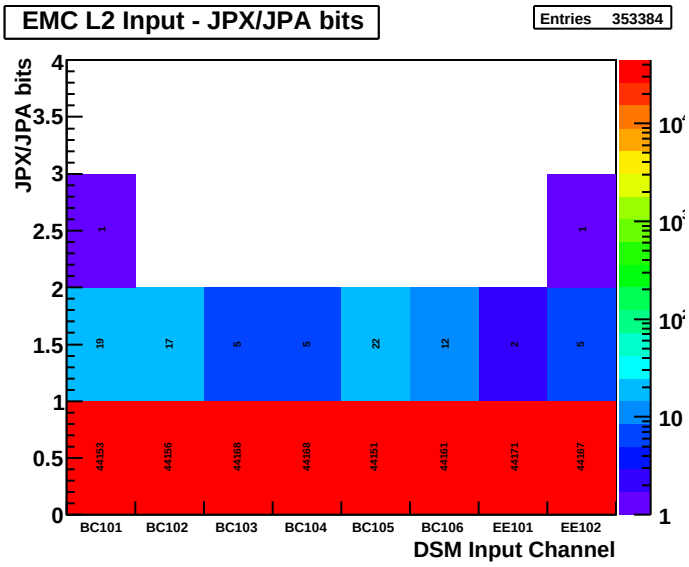

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

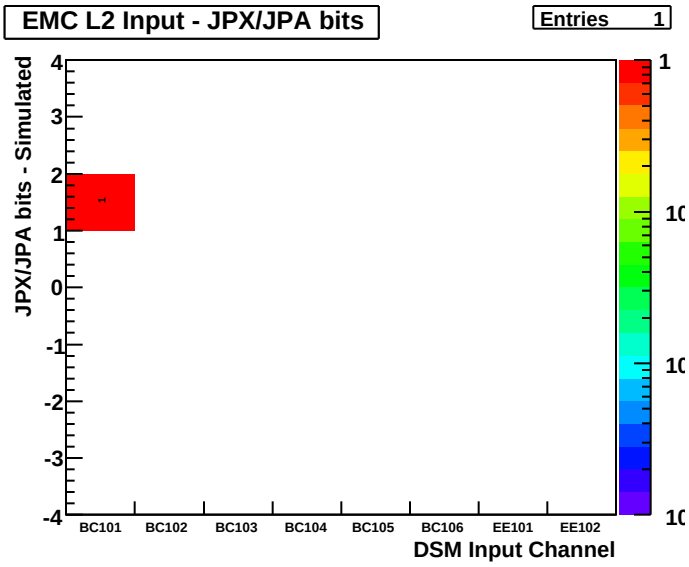

Endcap EMC L1 Input - High Eta Sum
Entries 530076
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

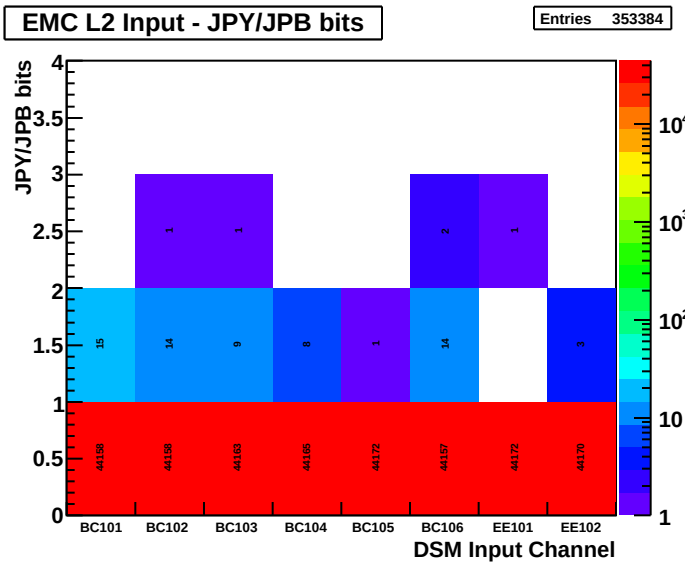

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

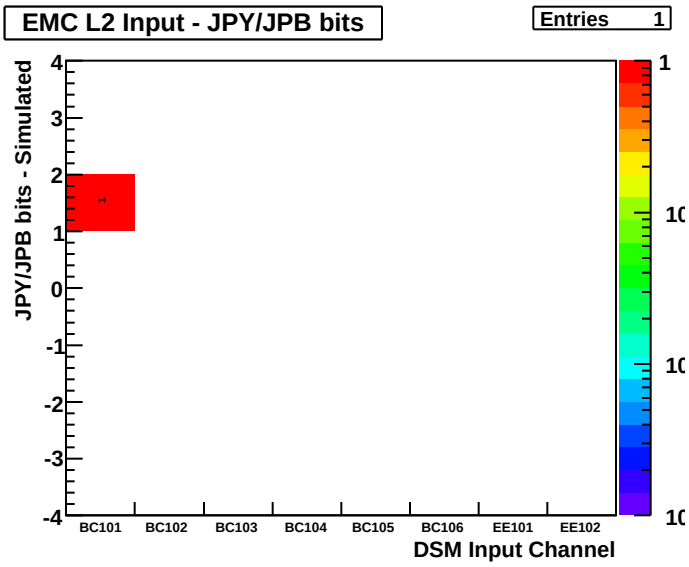

Endcap EMC L1 Input - High Tower Bits
Entries 530076
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

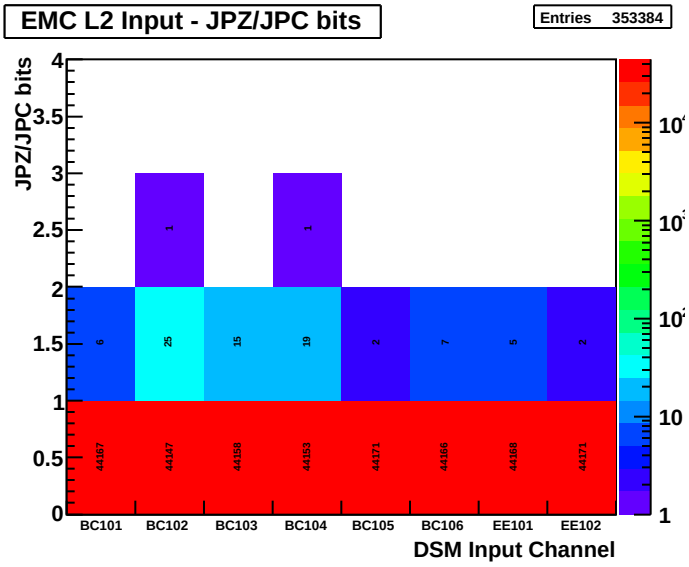

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

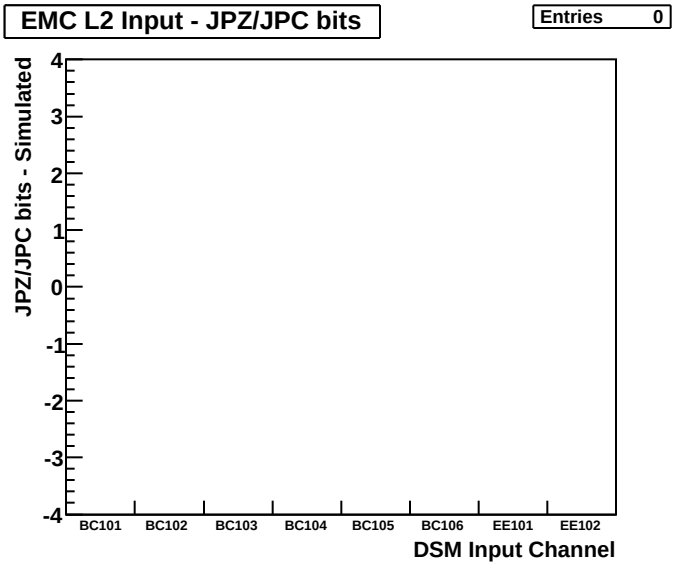

EMC L2 Input - JPX/JPA bits
Entries 353384
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

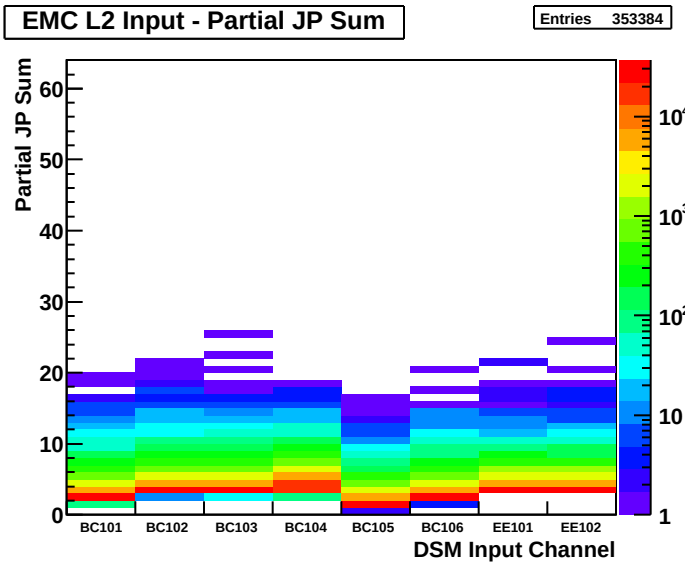

EMC L2 Input - JPX/JPA bits
Entries 1
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

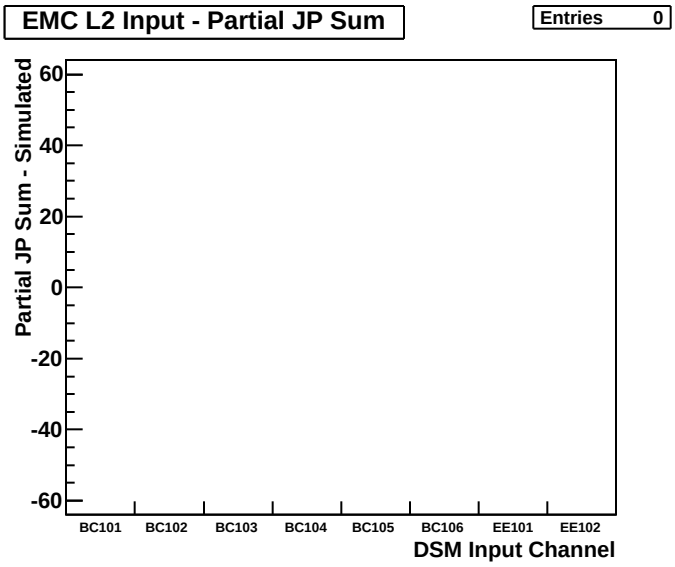

EMC L2 Input - JPY/JPB bits
Entries 353384
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

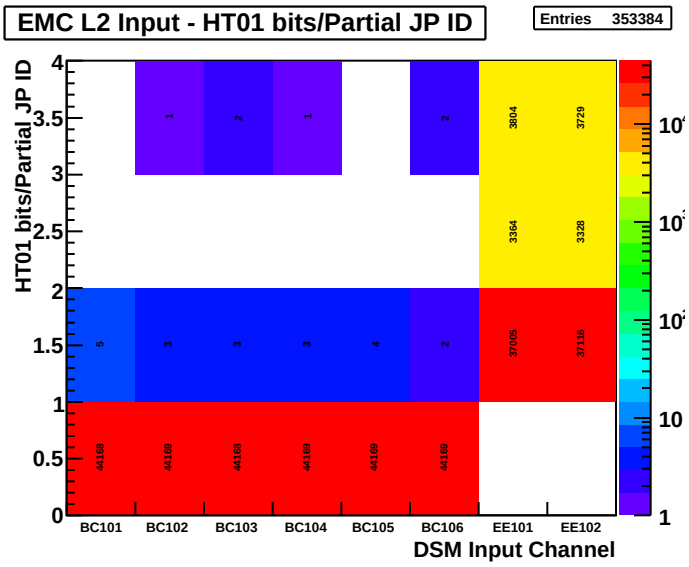

EMC L2 Input - JPY/JPB bits
Entries 1
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

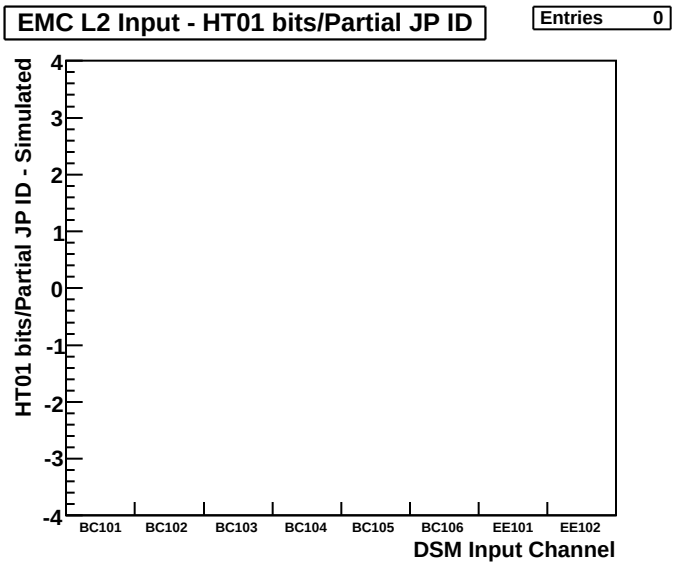

EMC L2 Input - JPZ/JPC bits
Entries 353384
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

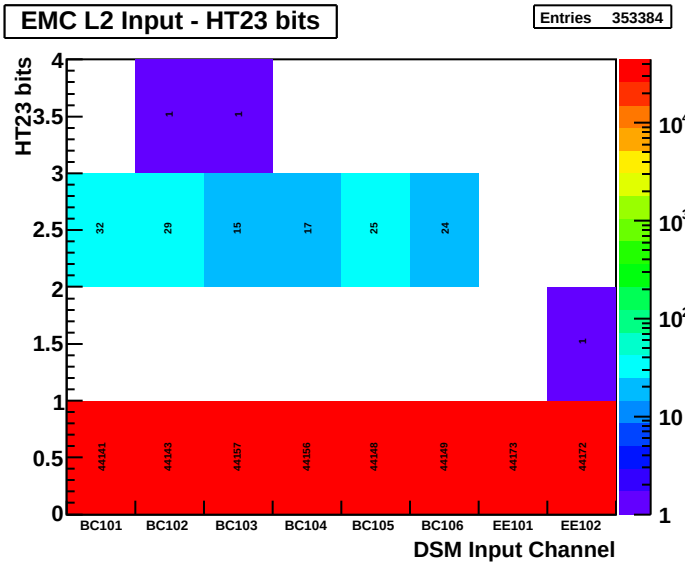

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

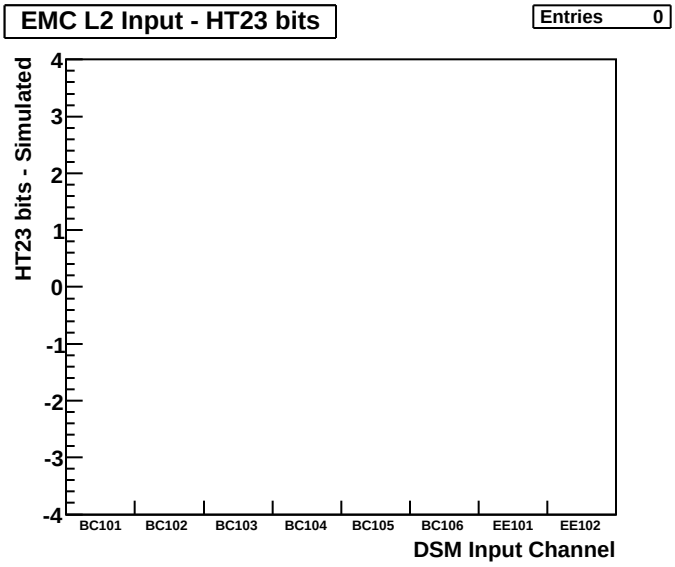

EMC L2 Input - Partial JP Sum
Entries 353384
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

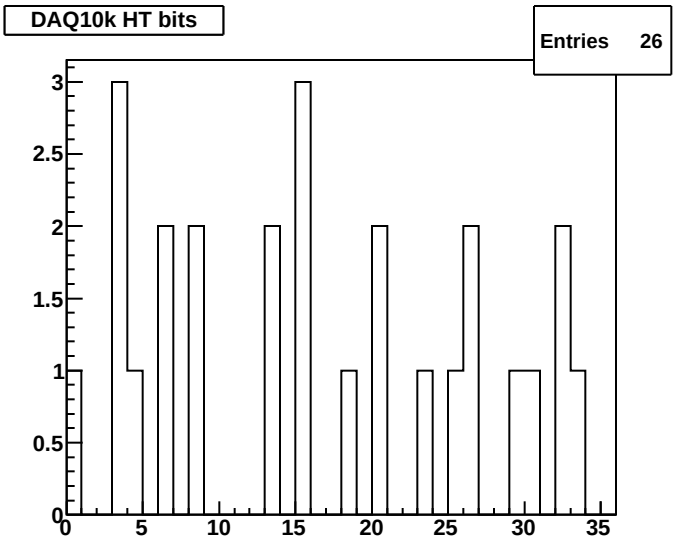

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

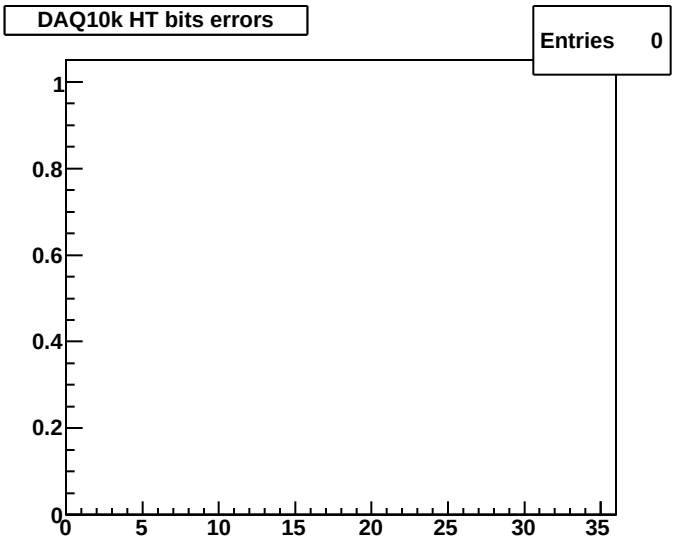

EMC L2 Input - HT01 bits/Partial JP ID
Entries 353384
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

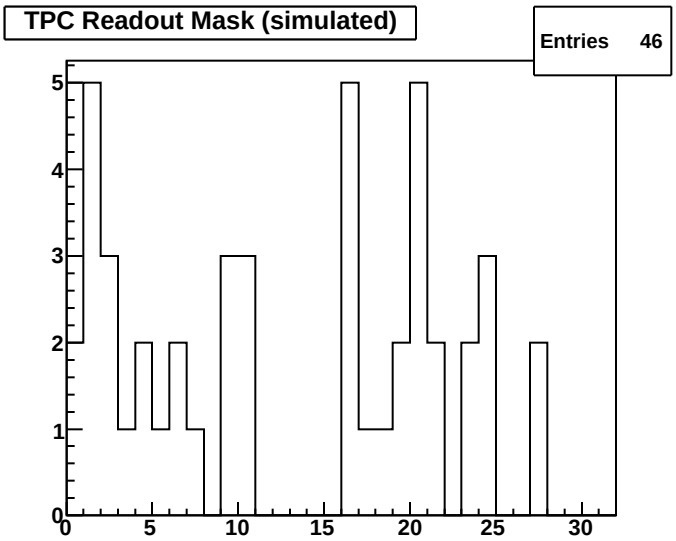

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

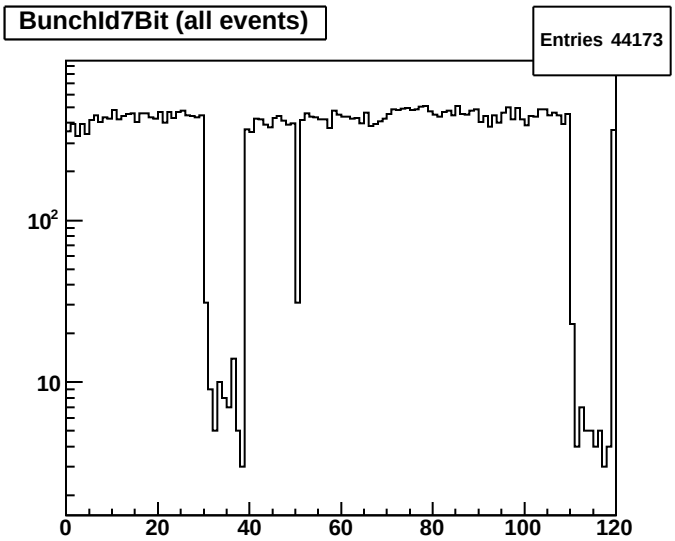

EMC L2 Input - HT23 bits
Entries 353384
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

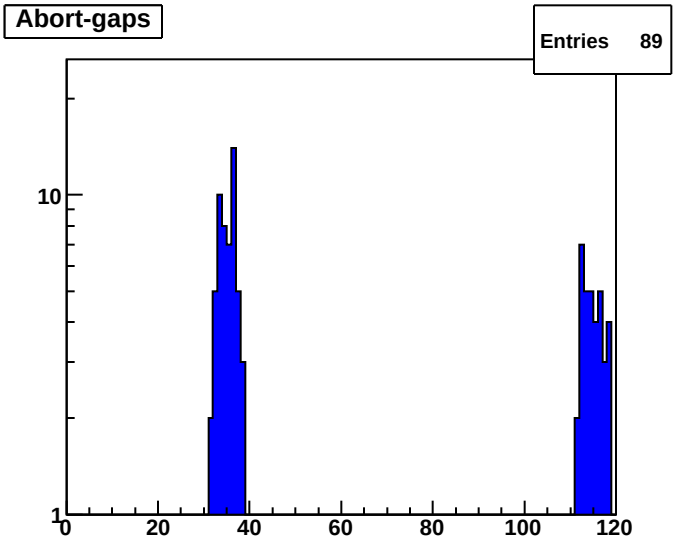

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

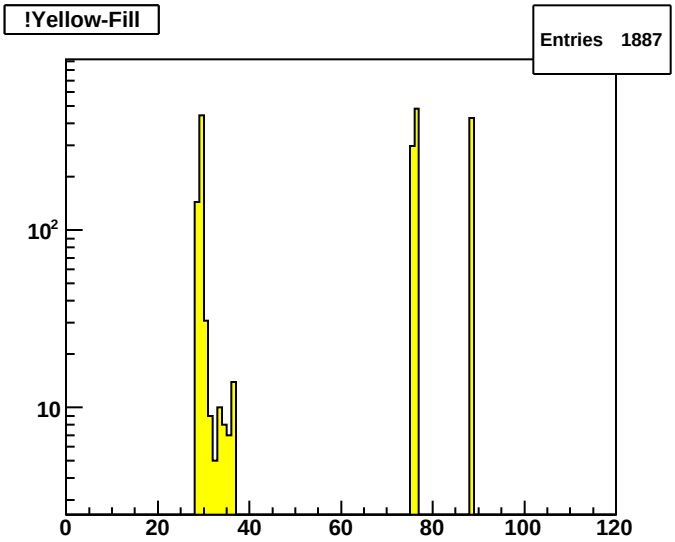

DAQ10k HT bits
Entries 26

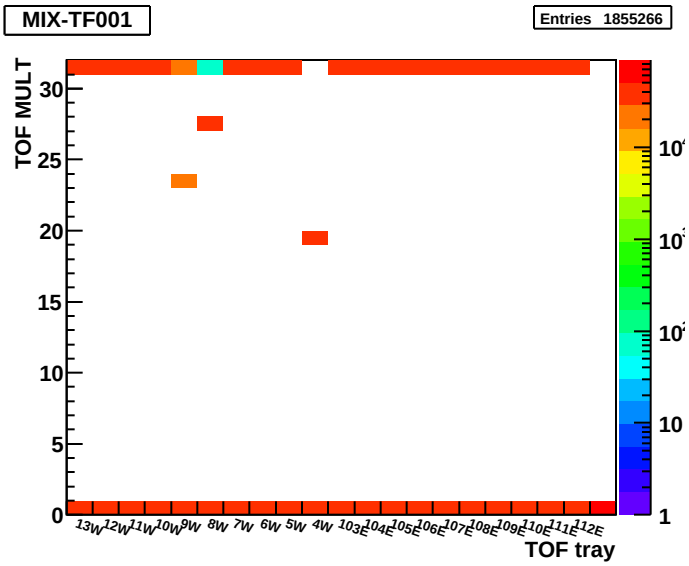

DAQ10k HT bits errors
Entries 0

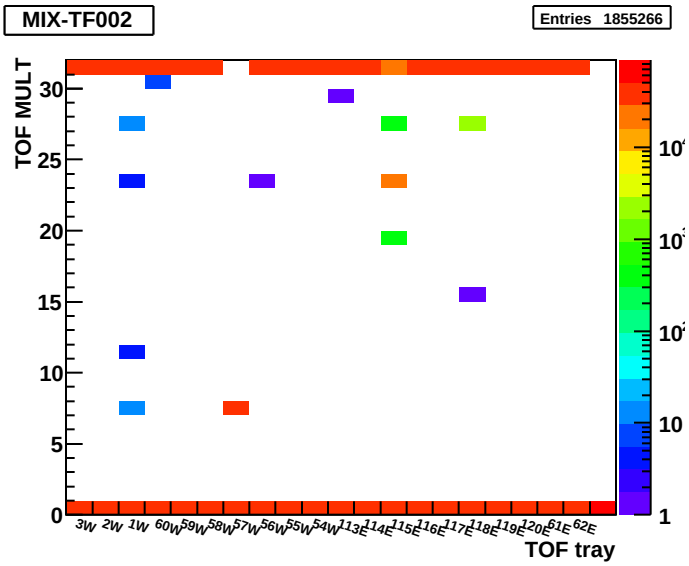

TPC Readout Mask (simulated)
Entries 46

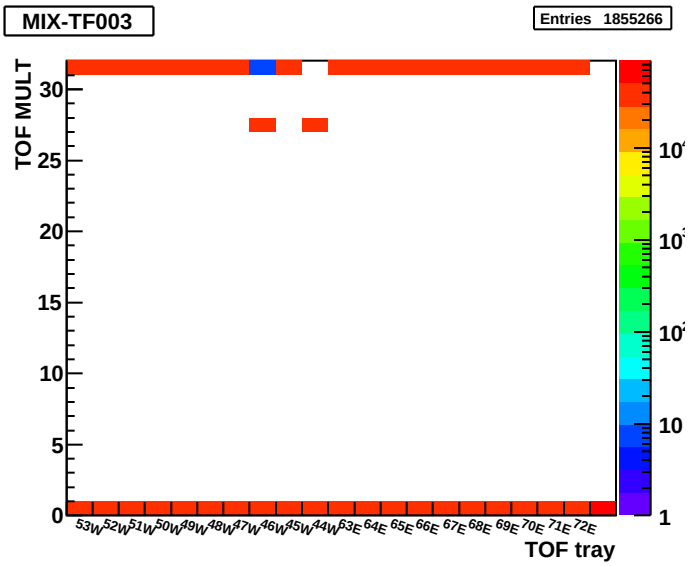

BunchId7Bit (all events)
Entries 44173

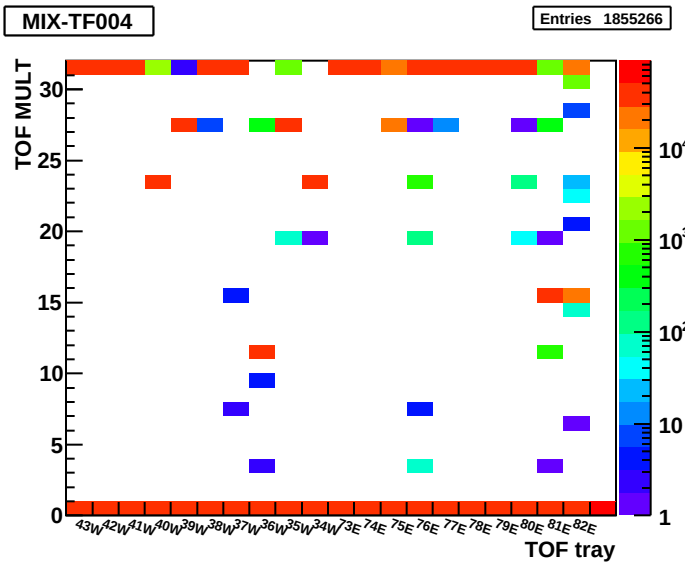

Abort-gaps
Entries 89

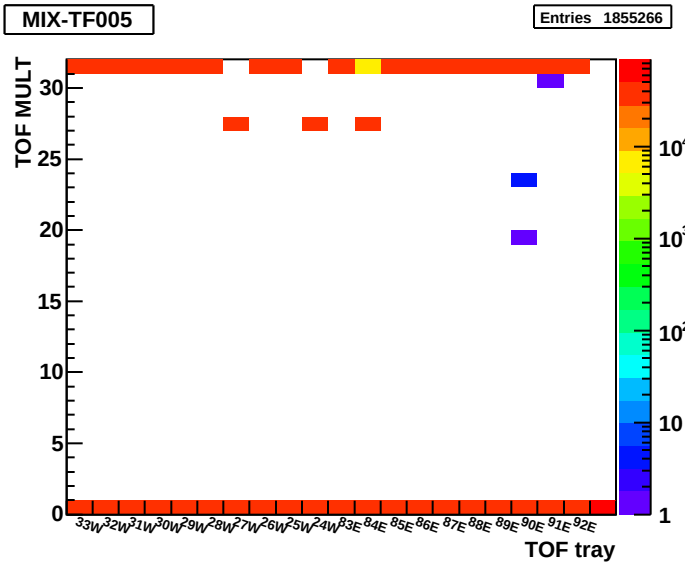

!Yellow-Fill
Entries 1887

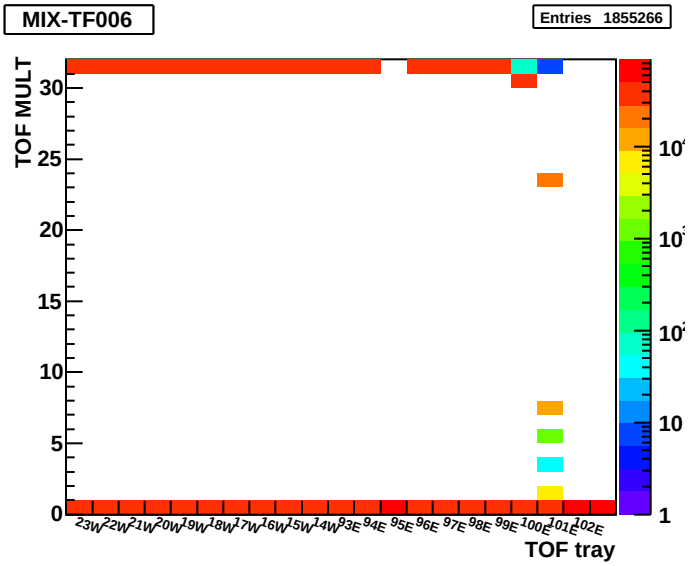

MIX-TF001
Entries 1855266
TOF MULT
TOF tray

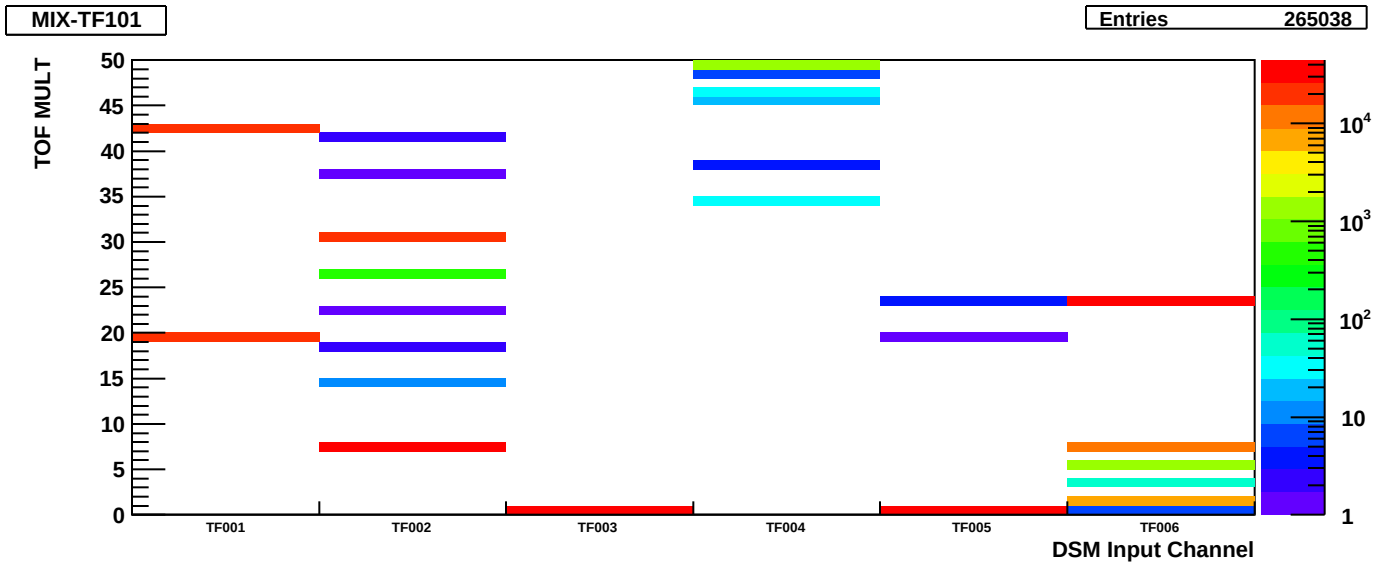

MIX-TF002
Entries 1855266
TOF MULT
TOF tray

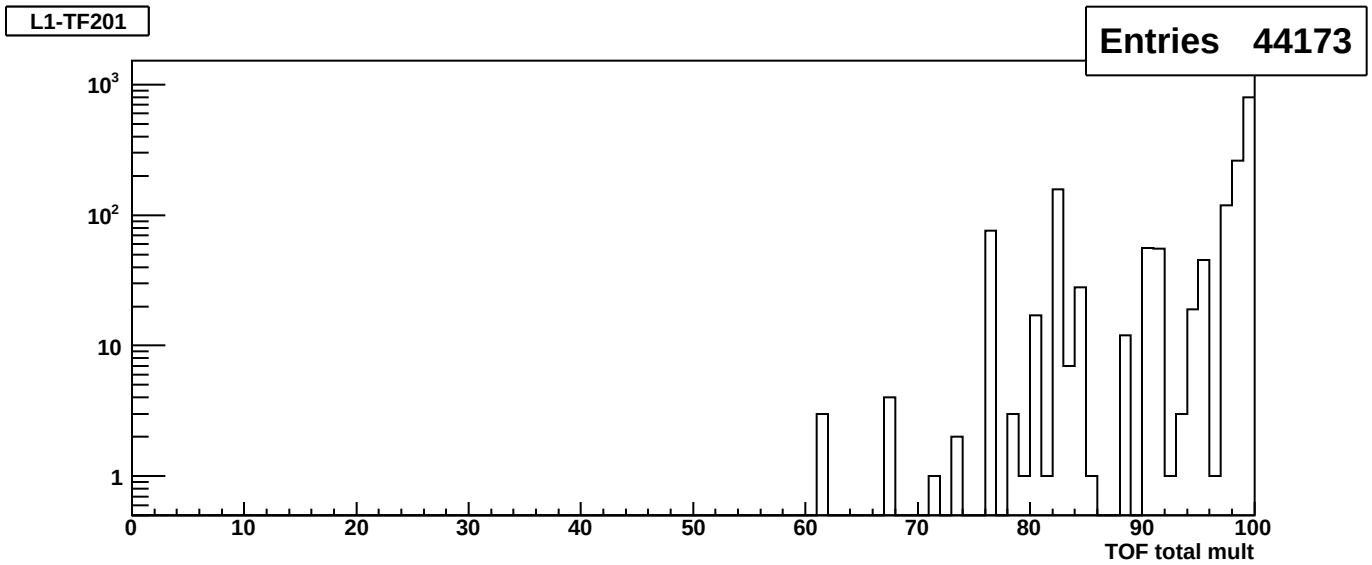

MIX-TF003
Entries 1855266
TOF MULT
TOF tray

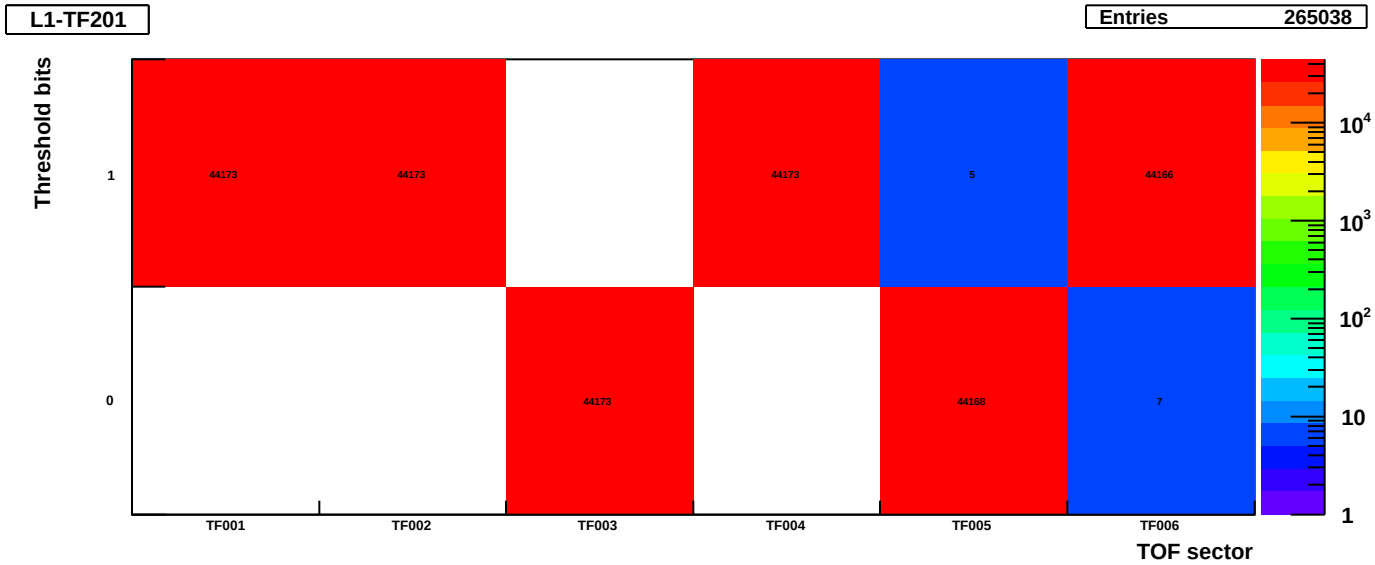

MIX-TF004
Entries 1855266
TOF MULT
TOF tray

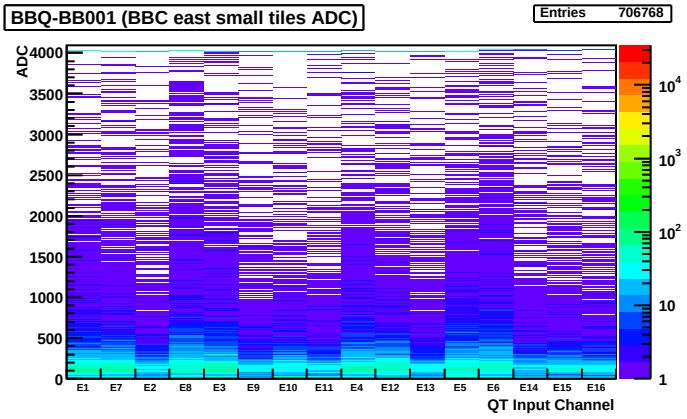

MIX-TF005
Entries 1855266
TOF MULT
TOF tray

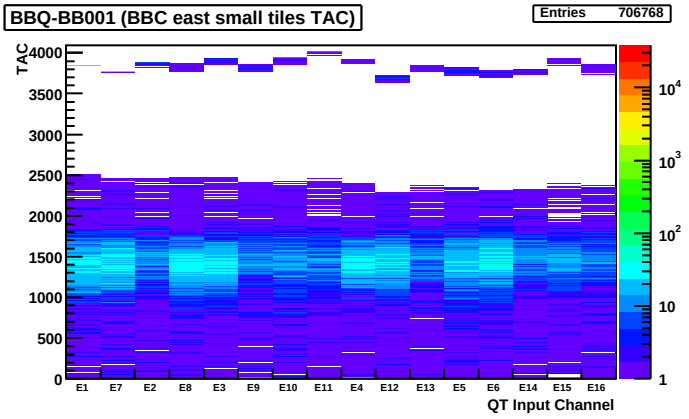

MIX-TF006
Entries 1855266
TOF MULT
TOF tray

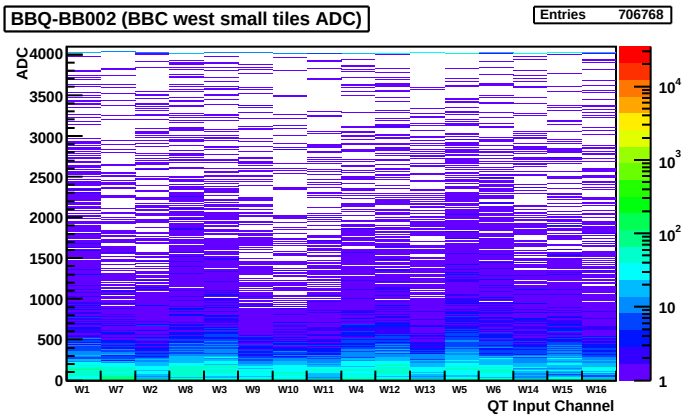

MIX-TF101
Entries 265038
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

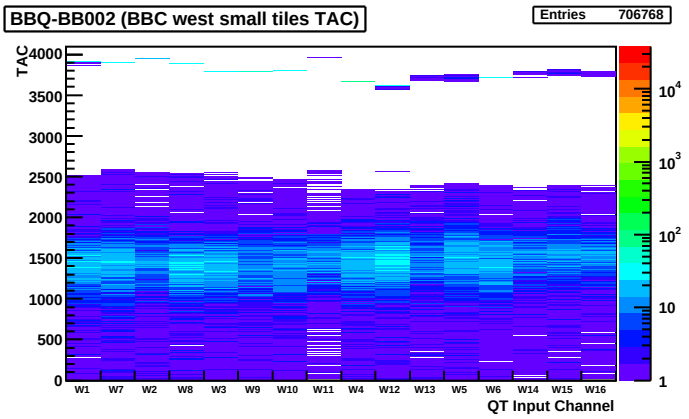

L1-TF201
Entries 44173
TOF total mult

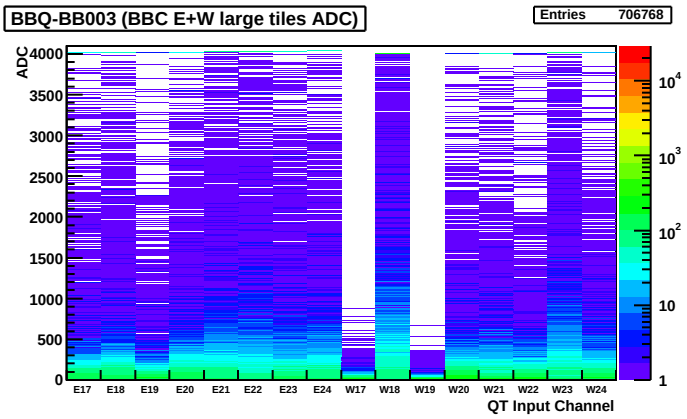

L1-TF201
Entries 265038
Threshold bits
TOF sector
TF001
TF002
TF003
TF004
TF005
TF006
44173
44173
44173
5
44166
44173
44168
7

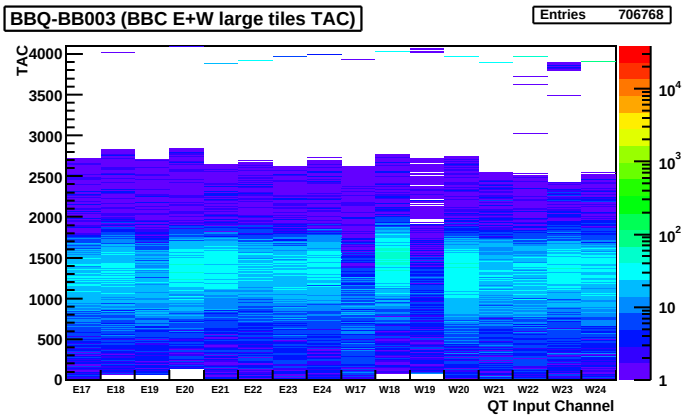

BBQ-BB001 (BBC east small tiles ADC)
Entries 706768
ADC
QT Input Channel

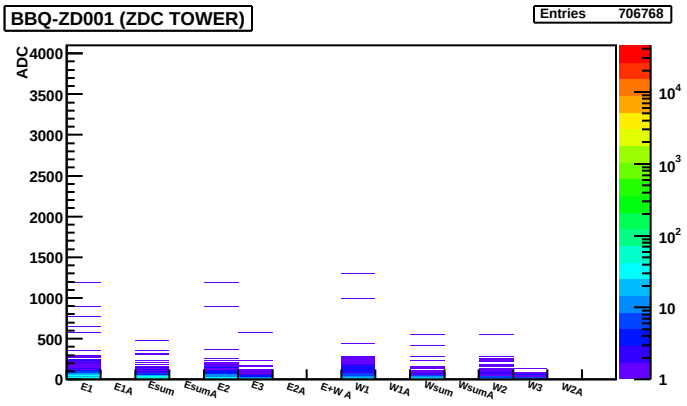

BBQ-BB001 (BBC east small tiles TAC)
Entries 706768
TAC
QT Input Channel

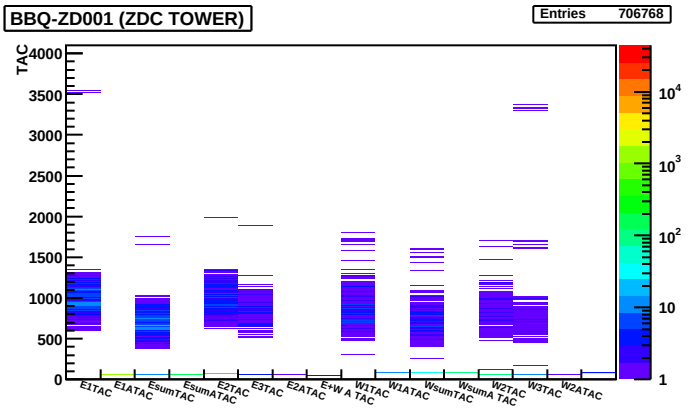

BBQ-BB002 (BBC west small tiles ADC)
Entries 706768
ADC
QT Input Channel

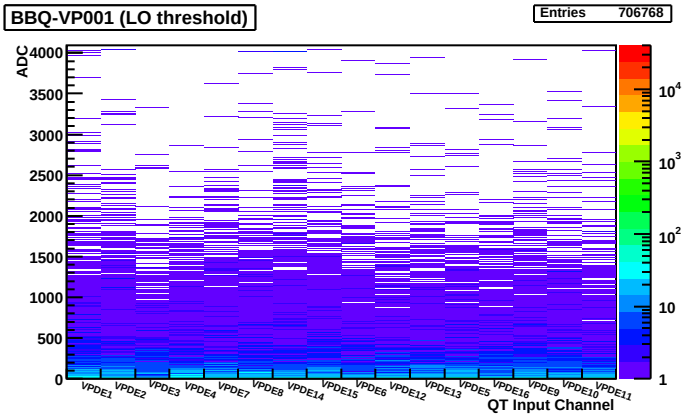

BBQ-BB002 (BBC west small tiles TAC)
Entries 706768
TAC
QT Input Channel

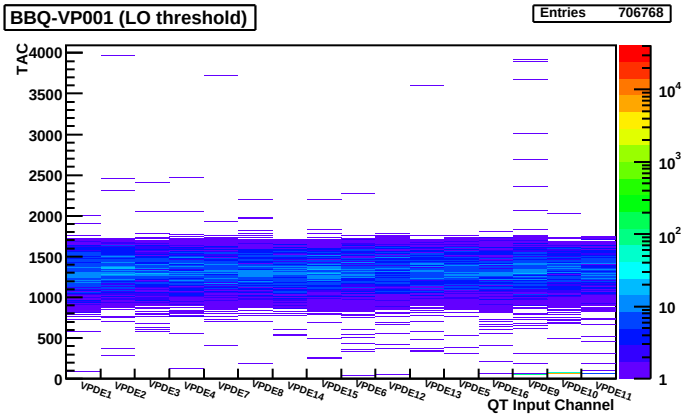

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 706768
ADC
QT Input Channel

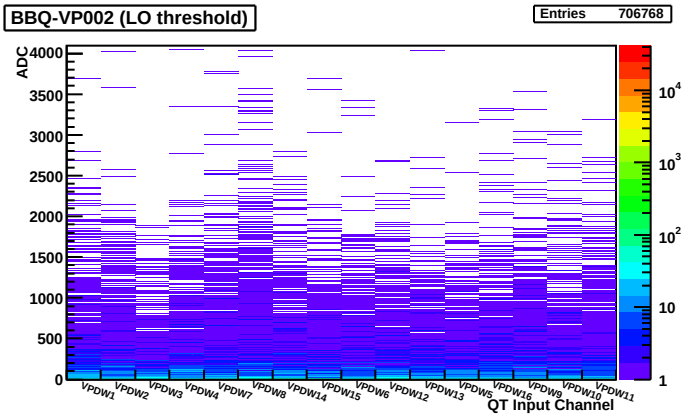

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 706768
TAC
QT Input Channel

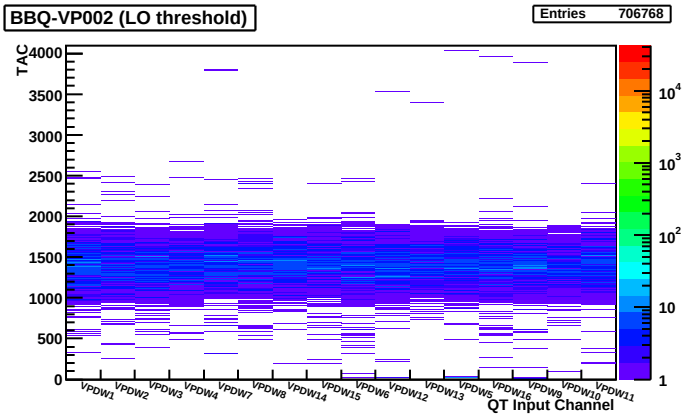

BBQ-ZD001 (ZDC TOWER)
Entries 706768
ADC

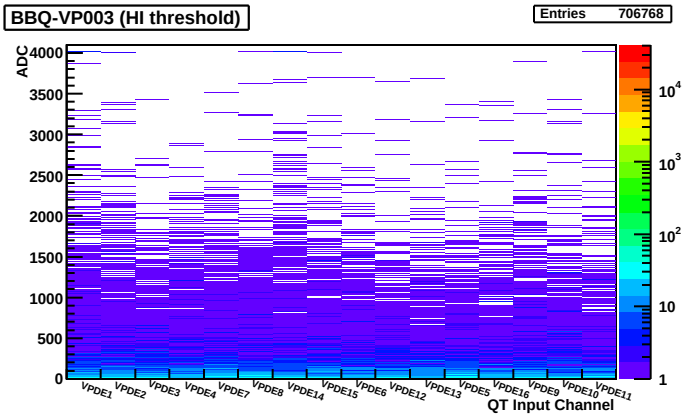

BBQ-ZD001 (ZDC TOWER)
Entries 706768
TAC

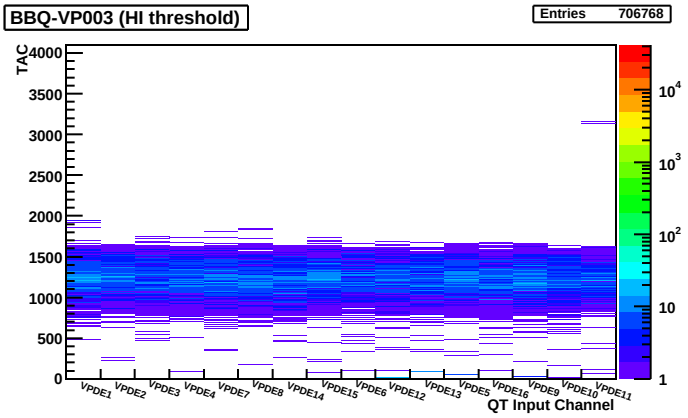

BBQ-VP001 (LO threshold)
Entries 706768
ADC
QT Input Channel

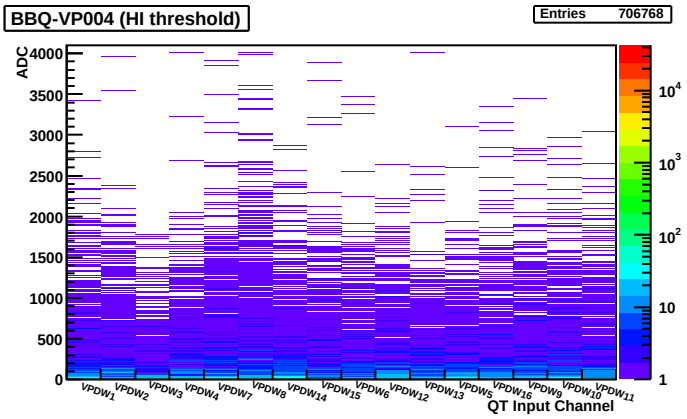

BBQ-VP001 (LO threshold)
Entries 706768
TAC
QT Input Channel

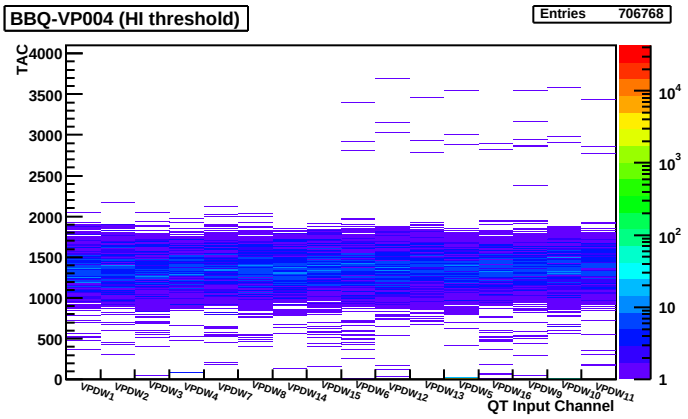

BBQ-VP002 (LO threshold)
Entries 706768
ADC
QT Input Channel

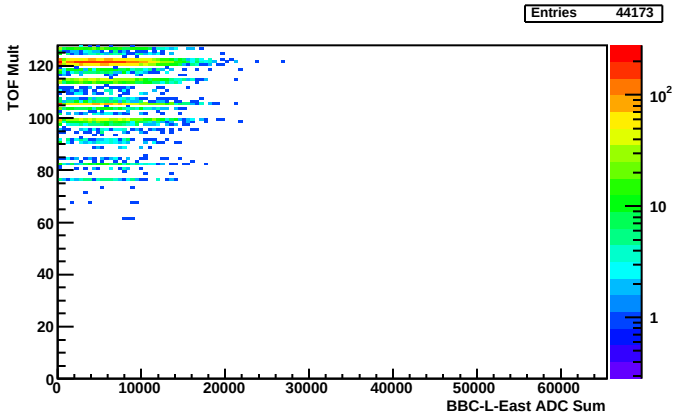

BBQ-VP002 (LO threshold)
Entries 706768
TAC
QT Input Channel

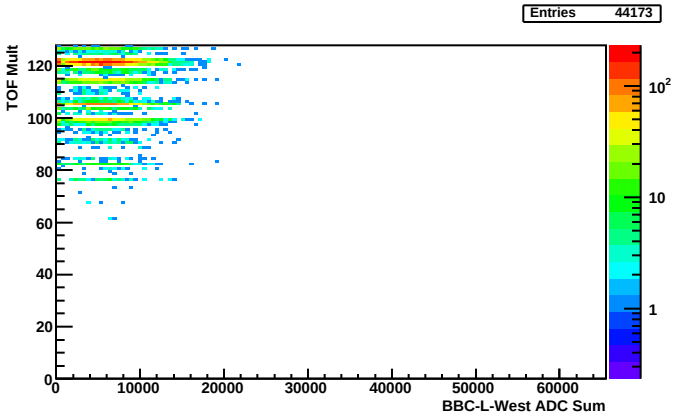

BBQ-VP003 (HI threshold)
Entries 706768
ADC
QT Input Channel

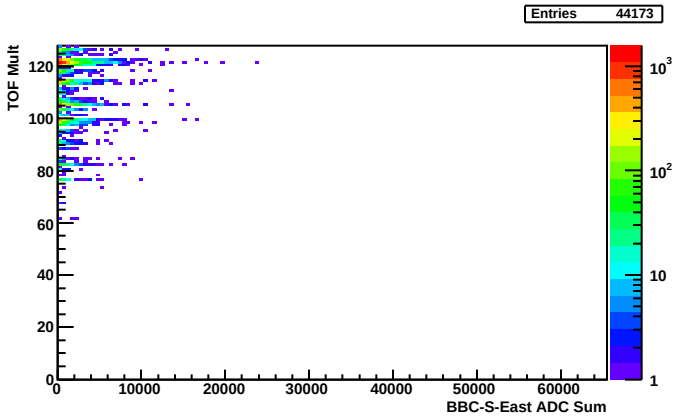

BBQ-VP003 (HI threshold)
Entries 706768
TAC
QT Input Channel

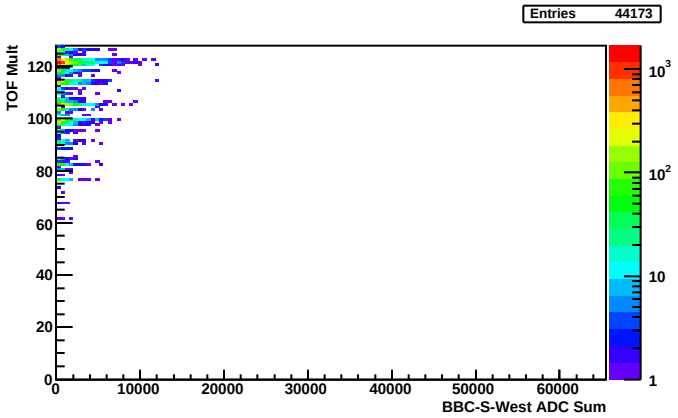

BBQ-VP004 (HI threshold)
Entries 706768
ADC
QT Input Channel

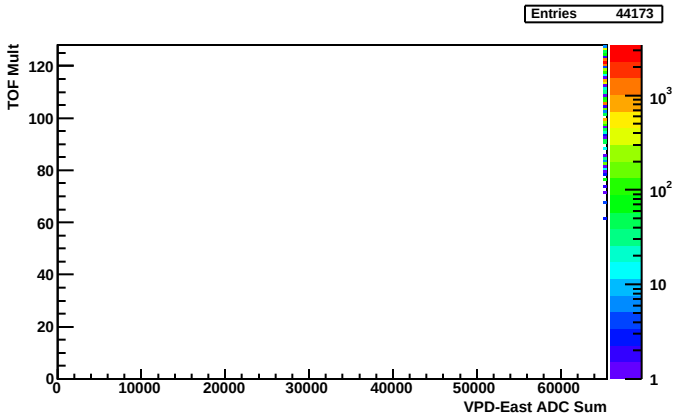

BBQ-VP004 (HI threshold)
Entries 706768
TAC
QT Input Channel

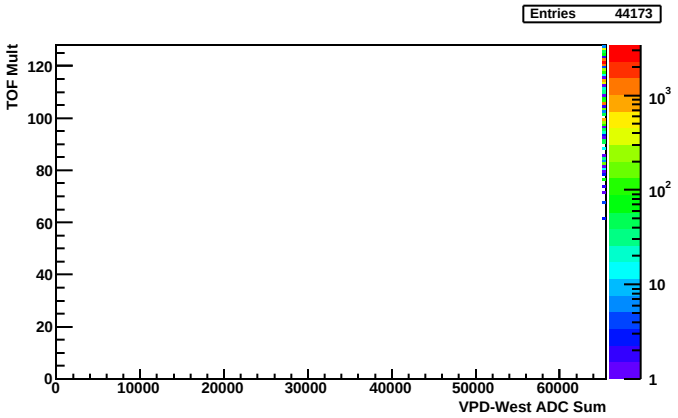

Entries 44173
TOF Mult
BBC-L-East ADC Sum

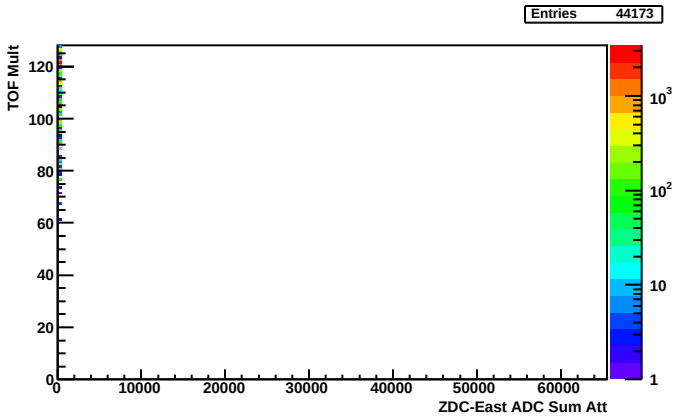

Entries 44173
TOF Mult
BBC-L-West ADC Sum

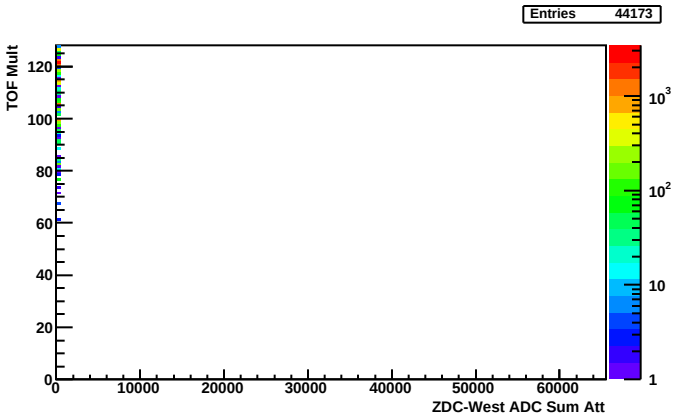

Entries 44173
TOF Mult
BBC-S-East ADC Sum

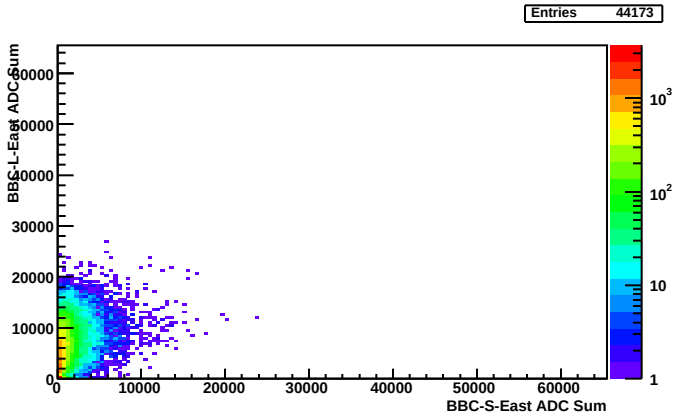

Entries 44173
TOF Mult
BBC-S-West ADC Sum

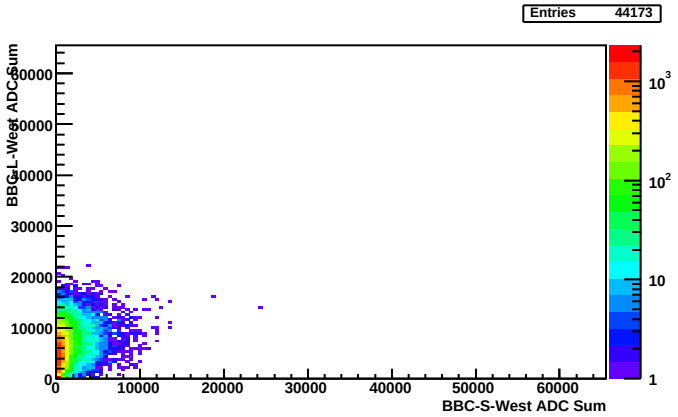

Entries 44173
TOF Mult
VPD-East ADC Sum

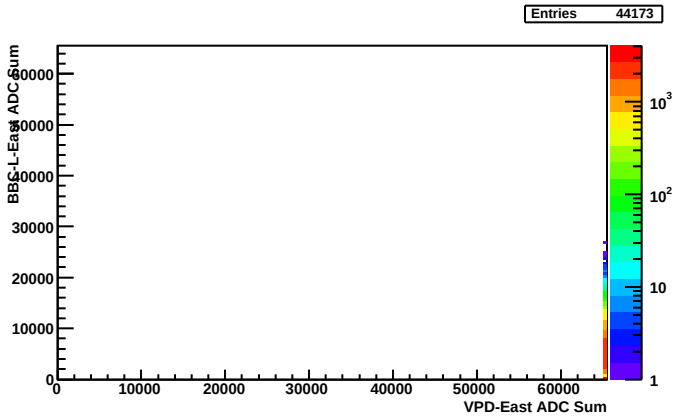

Entries 44173
TOF Mult
VPD-West ADC Sum

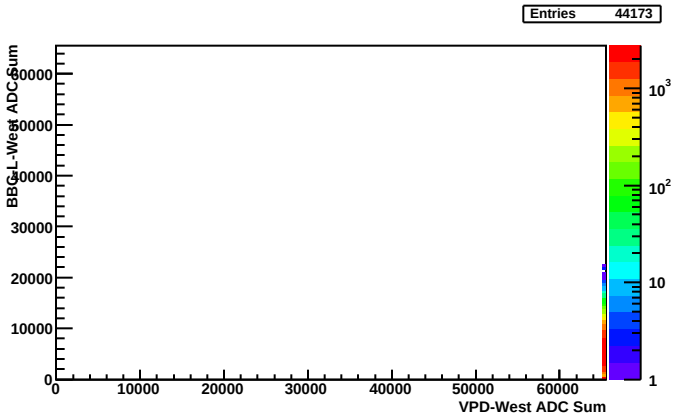

Entries 44173
TOF Mult
ZDC-East ADC Sum Att

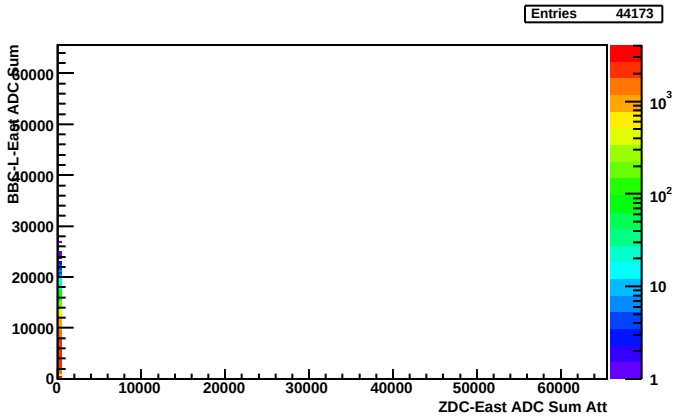

Entries 44173
TOF Mult
ZDC-West ADC Sum Att

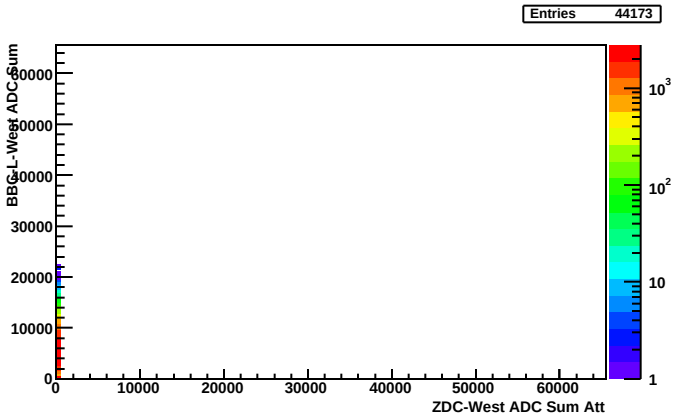

Entries 44173
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 44173
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 44173
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 44173
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 44173
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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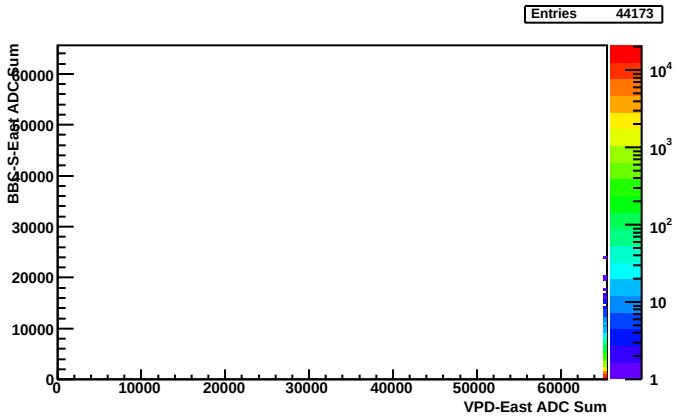
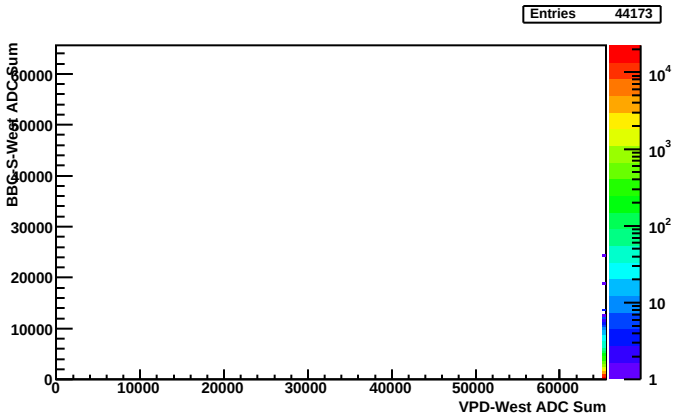
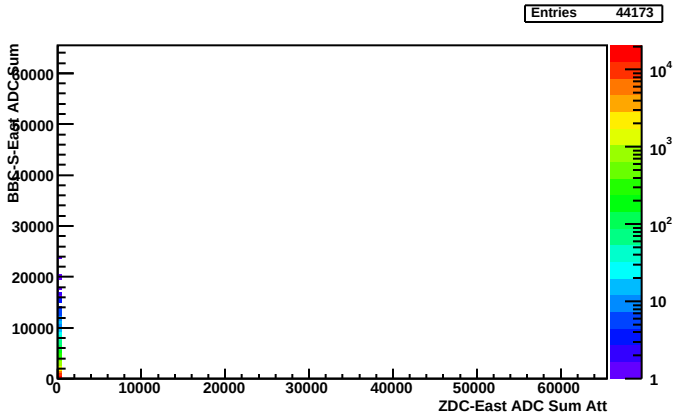
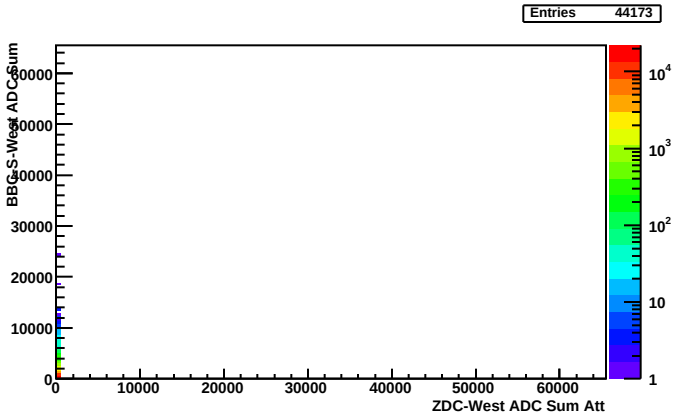
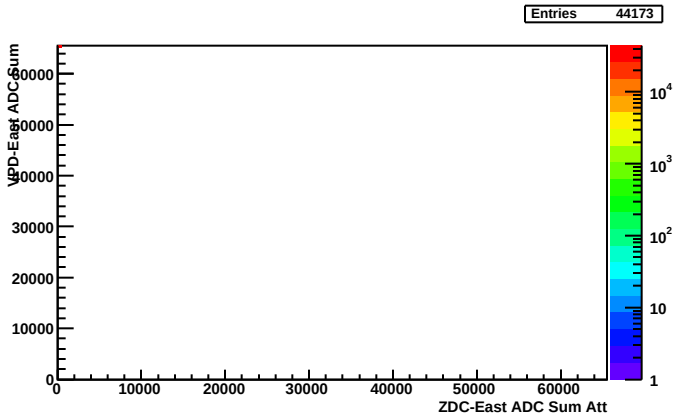
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

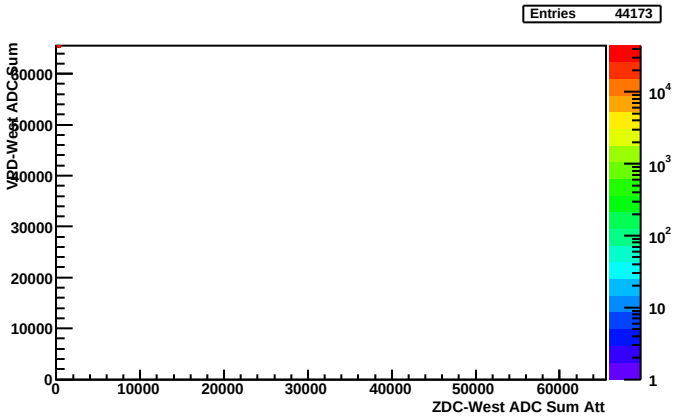

Entries 44173
BBC-S-East ADC Sum
VPD-East ADC Sum

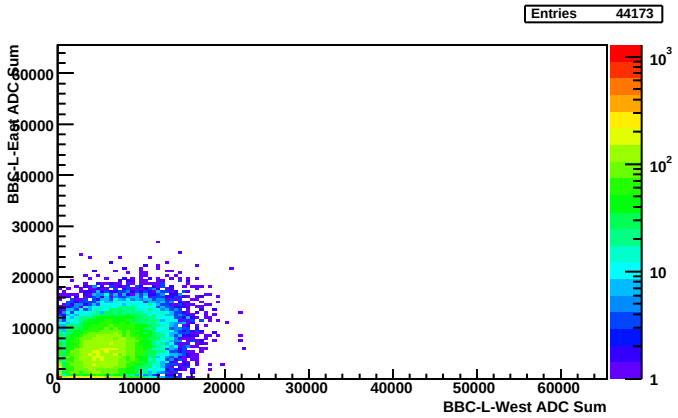

Entries 44173
BBC-S-West ADC Sum
VPD-West ADC Sum

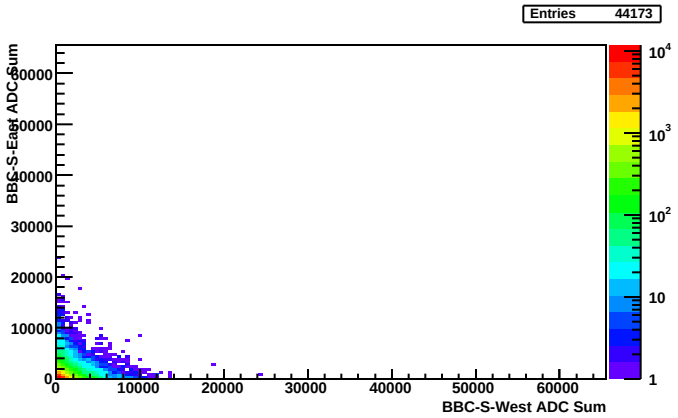

Entries 44173
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

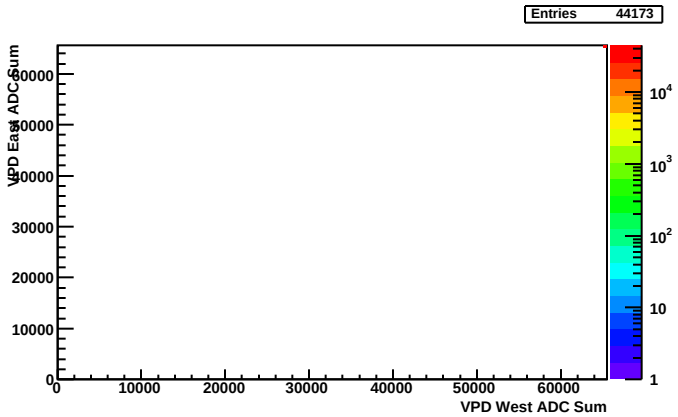

Entries 44173
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

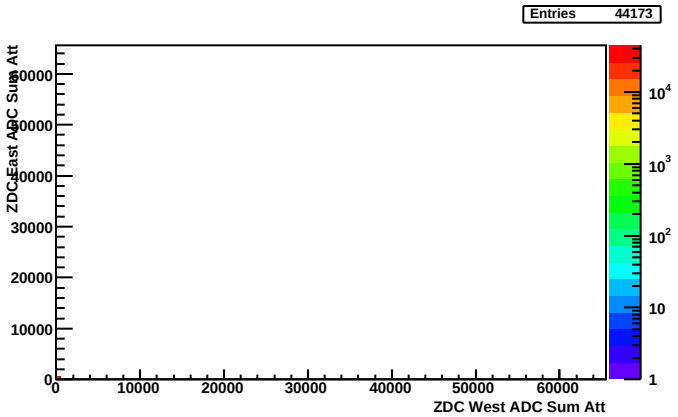

Entries 44173
VPD-East ADC Sum
ZDC-East ADC Sum Att

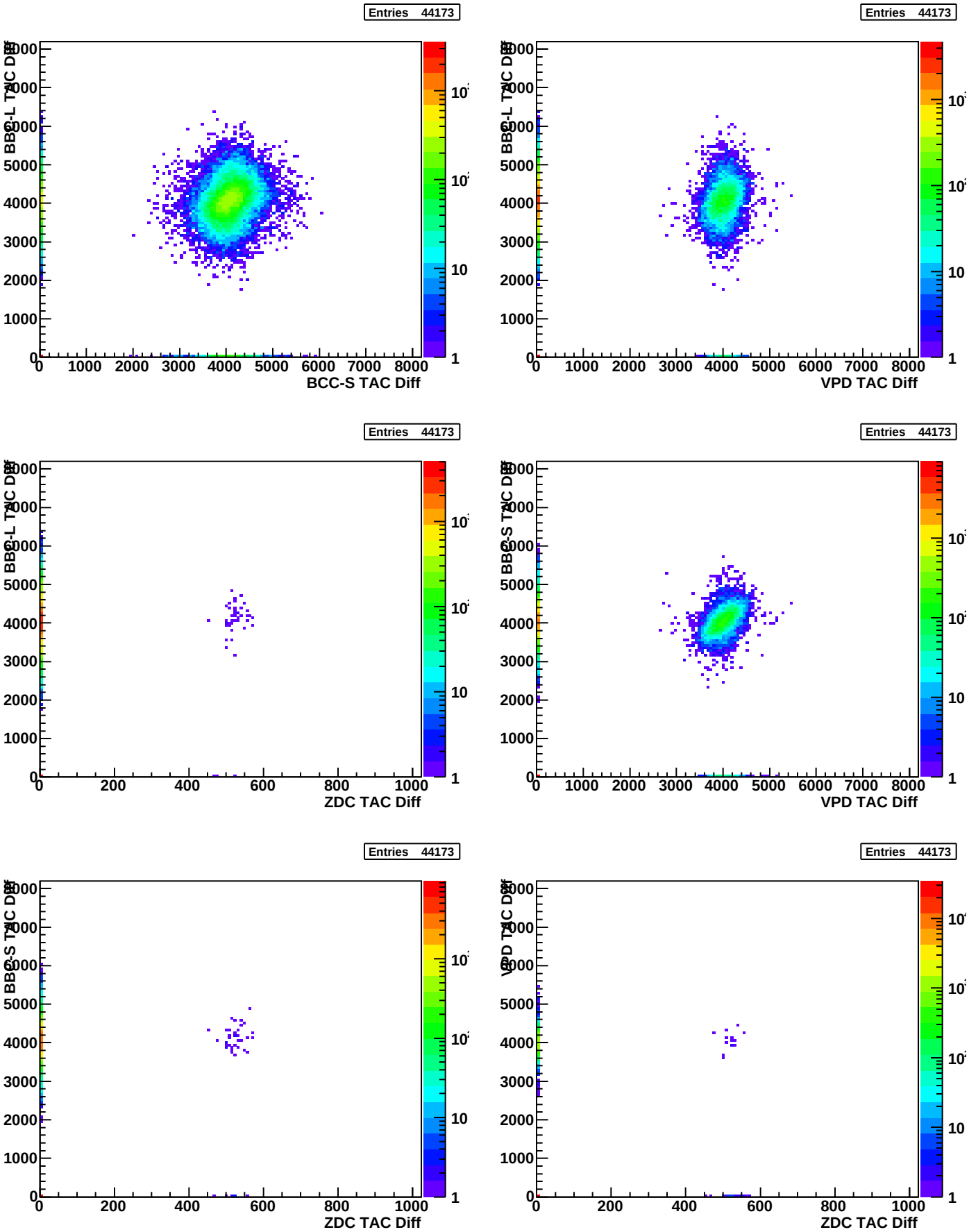

Entries 44173
VPD-West ADC Sum
ZDC-West ADC Sum Att

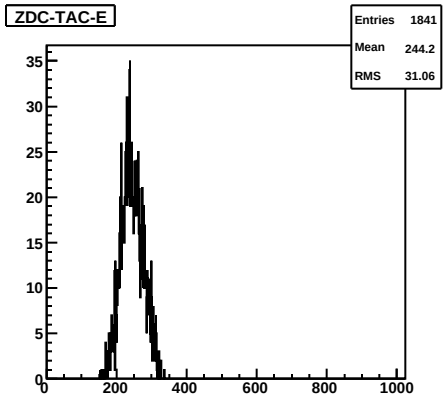

Entries 44173
BBC-L-East ADC Sum
BBC-L-West ADC Sum

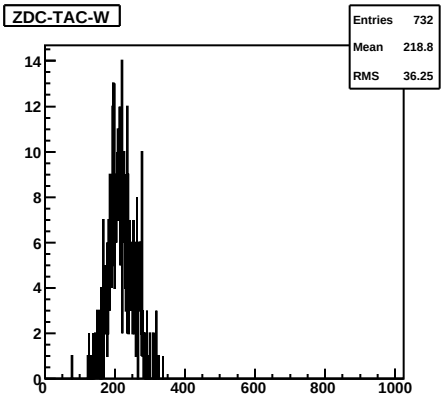

Entries 44173
BBC-S-East ADC Sum
BBC-S-West ADC Sum

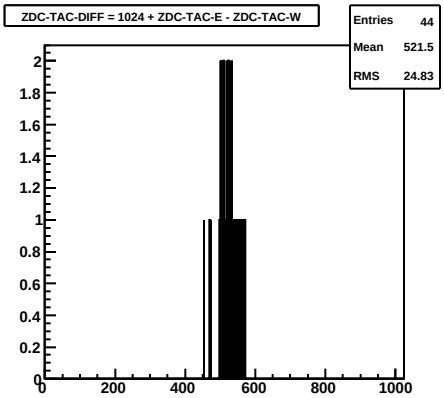

Entries 44173
VPD East ADC Sum
VPD West ADC Sum

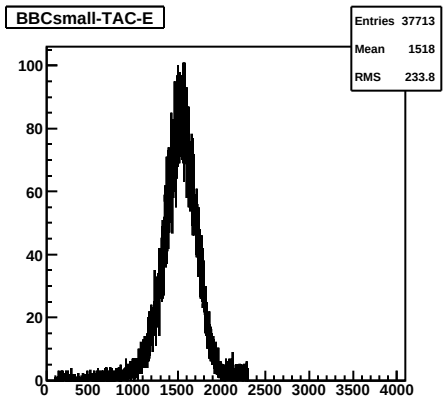

Entries 44173
ZDC East ADC Sum Att
ZDC West ADC Sum Att

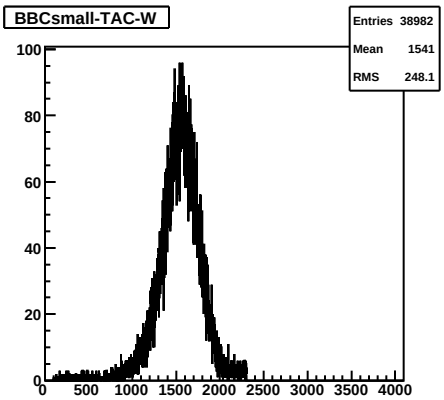

Entries 44173
BBC-L TAC Diff
BCC-S TAC Diff
Entries 44173
BBC-L TAC Diff
VPD TAC Diff
Entries 44173
BBC-L TAC Diff
ZDC TAC Diff
Entries 44173
BBC-S TAC Diff
VPD TAC Diff
Entries 44173
BBC-S TAC Diff
ZDC TAC Diff
Entries 44173
VPD TAC Diff
ZDC TAC Diff

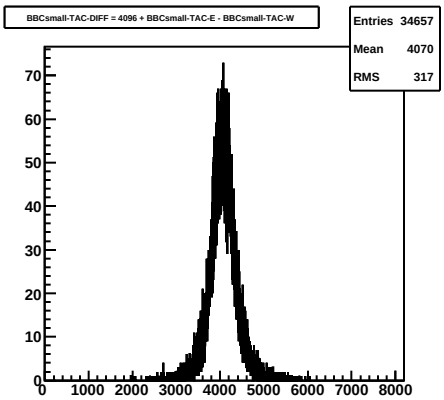

ZDC-TAC-E
Entries 1841
Mean 244.2
RMS 31.06

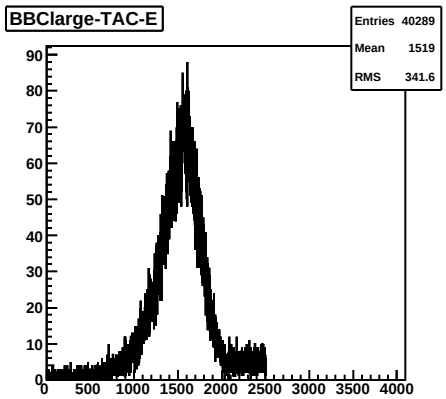

ZDC-TAC-W
Entries 732
Mean 218.8
RMS 36.25

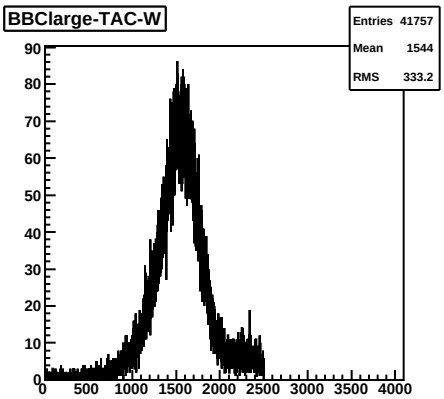

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 44
Mean 521.5
RMS 24.83

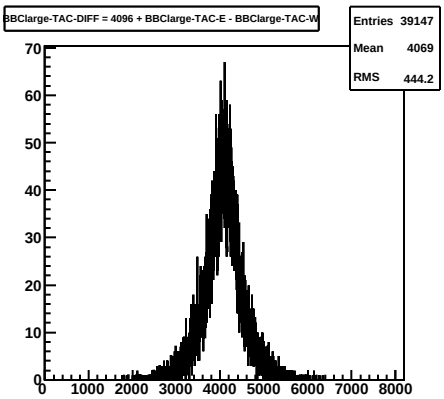

BBCsmall-TAC-E
Entries 37713
Mean 1518
RMS 233.8

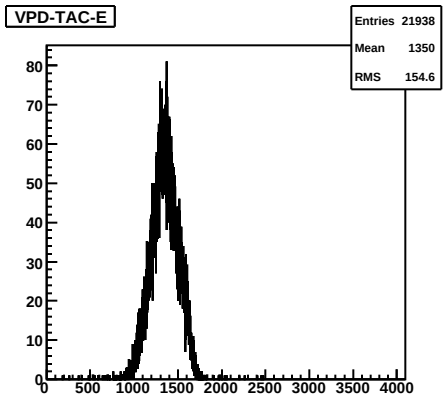

BBCsmall-TAC-W
Entries 38982
Mean 1541
RMS 248.1

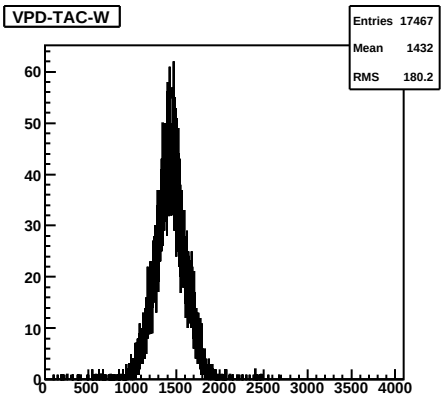

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 34657
Mean 4070
RMS 317

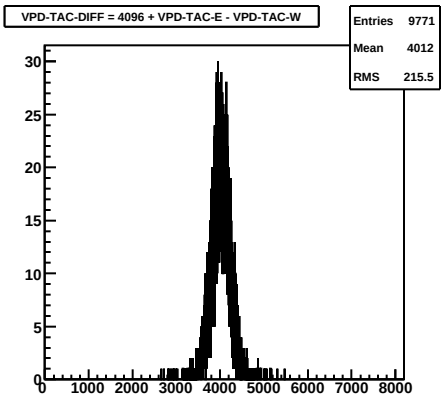

BBClarge-TAC-E
Entries 40289
Mean 1519
RMS 341.6

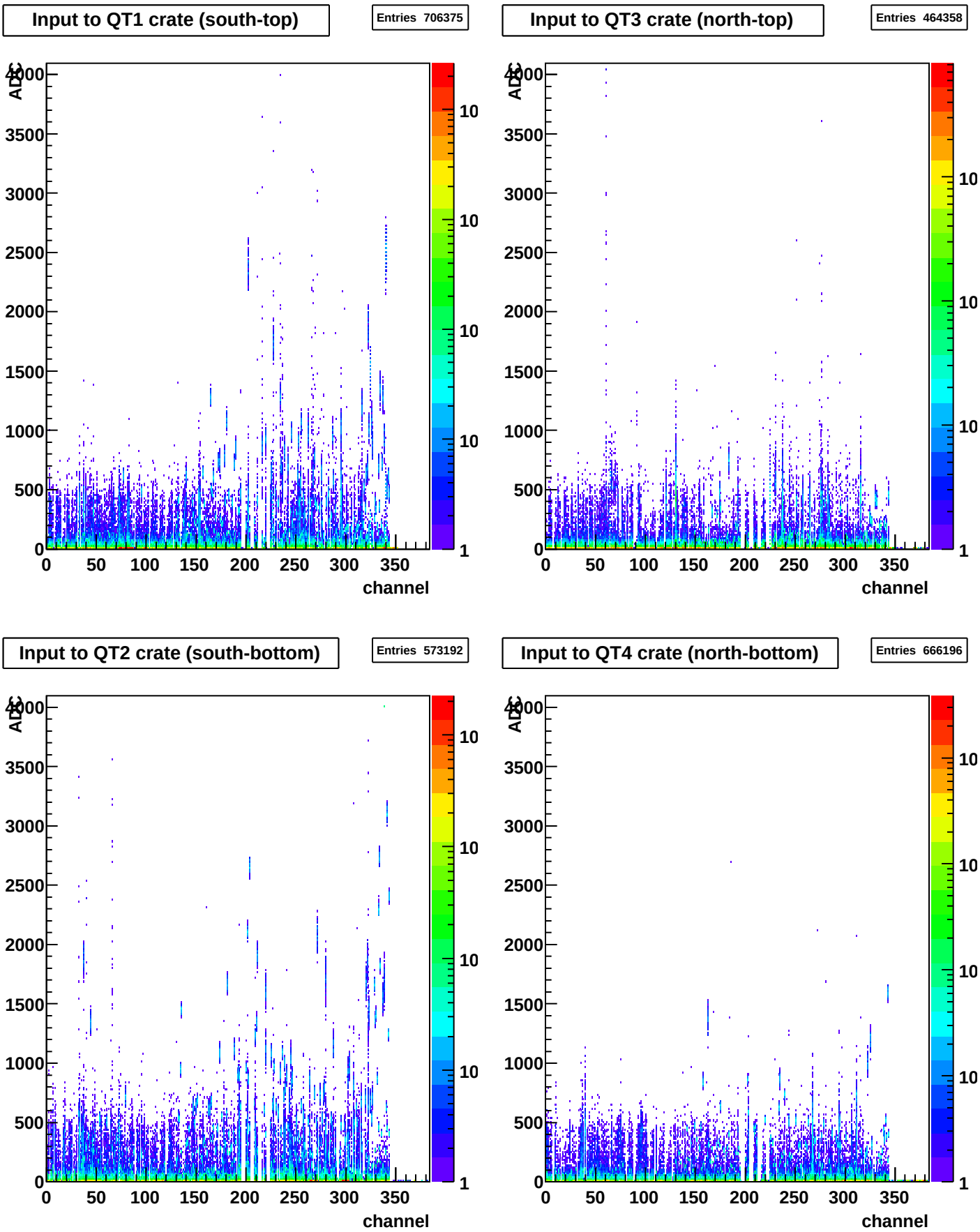

BBClarge-TAC-W
Entries 41757
Mean 1544
RMS 333.2

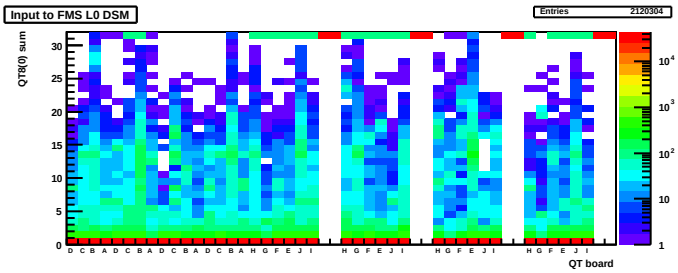

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 39147
Mean 4069
RMS 444.2

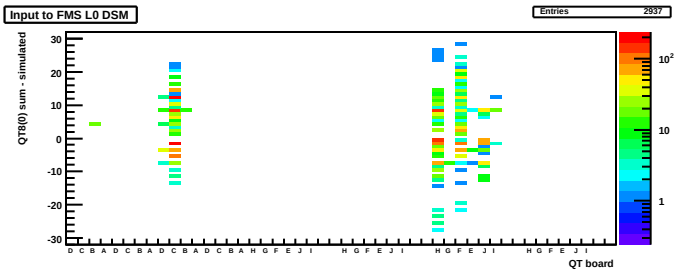

VPD-TAC-E
Entries 21938
Mean 1350
RMS 154.6

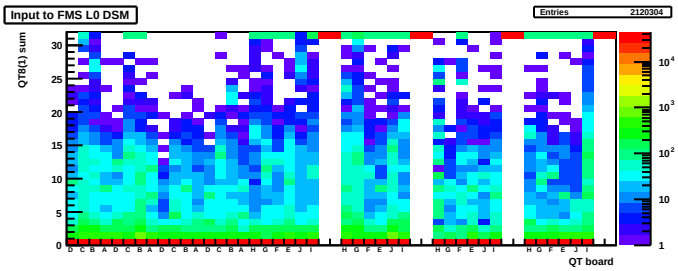

VPD-TAC-W
Entries 17467
Mean 1432
RMS 180.2

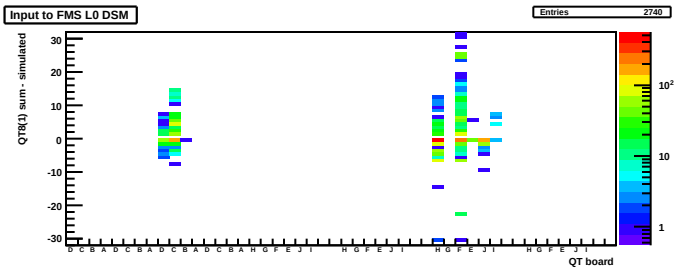

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 9771
Mean 4012
RMS 215.5

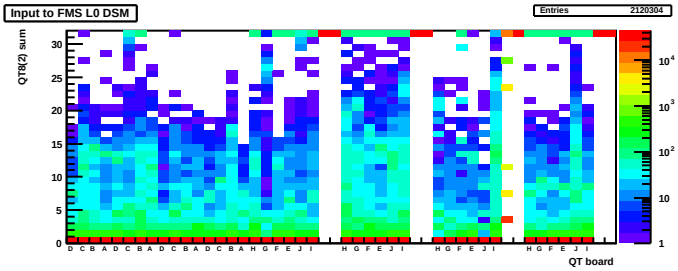

Input to QT1 crate (south-top)
Entries 706375
Input to QT3 crate (north-top)
Entries 464358
Input to QT2 crate (south-bottom)
Entries 573192
Input to QT4 crate (north-bottom)
Entries 666196
ADC
channel
0
500
1000
1500
2000
2500
3000
3500
4000
0
50
100
150
200
250
300
350
1
10

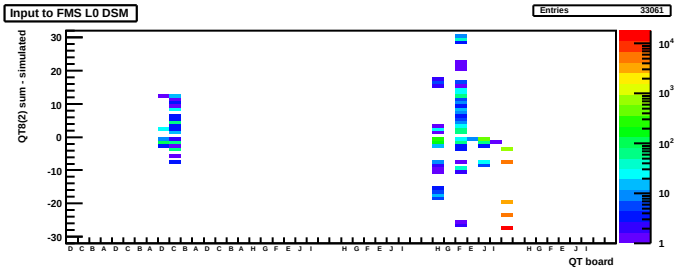

Input to FMS L0 DSM
Entries 2120304
QT8(0) sum
QT board

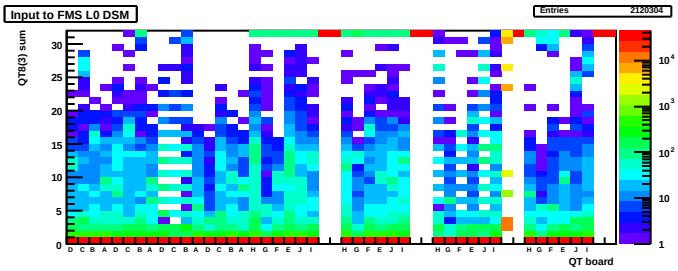

Input to FMS L0 DSM
Entries 2937
QT8(0) sum - simulated
QT board

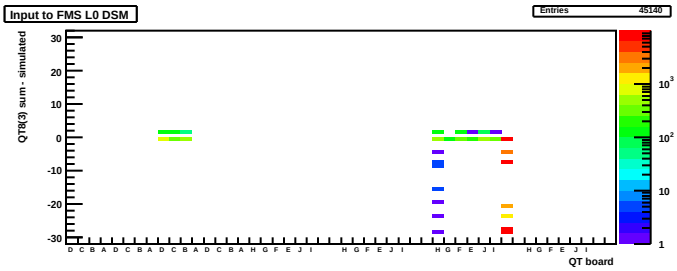

Input to FMS L0 DSM
Entries 2120304
QT8(1) sum
QT board

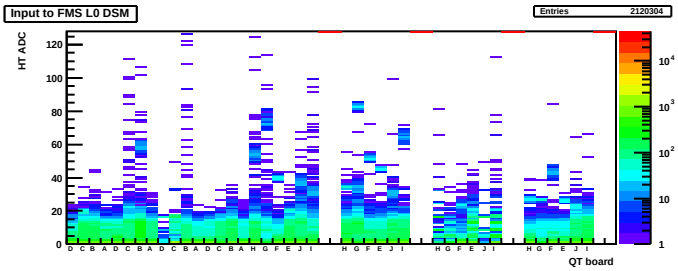

Input to FMS L0 DSM
Entries 2740
QT8(1) sum - simulated
QT board

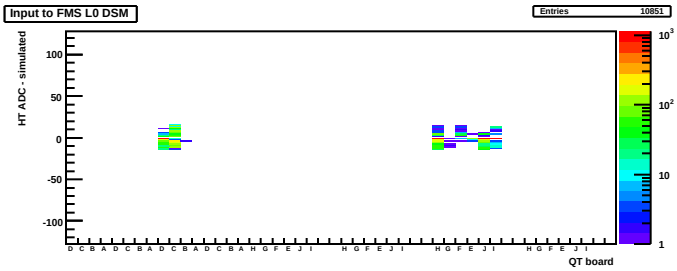

Input to FMS L0 DSM
Entries 2120304
QT8(2) sum
QT board

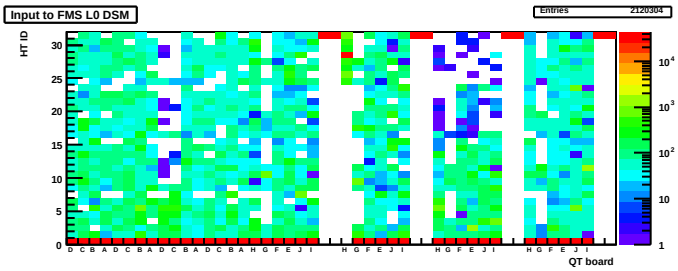

Input to FMS L0 DSM
Entries 33061
QT8(2) sum - simulated
QT board

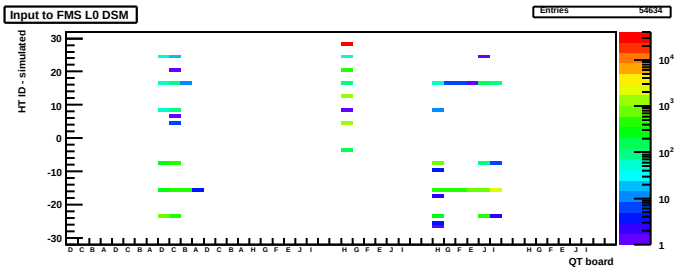

Input to FMS L0 DSM
Entries 2120304
QT8(3) sum
QT board

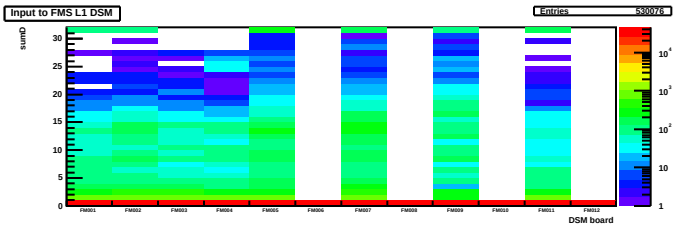

Input to FMS L0 DSM
Entries 45140
QT8(3) sum - simulated
QT board

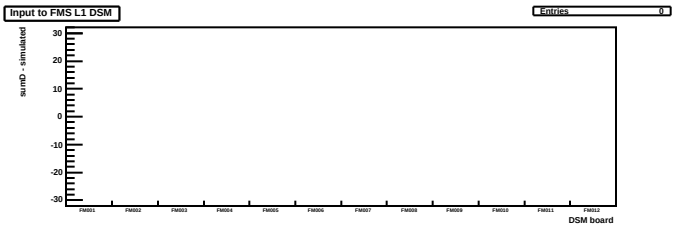

Input to FMS L0 DSM
Entries 2120304
HT ADC
QT board

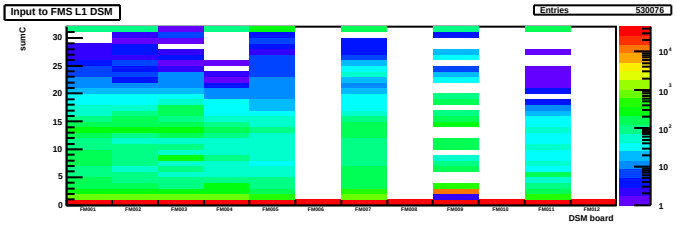

Input to FMS L0 DSM
Entries 10851
HT ADC - simulated
QT board

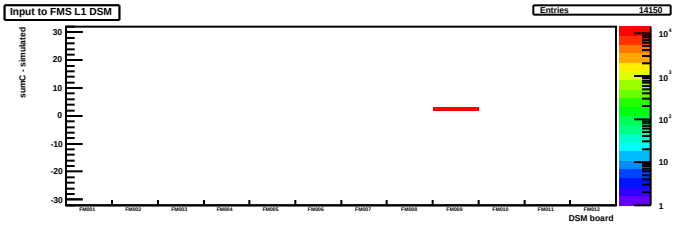

Input to FMS L0 DSM
Entries 2120304
HT ID
QT board

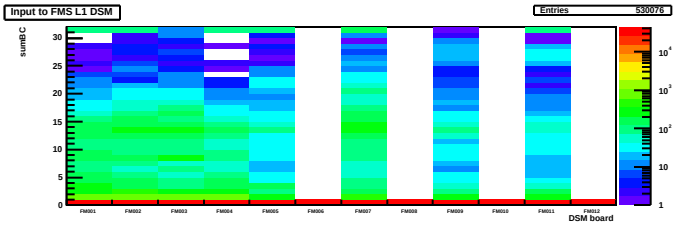

Input to FMS L0 DSM
Entries 54634
HT ID - simulated
QT board

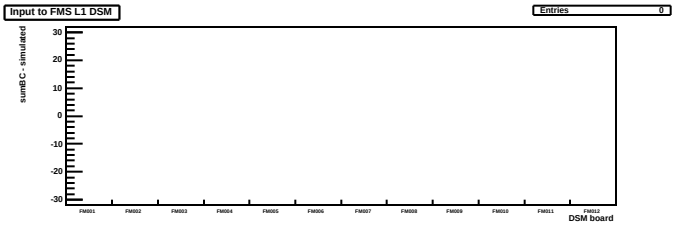

Input to FMS L1 DSM
Entries 530076
sumD
DSM board

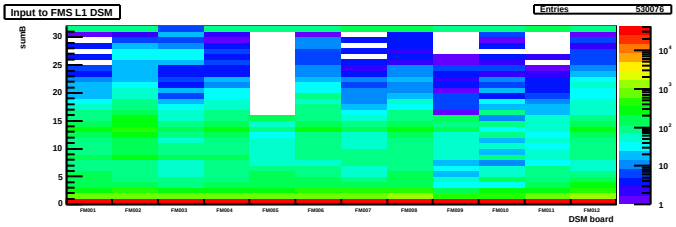

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

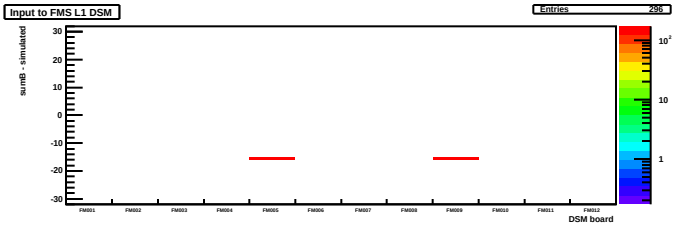

Input to FMS L1 DSM
Entries 530076
sumC
DSM board

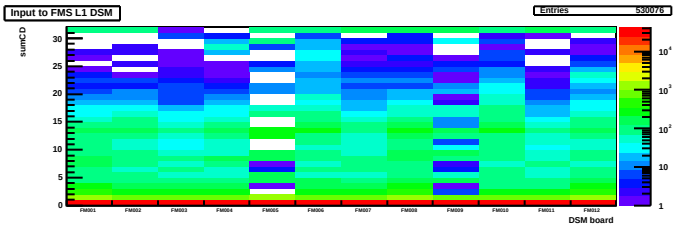

Input to FMS L1 DSM
Entries 14150
sumC - simulated
DSM board

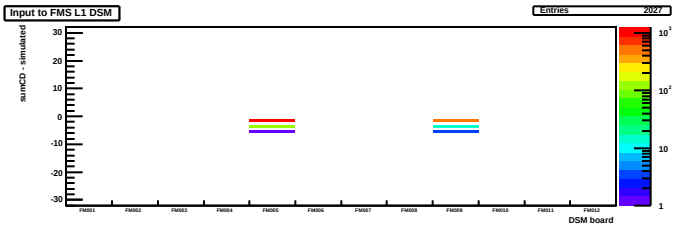

Input to FMS L1 DSM
Entries 530076
sumBC
DSM board

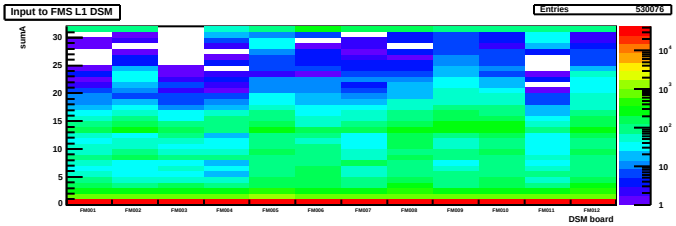

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

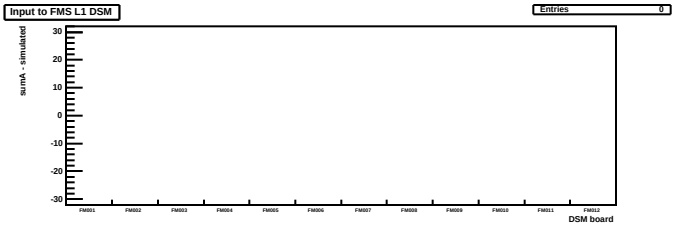

Input to FMS L1 DSM
Entries 530076
sumB
DSM board

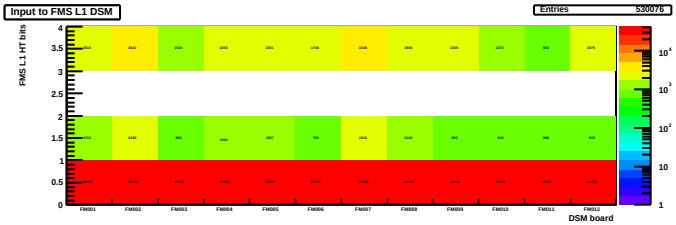

Input to FMS L1 DSM
Entries 296
sumB - simulated
DSM board

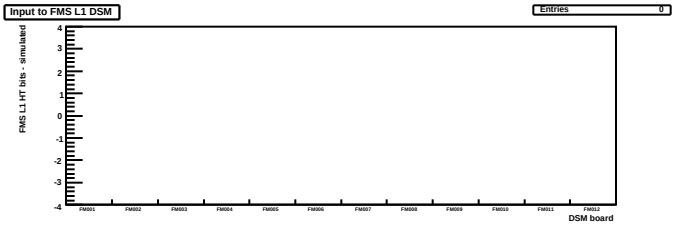

Input to FMS L1 DSM
Entries 530076
sumCD
DSM board

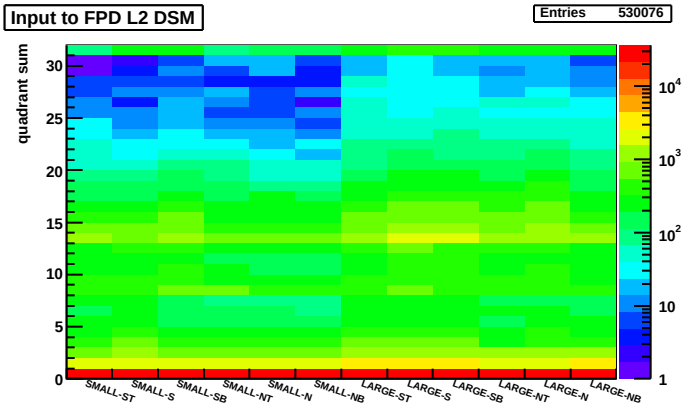

Input to FMS L1 DSM
Entries 2027
sumCD - simulated
DSM board

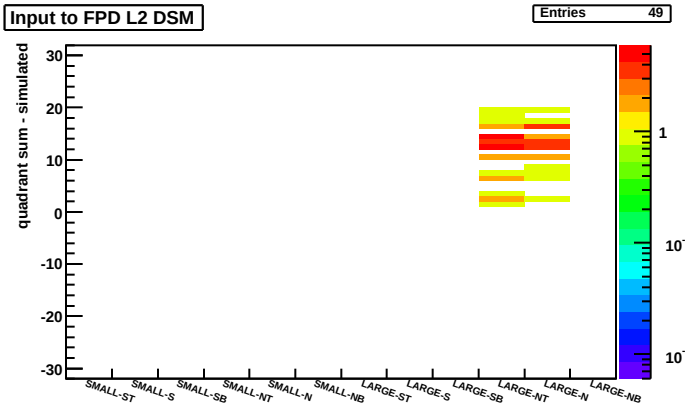

Input to FMS L1 DSM
Entries 530076
sumA
DSM board

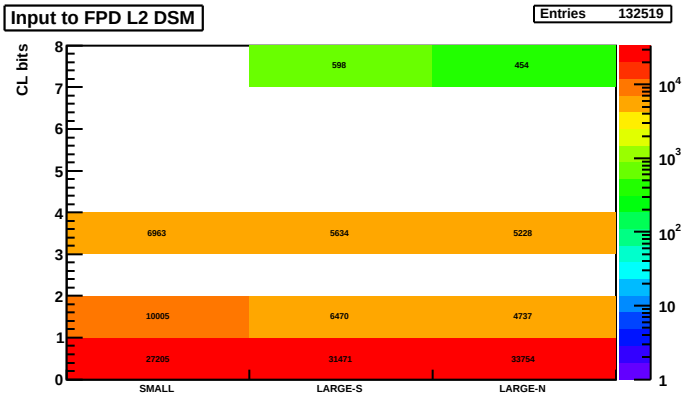

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

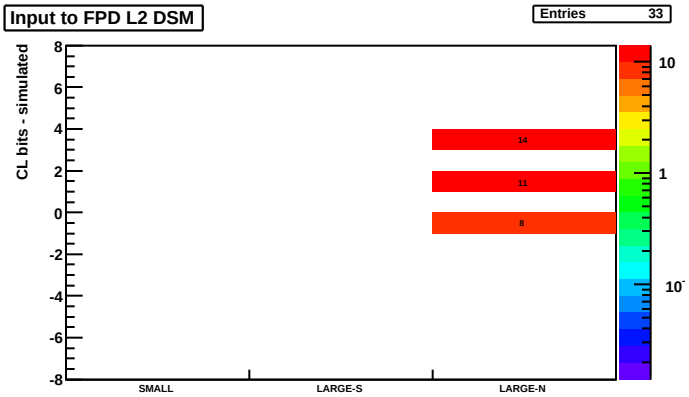

Input to FMS L1 DSM
Entries 530076
FMS L1 HT bits
DSM board

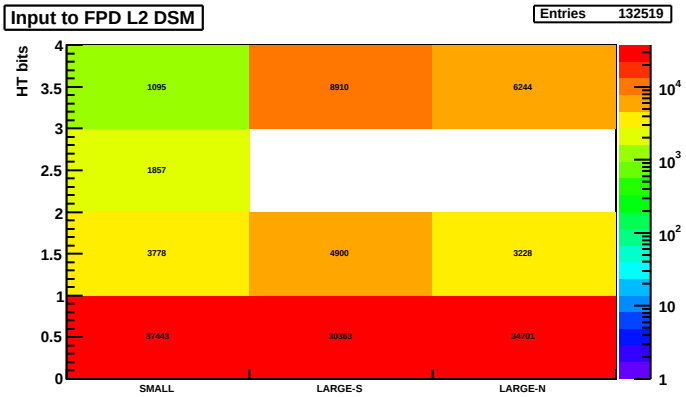

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

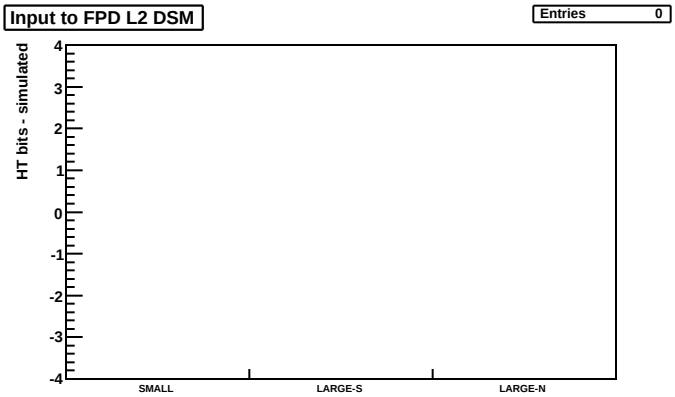

Input to FPD L2 DSM
Entries 530076
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

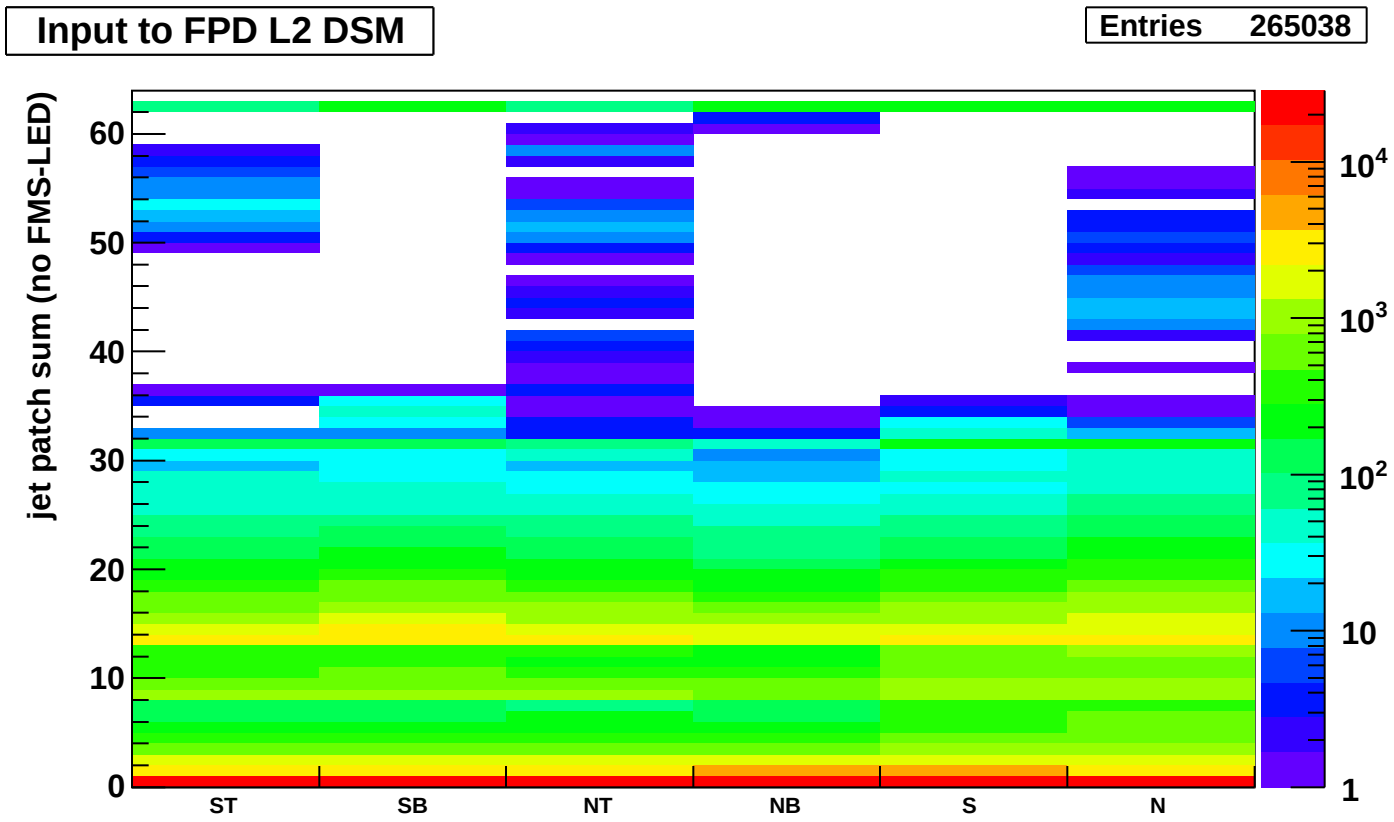

Input to FPD L2 DSM
Entries 49
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

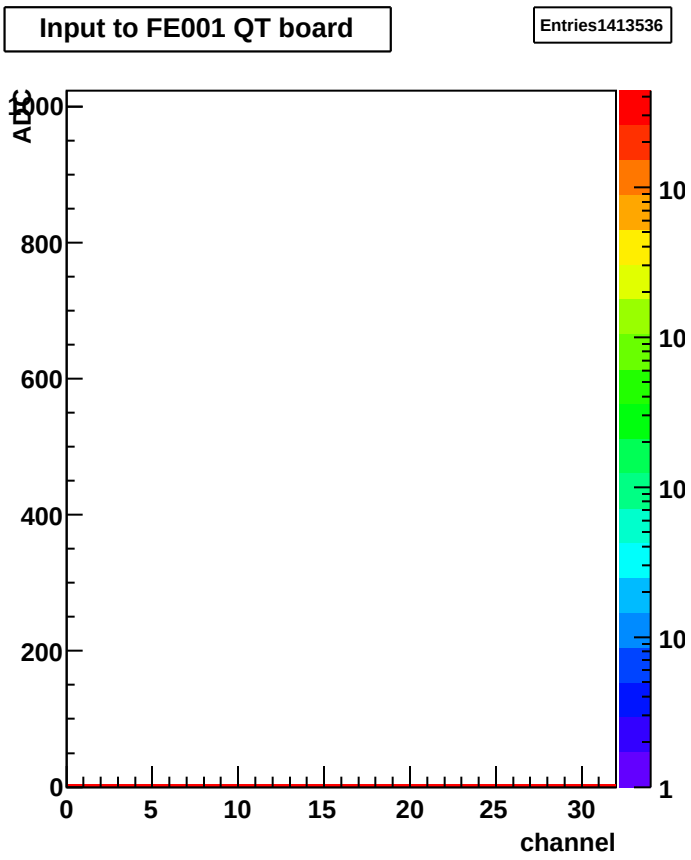

Input to FPD L2 DSM
Entries 132519
CL bits
598
454
6963
5634
5228
10005
6470
4737
27205
31471
33754
SMALL
LARGE-S
LARGE-N

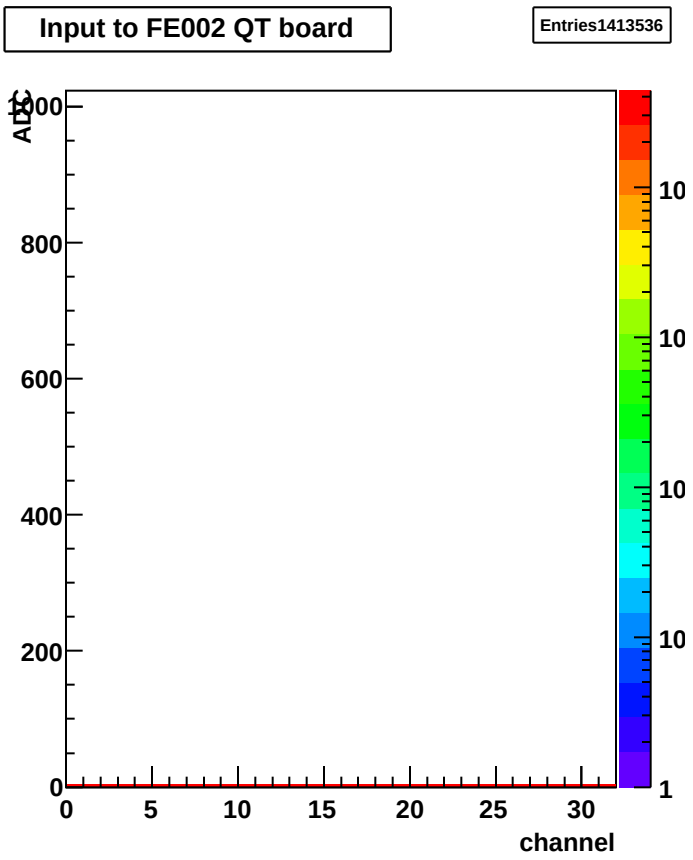

Input to FPD L2 DSM
Entries 33
CL bits - simulated
14
11
8
SMALL
LARGE-S
LARGE-N

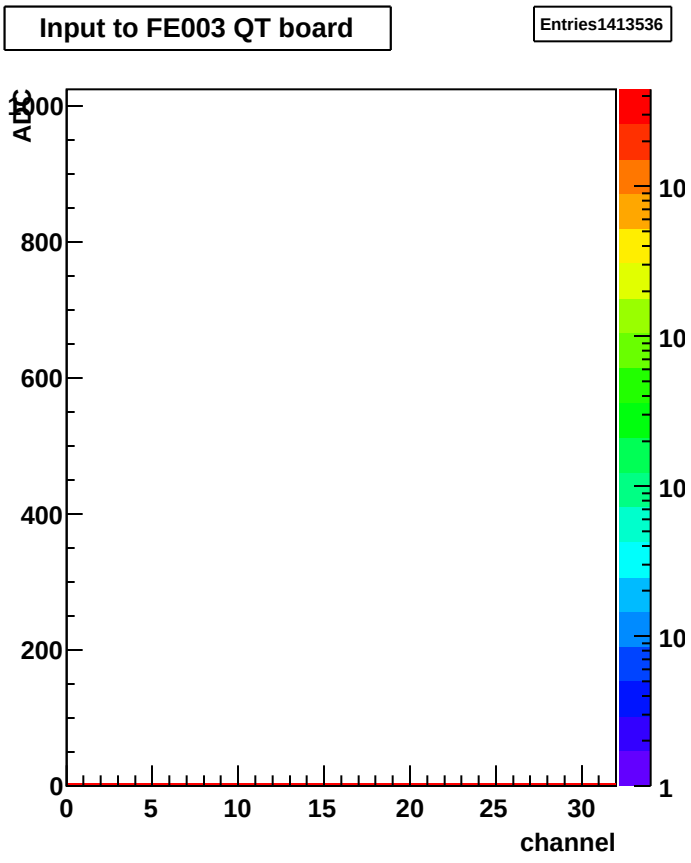

Input to FPD L2 DSM
Entries 132519
HT bits
1095
8910
6244
1857
3778
4900
3228
37443
30363
34701
SMALL
LARGE-S
LARGE-N

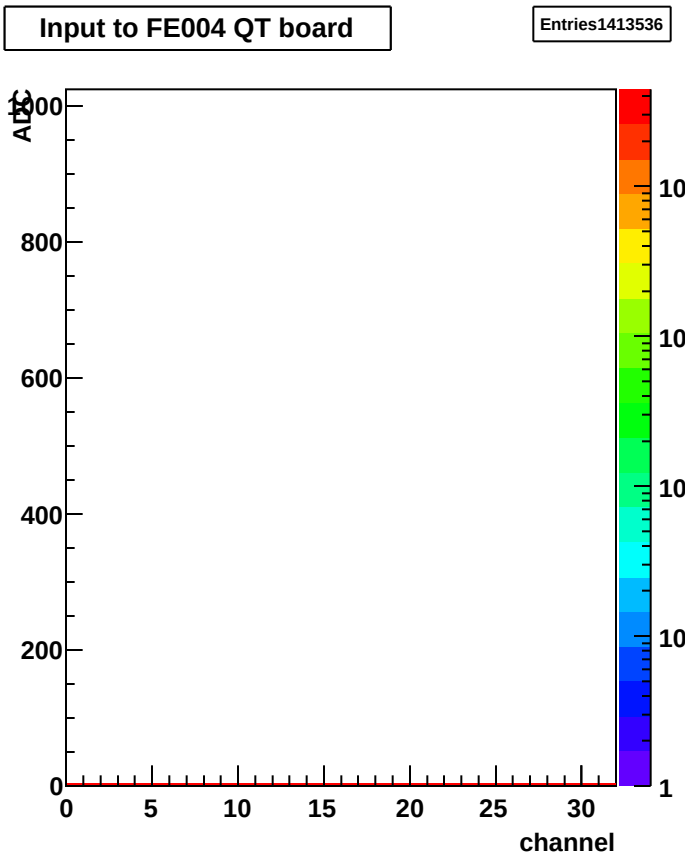

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

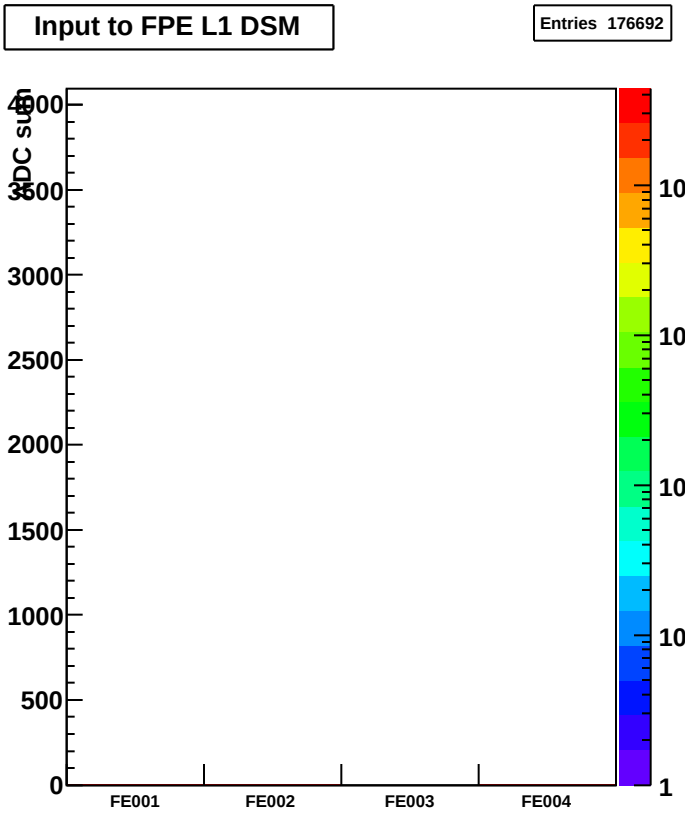

Input to FPD L2 DSM
Entries 265038
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

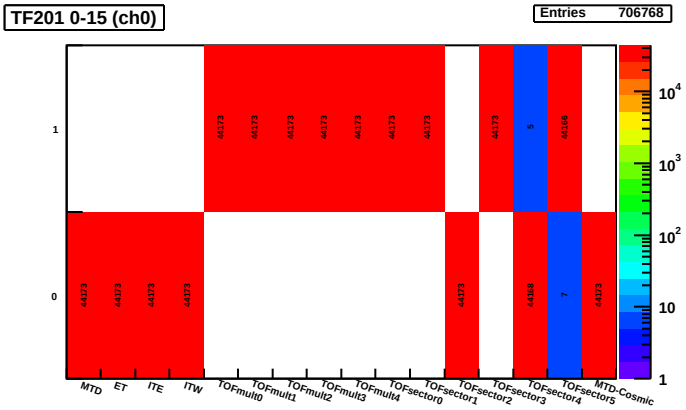

Input to FE001 QT board
Entries1413536
ADC
channel

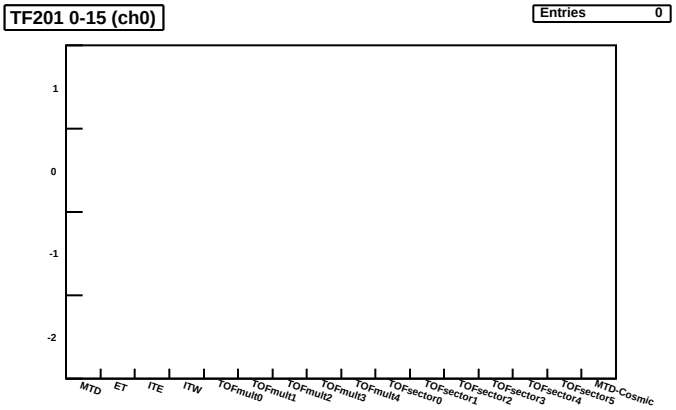

Input to FE002 QT board
Entries1413536
ADC
channel

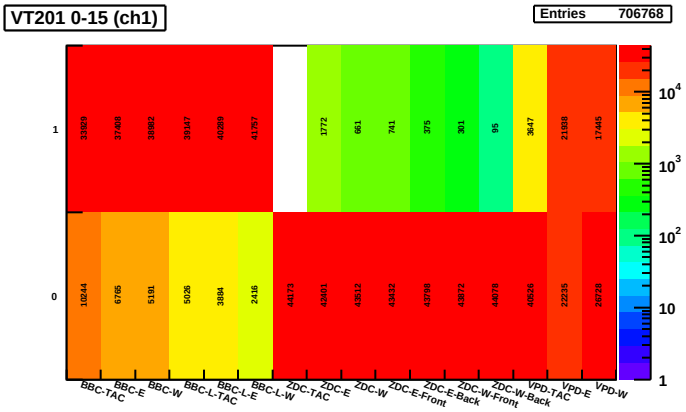

Input to FE003 QT board
Entries1413536
ADC
channel

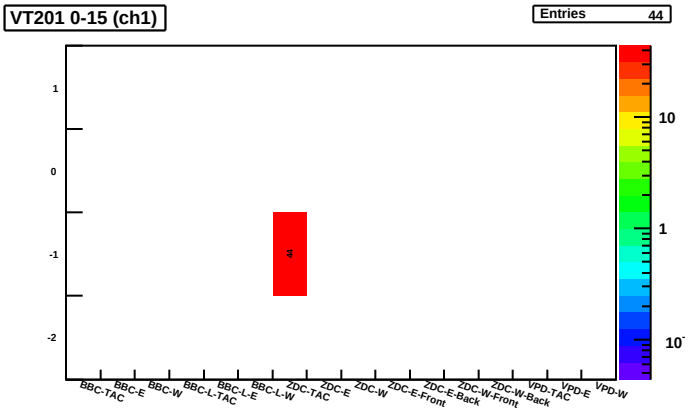

Input to FE004 QT board
Entries1413536
ADC
channel

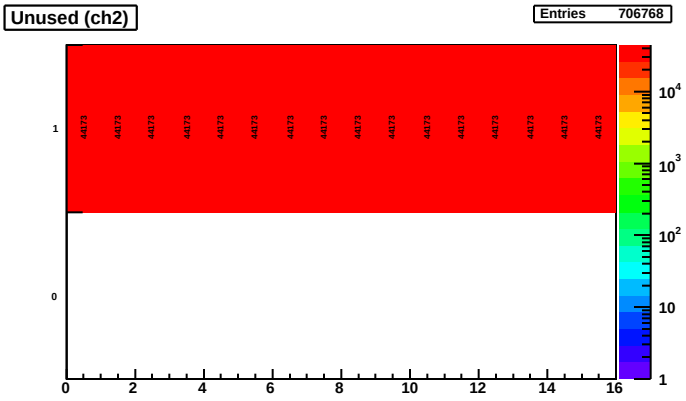

Input to FPE L1 DSM
Entries 176692
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

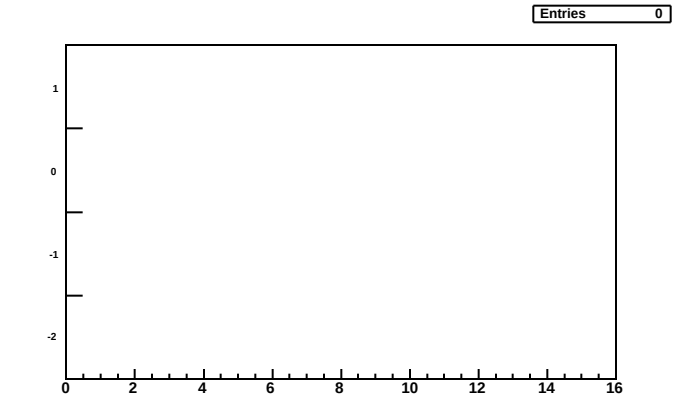

TF201 0-15 (ch0)
Entries 706768
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

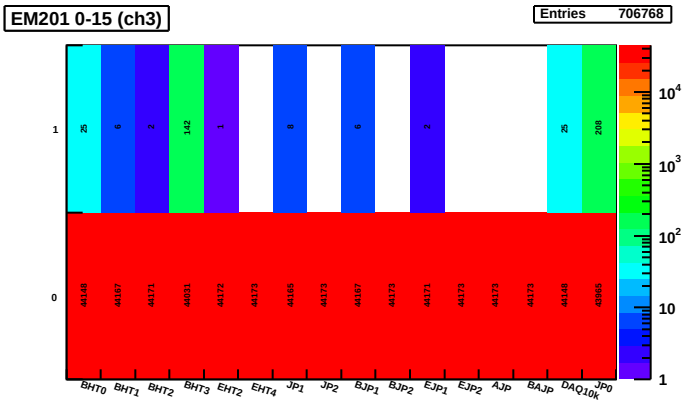

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

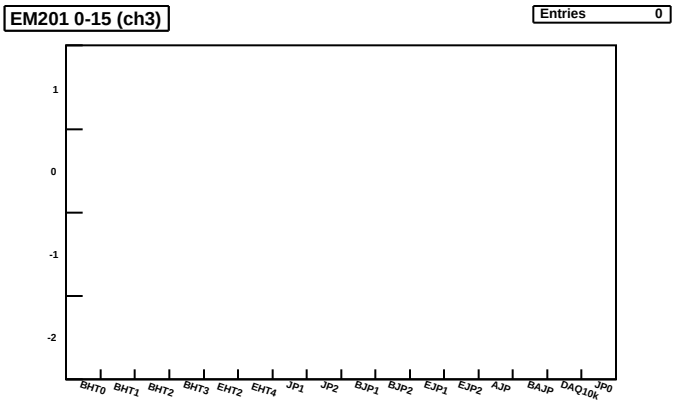

VT201 0-15 (ch1)
Entries 706768
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 44
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

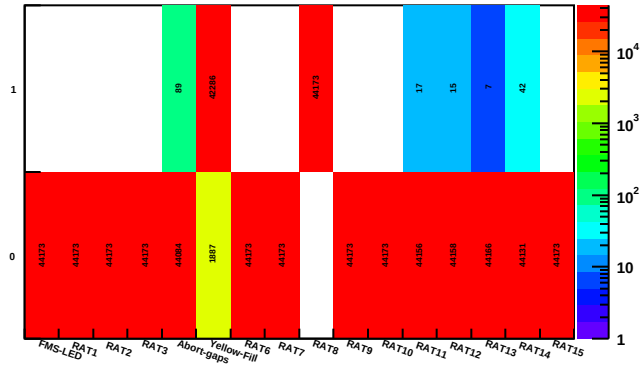

Unused (ch2)
Entries 706768

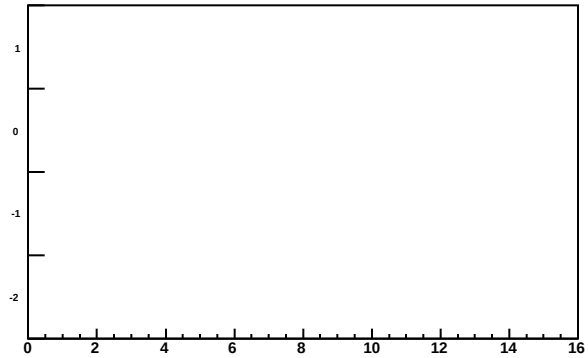

Entries 0


EM201 0-15 (ch3)
Entries 706768
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

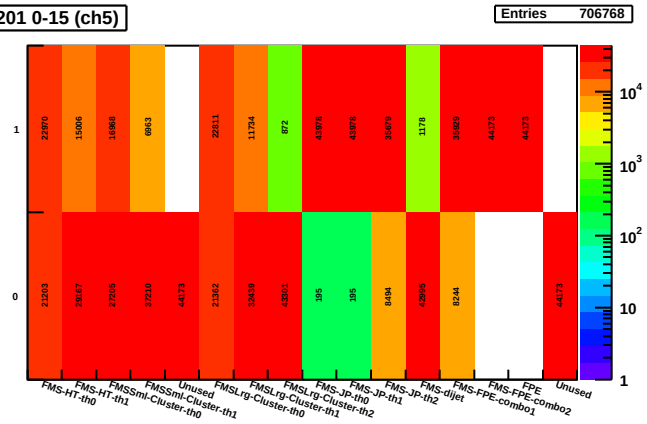

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)





FP201 0-15 (ch5)



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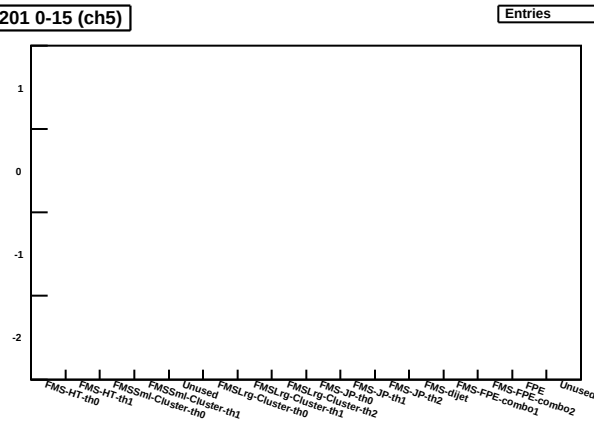
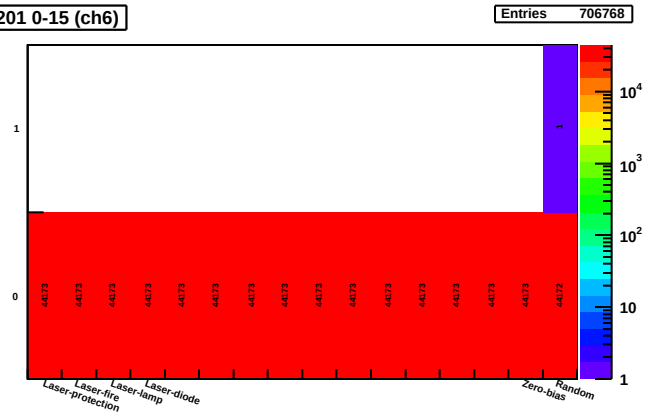
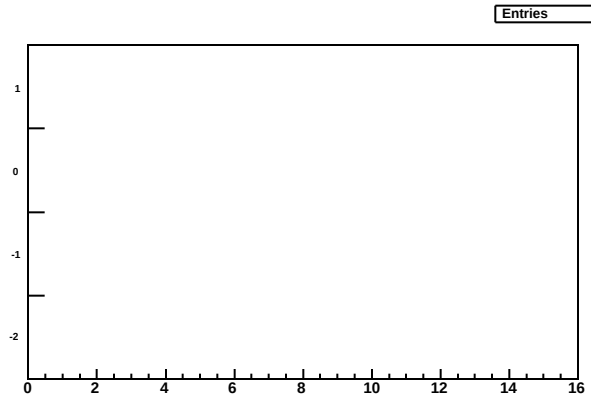
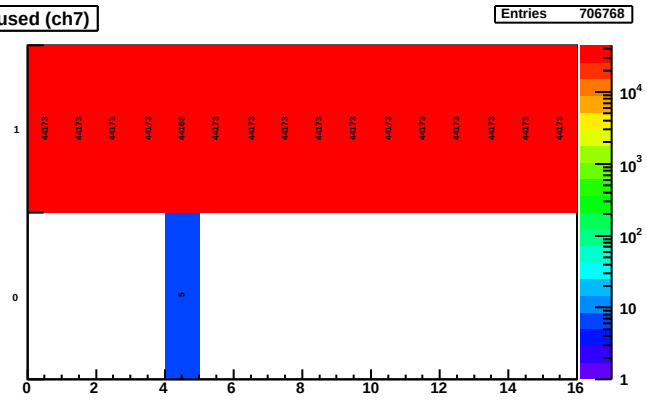
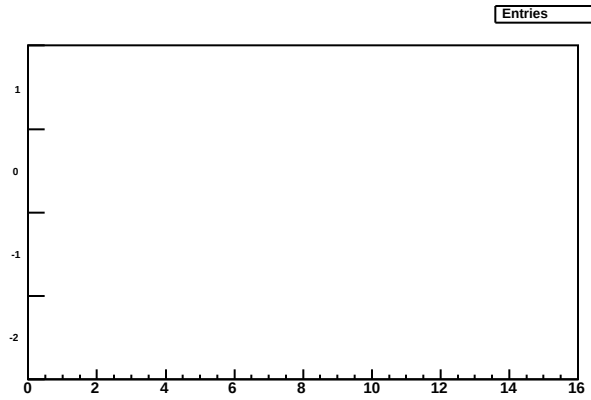


ST201 0-15 (ch6)





Unused (ch7)