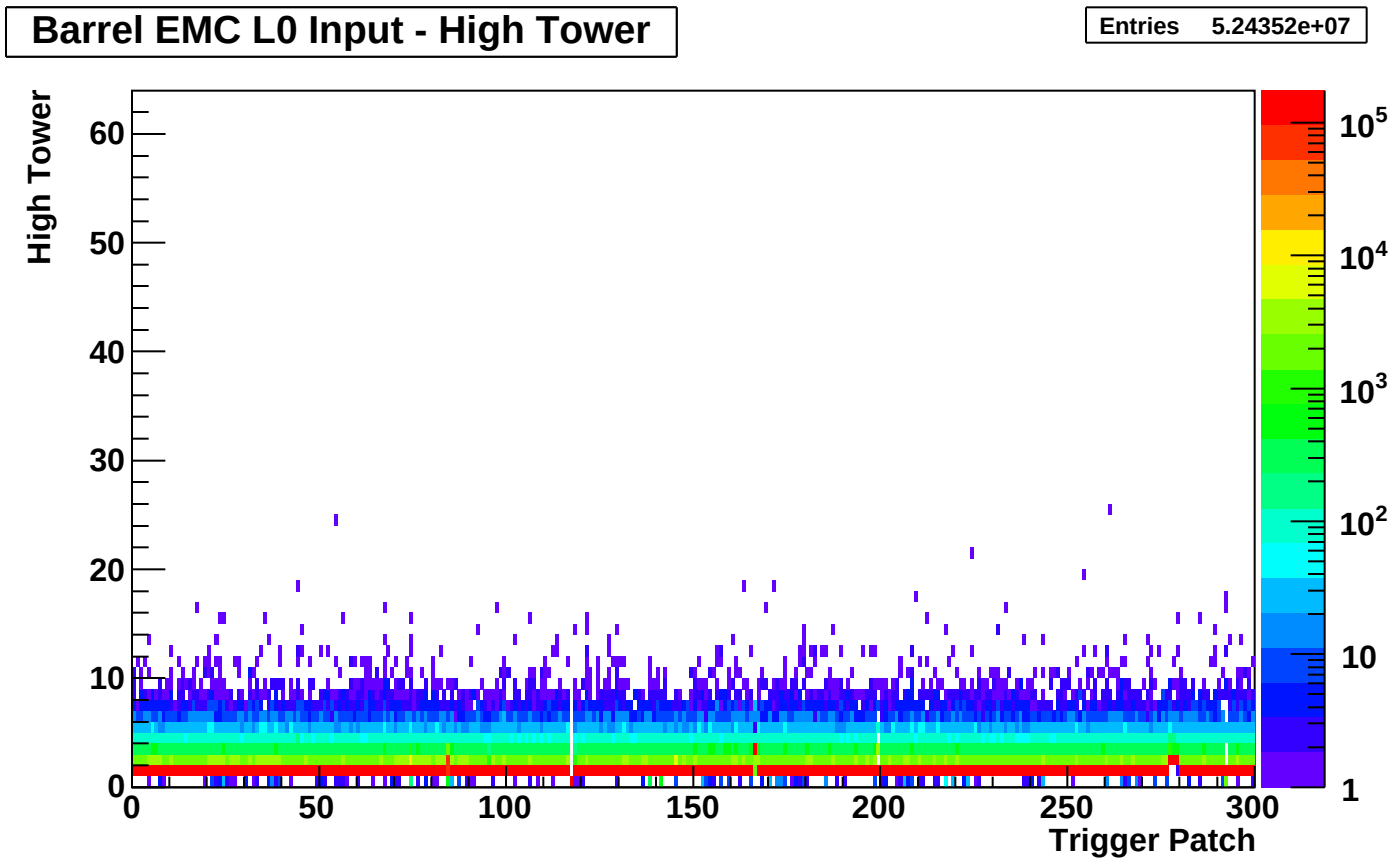
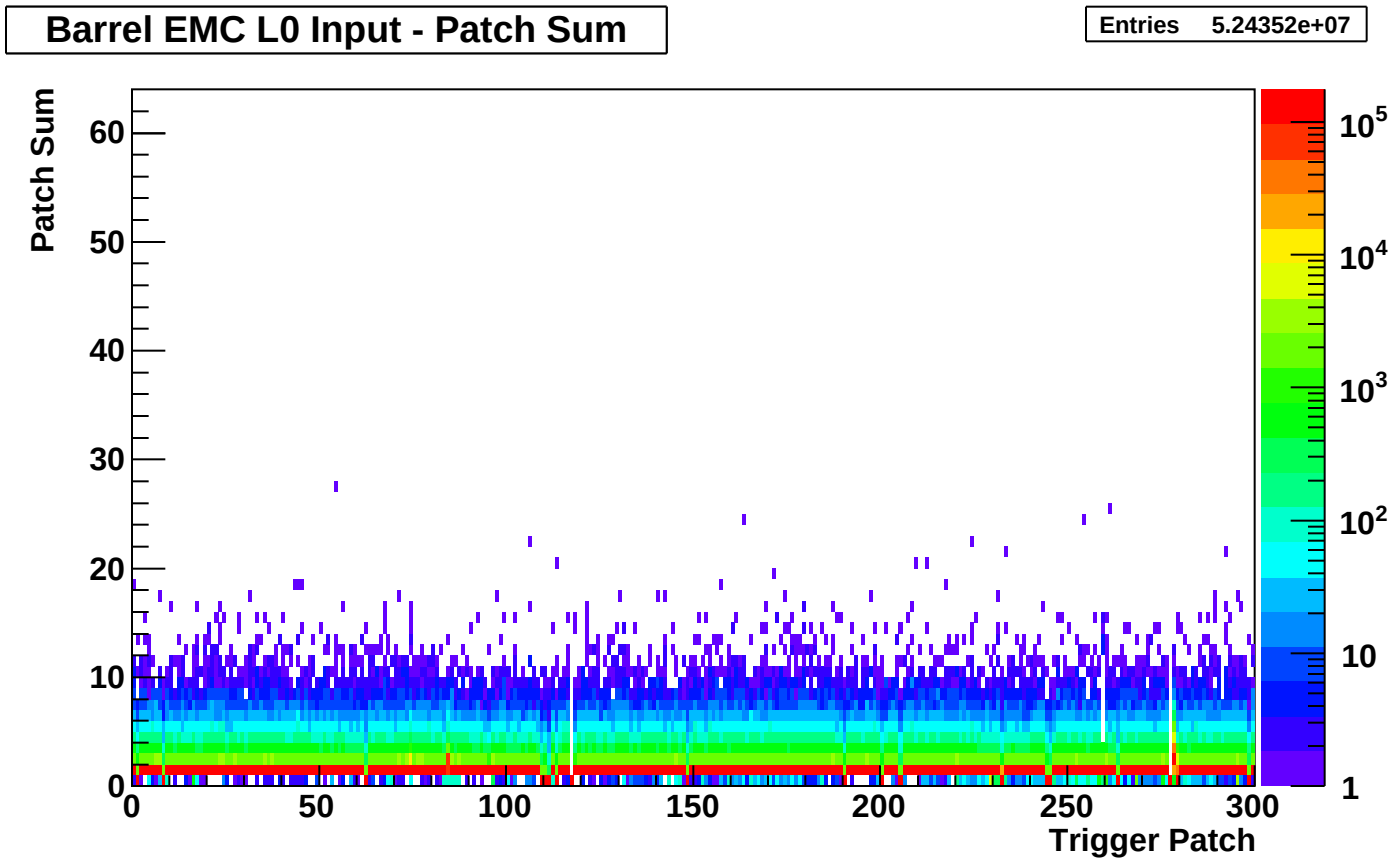
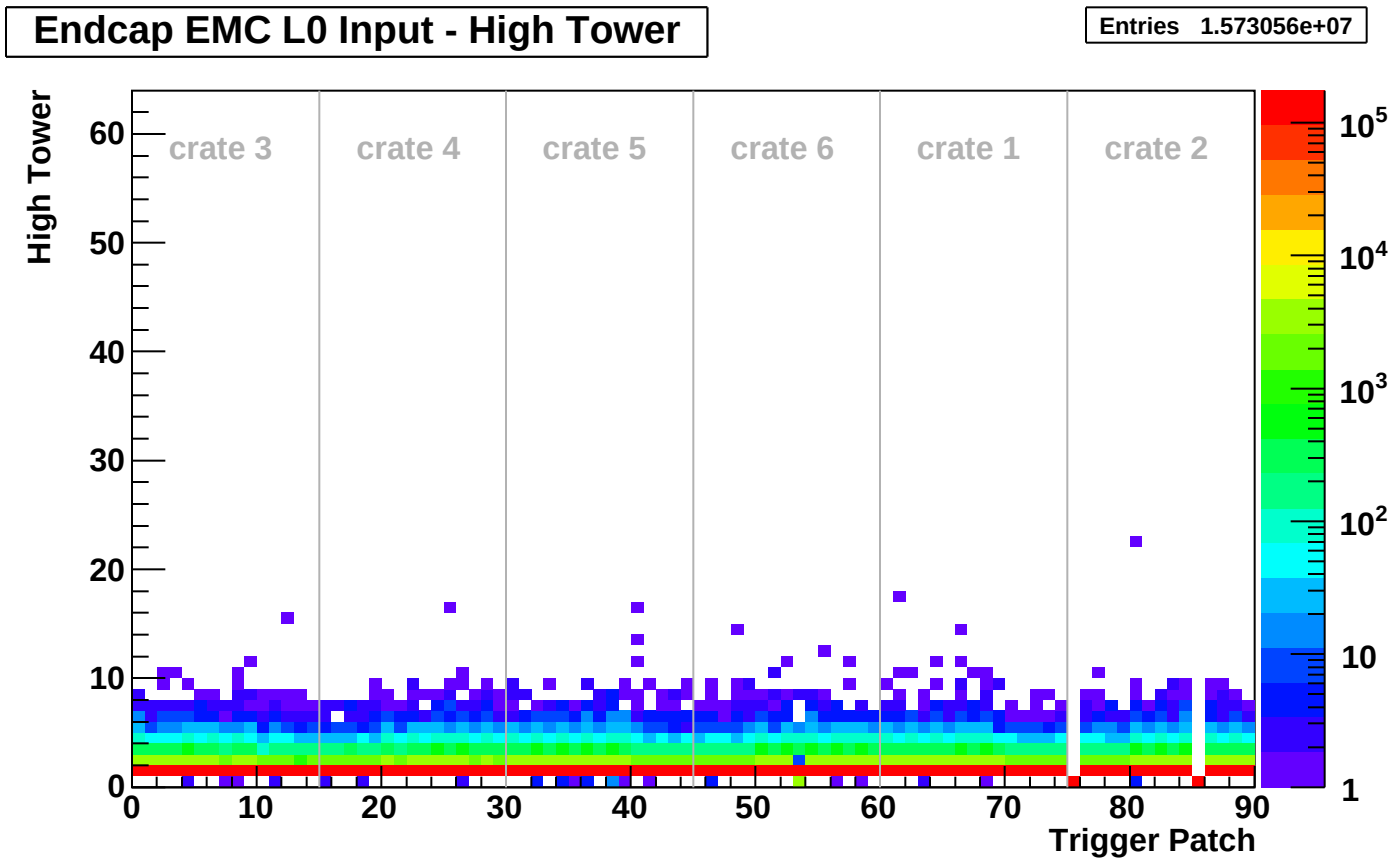
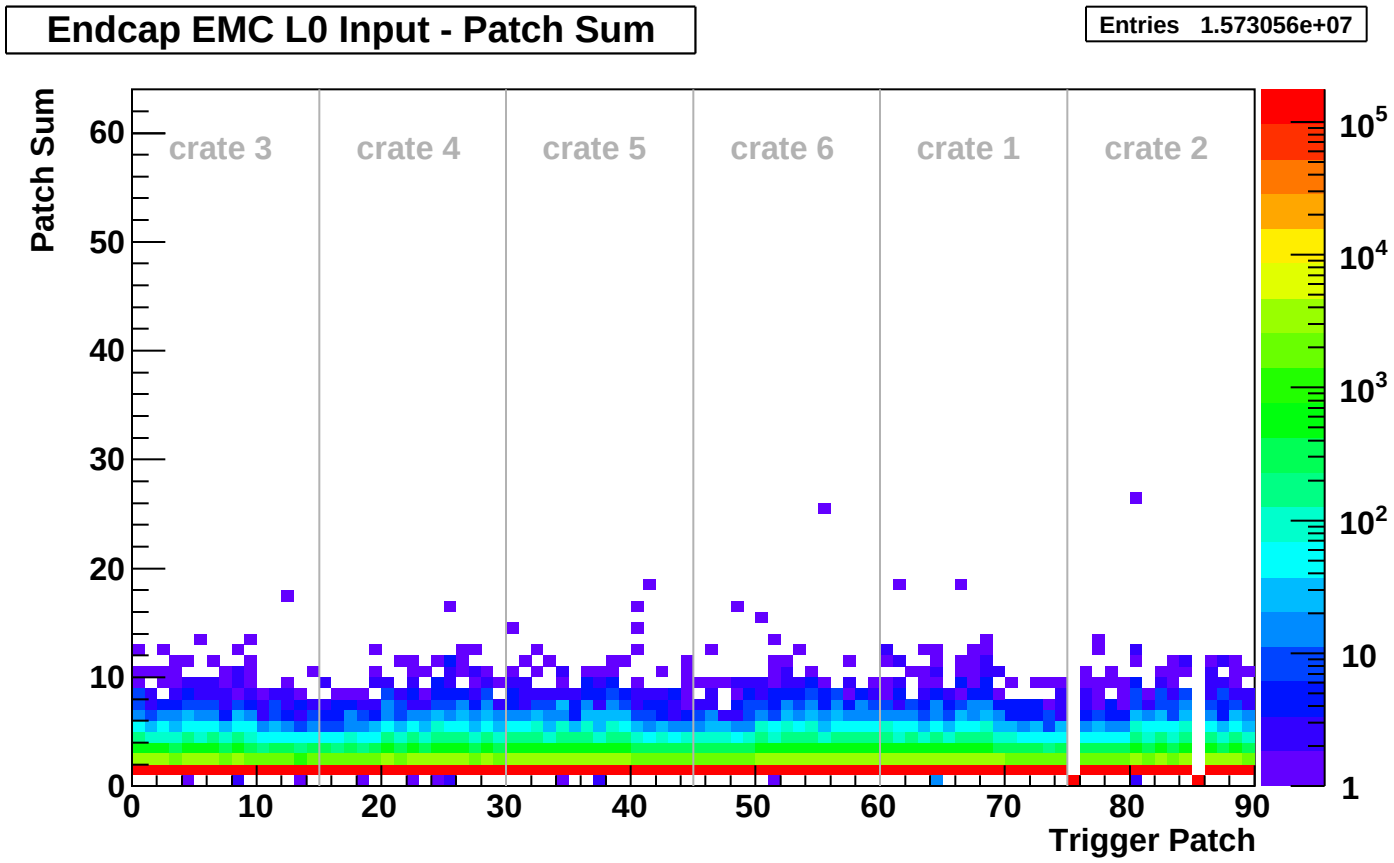
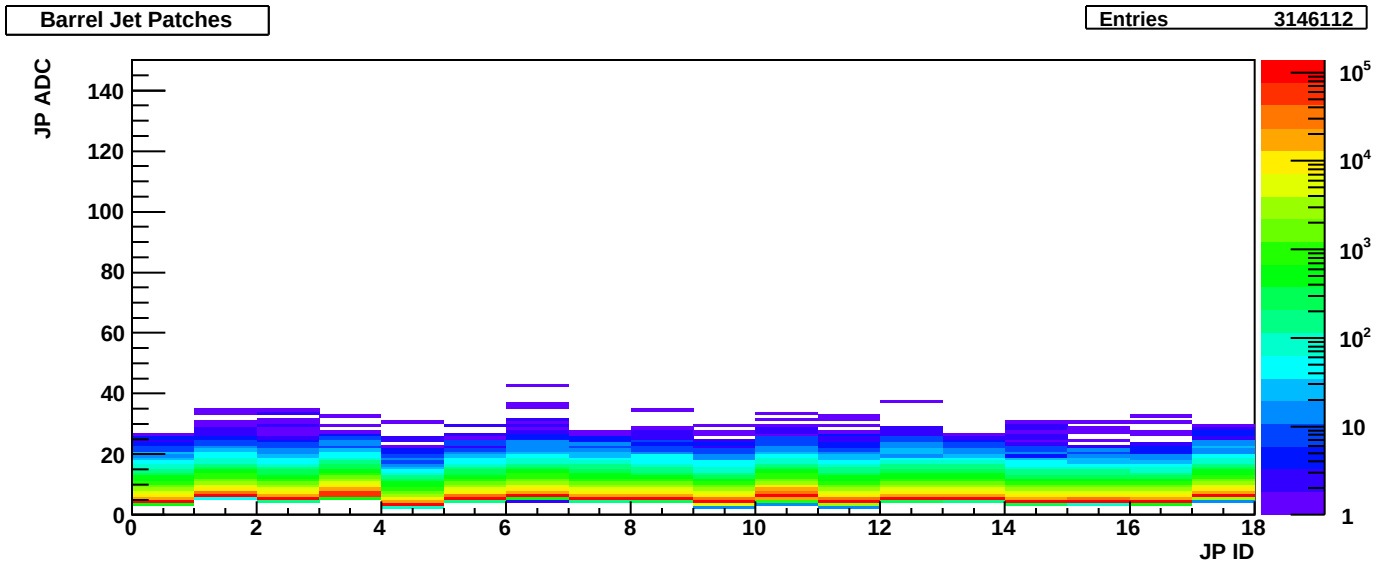
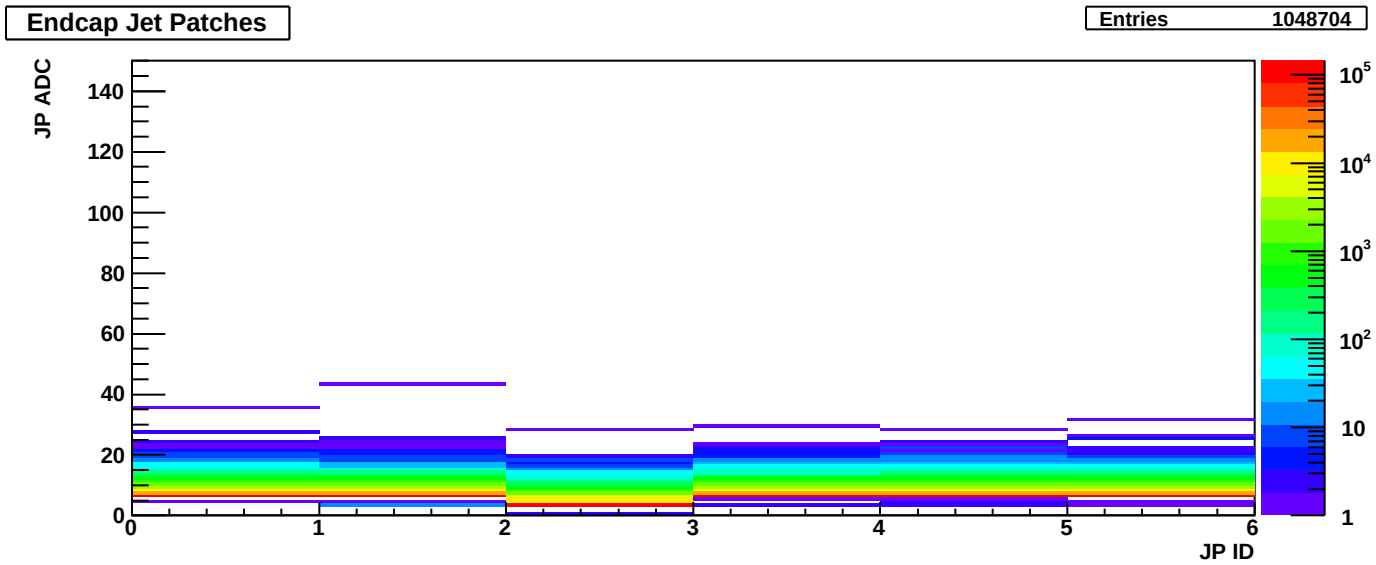

Barrel EMC L0 Input - High Tower
Entries 5.24352e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

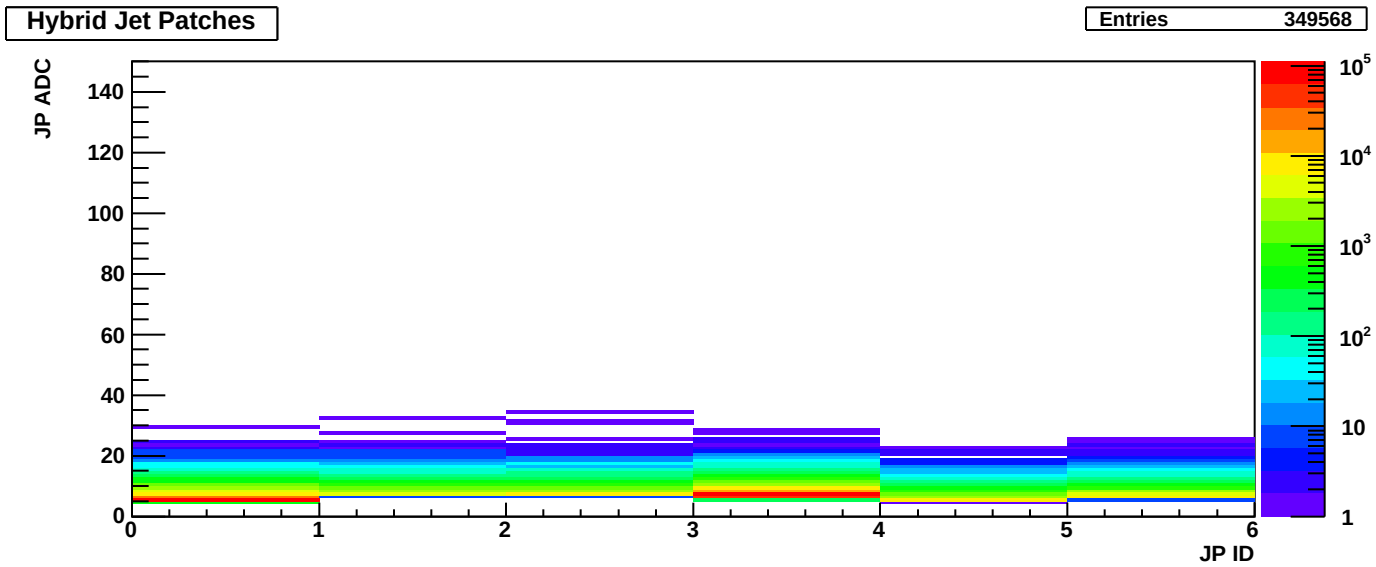

Barrel EMC L0 Input - Patch Sum
Entries 5.24352e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

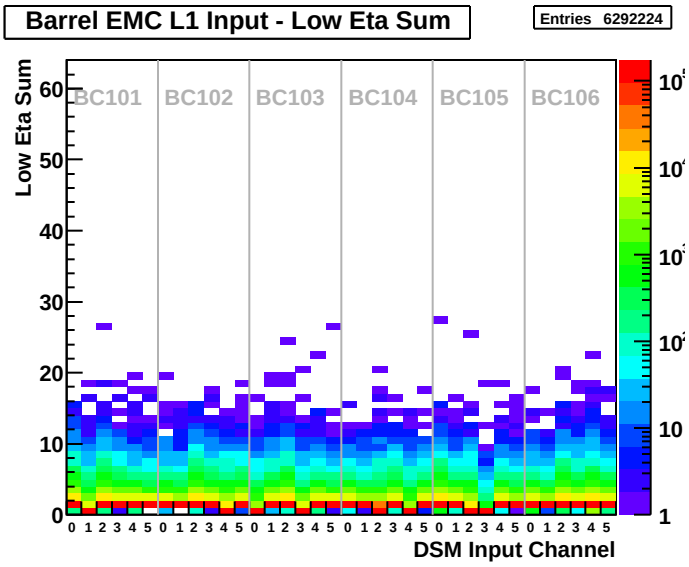

Endcap EMC L0 Input - High Tower
Entries 1.573056e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

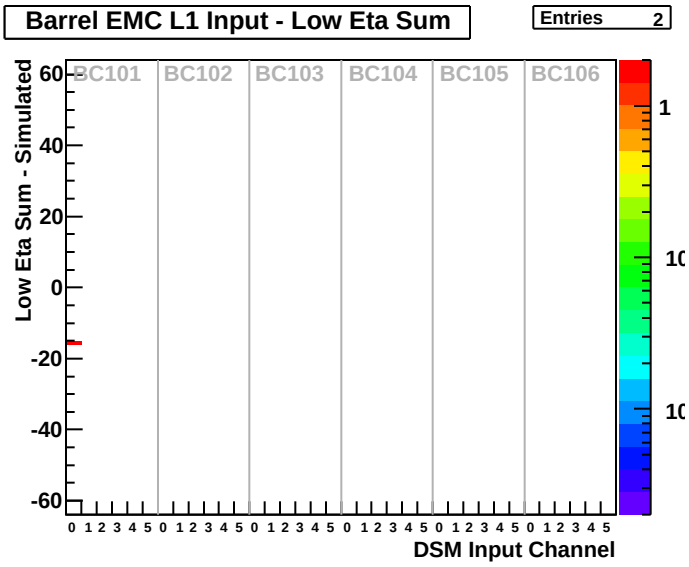

Endcap EMC L0 Input - Patch Sum
Entries 1.573056e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

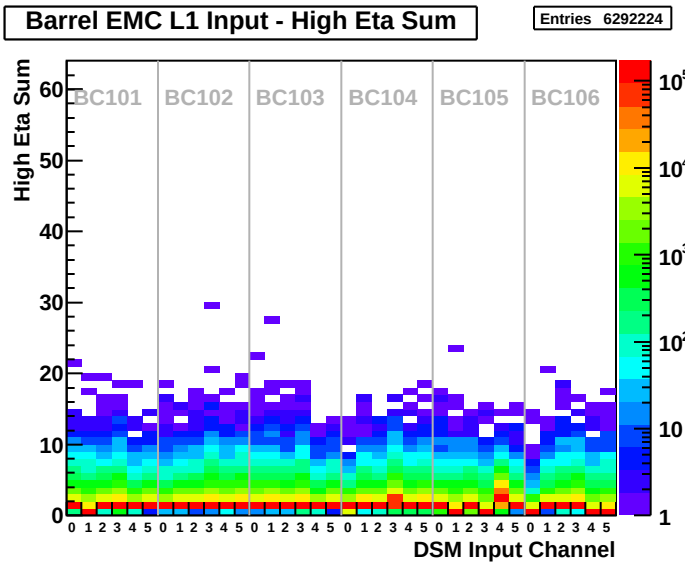

Barrel Jet Patches
Entries 3146112
JP ADC
JP ID

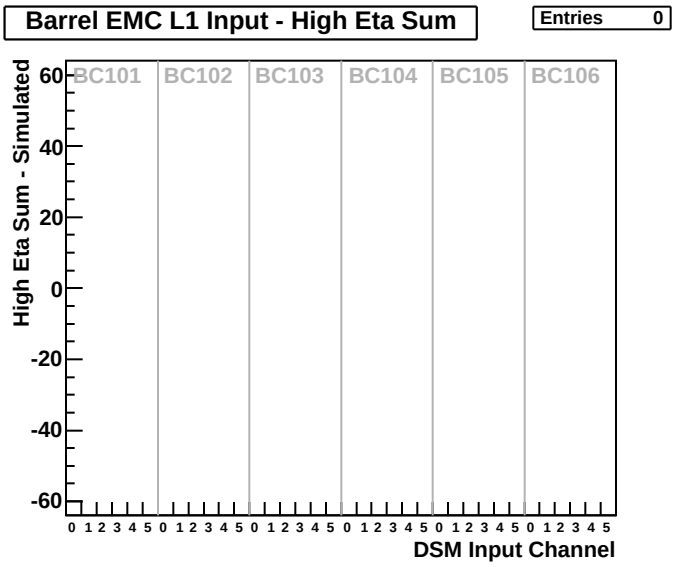

Endcap Jet Patches
Entries 1048704
JP ADC
JP ID

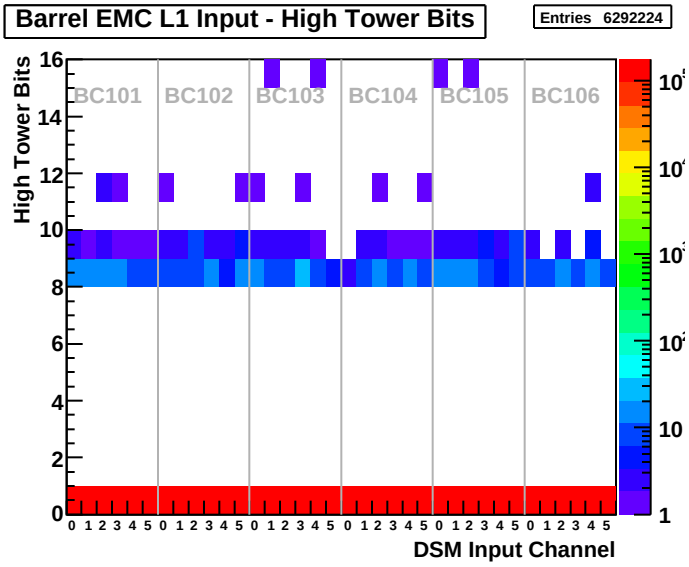

Hybrid Jet Patches
Entries 349568
JP ADC
JP ID

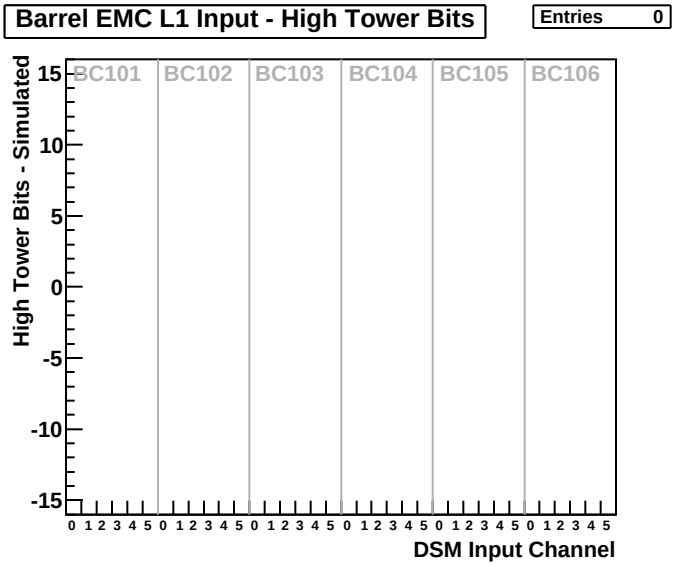

Barrel EMC L1 Input - Low Eta Sum
Entries 6292224
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

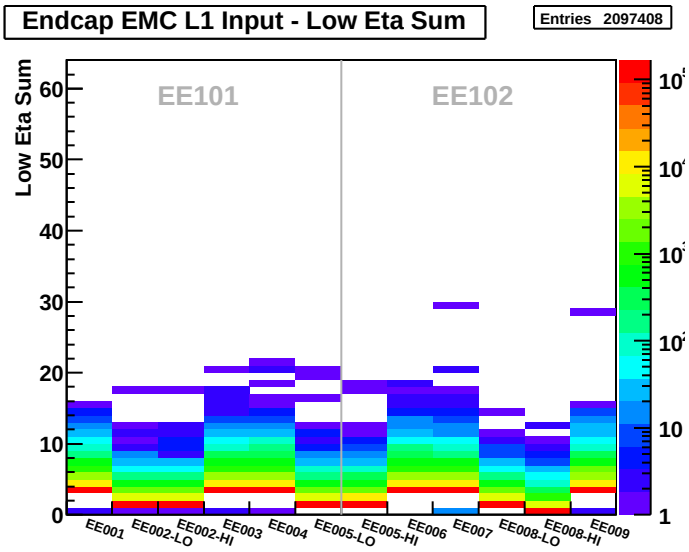

Barrel EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

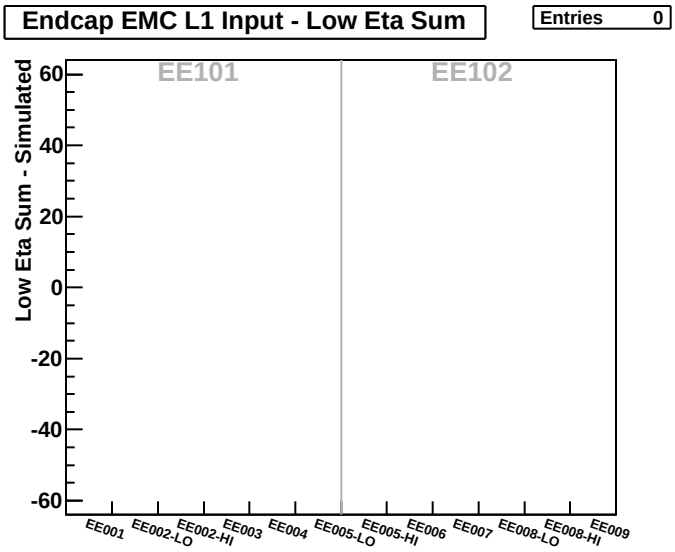

Barrel EMC L1 Input - High Eta Sum
Entries 6292224
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

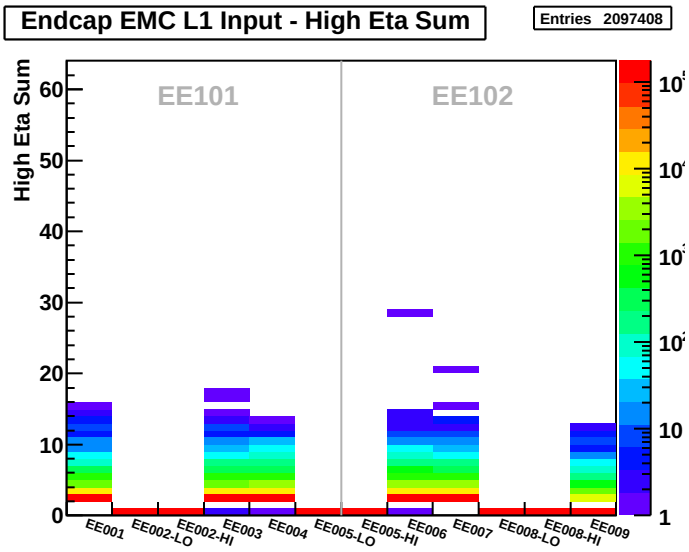

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

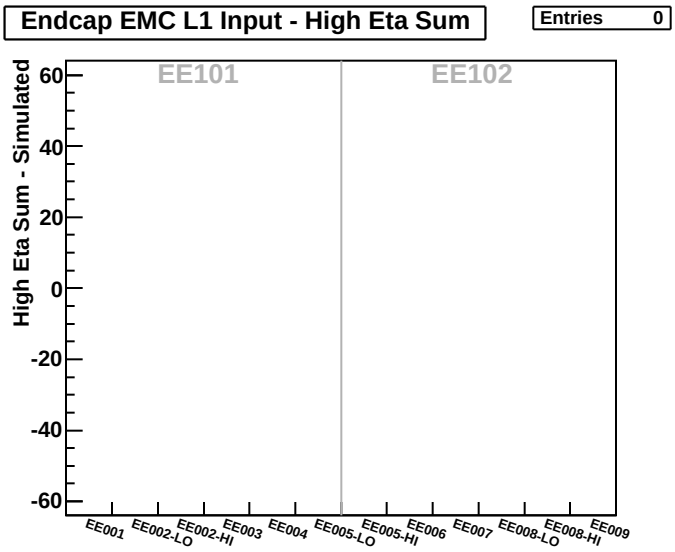

Barrel EMC L1 Input - High Tower Bits
Entries 6292224
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

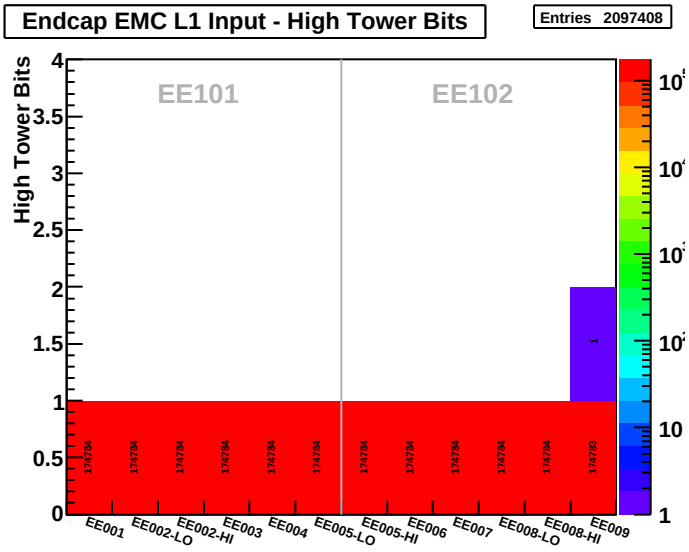

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

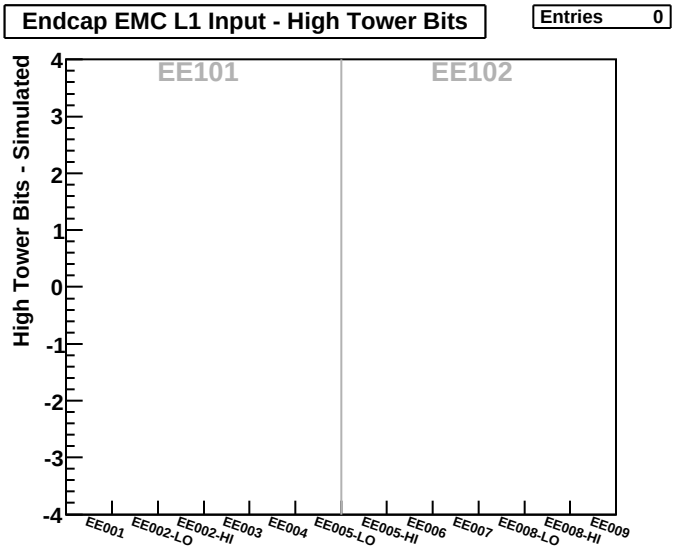

Endcap EMC L1 Input - Low Eta Sum
Entries 2097408
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

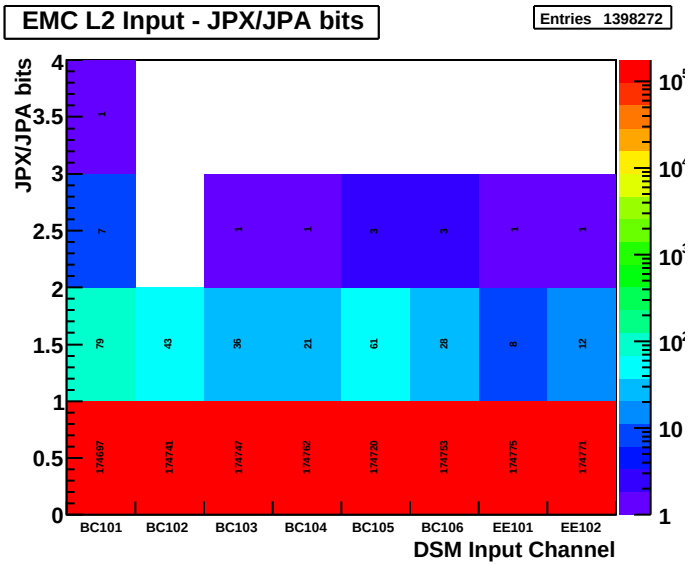

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

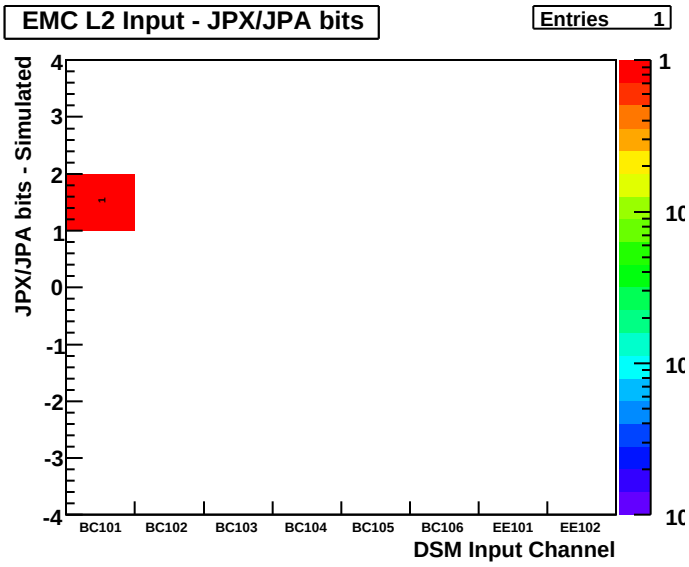

Endcap EMC L1 Input - High Eta Sum
Entries 2097408
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

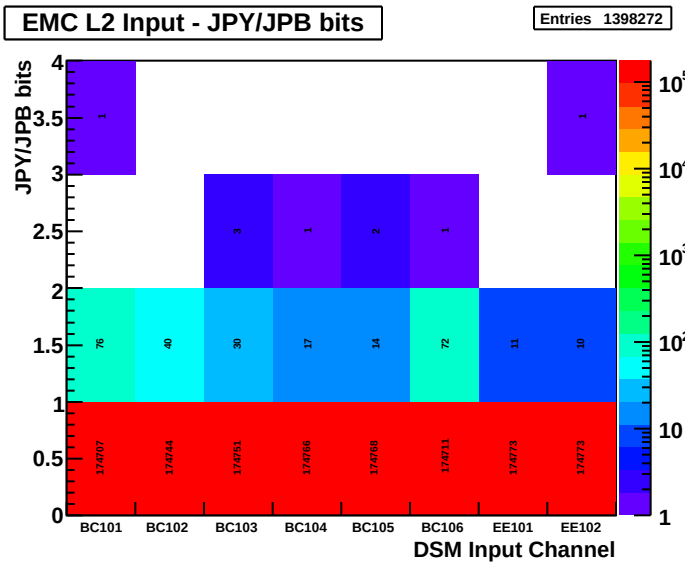

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

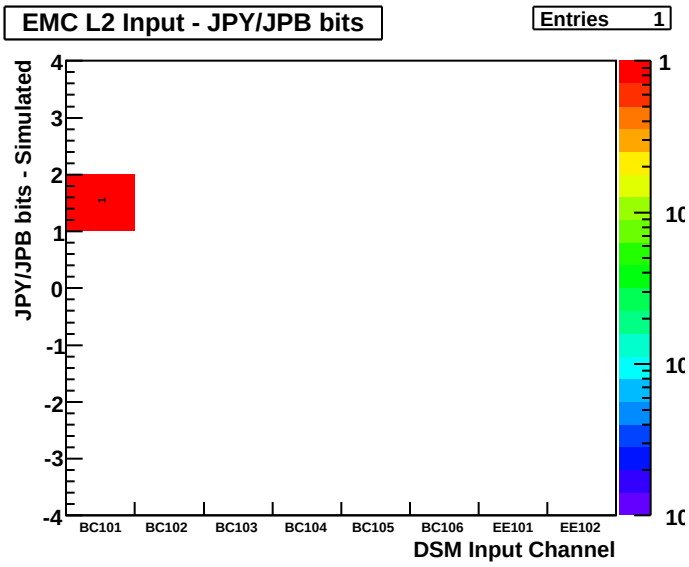

Endcap EMC L1 Input - High Tower Bits
Entries 2097408
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

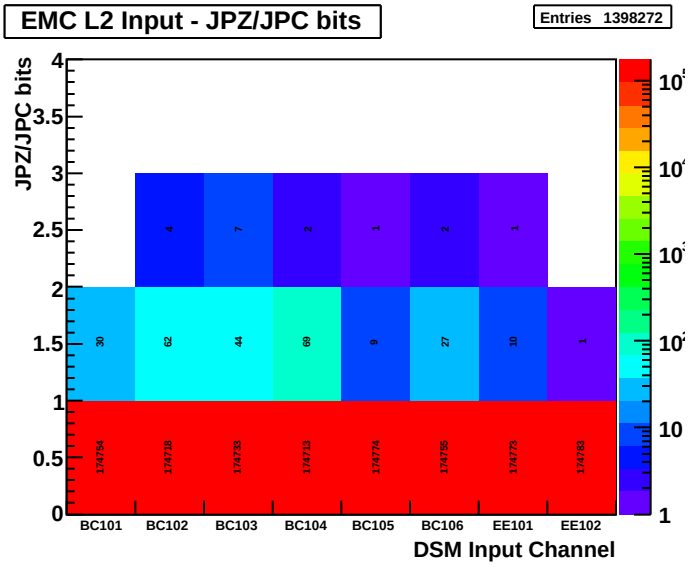

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

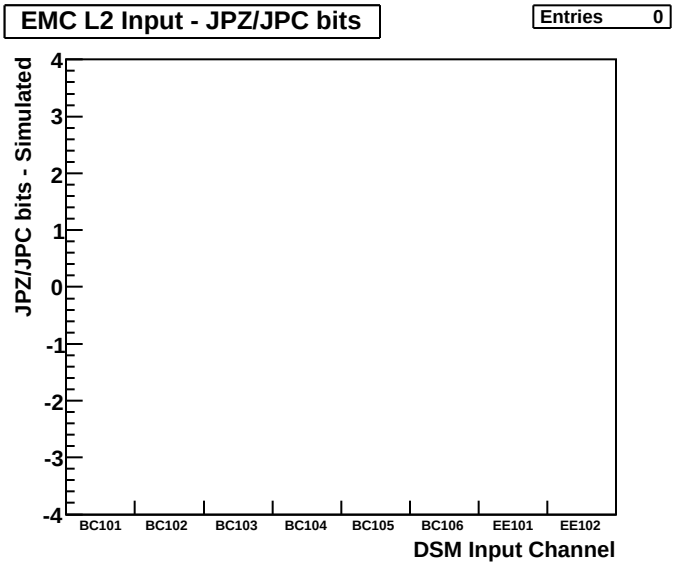

EMC L2 Input - JPX/JPA bits
Entries 1398272
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

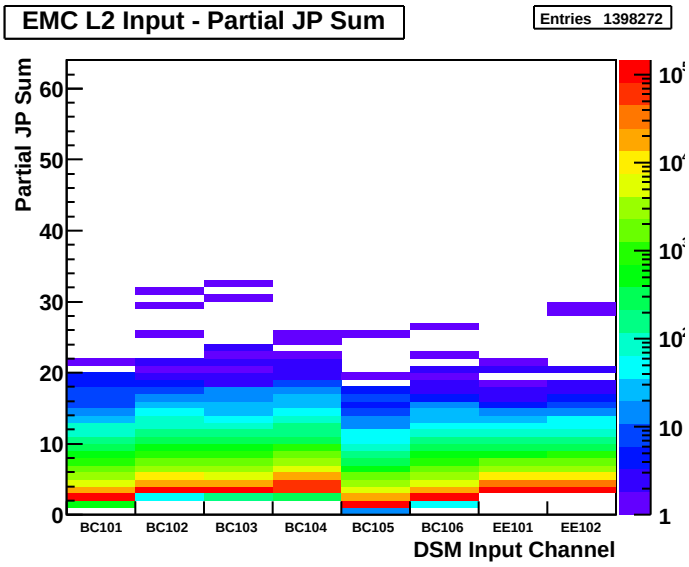

EMC L2 Input - JPX/JPA bits
Entries 1
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

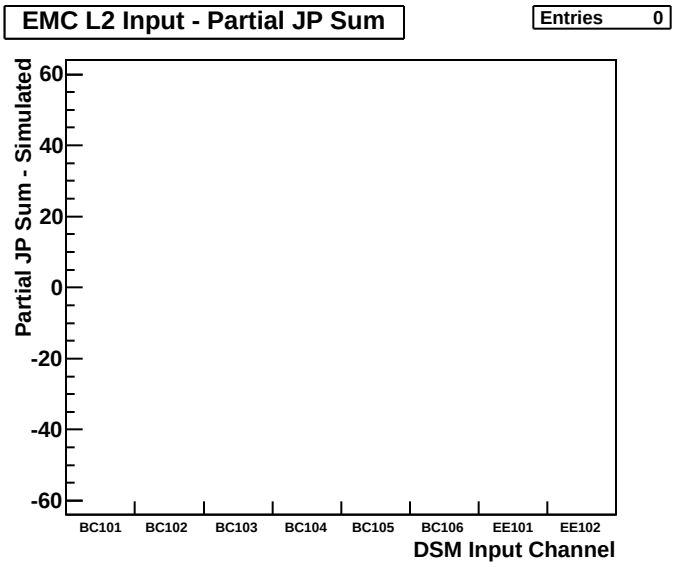

EMC L2 Input - JPY/JPB bits
Entries 1398272
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

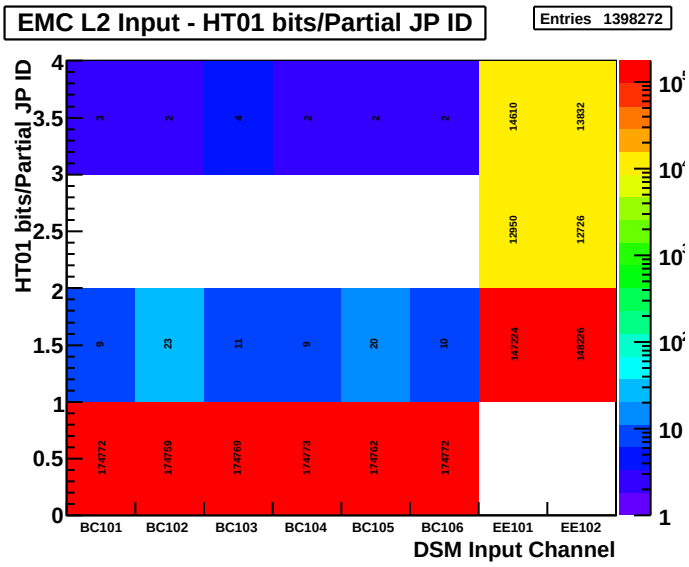

EMC L2 Input - JPY/JPB bits
Entries 1
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

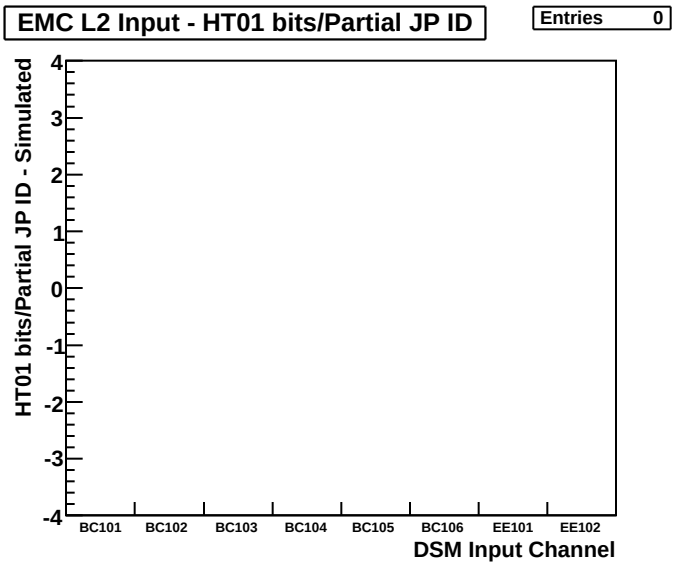

EMC L2 Input - JPZ/JPC bits
Entries 1398272
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

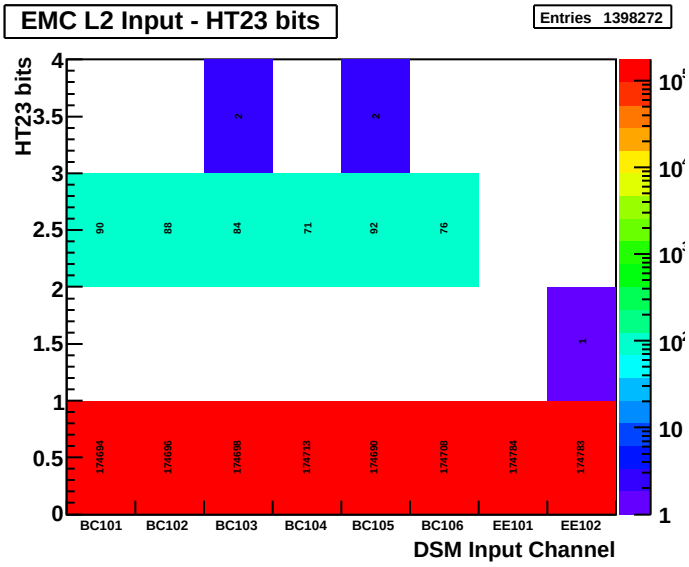

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

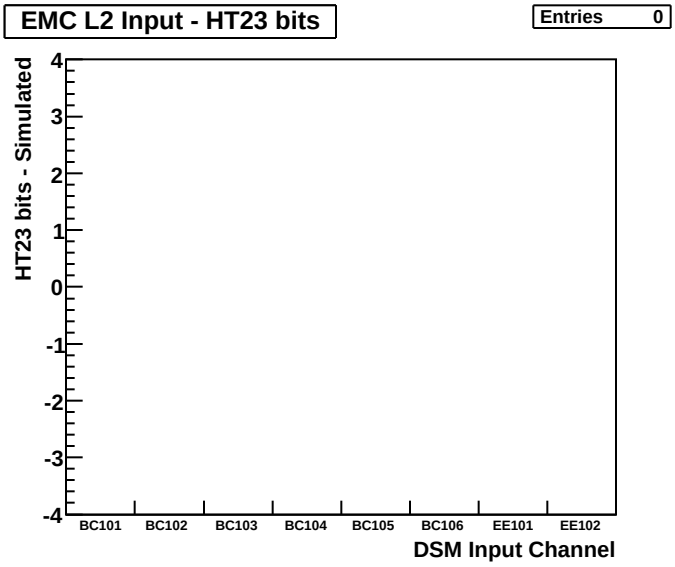

EMC L2 Input - Partial JP Sum
Entries 1398272
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

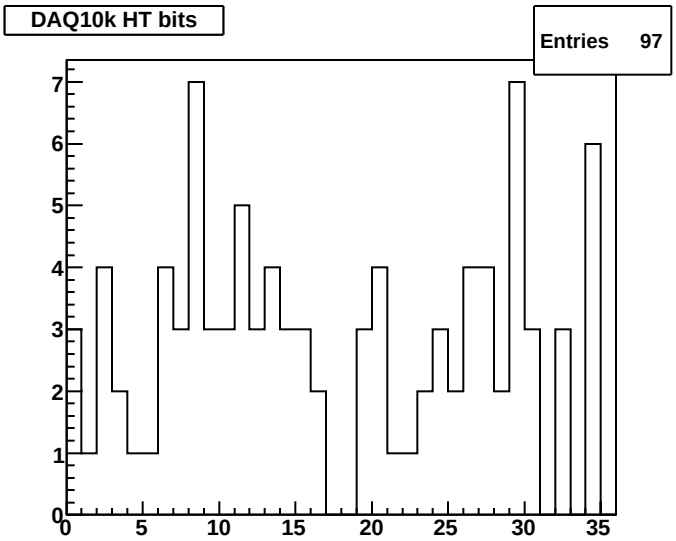

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

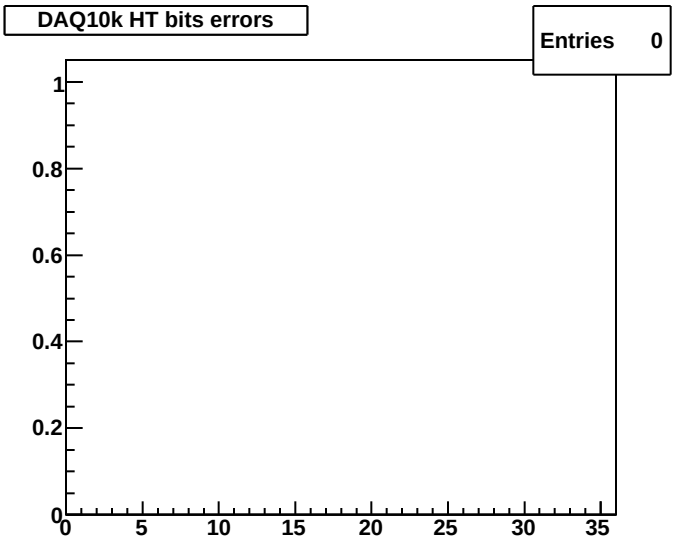

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1398272
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

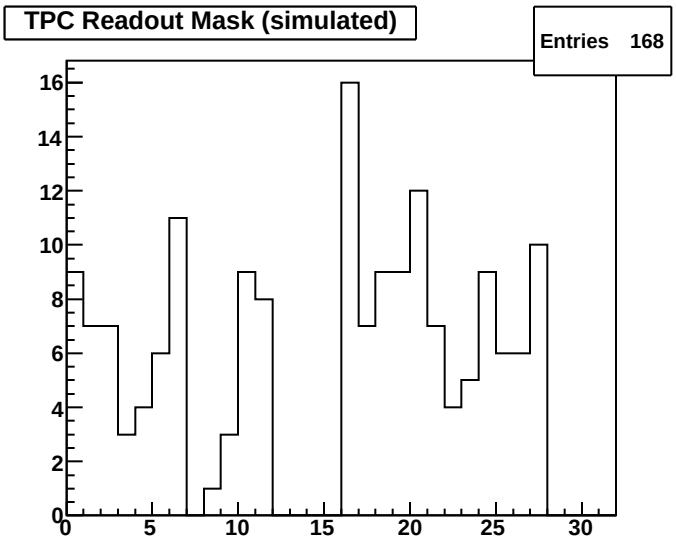

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

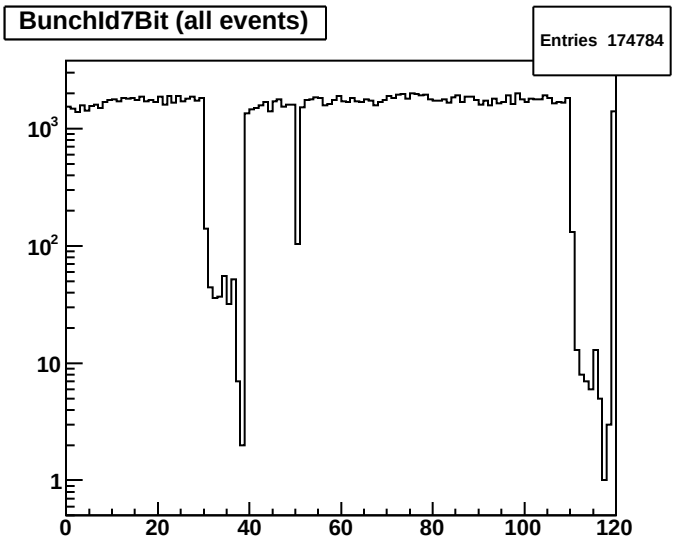

EMC L2 Input - HT23 bits
Entries 1398272
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

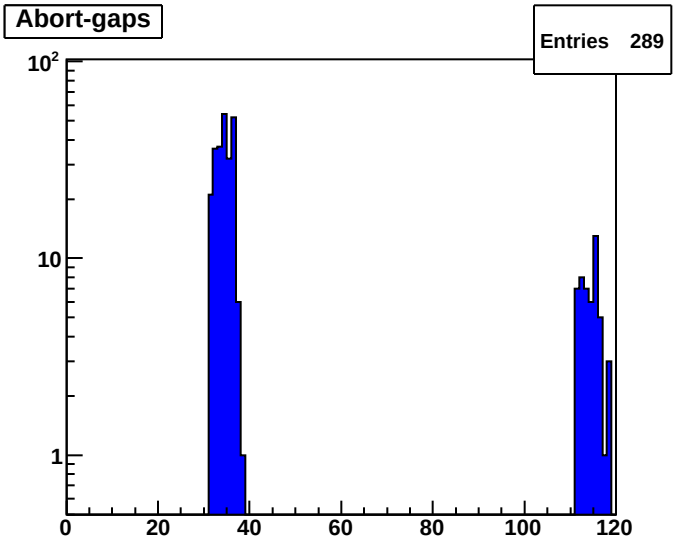

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

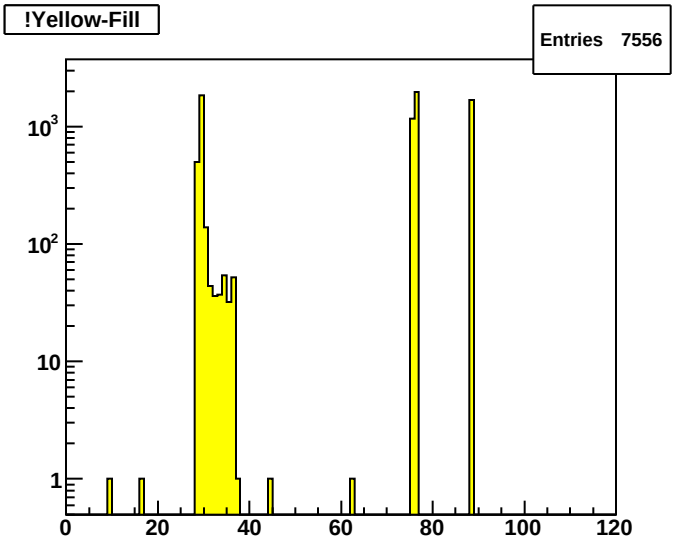

DAQ10k HT bits
Entries 97

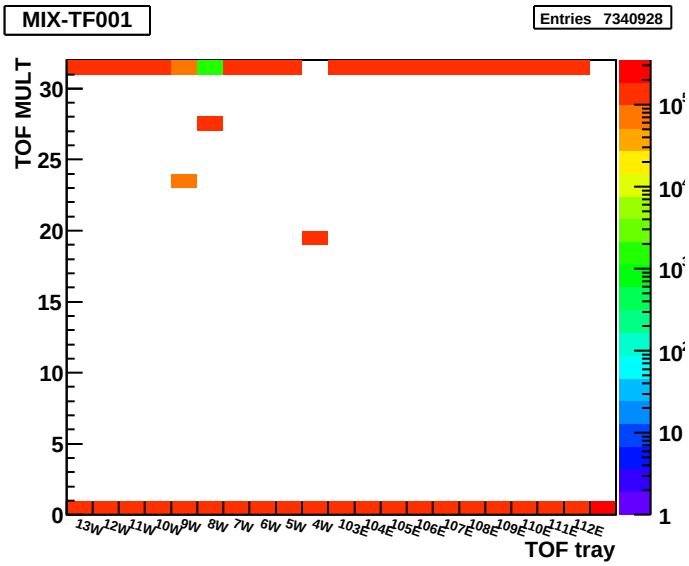

DAQ10k HT bits errors
Entries 0

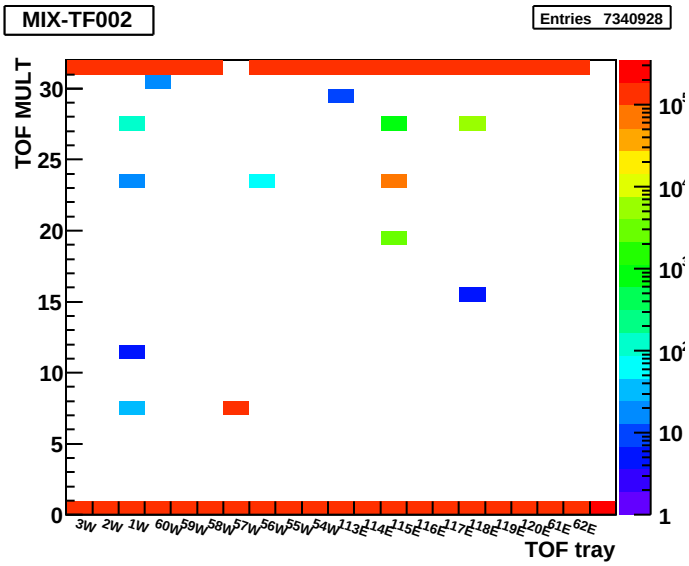

TPC Readout Mask (simulated)
Entries 168

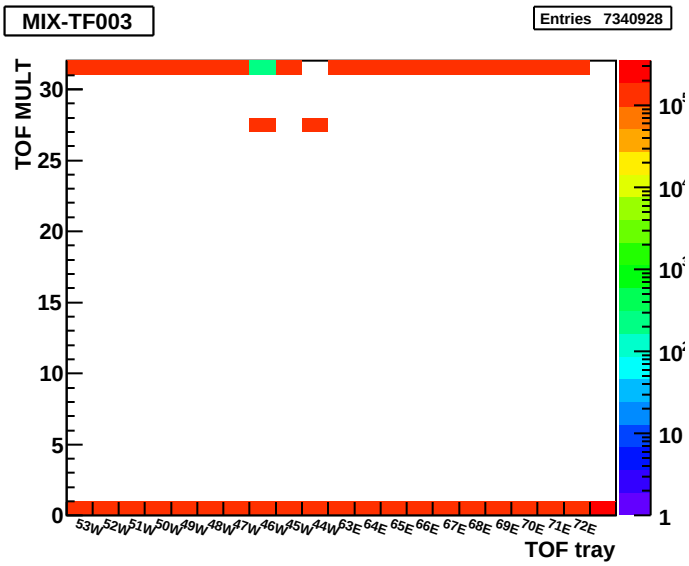

BunchId7Bit (all events)
Entries 174784

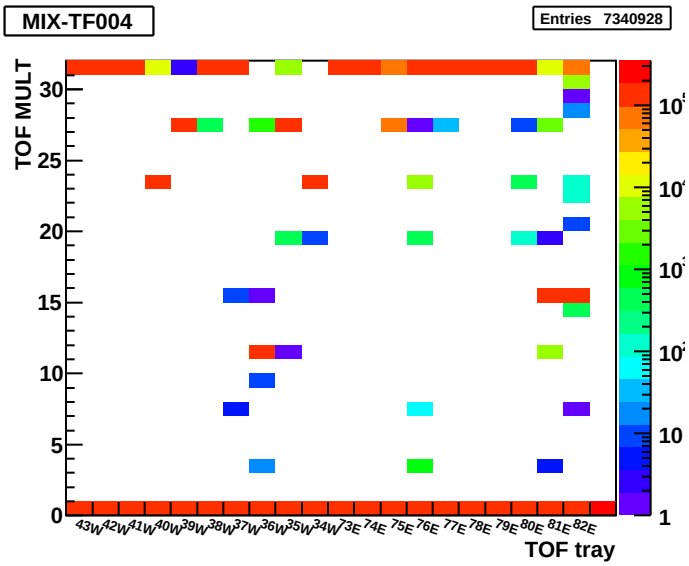

Abort-gaps
Entries 289

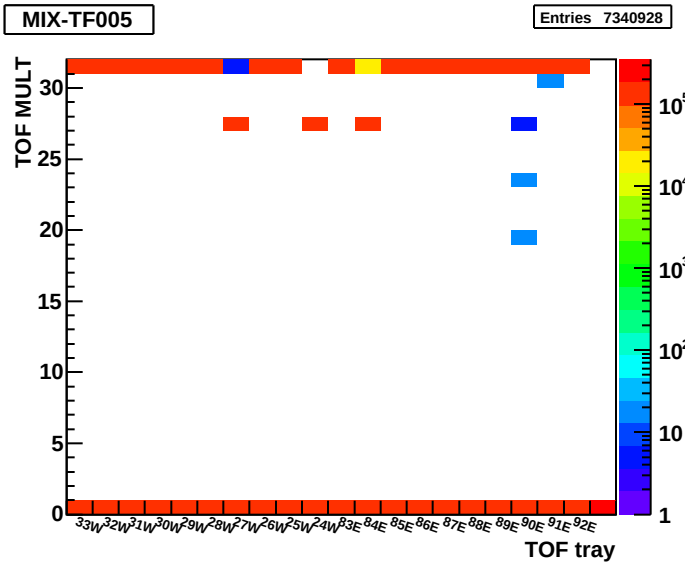

!Yellow-Fill
Entries 7556

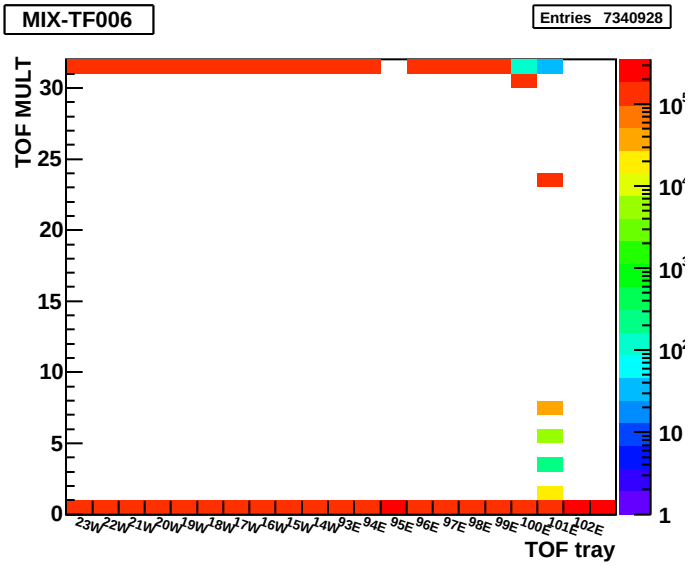

MIX-TF001
Entries 7340928
TOF MULT
TOF tray

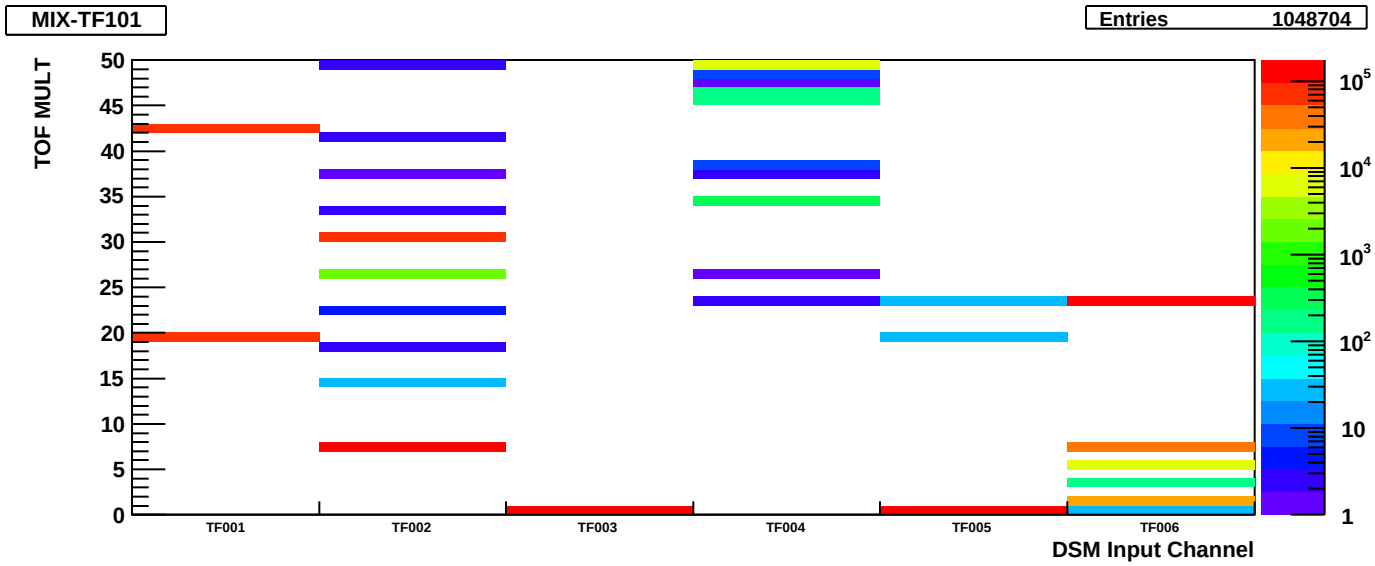

MIX-TF002
Entries 7340928
TOF MULT
TOF tray

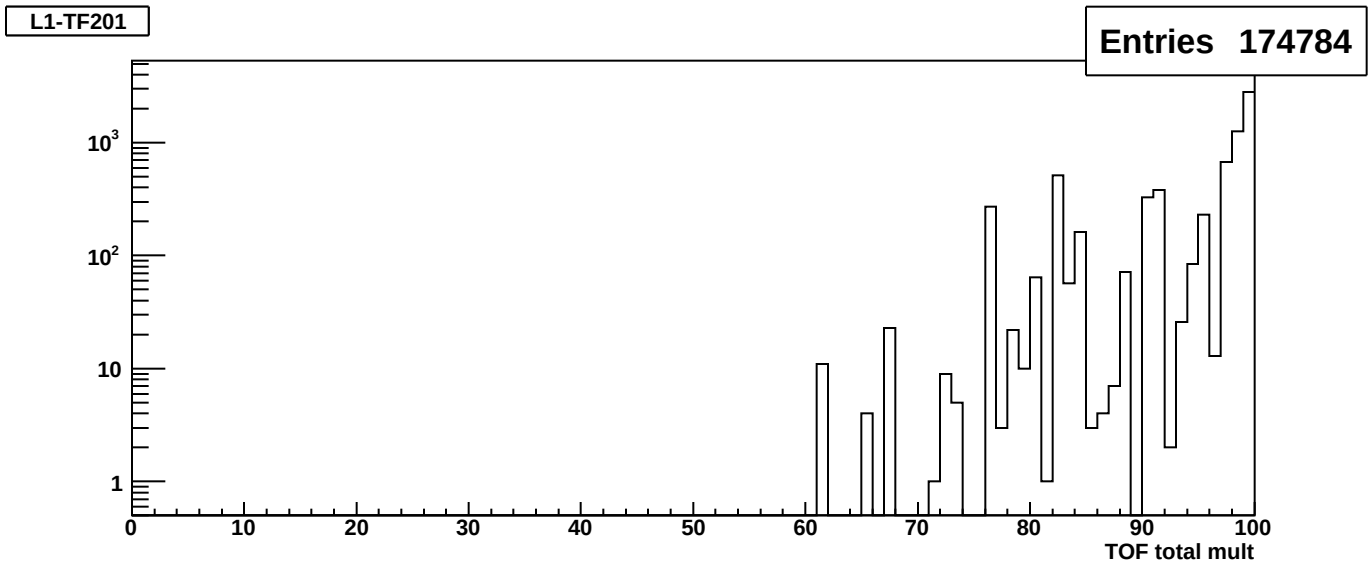

MIX-TF003
Entries 7340928
TOF MULT
TOF tray

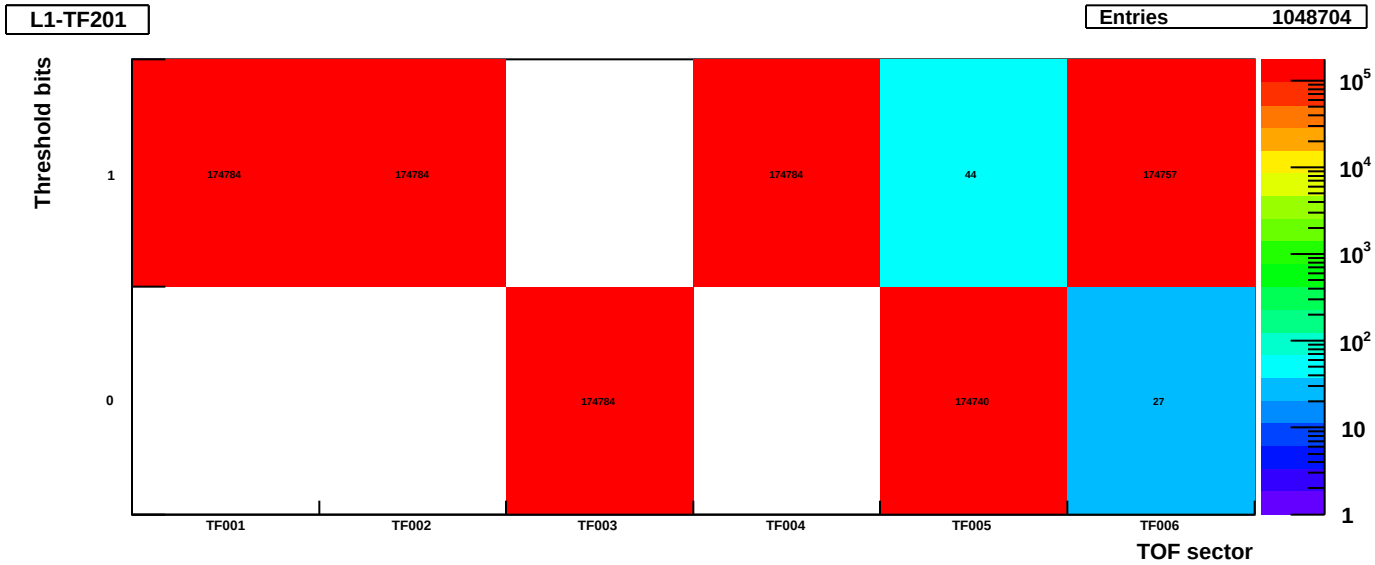

MIX-TF004
Entries 7340928
TOF MULT
TOF tray

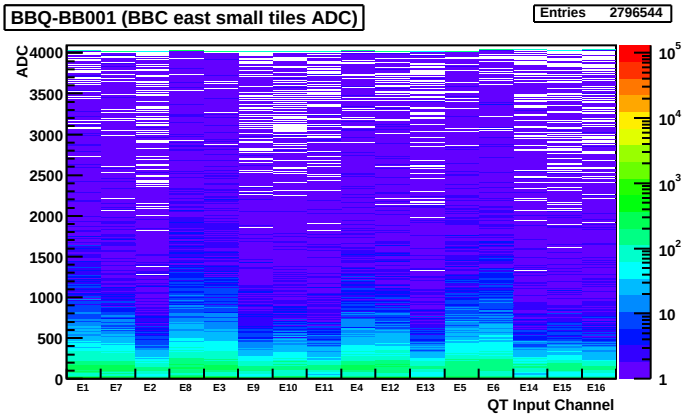

MIX-TF005
Entries 7340928
TOF MULT
TOF tray

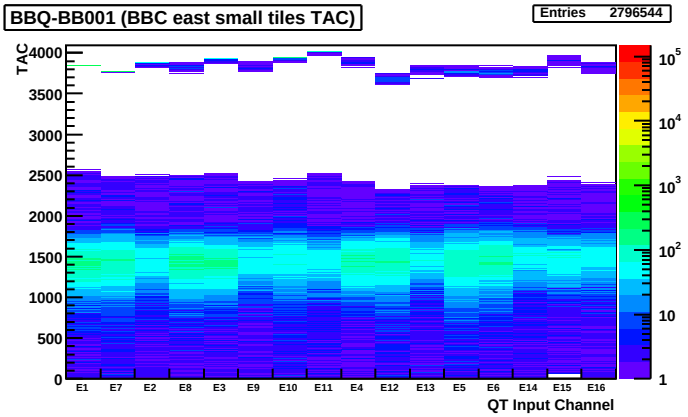

MIX-TF006
Entries 7340928
TOF MULT
TOF tray

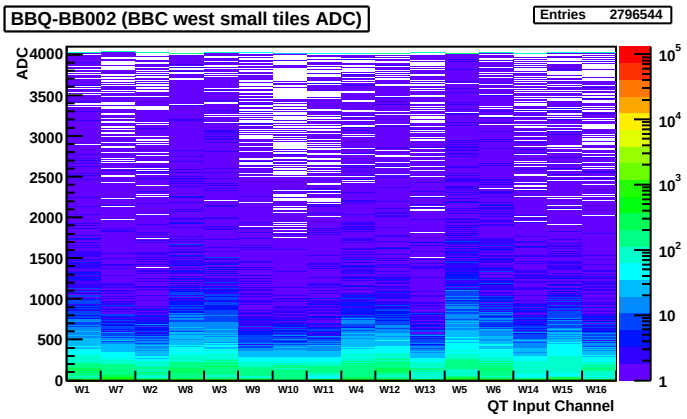

MIX-TF101
Entries 1048704
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

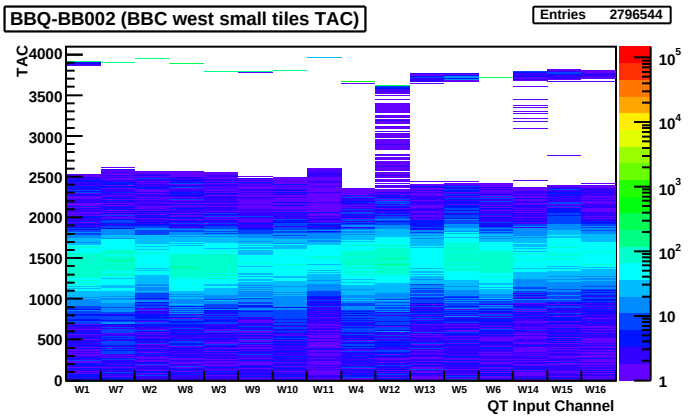

L1-TF201
Entries 174784
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

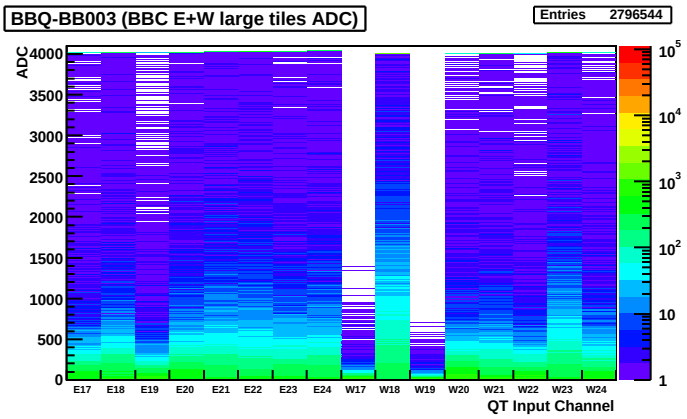

L1-TF201
Entries 1048704
Threshold bits
1
0
174784
174784
174784
44
174757
174784
174740
27
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4
10^3
10^2
10
1

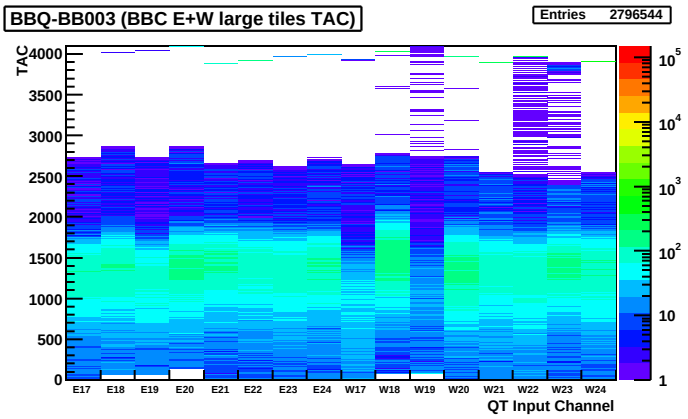

BBQ-BB001 (BBC east small tiles ADC)
Entries 2796544
ADC
QT Input Channel

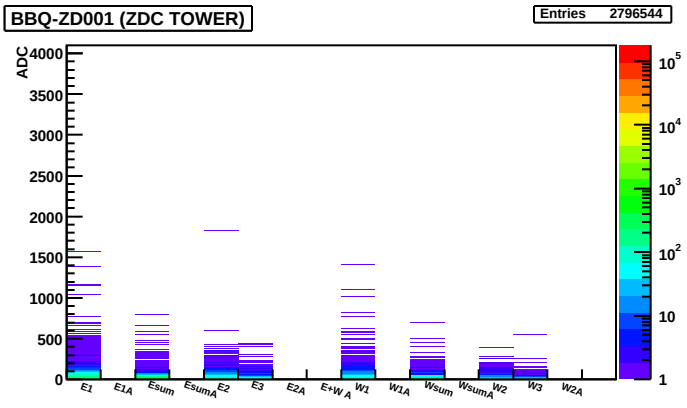

BBQ-BB001 (BBC east small tiles TAC)
Entries 2796544
TAC
QT Input Channel

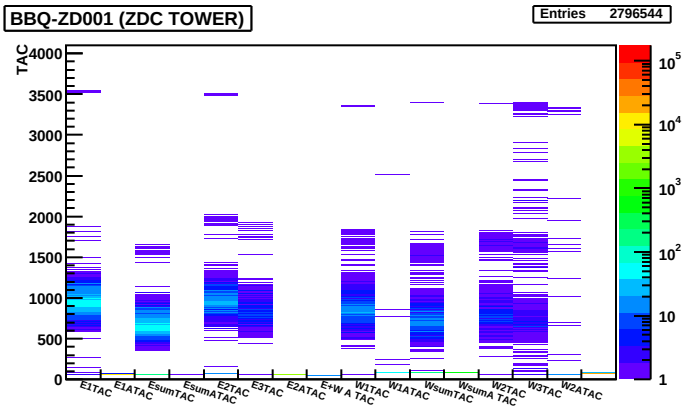

BBQ-BB002 (BBC west small tiles ADC)
Entries 2796544
ADC
QT Input Channel

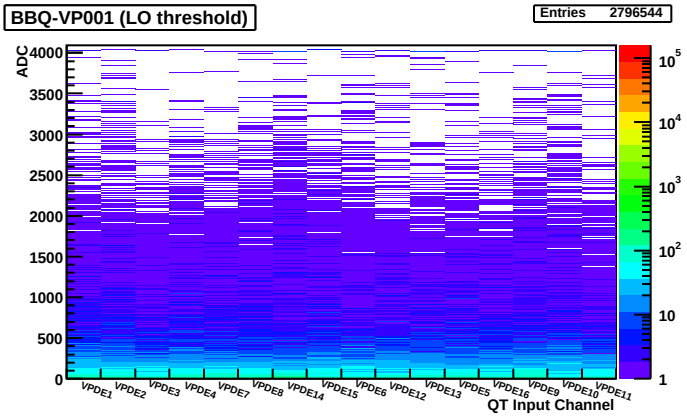

BBQ-BB002 (BBC west small tiles TAC)
Entries 2796544
TAC
QT Input Channel

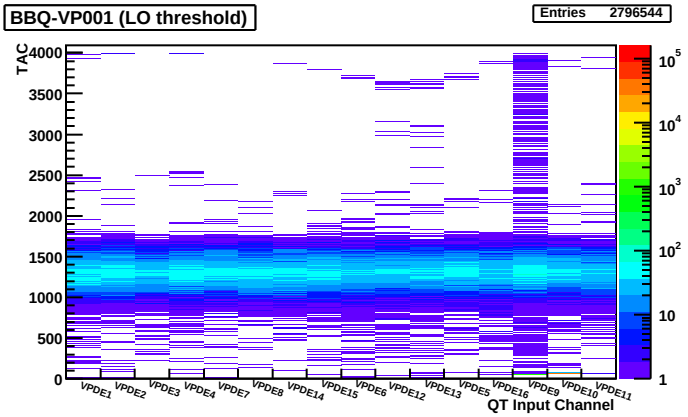

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2796544
ADC
QT Input Channel

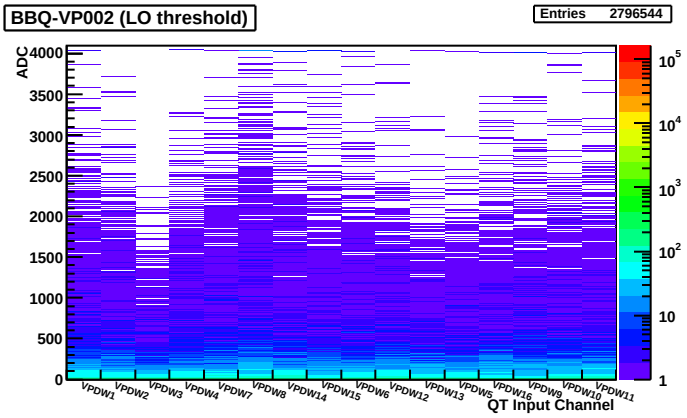

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2796544
TAC
QT Input Channel

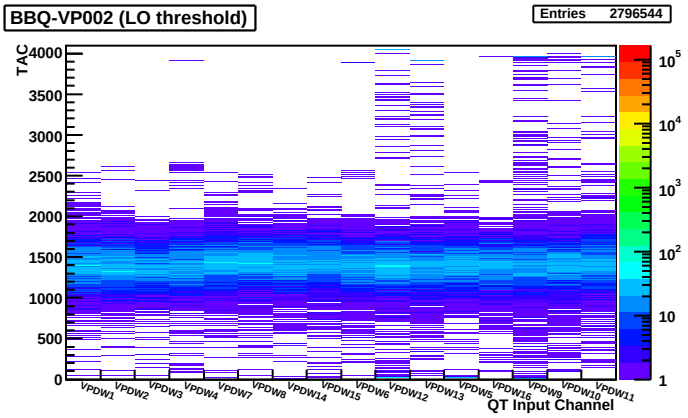

BBQ-ZD001 (ZDC TOWER)
Entries 2796544
ADC

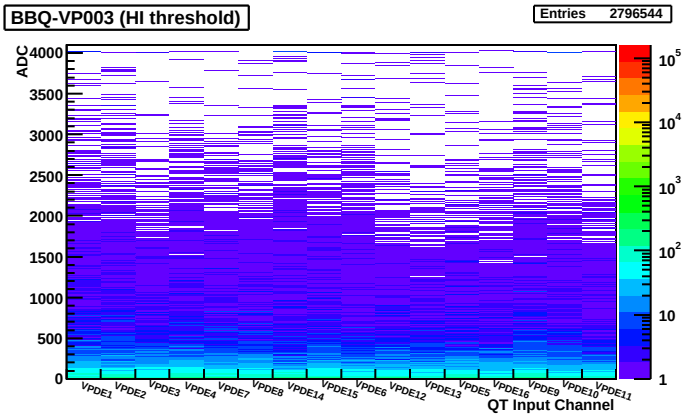

BBQ-ZD001 (ZDC TOWER)
Entries 2796544
TAC

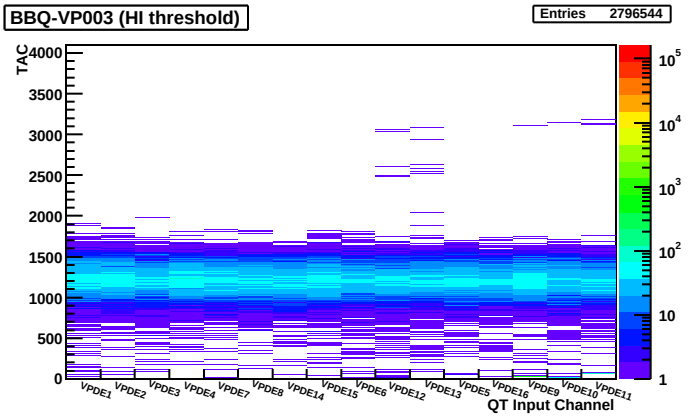

BBQ-VP001 (LO threshold)
Entries 2796544
ADC
QT Input Channel

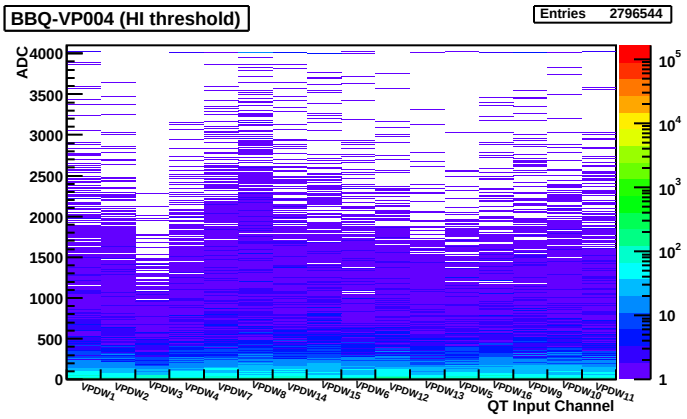

BBQ-VP001 (LO threshold)
Entries 2796544
TAC
QT Input Channel

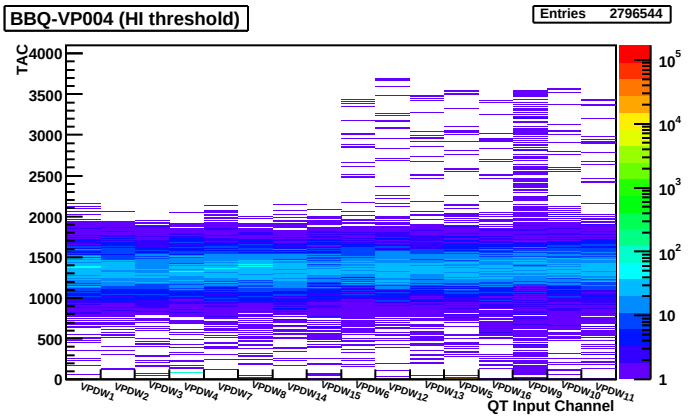

BBQ-VP002 (LO threshold)
Entries 2796544
ADC
QT Input Channel

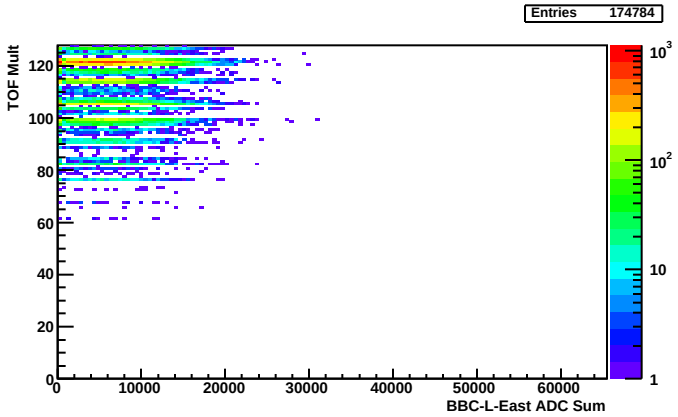

BBQ-VP002 (LO threshold)
Entries 2796544
TAC
QT Input Channel

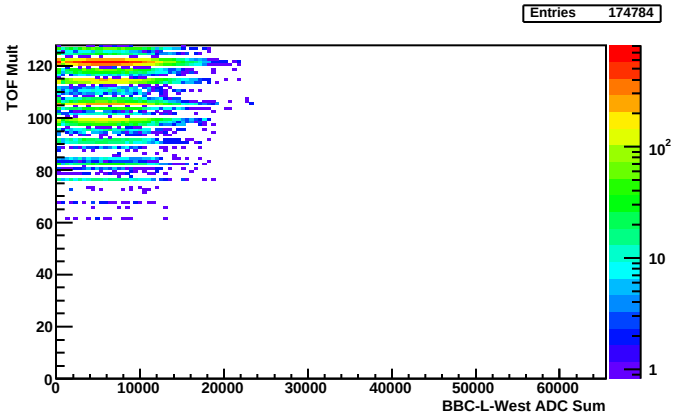

BBQ-VP003 (HI threshold)
Entries 2796544
ADC
QT Input Channel

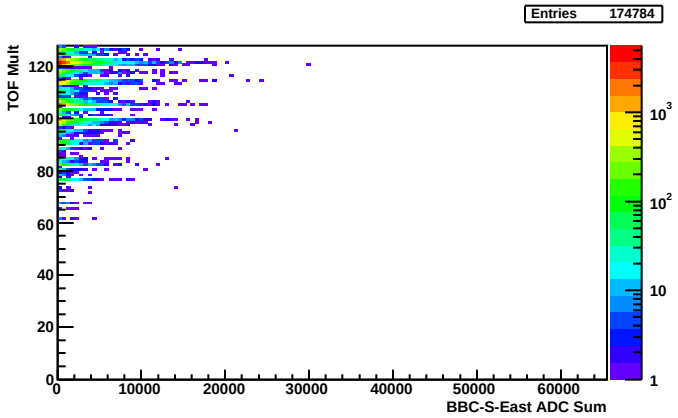

BBQ-VP003 (HI threshold)
Entries 2796544
TAC
QT Input Channel

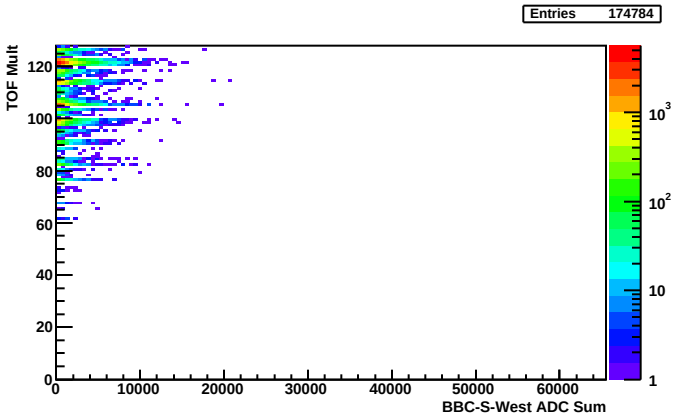

BBQ-VP004 (HI threshold)
Entries 2796544
ADC
QT Input Channel

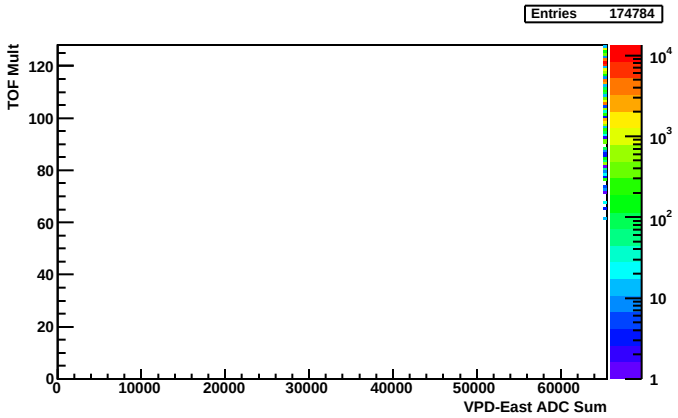

BBQ-VP004 (HI threshold)
Entries 2796544
TAC
QT Input Channel

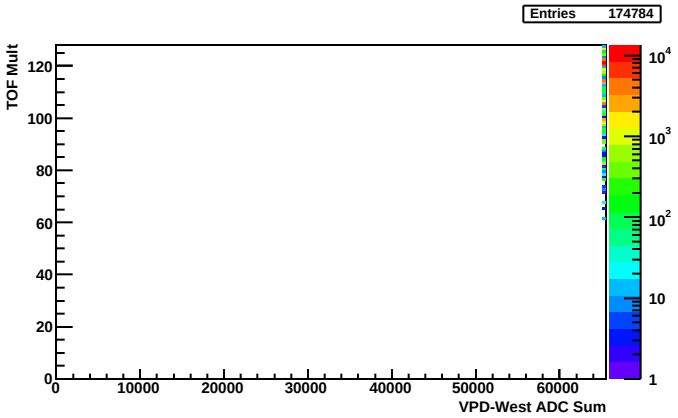

Entries 174784
TOF Mult
BBC-L-East ADC Sum

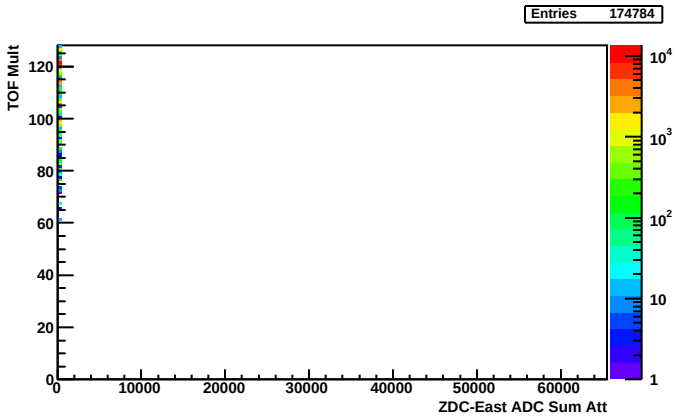

Entries 174784
TOF Mult
BBC-L-West ADC Sum

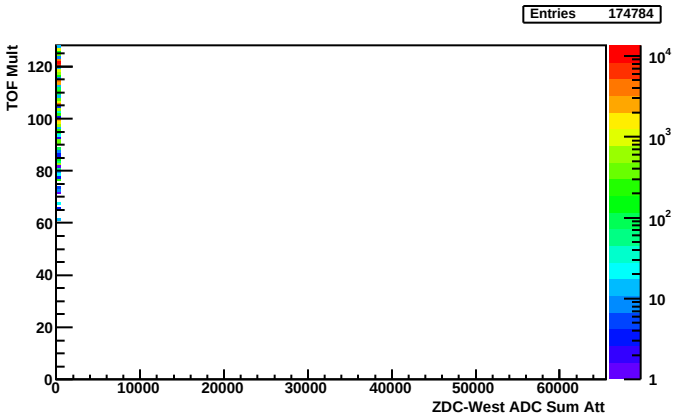

Entries 174784
TOF Mult
BBC-S-East ADC Sum

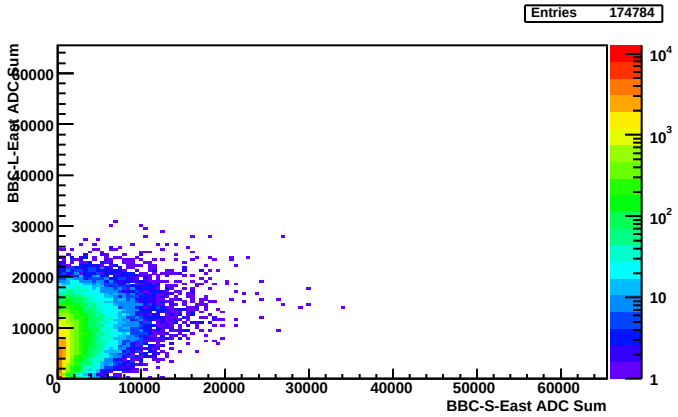

Entries 174784
TOF Mult
BBC-S-West ADC Sum


Entries 174784
TOF Mult
VPD-East ADC Sum


Entries 174784
TOF Mult
VPD-West ADC Sum


Entries 174784
TOF Mult
ZDC-East ADC Sum Att


Entries 174784
TOF Mult
ZDC-West ADC Sum Att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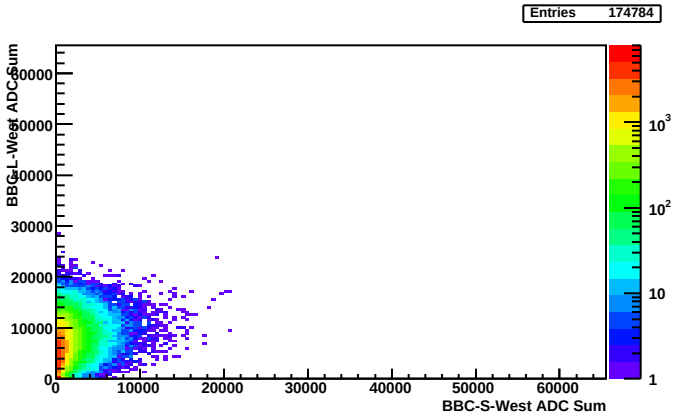
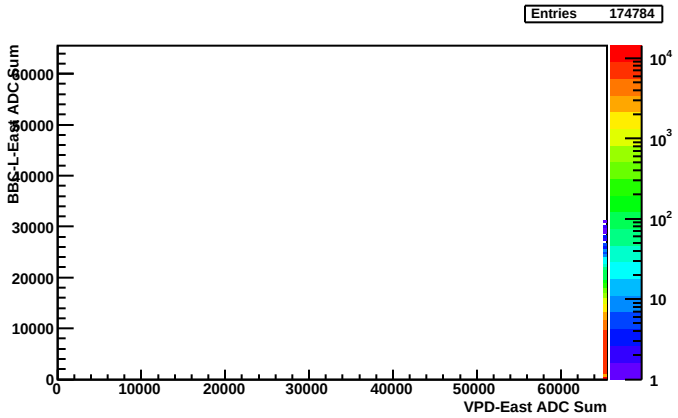
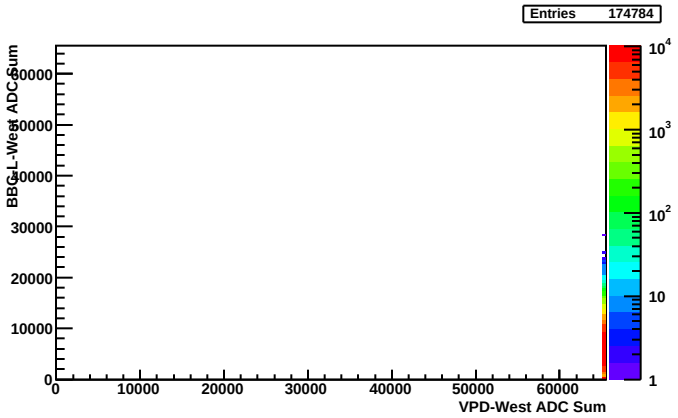
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 174784
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 174784
BBC-L-East ADC Sum
VPD-East ADC Sum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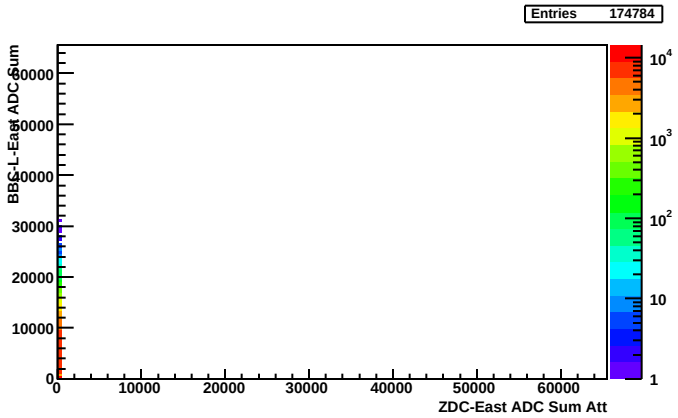
BBC-L-West ADC Sum
VPD-West ADC Sum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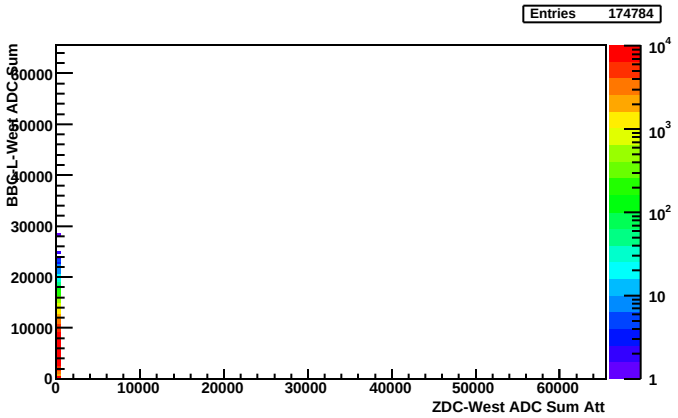
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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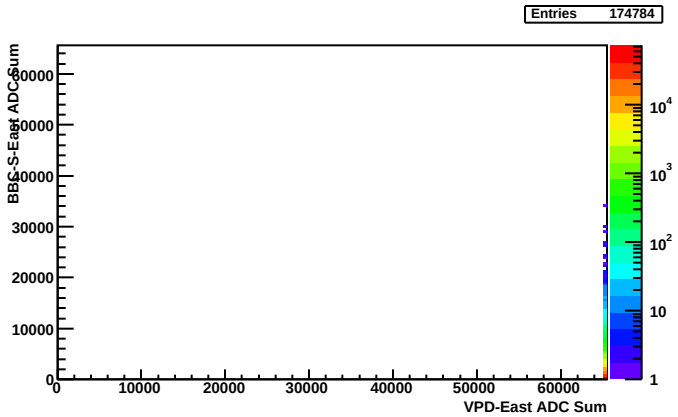
ZDC-West ADC Sum Att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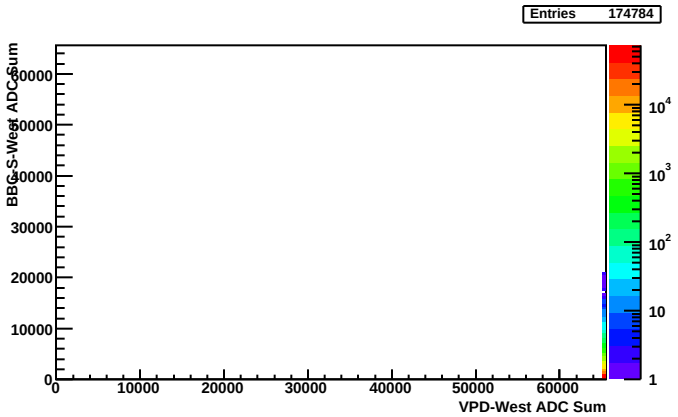
BBC-S-East ADC Sum
VPD-East ADC Sum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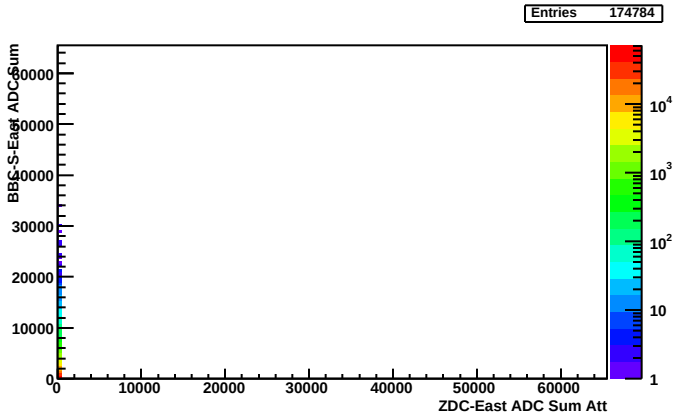
BBC-S-West ADC Sum
VPD-West ADC Sum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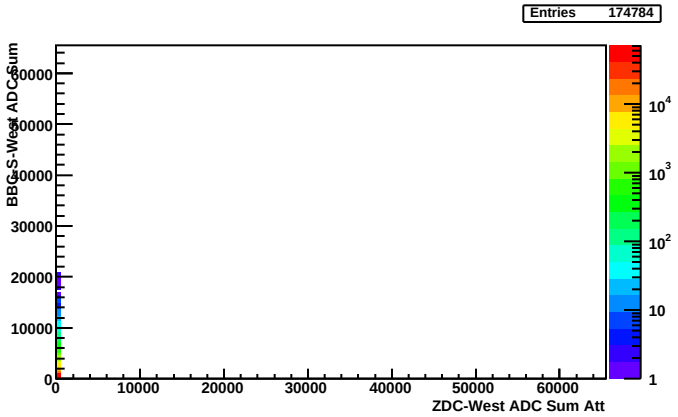
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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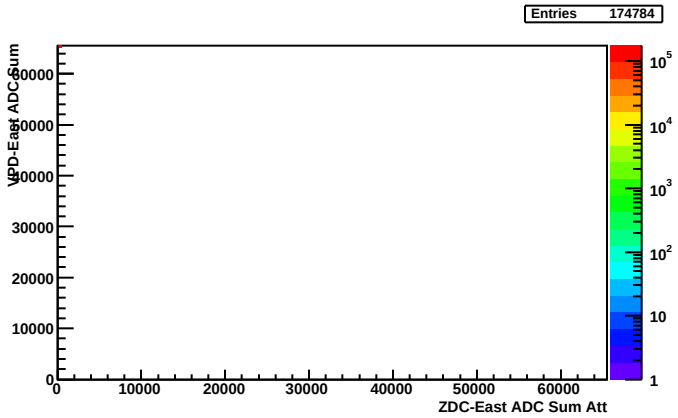
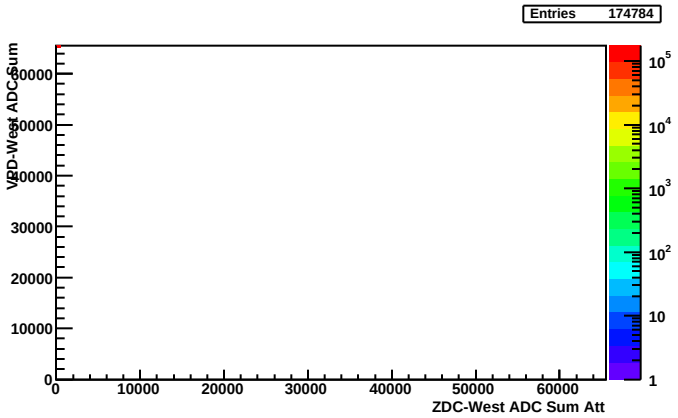
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 174784
VPD-East ADC Sum
ZDC-East ADC Sum Att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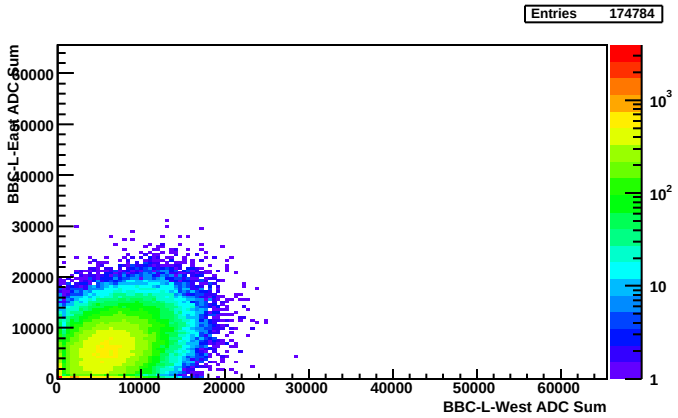
ZDC-West ADC Sum Att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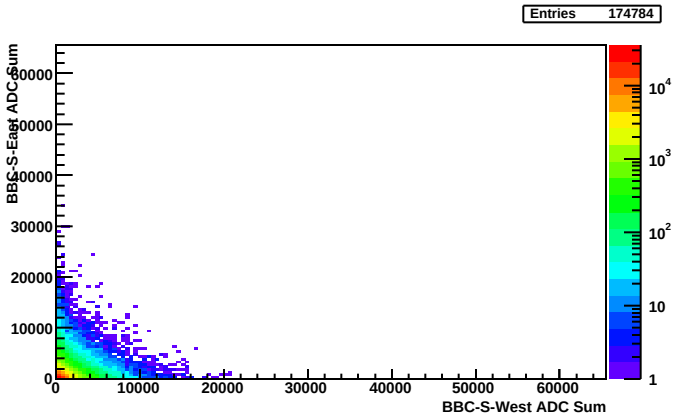
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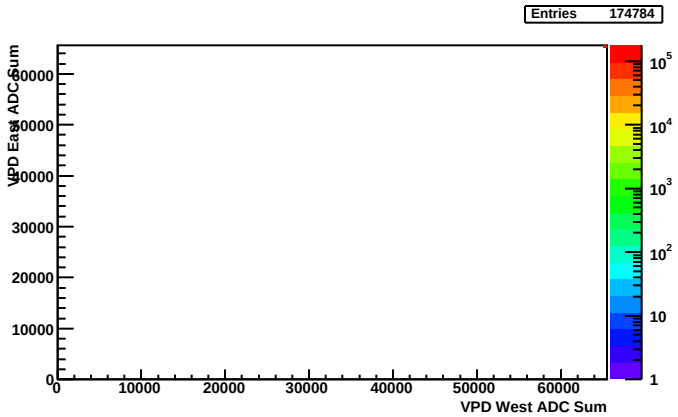
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 174784
VPD East ADC Sum
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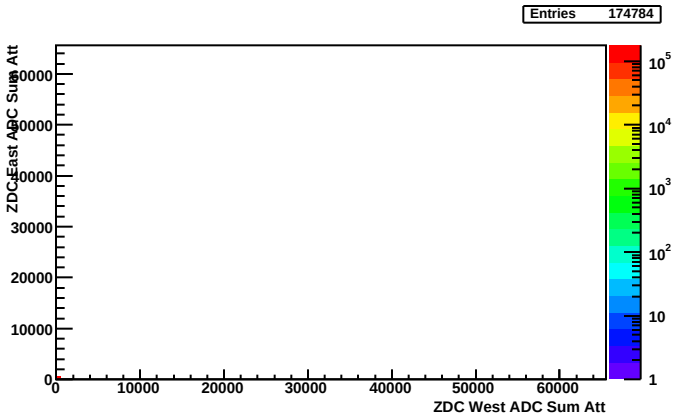
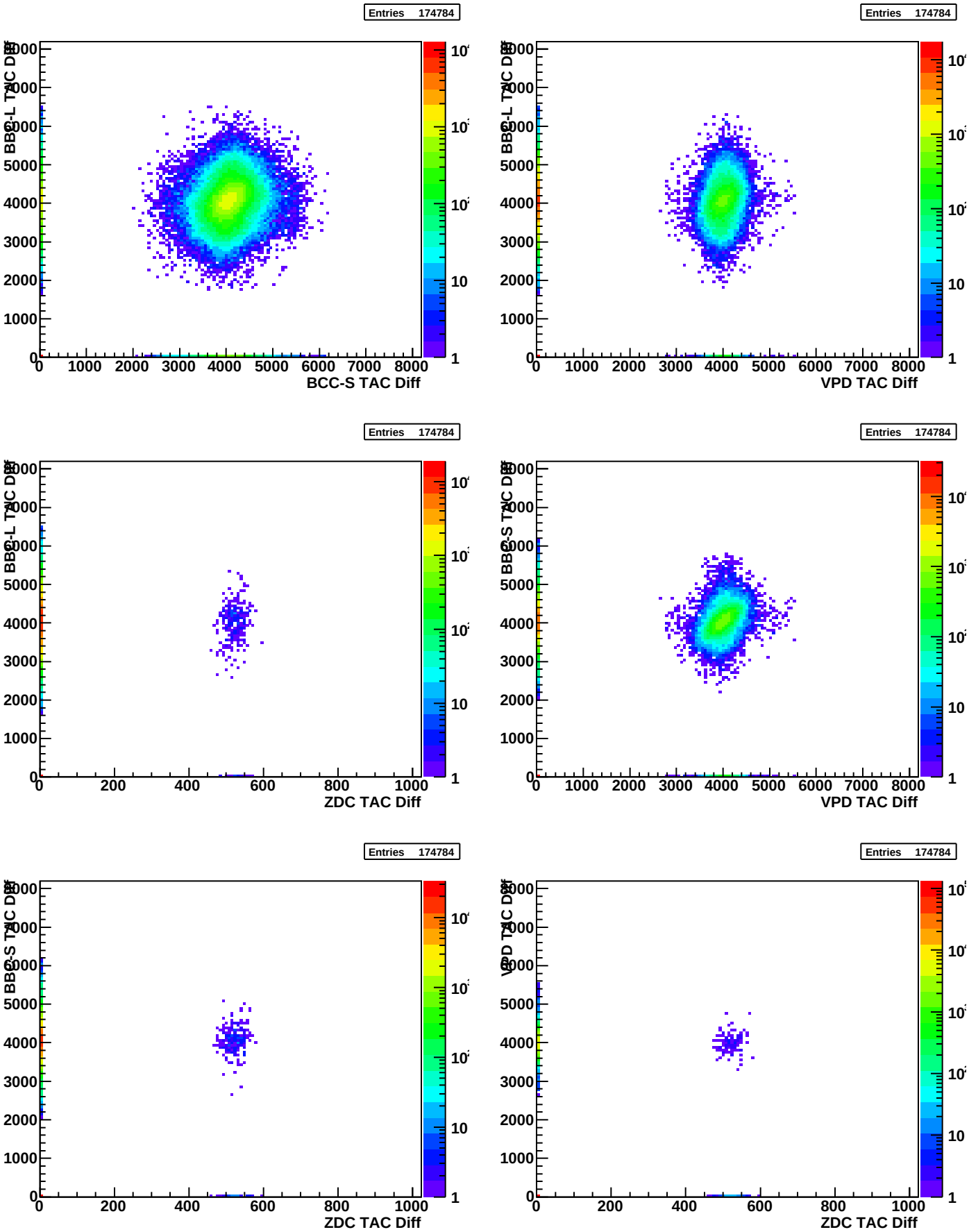
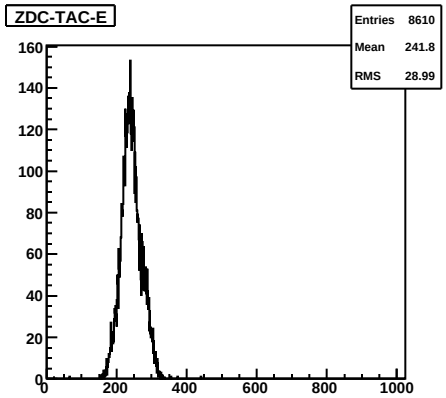
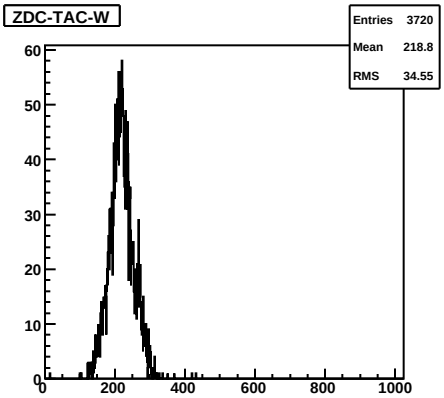
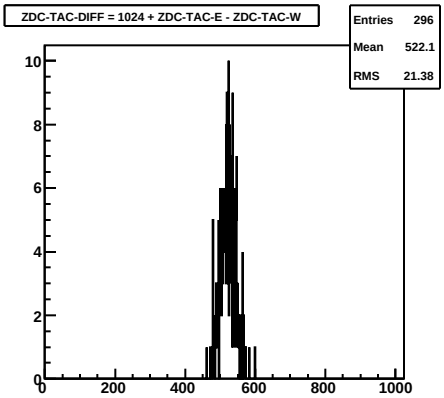
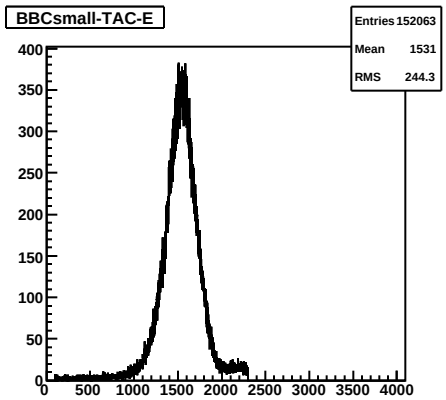

Entries 174784
ZDC East ADC Sum Att
ZDC West ADC Sum Att

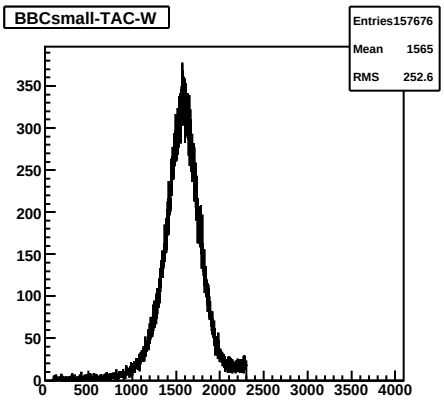

Entries 174784
BBC-L TAC Diff
BCC-S TAC Diff
Entries 174784
BBC-L TAC Diff
VPD TAC Diff
Entries 174784
BBC-L TAC Diff
ZDC TAC Diff
Entries 174784
BBC-S TAC Diff
VPD TAC Diff
Entries 174784
BBC-S TAC Diff
ZDC TAC Diff
Entries 174784
VPD TAC Diff
ZDC TAC Diff

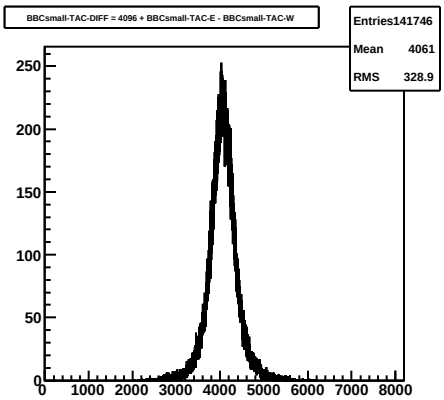

ZDC-TAC-E
Entries 8610
Mean 241.8
RMS 28.99

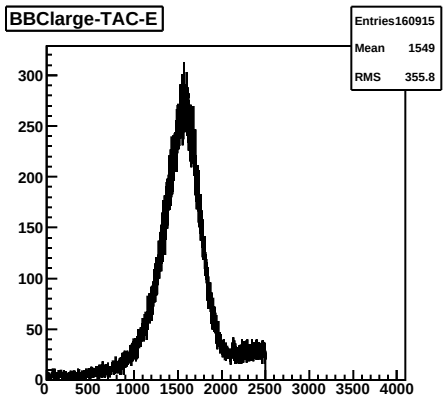

ZDC-TAC-W
Entries 3720
Mean 218.8
RMS 34.55

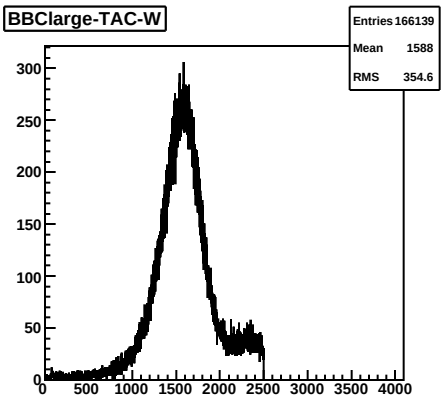

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 296
Mean 522.1
RMS 21.38

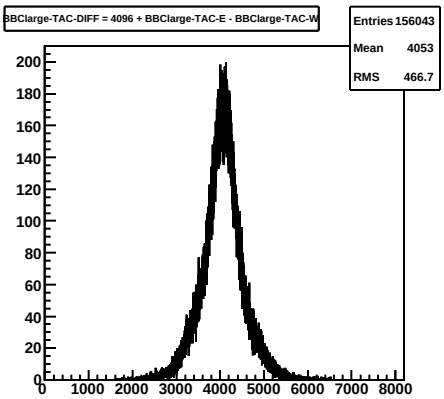

BBCsmall-TAC-E
Entries 152063
Mean 1531
RMS 244.3

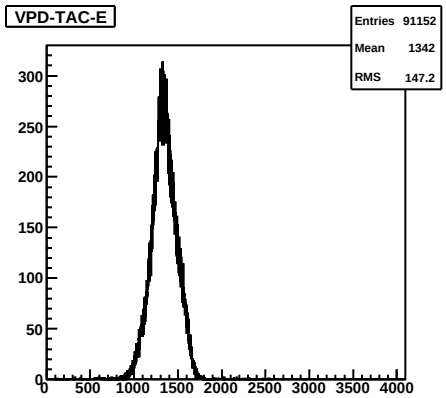

BBCsmall-TAC-W
Entries 157676
Mean 1565
RMS 252.6

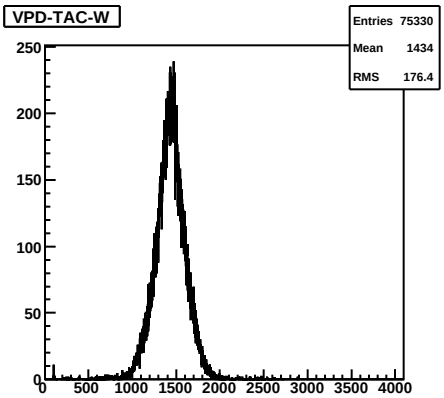

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 141746
Mean 4061
RMS 328.9

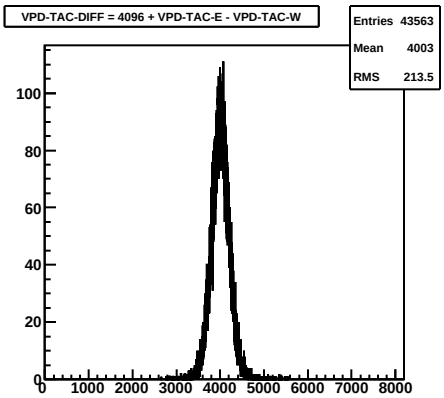

BBClarge-TAC-E
Entries 160915
Mean 1549
RMS 355.8

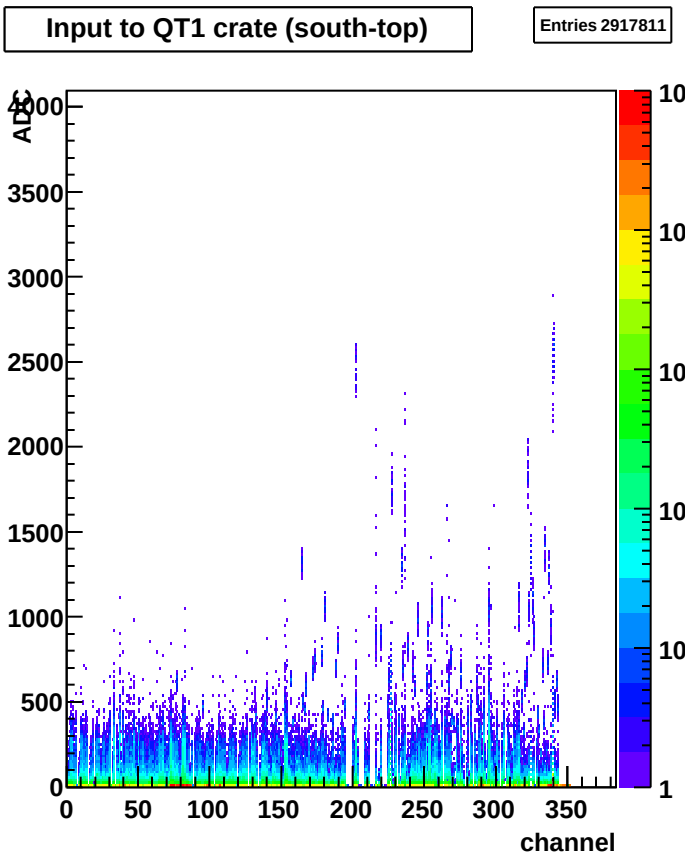

BBClarge-TAC-W
Entries 166139
Mean 1588
RMS 354.6

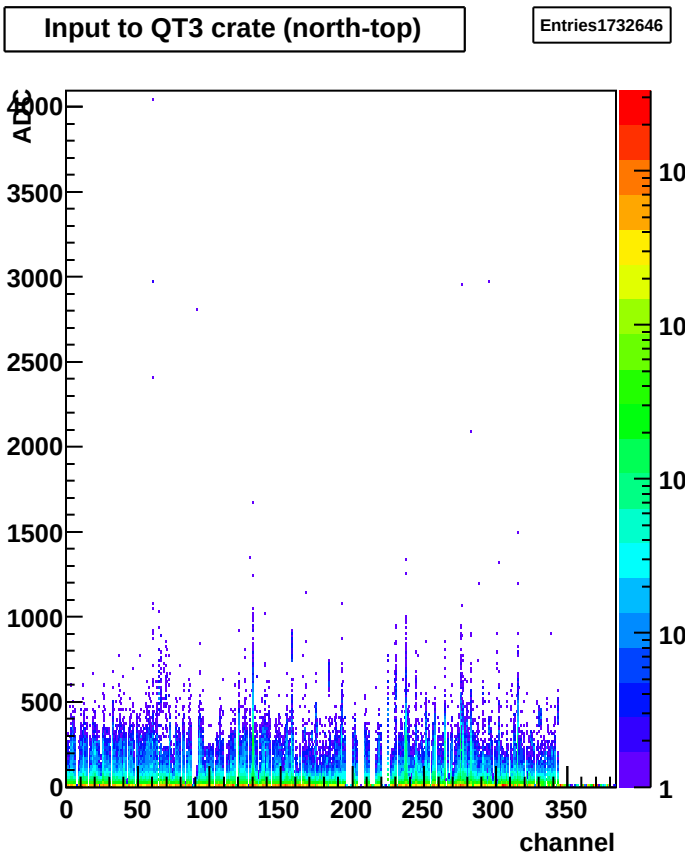

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 156043
Mean 4053
RMS 466.7

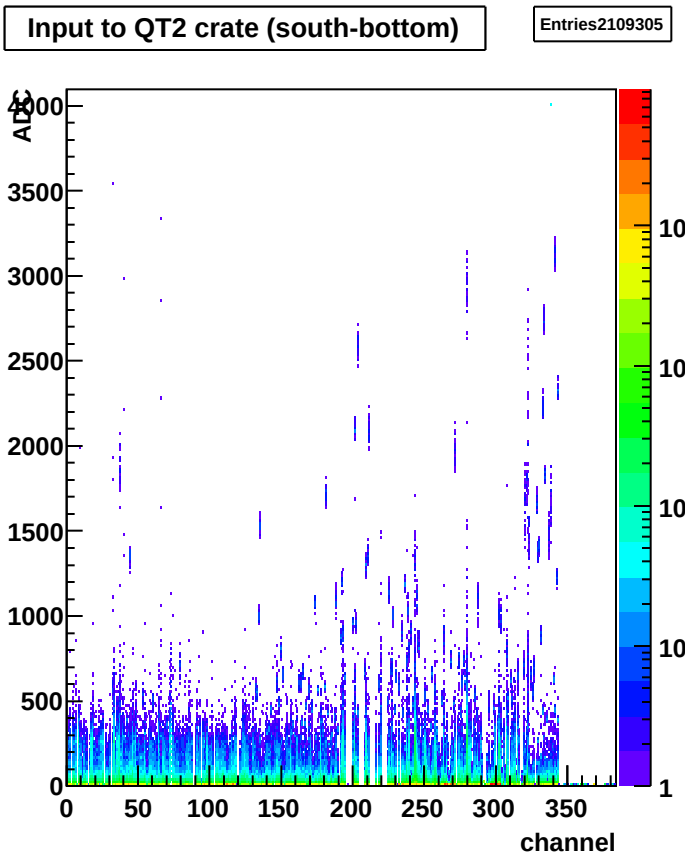

VPD-TAC-E
Entries 91152
Mean 1342
RMS 147.2

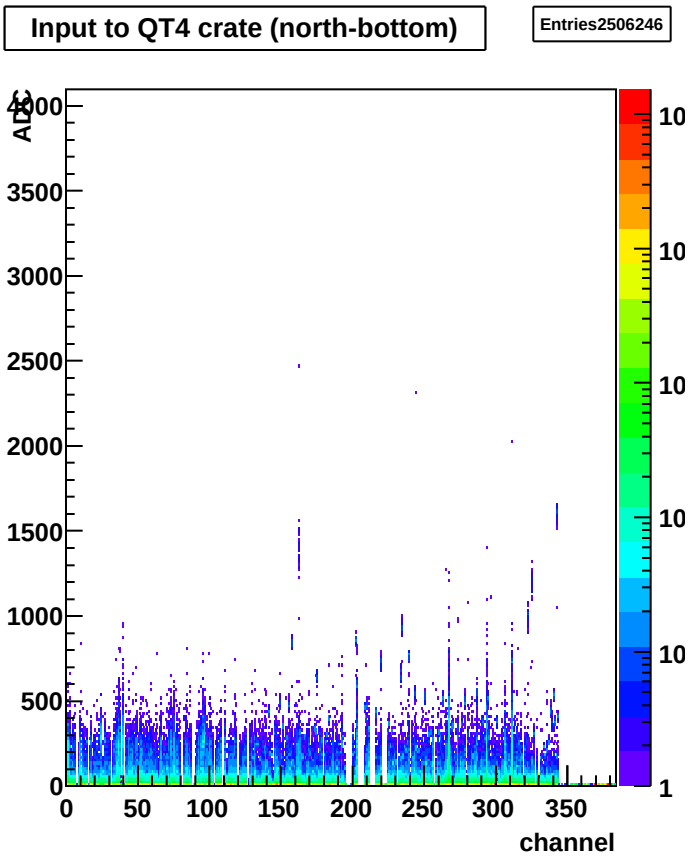

VPD-TAC-W
Entries 75330
Mean 1434
RMS 176.4

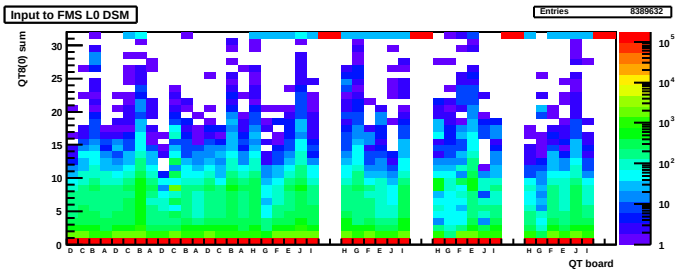

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 43563
Mean 4003
RMS 213.5

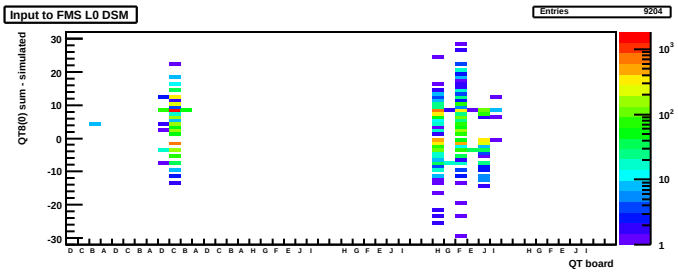

Input to QT1 crate (south-top)
Entries 2917811
ADC
channel

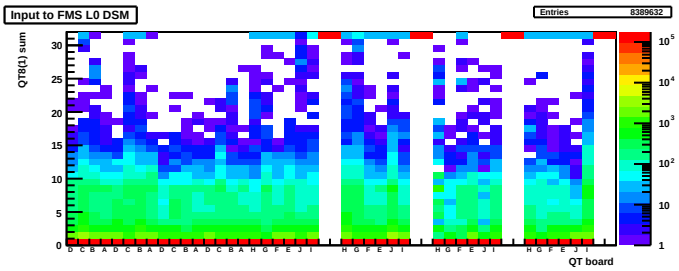

Input to QT3 crate (north-top)
Entries1732646
ADC
channel

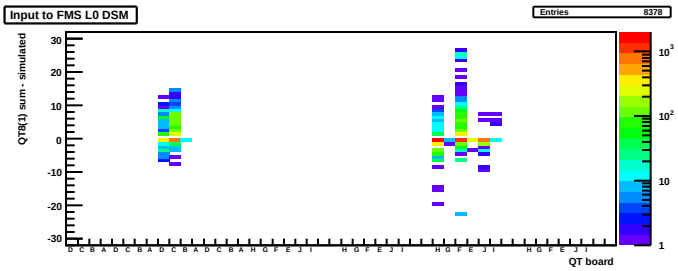

Input to QT2 crate (south-bottom)
Entries2109305
ADC
channel

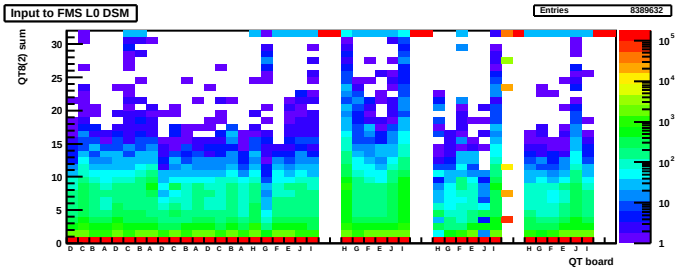

Input to QT4 crate (north-bottom)
Entries2506246
ADC
channel

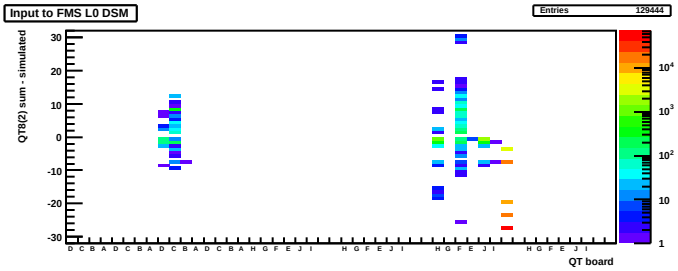

Input to FMS L0 DSM
Entries 8389632
QT8(0) sum
QT board

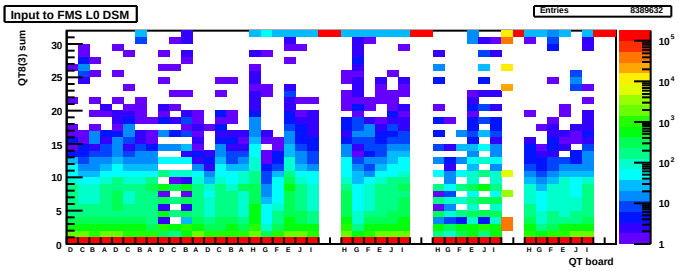

Input to FMS L0 DSM
Entries 9204
QT8(0) sum - simulated
QT board

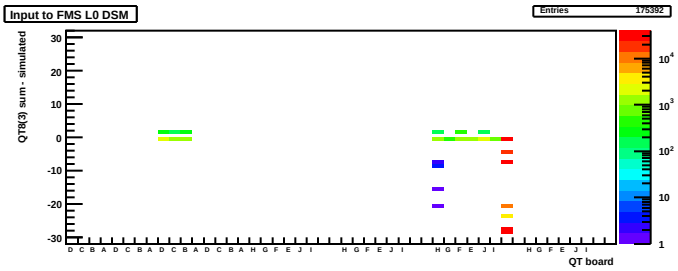

Input to FMS L0 DSM
Entries 8389632
QT8(1) sum
QT board

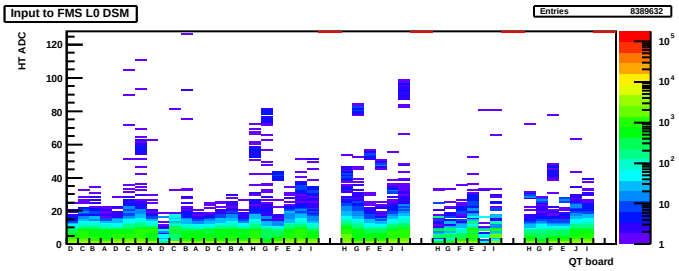

Input to FMS L0 DSM
Entries 8378
QT8(1) sum - simulated
QT board

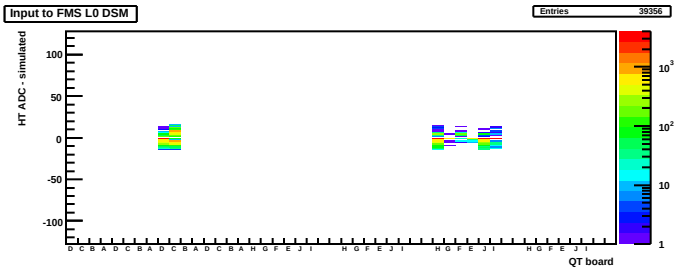

Input to FMS L0 DSM
Entries 8389632
QT8(2) sum
QT board

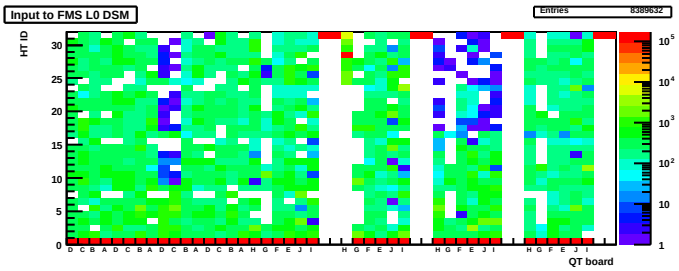

Input to FMS L0 DSM
Entries 129444
QT8(2) sum - simulated
QT board

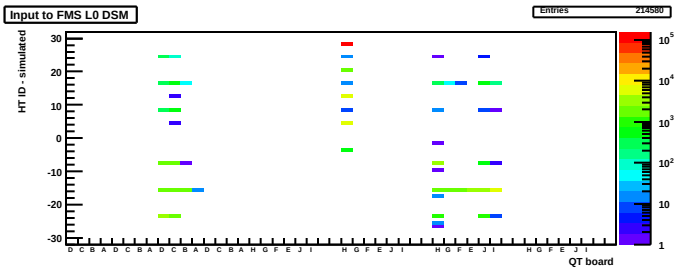

Input to FMS L0 DSM
Entries 8389632
QT8(3) sum
QT board

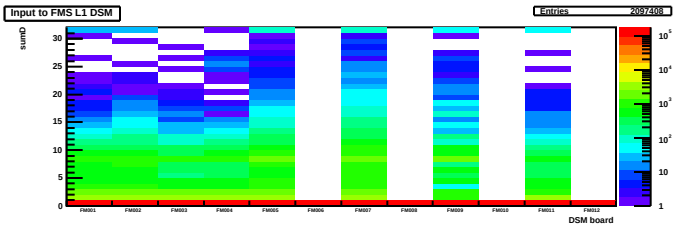

Input to FMS L0 DSM
Entries 175392
QT8(3) sum - simulated
QT board

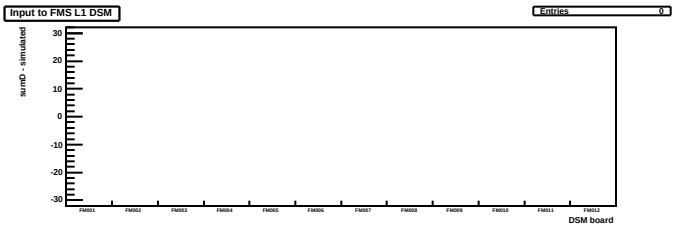

Input to FMS L0 DSM
Entries 8389632
HT ADC
QT board

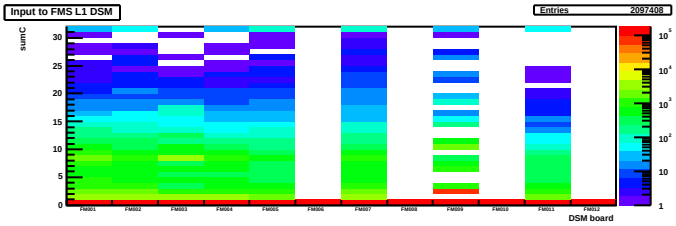

Input to FMS L0 DSM
Entries 39356
HT ADC - simulated
QT board

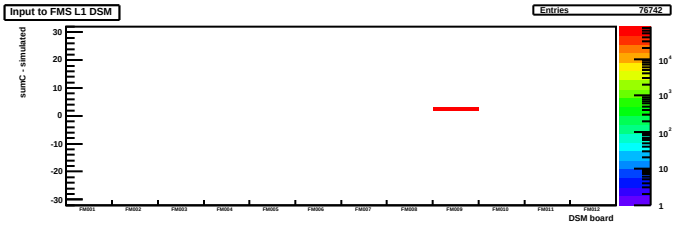

Input to FMS L0 DSM
Entries 8389632
HT ID
QT board

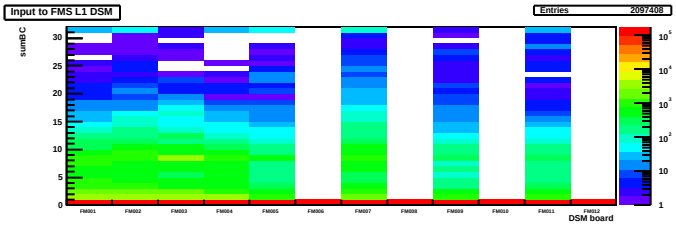

Input to FMS L0 DSM
Entries 214580
HT ID - simulated
QT board

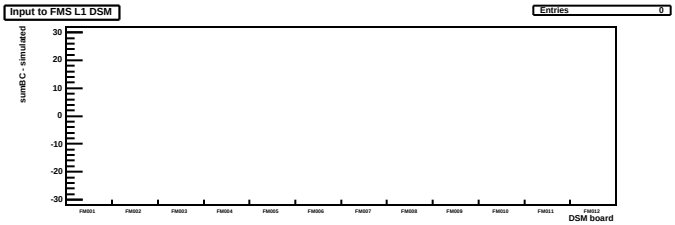

Input to FMS L1 DSM
Entries 2097408
sumD
DSM board

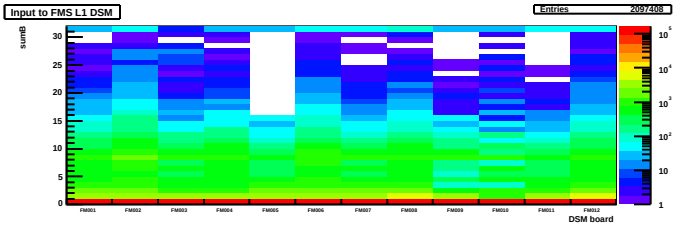

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

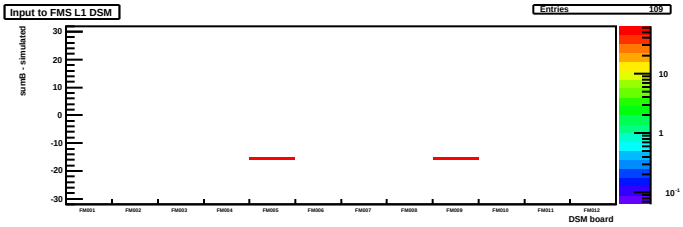

Input to FMS L1 DSM
Entries 2097408
sumC
DSM board

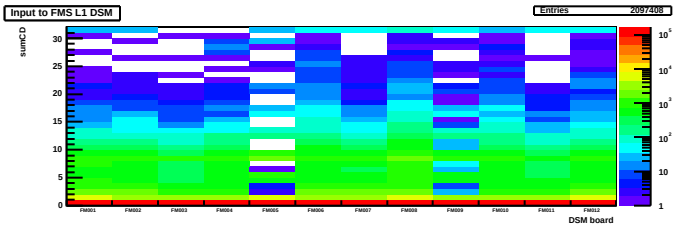

Input to FMS L1 DSM
Entries 76742
sumC - simulated
DSM board

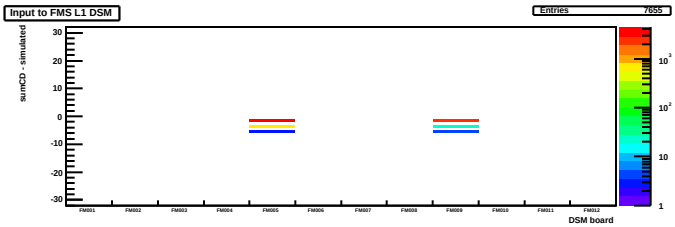

Input to FMS L1 DSM
Entries 2097408
sumBC
DSM board

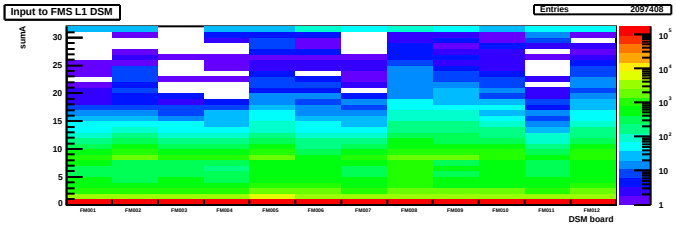

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

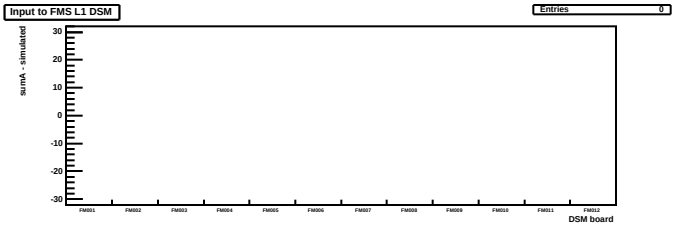

Input to FMS L1 DSM
Entries 2097408
sumB
DSM board

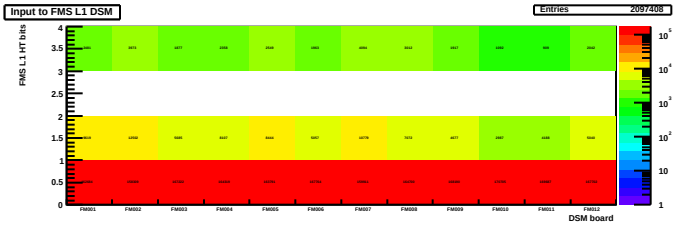

Input to FMS L1 DSM
Entries 109
sumB - simulated
DSM board

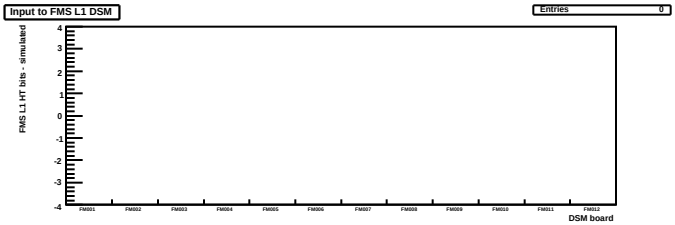

Input to FMS L1 DSM
Entries 2097408
sumCD
DSM board

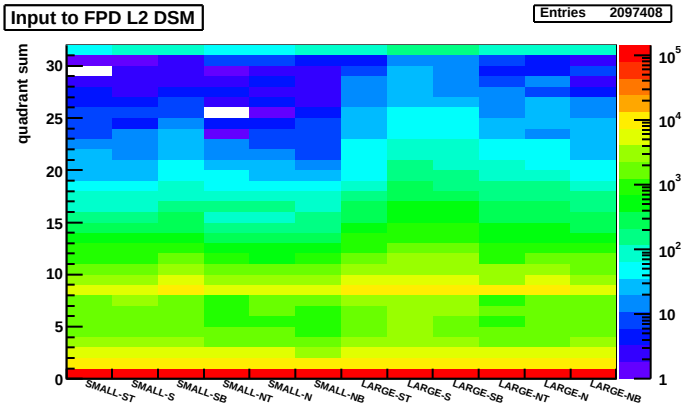

Input to FMS L1 DSM
Entries 7655
sumCD - simulated
DSM board

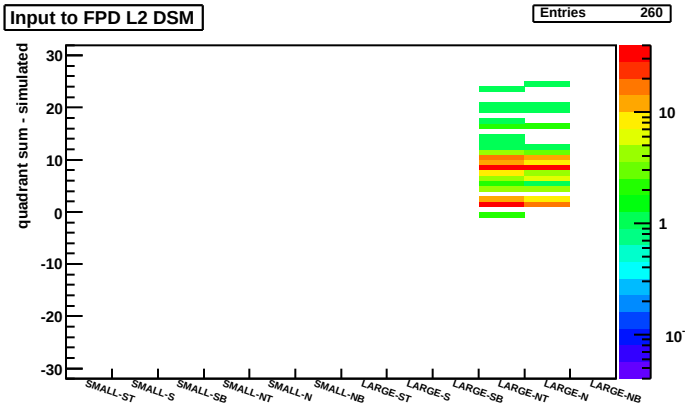

Input to FMS L1 DSM
Entries 2097408
sumA
DSM board

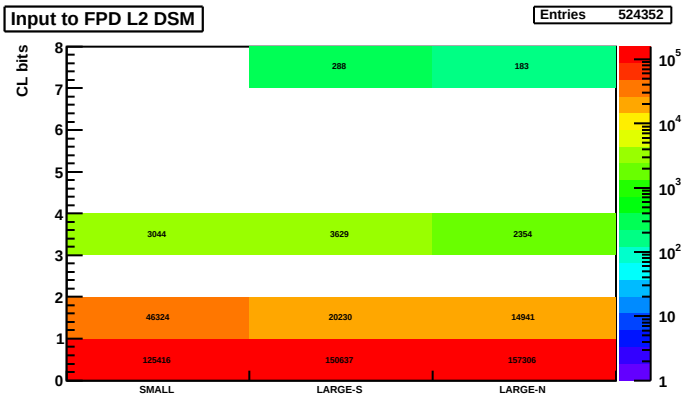

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

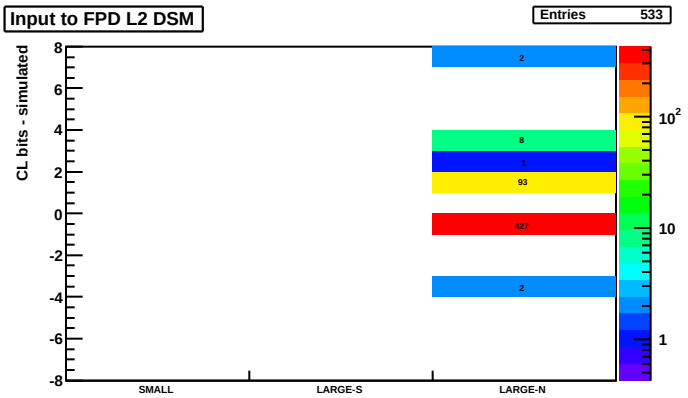

Input to FMS L1 DSM
Entries 2097408
FMS L1 HT bits
DSM board

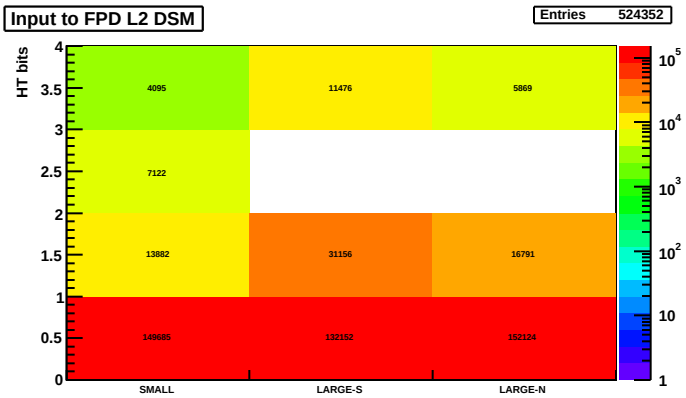

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

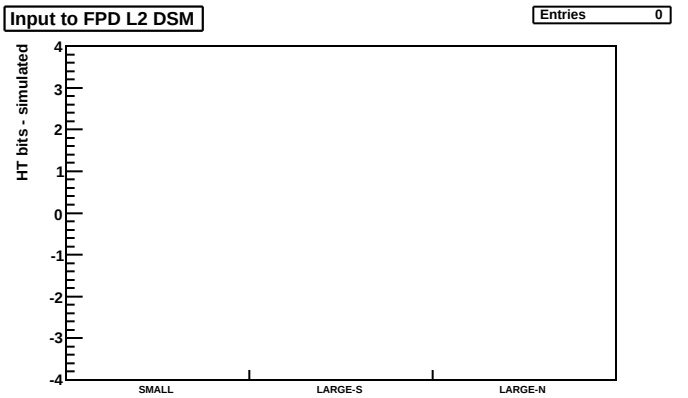

Input to FPD L2 DSM
Entries 2097408
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

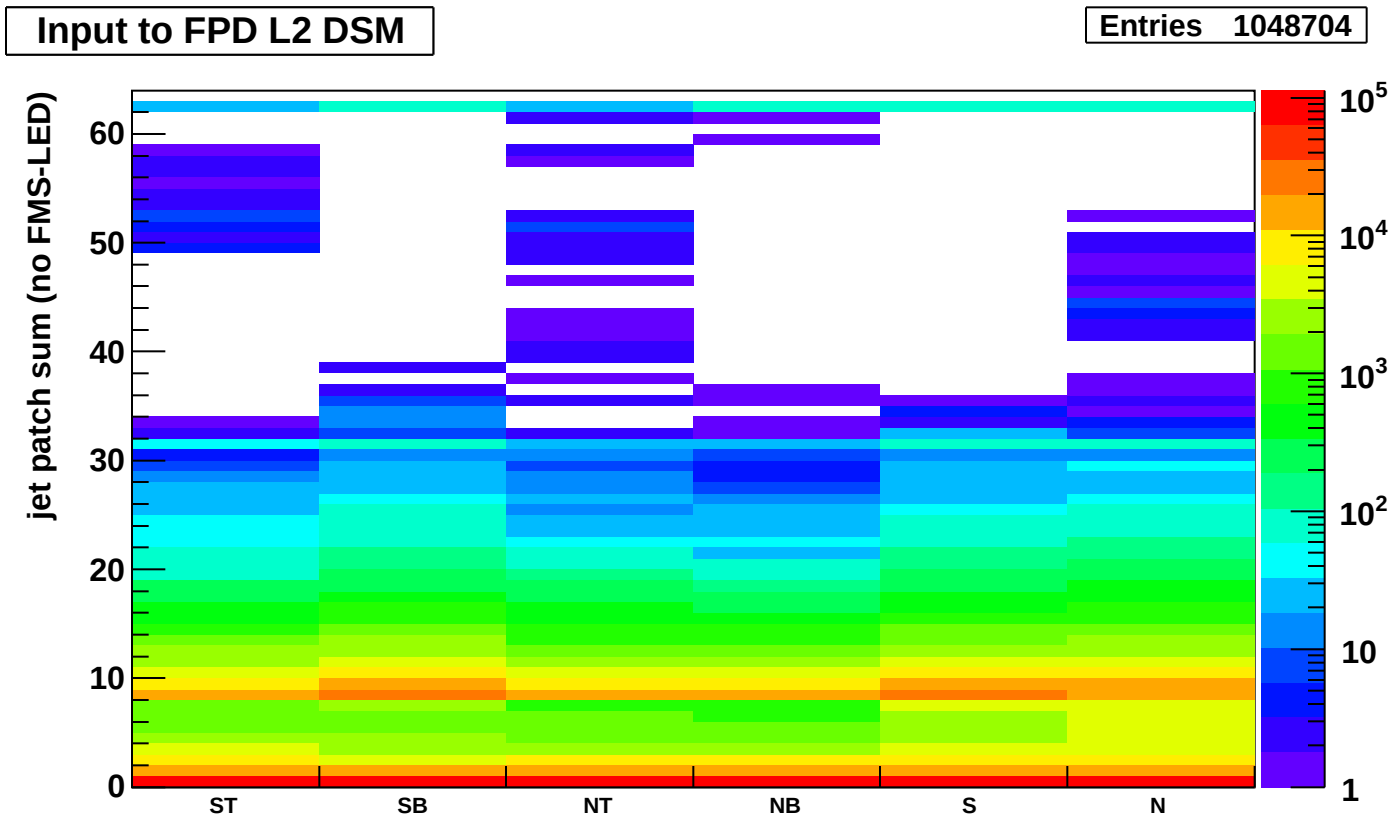

Input to FPD L2 DSM
Entries 260
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

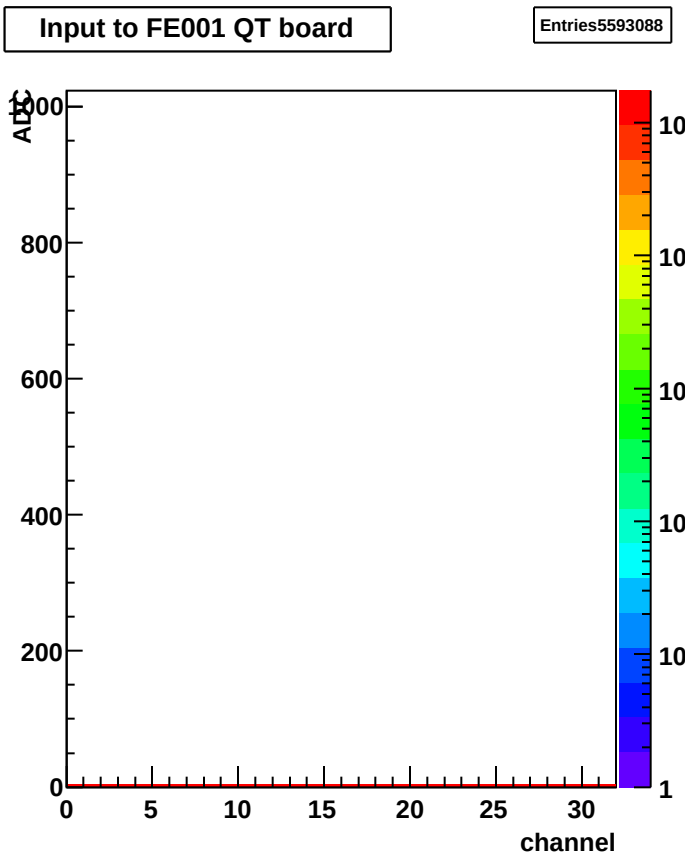

Input to FPD L2 DSM
Entries 524352
CL bits
288
183
3044
3629
2354
46324
20230
14941
125416
150637
157306
SMALL
LARGE-S
LARGE-N

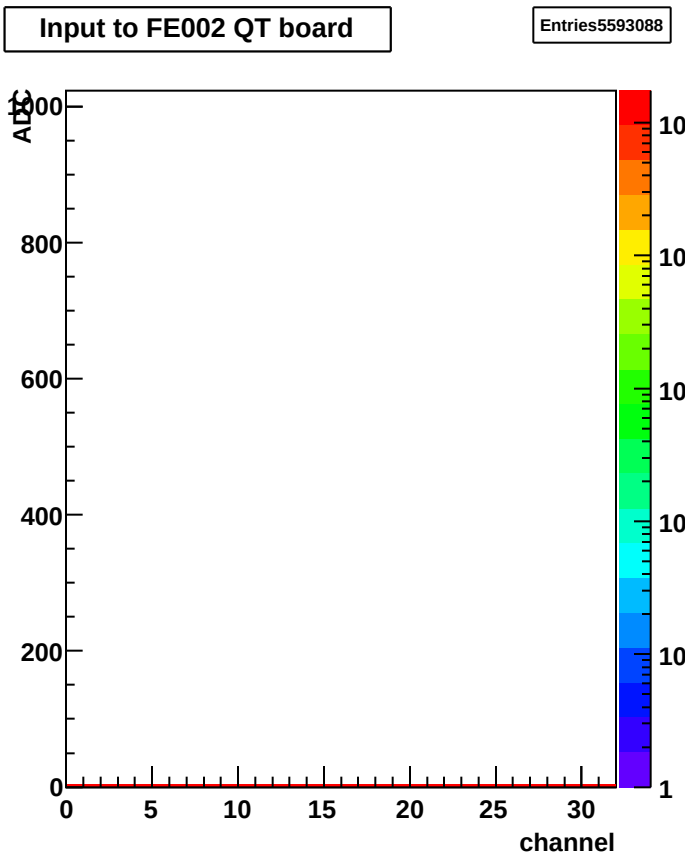

Input to FPD L2 DSM
Entries 533
CL bits - simulated
2
8
1
93
427
2
SMALL
LARGE-S
LARGE-N

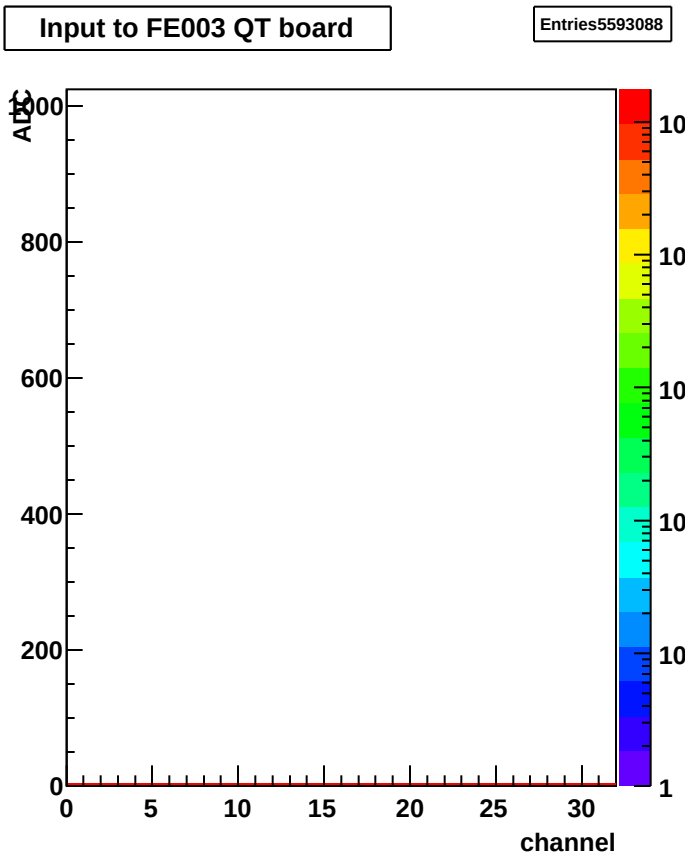

Input to FPD L2 DSM
Entries 524352
HT bits
4095
11476
5869
7122
13882
31156
16791
149685
132152
152124
SMALL
LARGE-S
LARGE-N

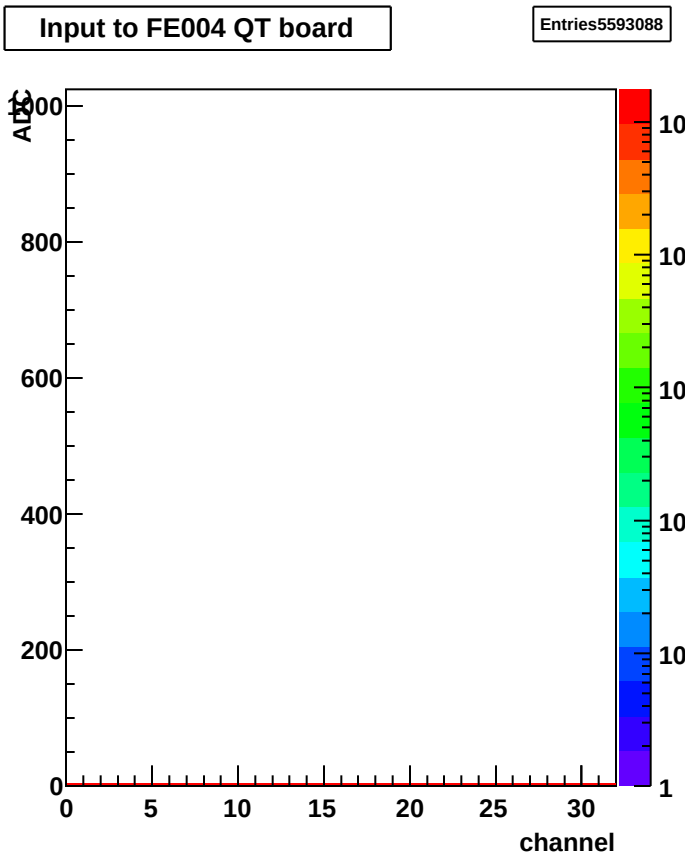

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

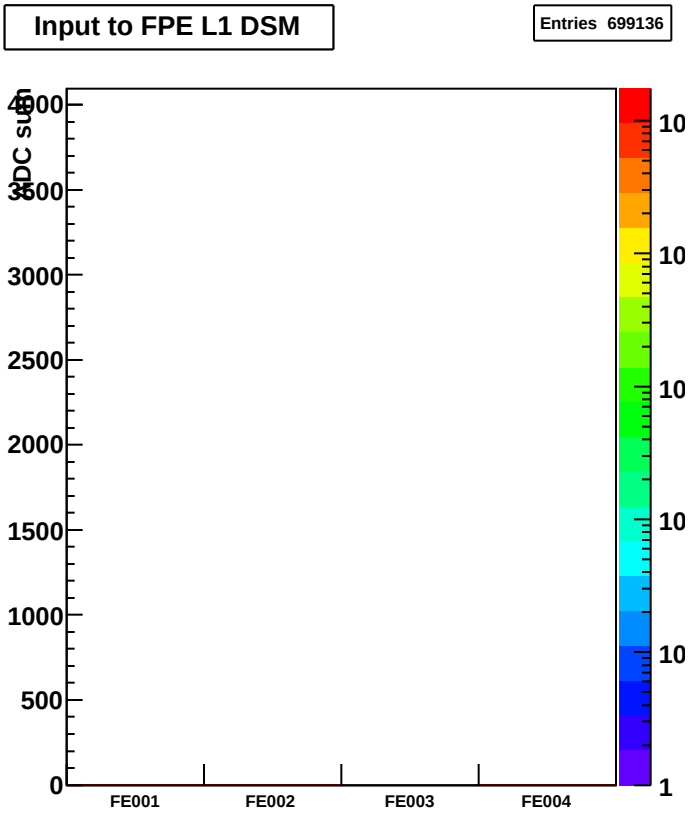

Input to FPD L2 DSM
Entries 1048704
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

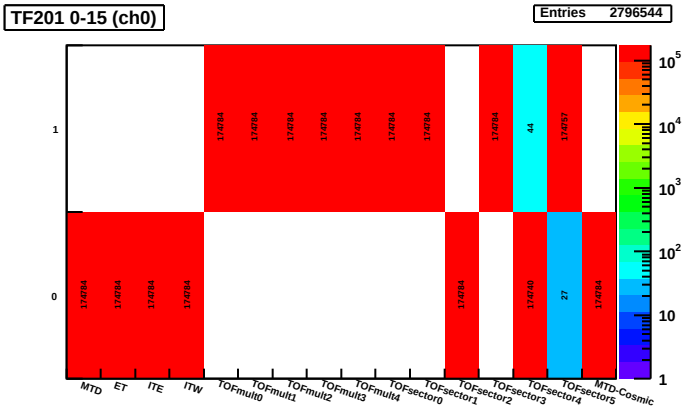

Input to FE001 QT board
Entries5593088
ADC
channel

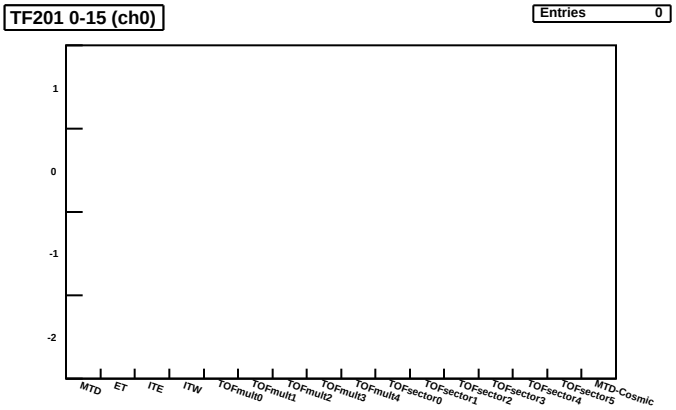

Input to FE002 QT board
Entries5593088
ADC
channel

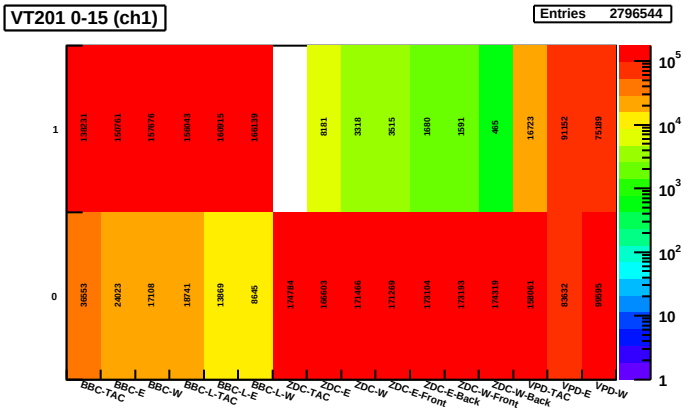

Input to FE003 QT board
Entries5593088
ADC
channel

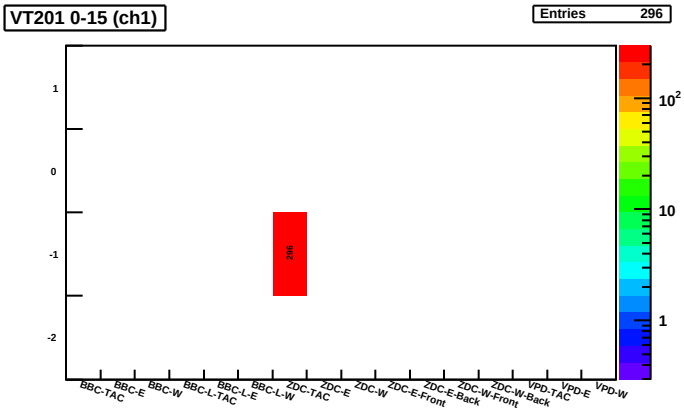

Input to FE004 QT board
Entries5593088
ADC
channel

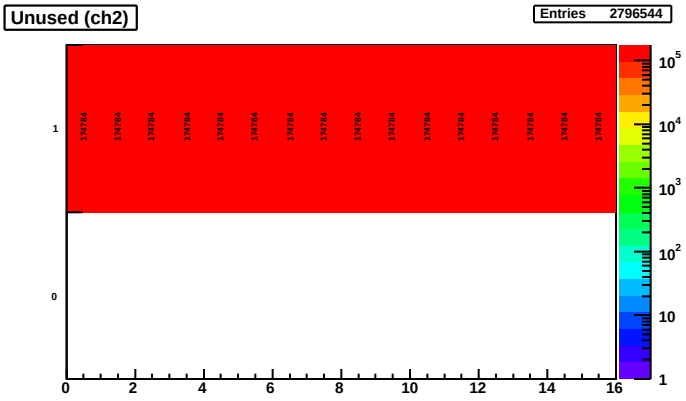

Input to FPE L1 DSM
Entries 699136
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

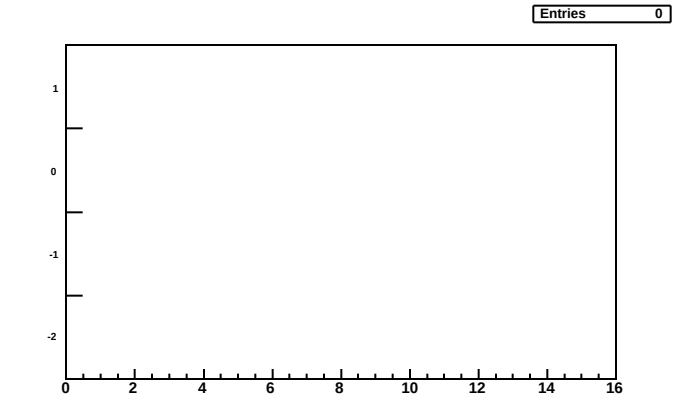

TF201 0-15 (ch0)
Entries 2796544
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 2796544
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 296
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 2796544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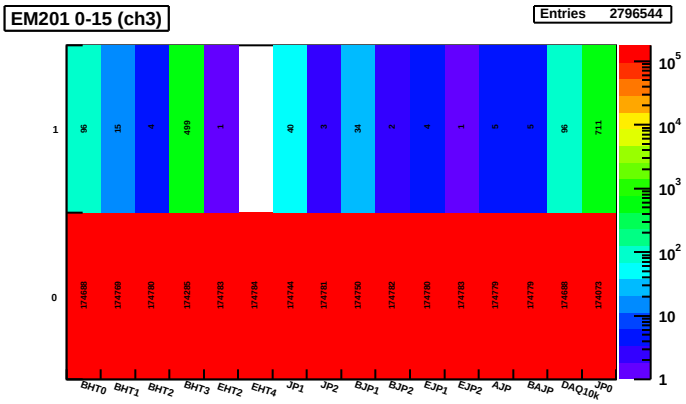
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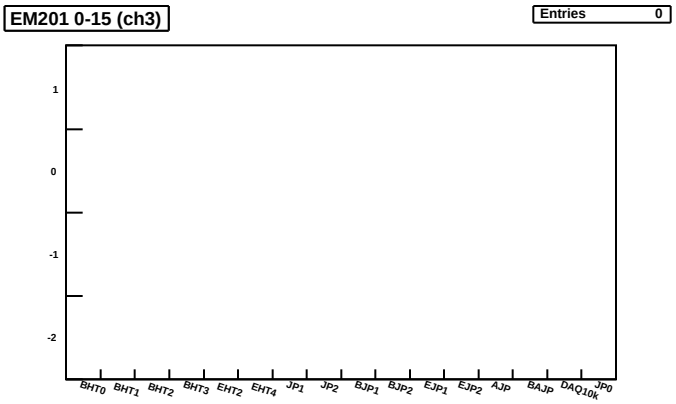
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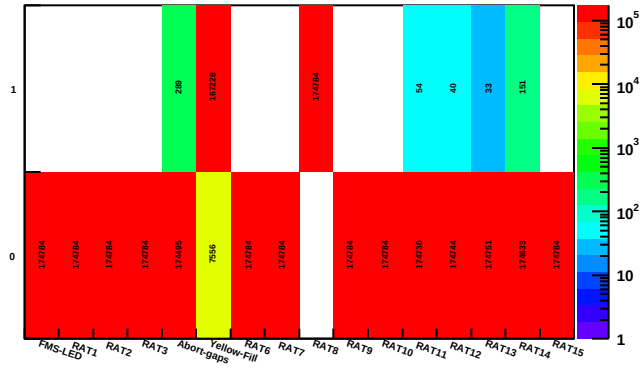
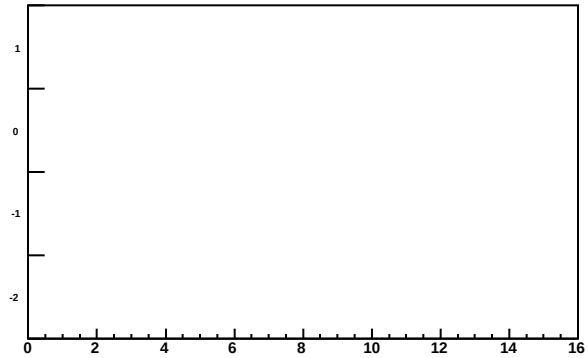
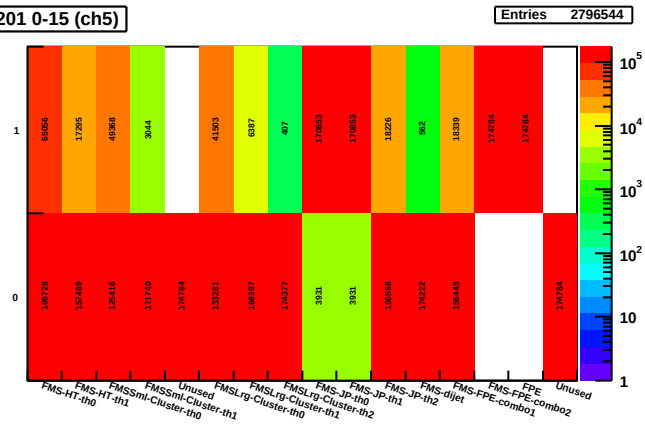
RAT board (ch4)





FP201 0-15 (ch5)



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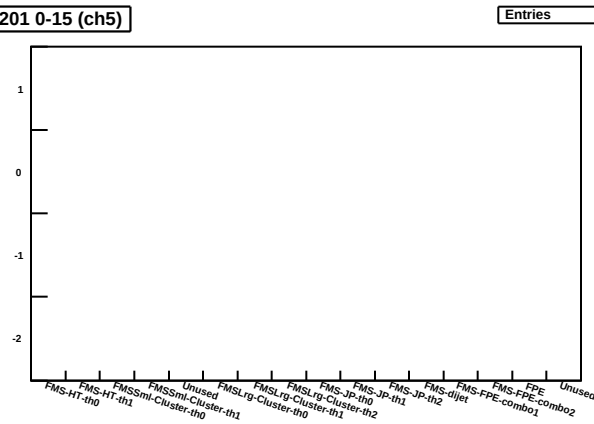

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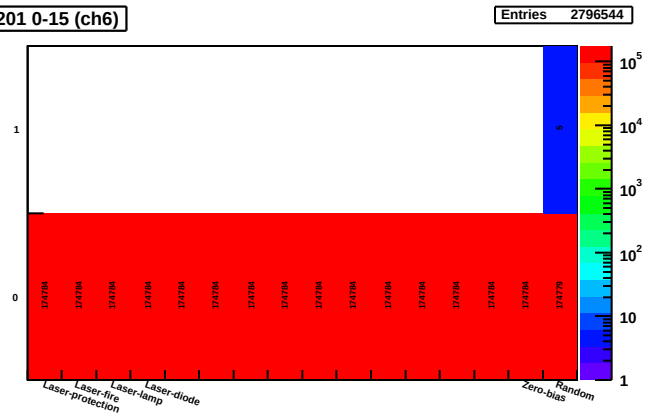

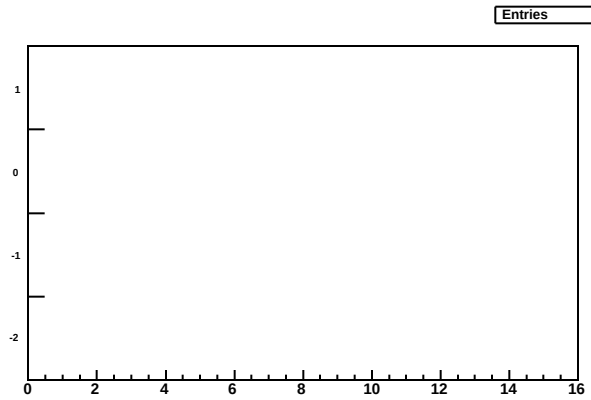
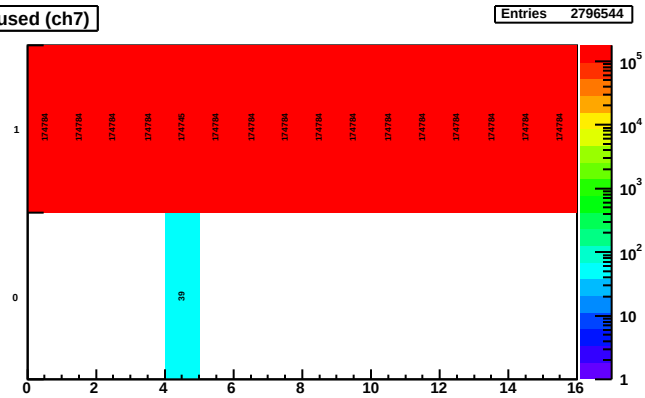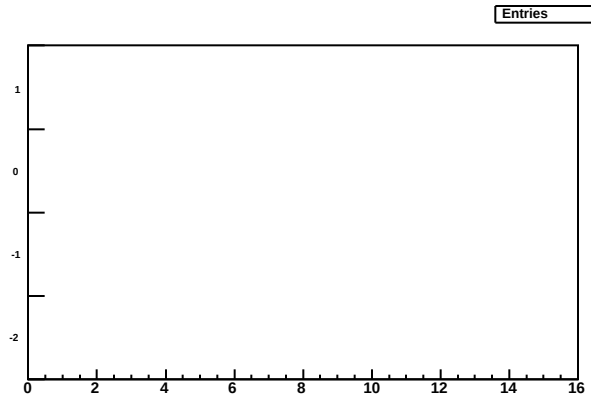
Unused (ch7)