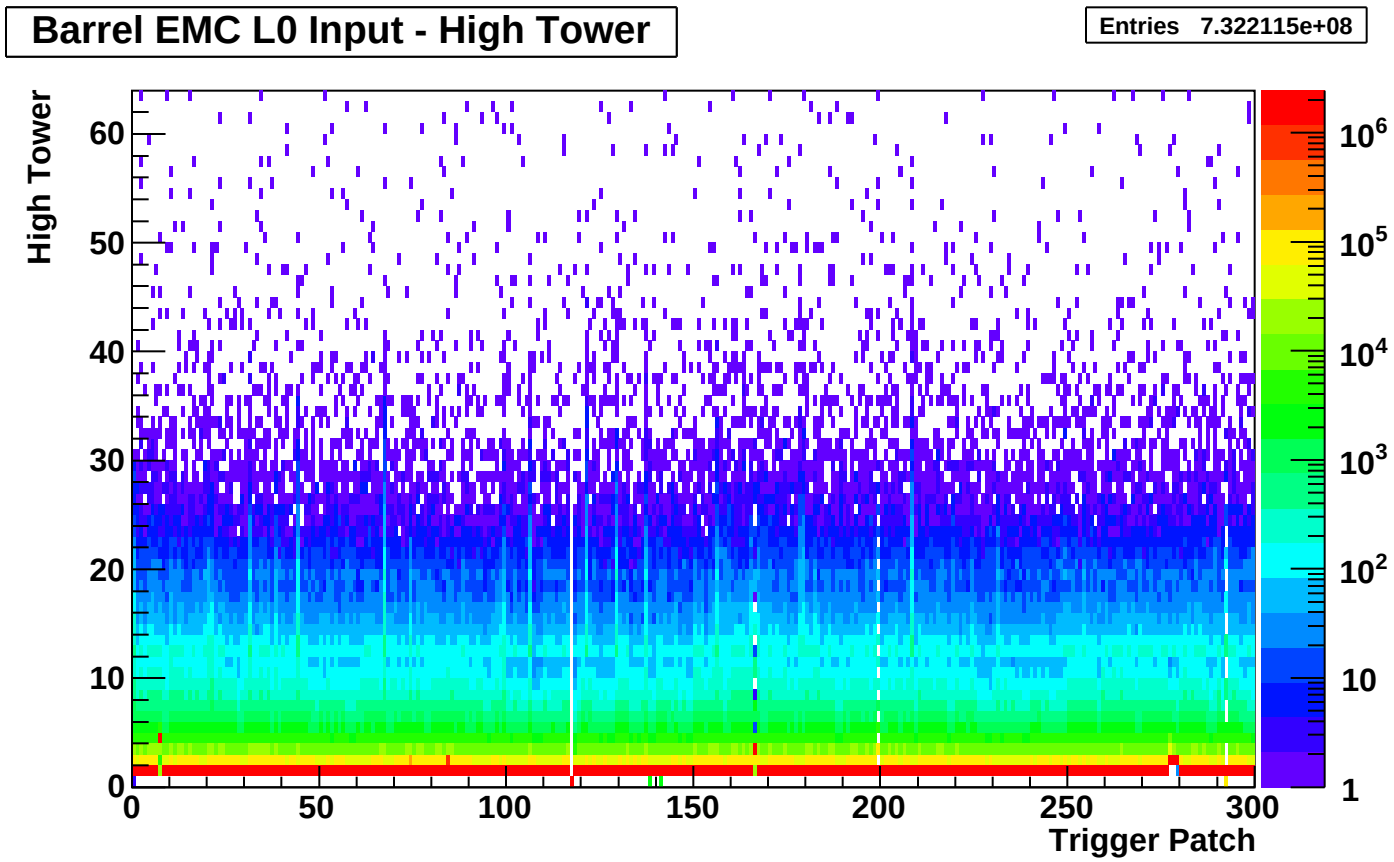
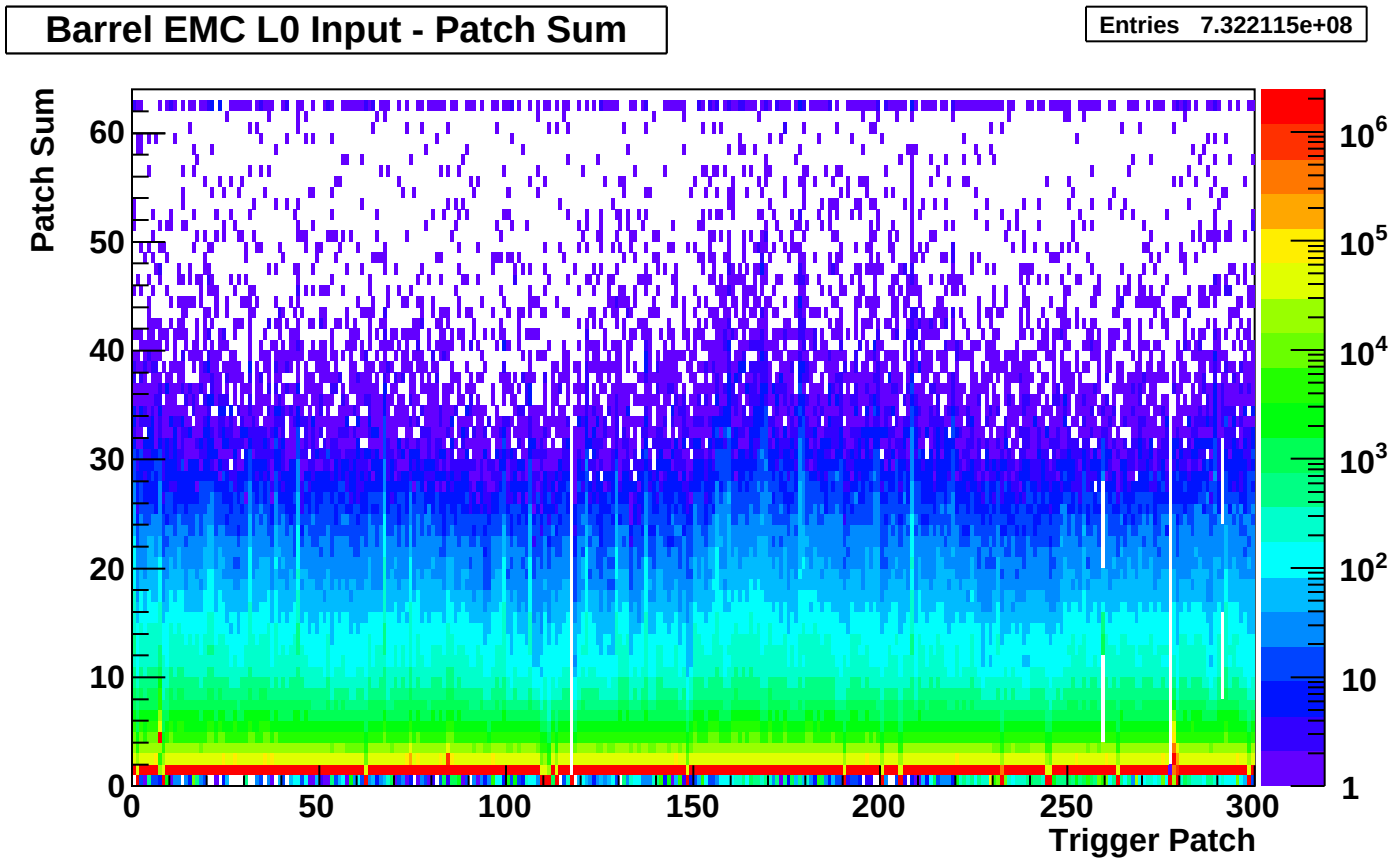
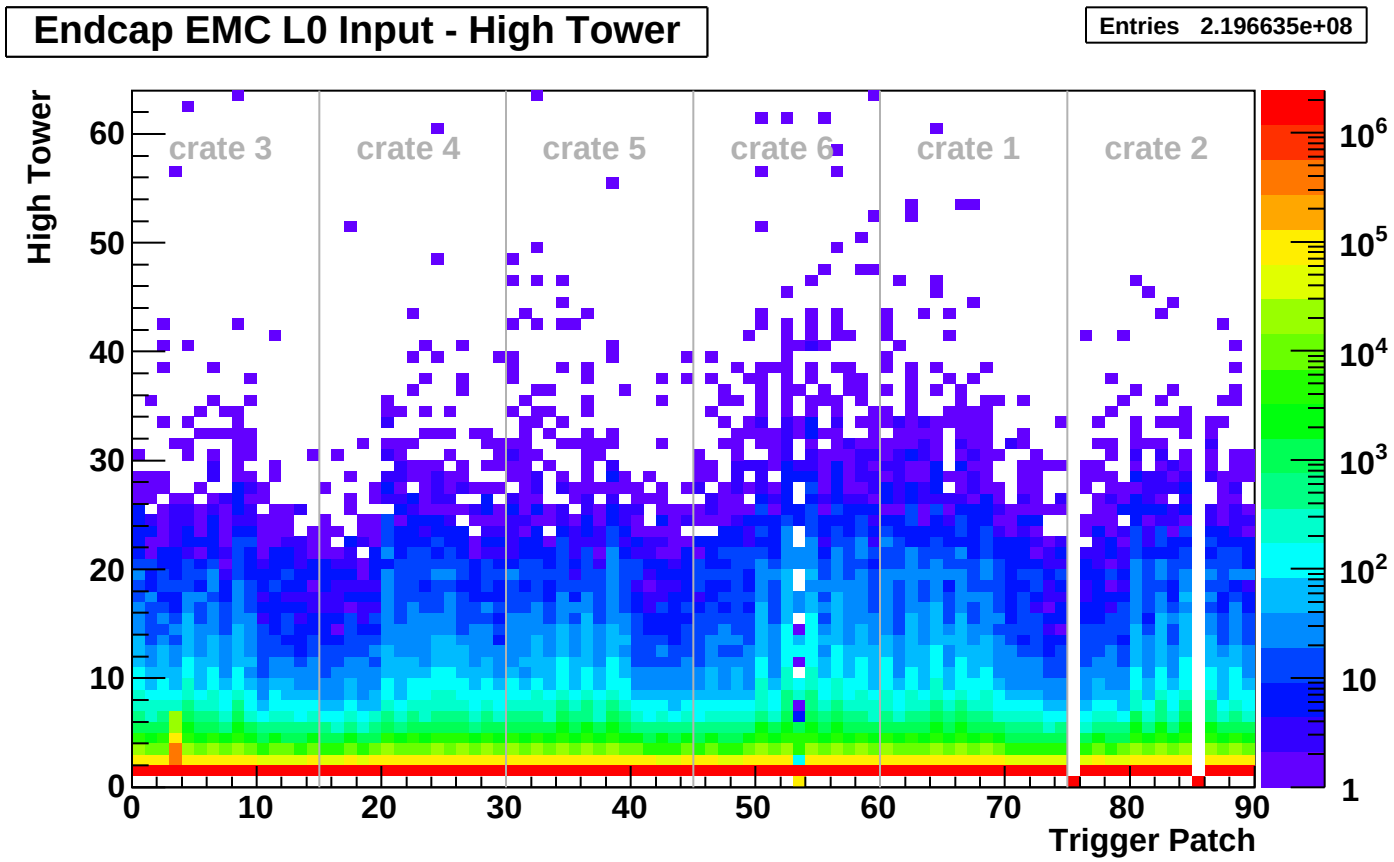
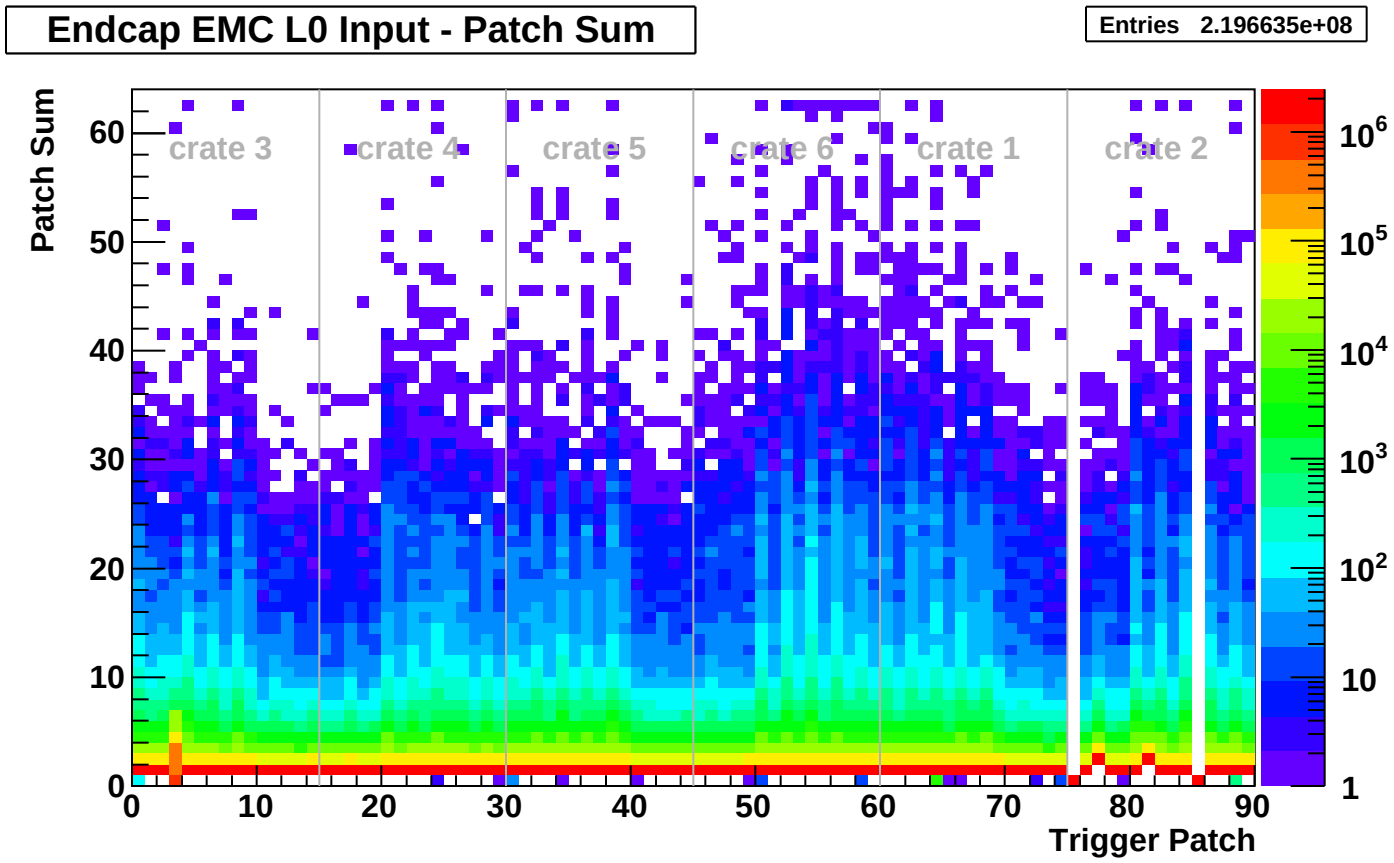
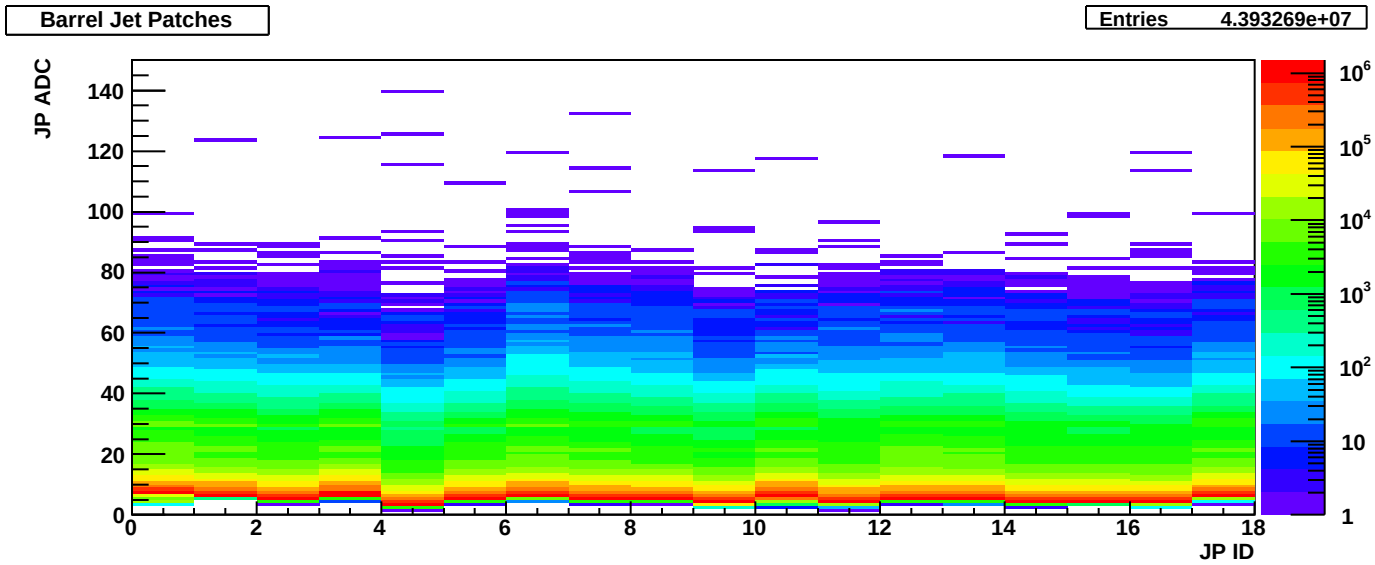
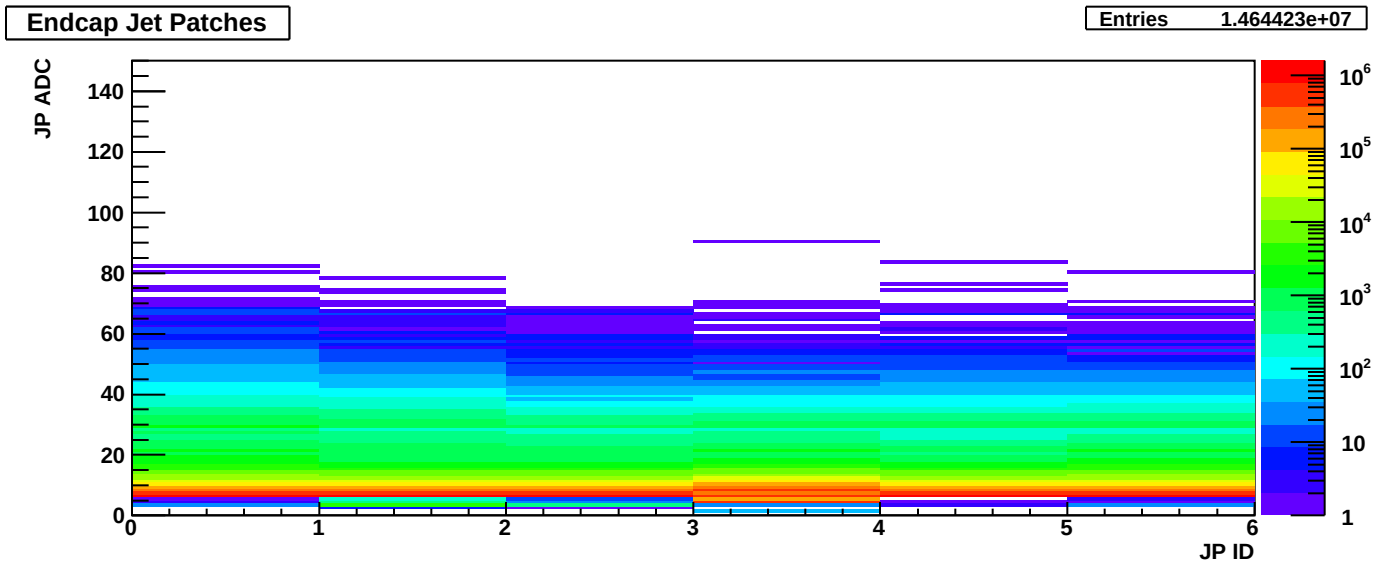

Barrel EMC L0 Input - High Tower
Entries 7.322115e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

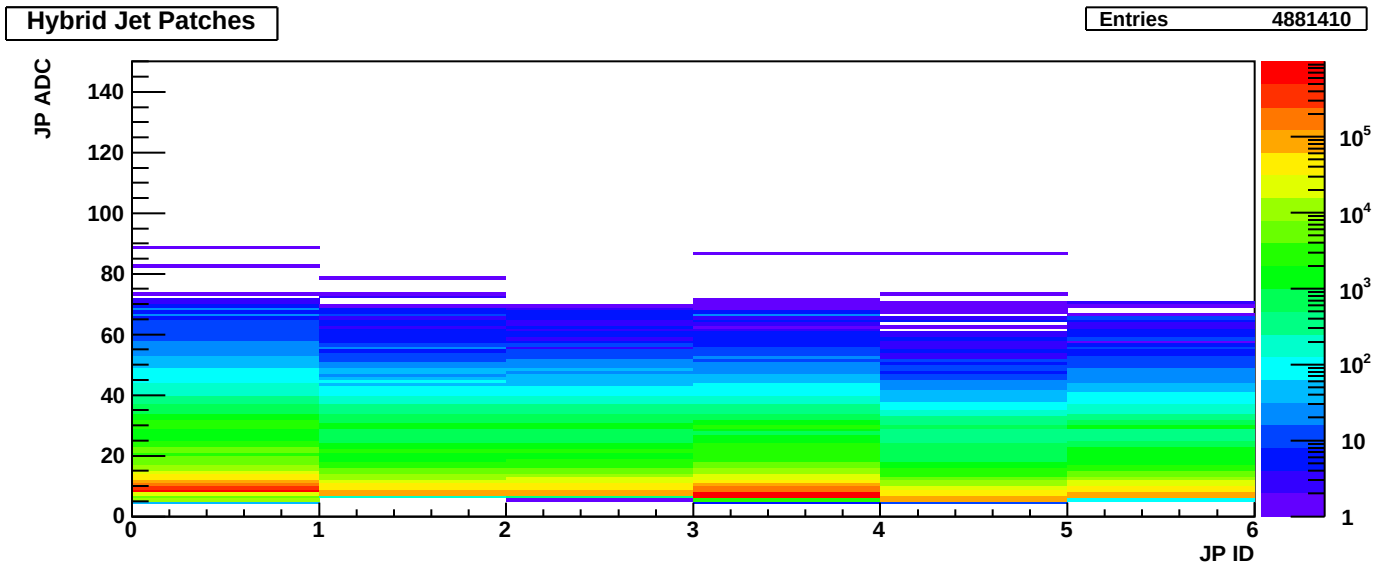

Barrel EMC L0 Input - Patch Sum
Entries 7.322115e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

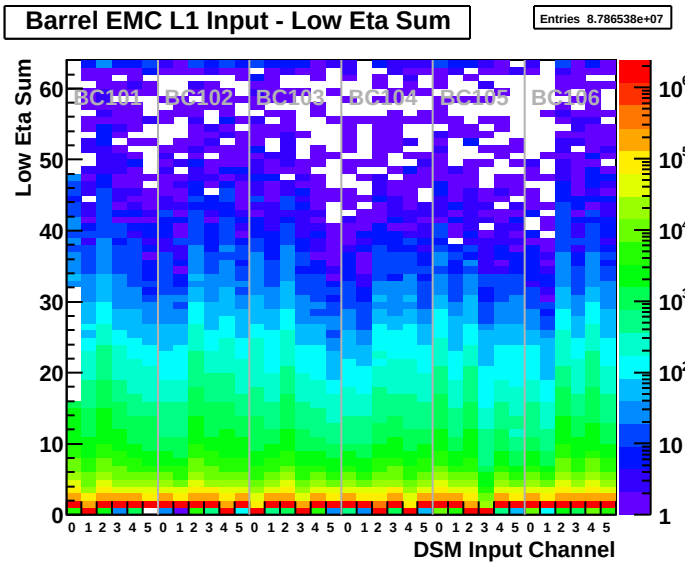

Endcap EMC L0 Input - High Tower
Entries 2.196635e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

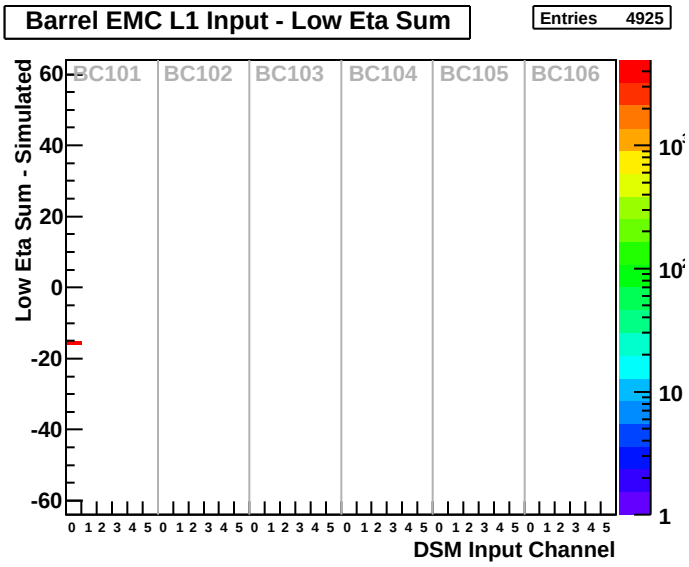

Endcap EMC L0 Input - Patch Sum
Entries 2.196635e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

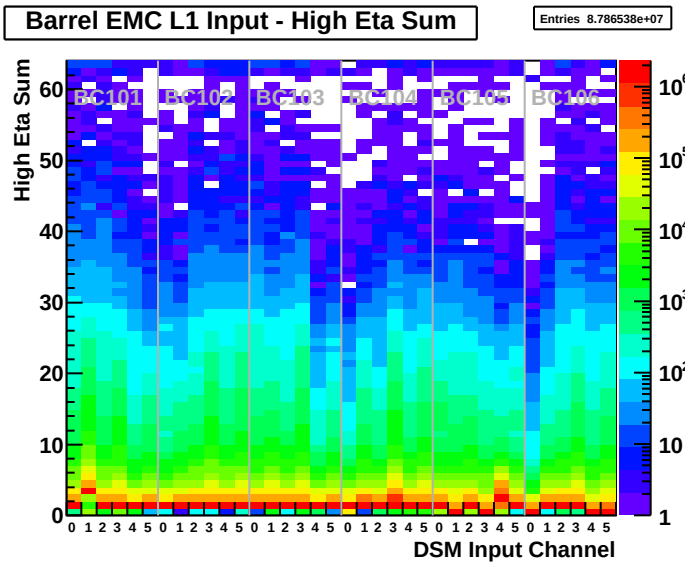

Barrel Jet Patches
Entries 4.393269e+07
JP ADC
JP ID

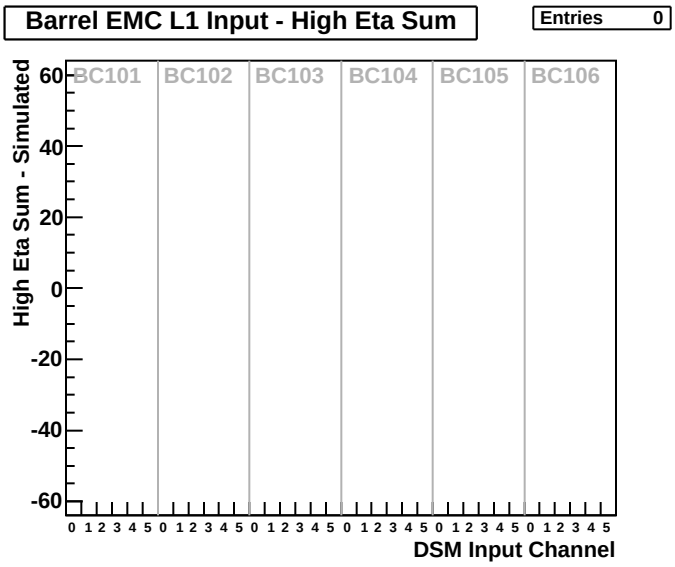

Endcap Jet Patches
Entries 1.464423e+07
JP ADC
JP ID

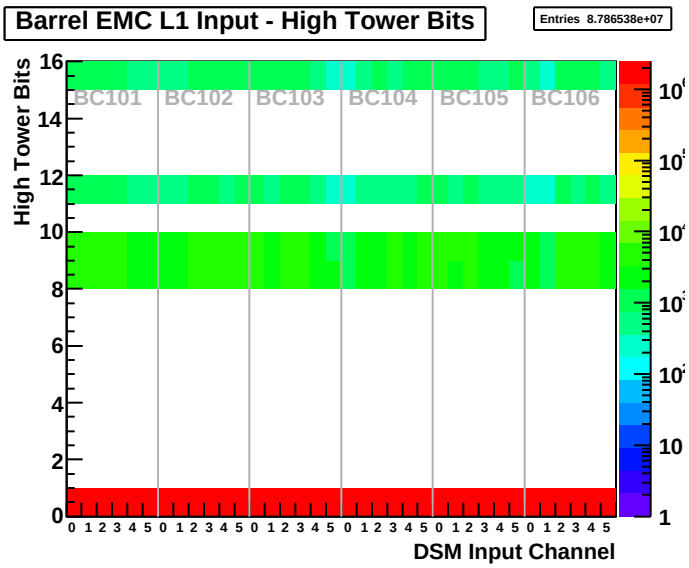

Hybrid Jet Patches
Entries 4881410
JP ADC
JP ID

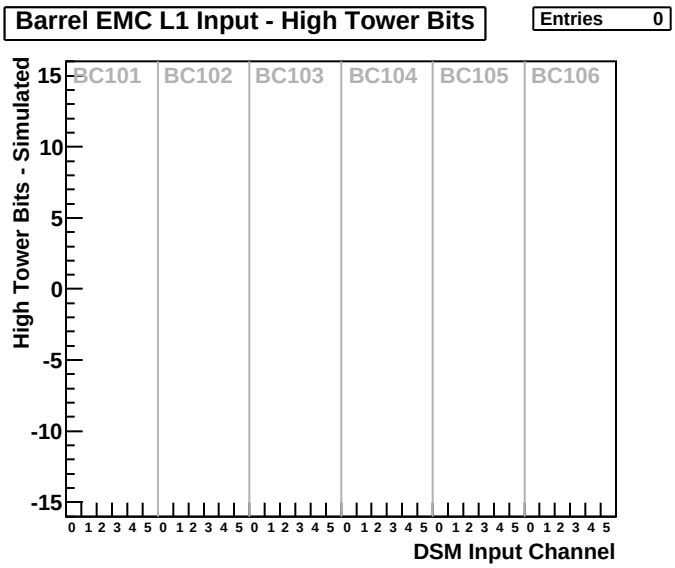

Barrel EMC L1 Input - Low Eta Sum
Entries 8.786538e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

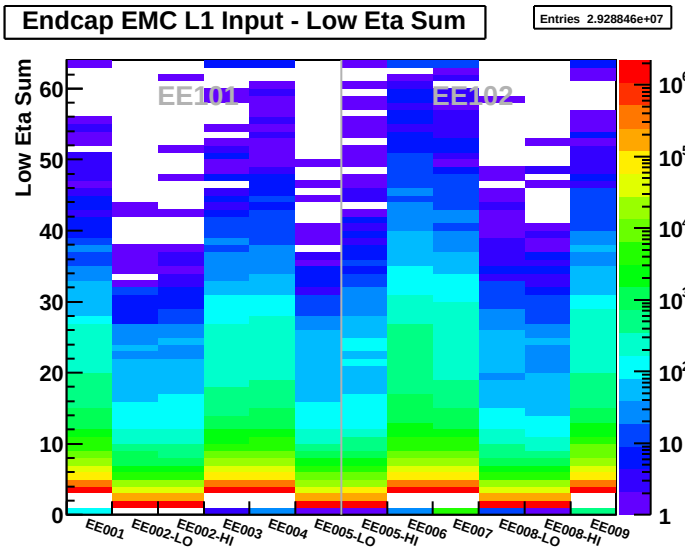

Barrel EMC L1 Input - Low Eta Sum
Entries 4925
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

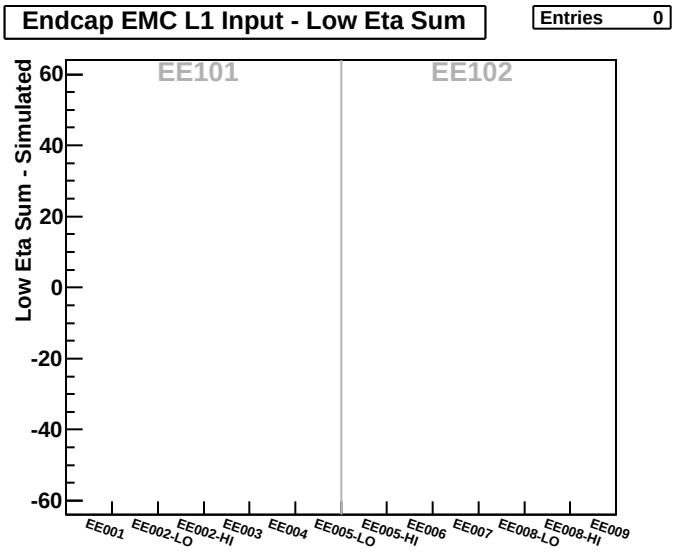

Barrel EMC L1 Input - High Eta Sum
Entries 8.786538e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

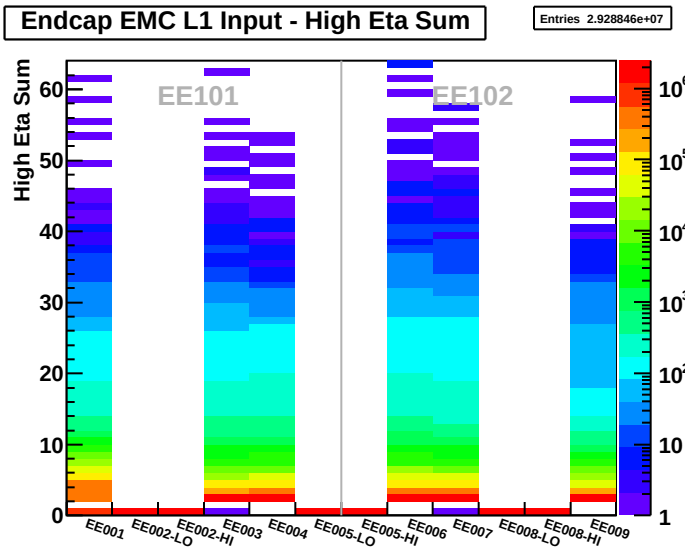

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

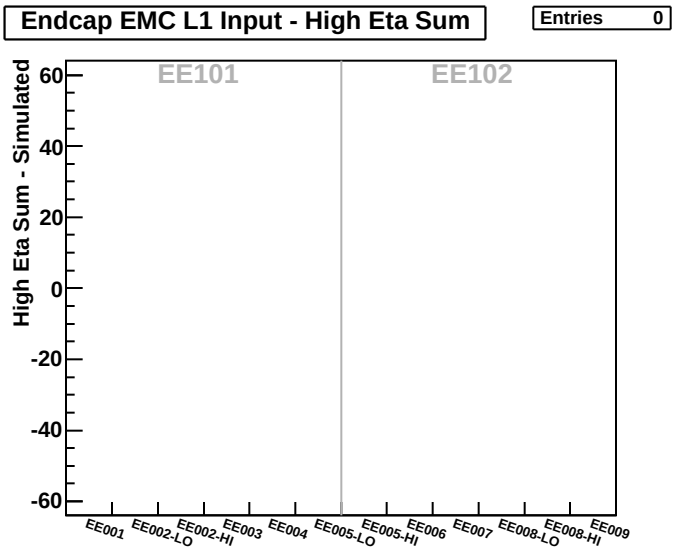

Barrel EMC L1 Input - High Tower Bits
Entries 8.786538e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

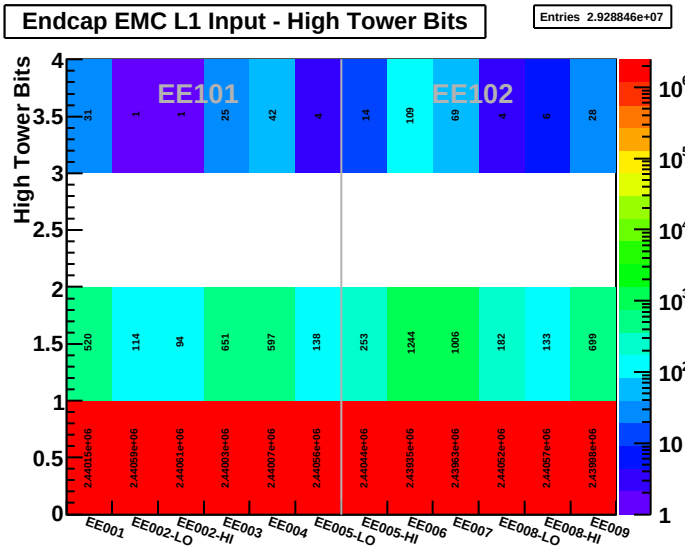

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

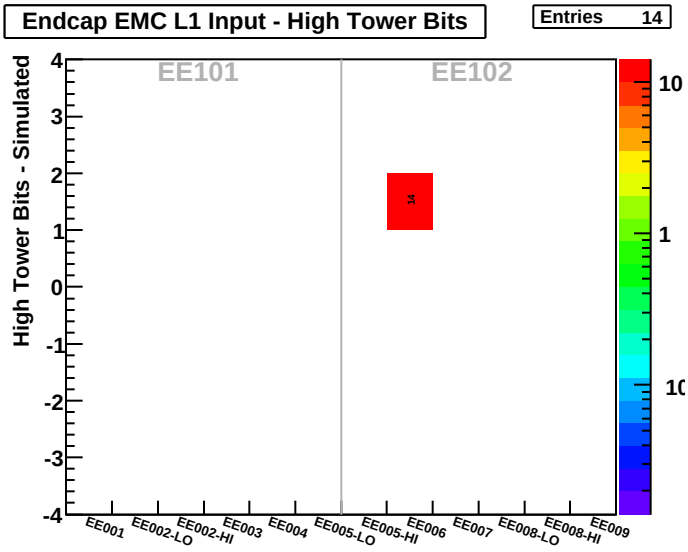

Endcap EMC L1 Input - Low Eta Sum
Entries 2.928846e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

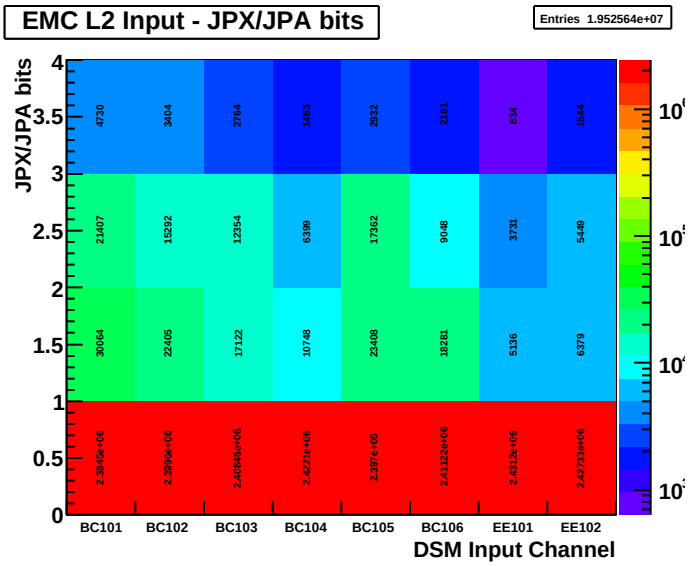

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

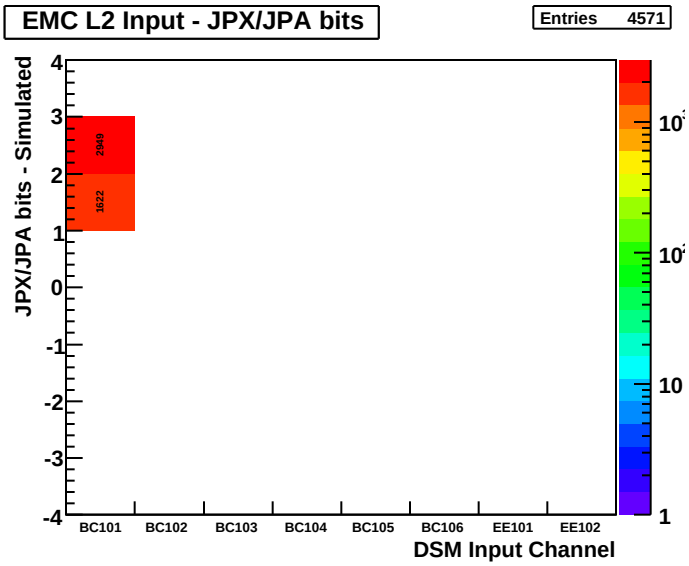

Endcap EMC L1 Input - High Eta Sum
Entries 2.928846e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

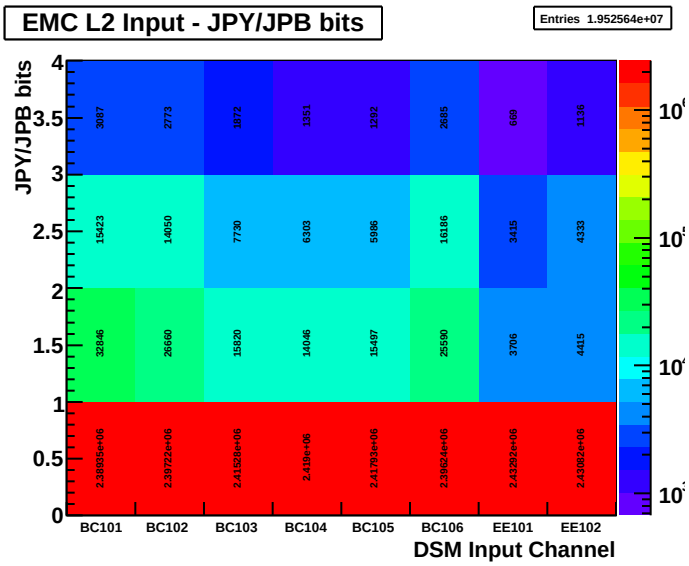

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

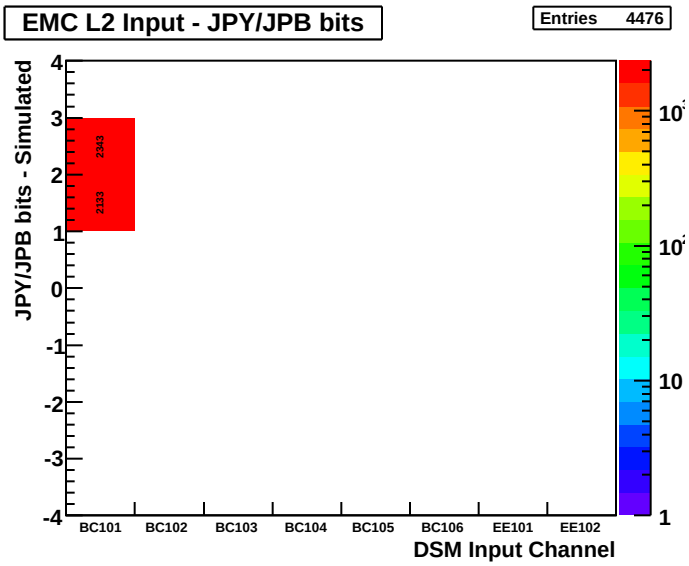

Endcap EMC L1 Input - High Tower Bits
Entries 2.928846e+07
High Tower Bits
EE101
EE102
31 1 1 25 42 4 14 109 69 4 6 28
520 114 94 651 597 138 253 1244 1006 182 133 698
2.44015e+06 2.44059e+06 2.44061e+06 2.44003e+06 2.44007e+06 2.44056e+06 2.44044e+06 2.43995e+06 2.43963e+06 2.44052e+06 2.44057e+06 2.43998e+06
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

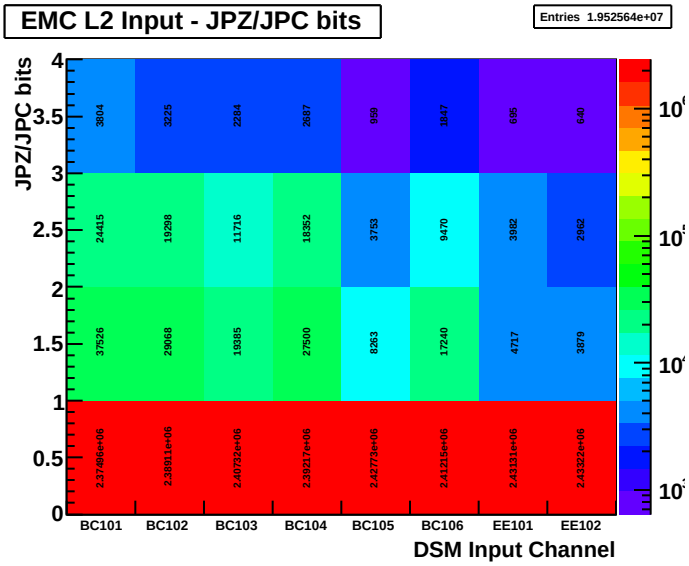

Endcap EMC L1 Input - High Tower Bits
Entries 14
High Tower Bits - Simulated
EE101
EE102
14
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

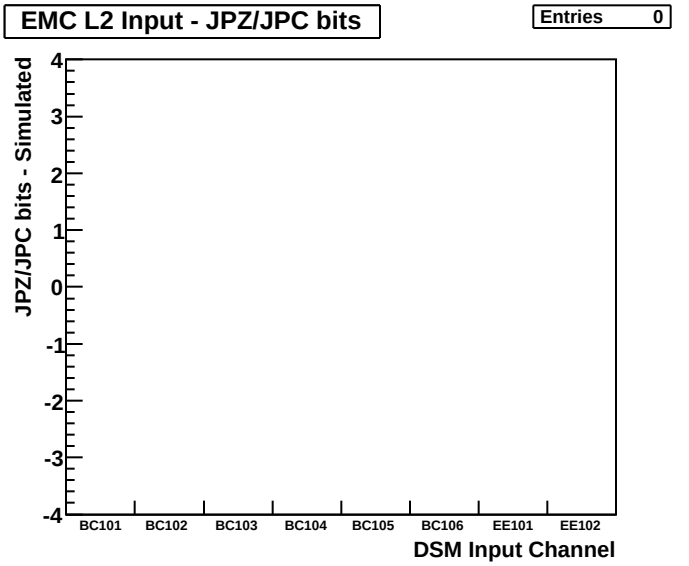

EMC L2 Input - JPX/JPA bits
Entries 1.952564e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4730 3404 2764 1463 2932 2161 634 1544
21407 15292 12354 6399 17362 9048 3731 5449
30064 22405 17122 10748 23408 10281 5136 6379
2.3845e+06 2.3996e+06 2.40846e+06 2.4221e+06 2.397e+06 2.41122e+06 2.4312e+06 2.42723e+06

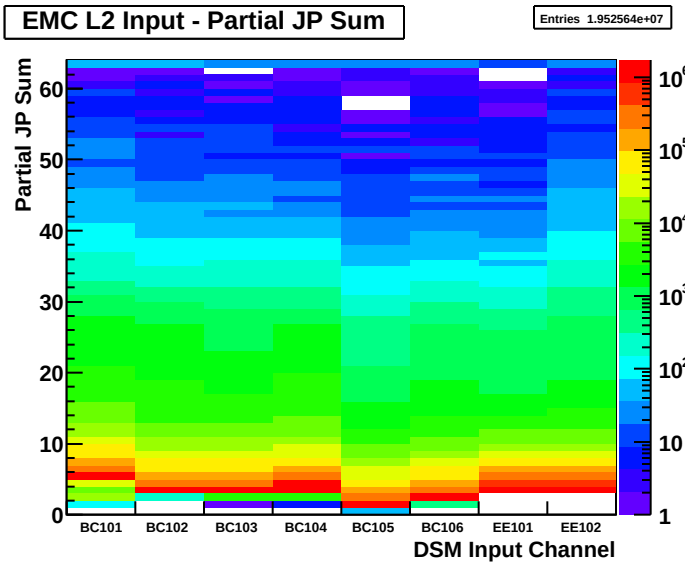

EMC L2 Input - JPX/JPA bits
Entries 4571
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2949
1622

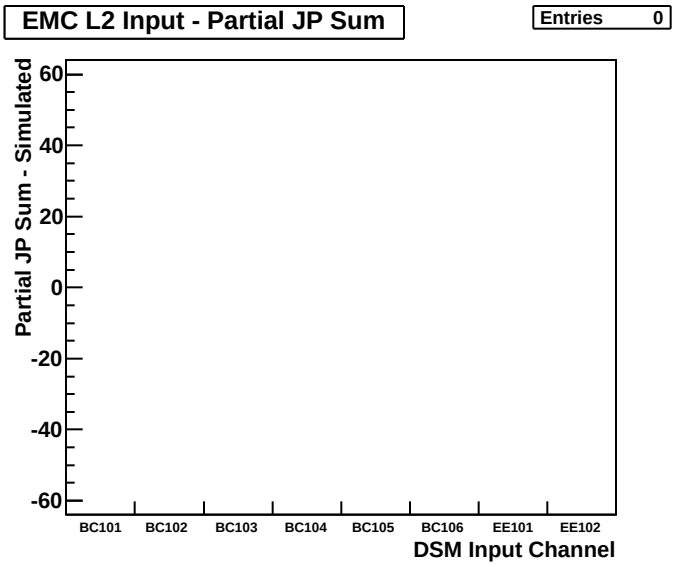

EMC L2 Input - JPY/JPB bits
Entries 1.952564e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3087 2773 1872 1351 1292 2685 669 1136
15423 14050 7730 6303 5986 16186 3415 4333
32846 26660 15820 14046 15497 25590 3706 4415
2.38935e+06 2.39722e+06 2.41528e+06 2.419e+06 2.41793e+06 2.39624e+06 2.43292e+06 2.43082e+06

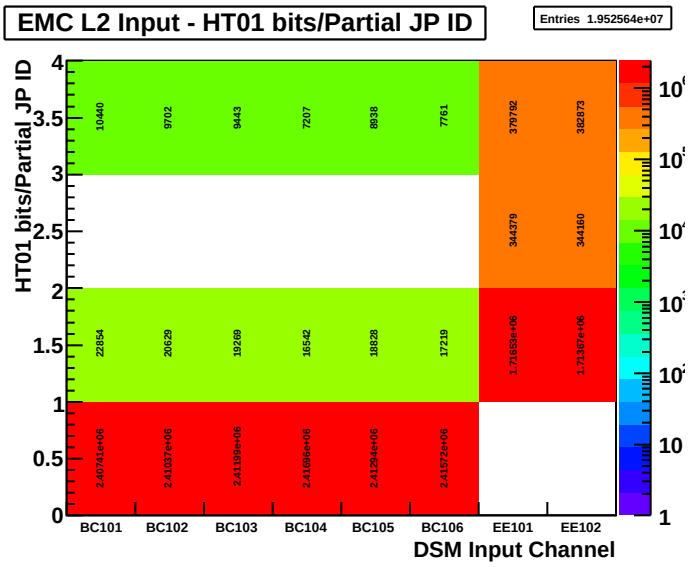

EMC L2 Input - JPY/JPB bits
Entries 4476
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2343
2133

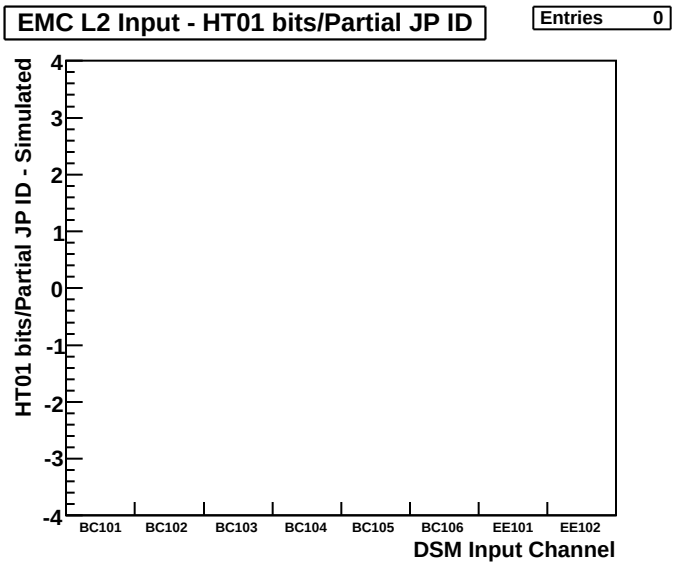

EMC L2 Input - JPZ/JPC bits
Entries 1.952564e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3804 3225 2284 2687 959 1847 695 640
24415 19298 11716 18352 3753 9470 3982 2982
37526 29068 19385 27500 8263 17240 4717 3879
2.37496e+06 2.38911e+06 2.40732e+06 2.39217e+06 2.42773e+06 2.41215e+06 2.43131e+06 2.43322e+06

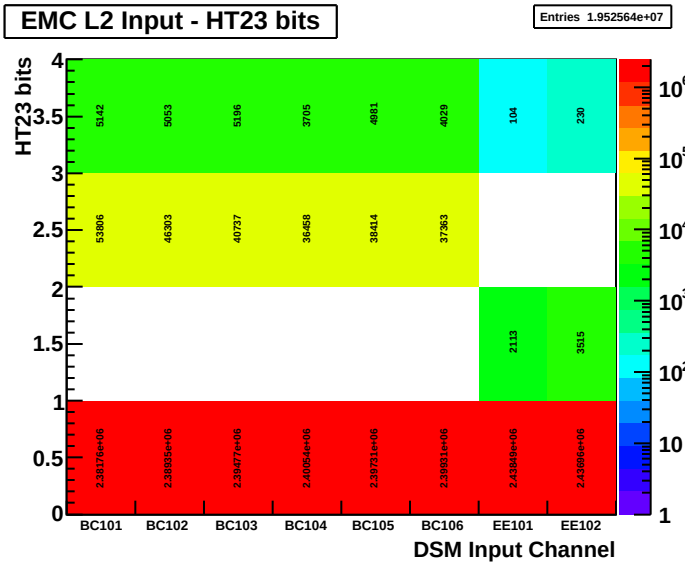

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

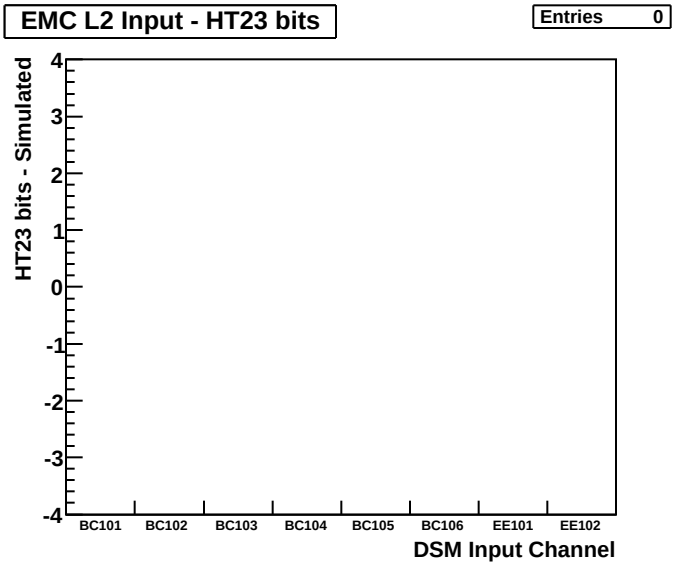

EMC L2 Input - Partial JP Sum
Entries 1.952564e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

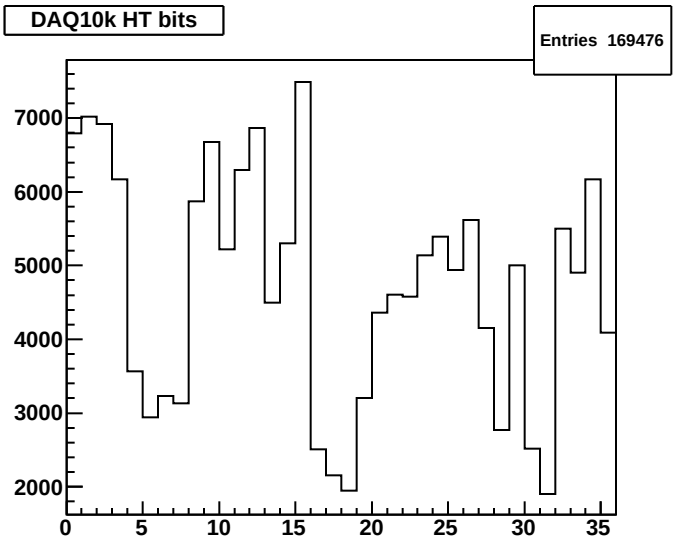

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

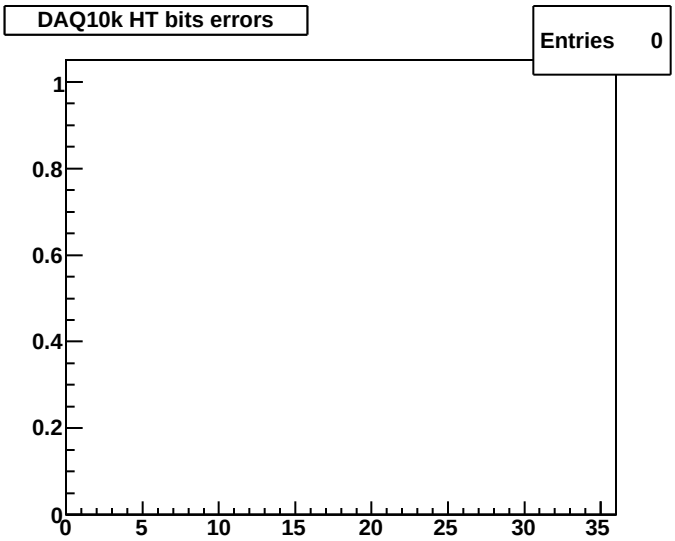

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.952564e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
10440 9702 9443 7207 8938 7761 379792 382873
344379 344160
22854 20629 19269 16542 18828 17219 1.71653e+06 1.71367e+06
2.40741e+06 2.41037e+06 2.41199e+06 2.41696e+06 2.41294e+06 2.41572e+06

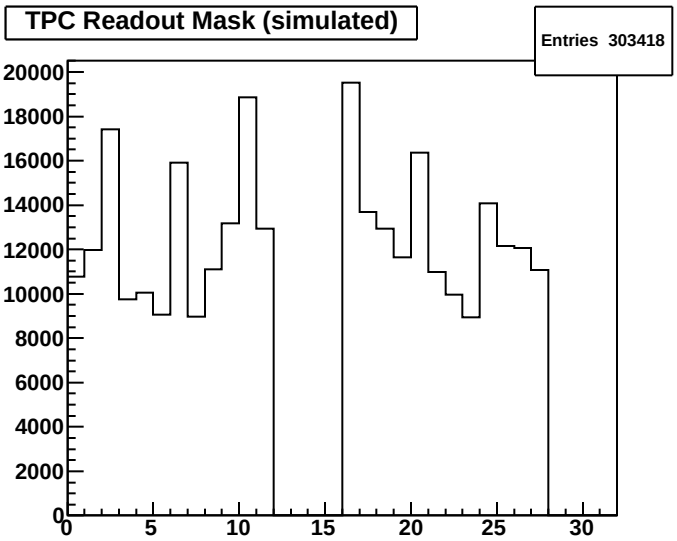

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

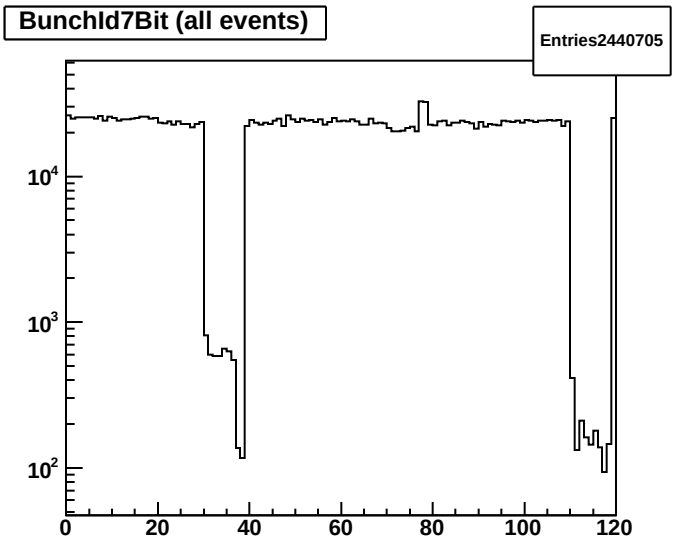

EMC L2 Input - HT23 bits
Entries 1.952564e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5142 5053 5196 3705 4981 4029 104 230
53806 46303 40737 36458 38414 37363
2113 3515
2.38176e+06 2.38935e+06 2.39477e+06 2.40054e+06 2.39731e+06 2.39931e+06 2.43849e+06 2.43696e+06

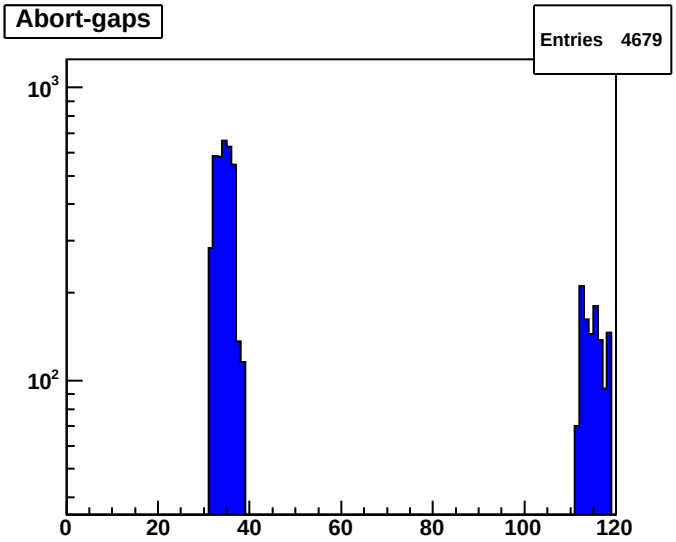

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

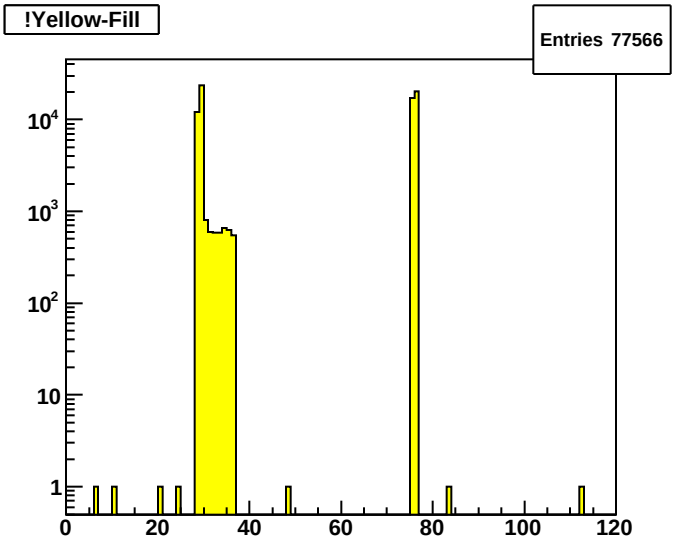

DAQ10k HT bits
Entries 169476

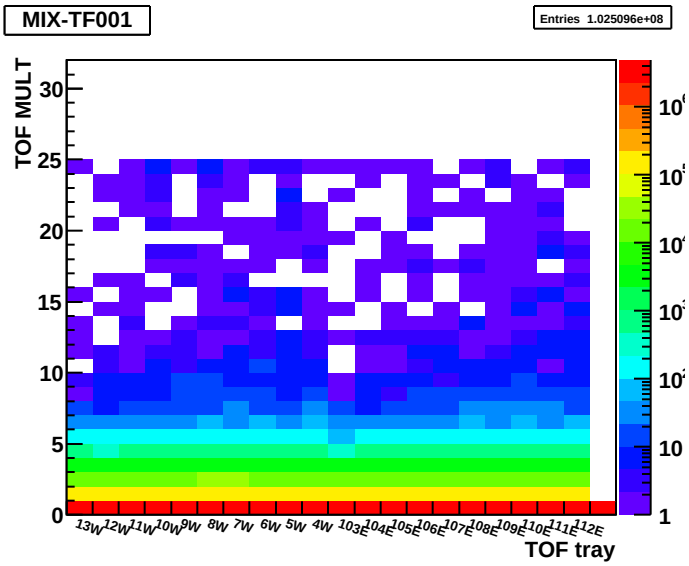

DAQ10k HT bits errors
Entries 0

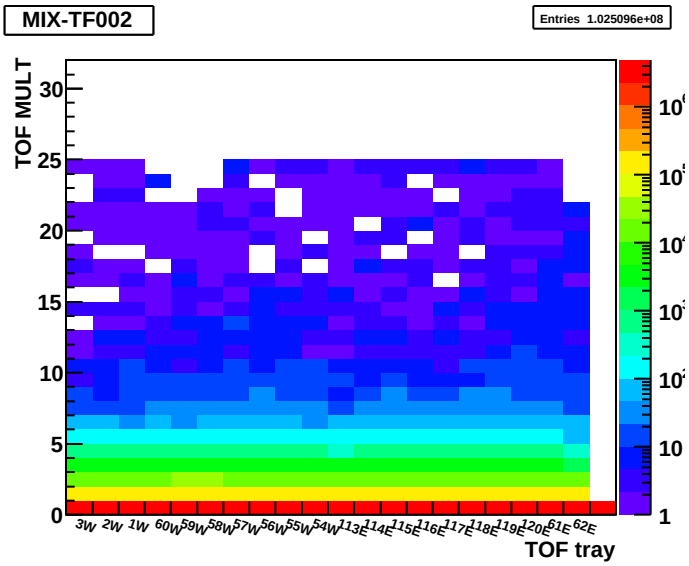

TPC Readout Mask (simulated)
Entries 303418

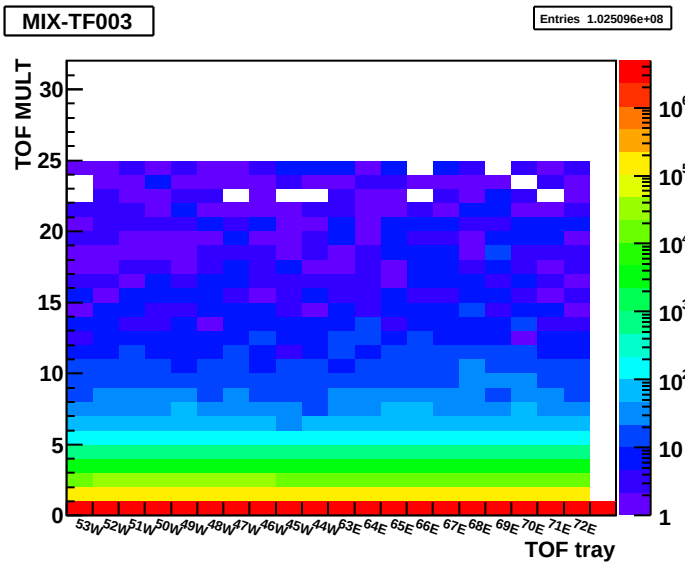

BunchId7Bit (all events)
Entries2440705

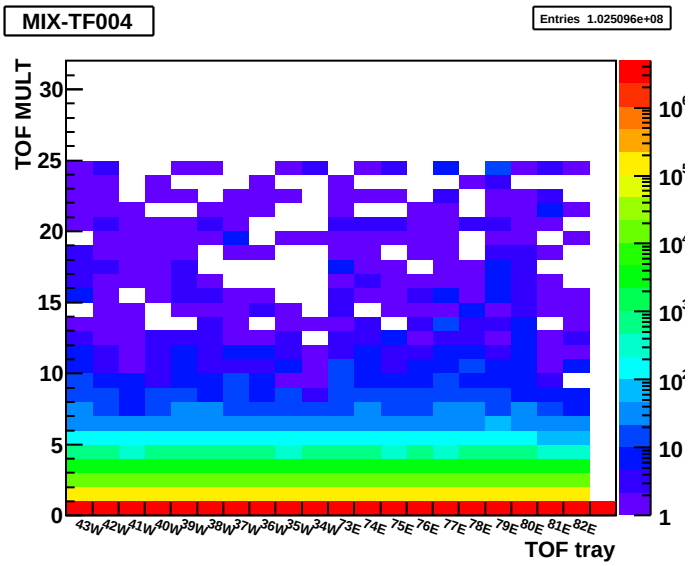

Abort-gaps
Entries 4679

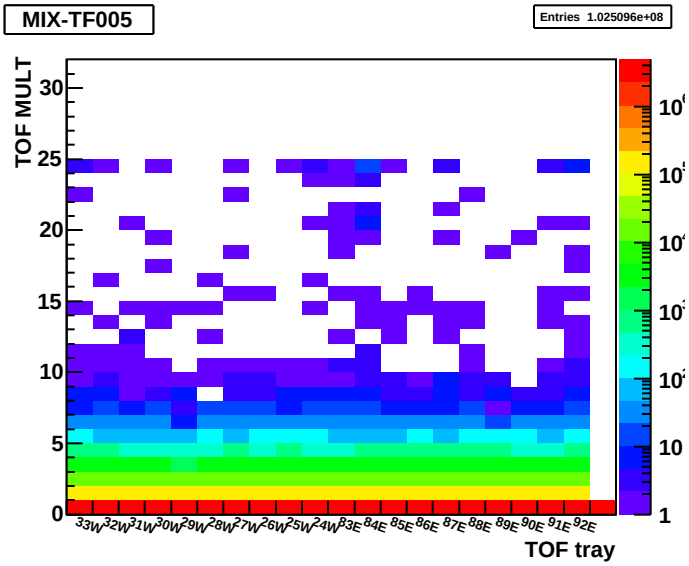

!Yellow-Fill
Entries 77566

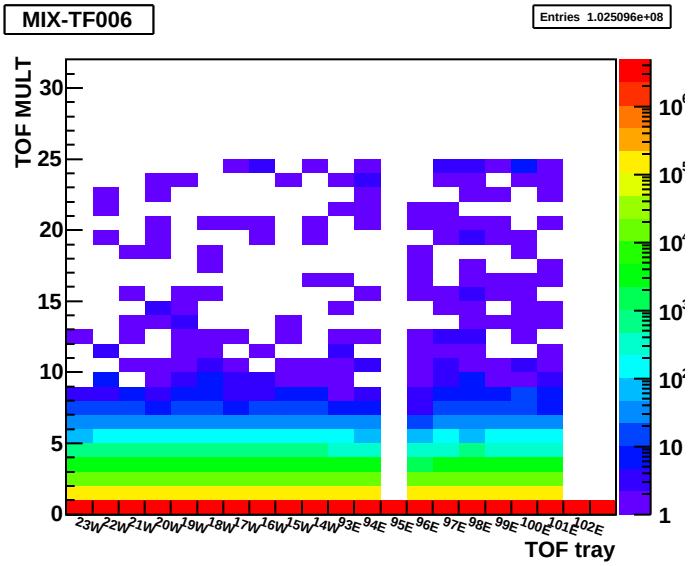

MIX-TF001
Entries 1.025096e+08
TOF MULT
TOF tray

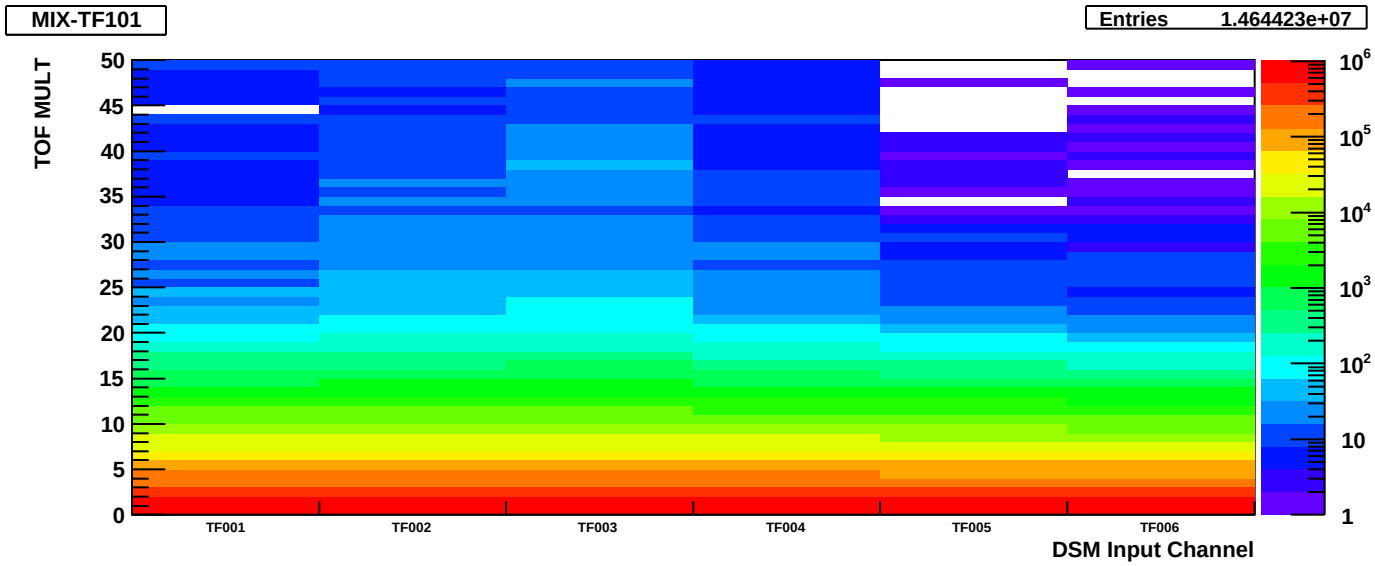

MIX-TF002
Entries 1.025096e+08
TOF MULT
TOF tray

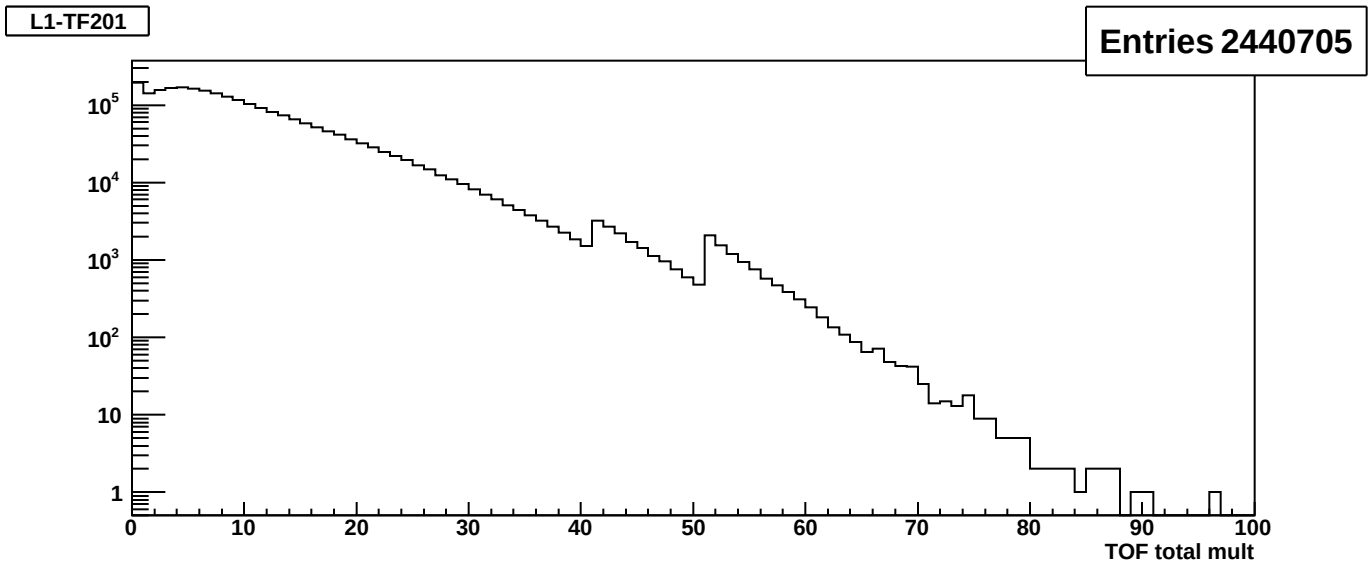

MIX-TF003
Entries 1.025096e+08
TOF MULT
TOF tray

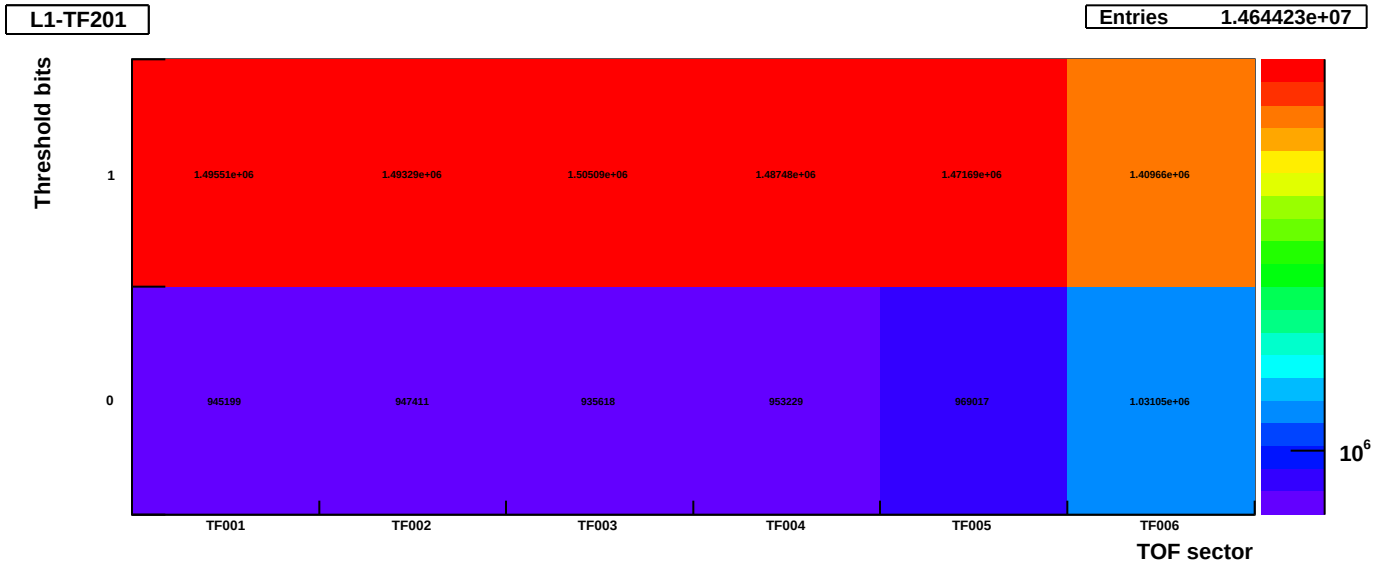

MIX-TF004
Entries 1.025096e+08
TOF MULT
TOF tray

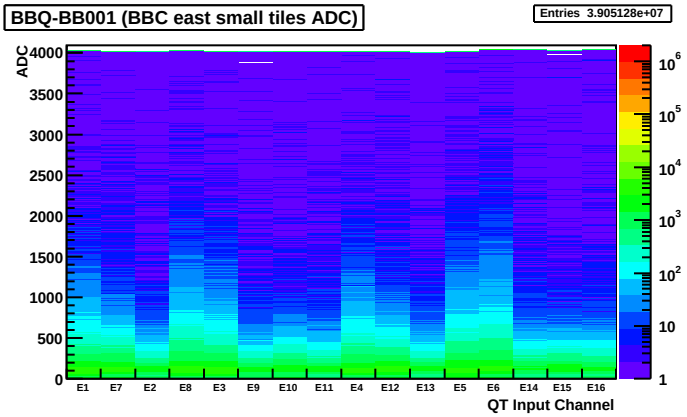

MIX-TF005
Entries 1.025096e+08
TOF MULT
TOF tray

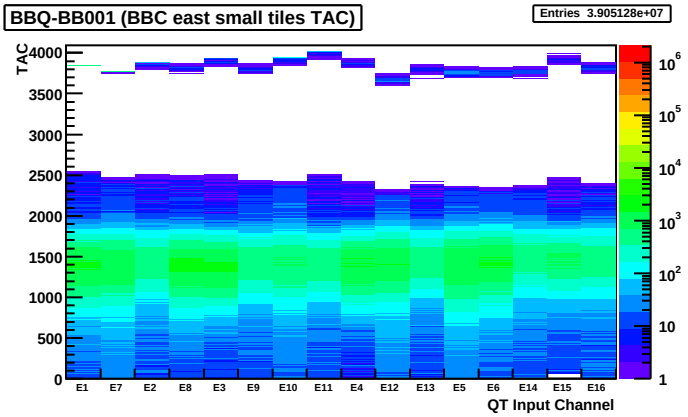

MIX-TF006
Entries 1.025096e+08
TOF MULT
TOF tray

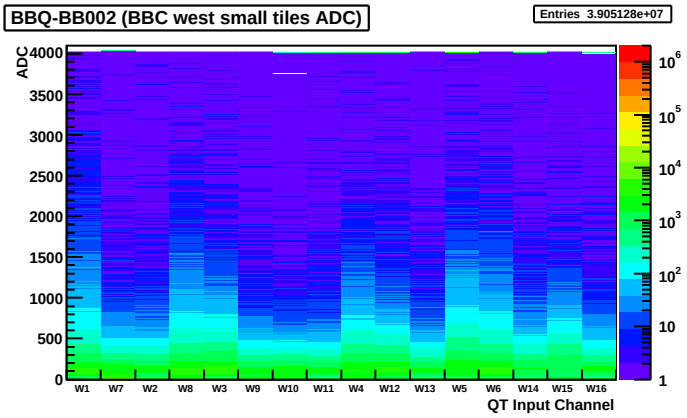

MIX-TF101
Entries 1.464423e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

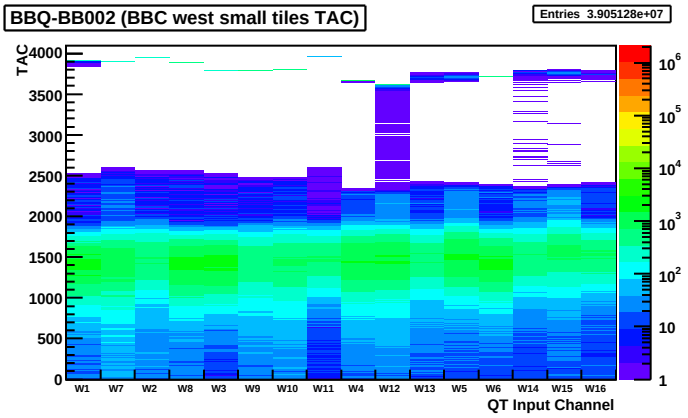

L1-TF201
Entries 2440705
TOF total mult

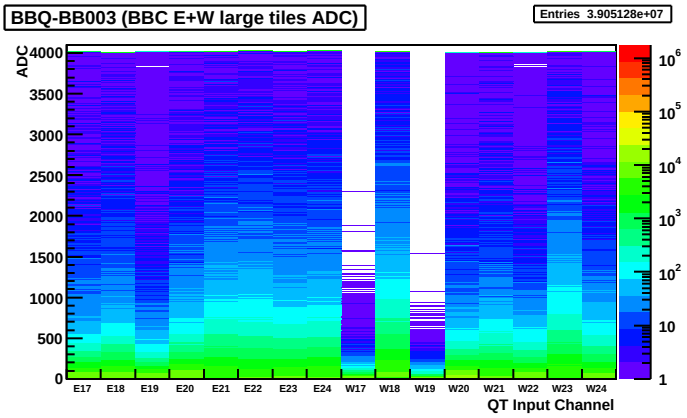

L1-TF201
Entries 1.464423e+07
Threshold bits
1.49551e+06
1.49329e+06
1.50509e+06
1.48748e+06
1.47169e+06
1.40966e+06
945199
947411
935618
953229
969017
1.03105e+06
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

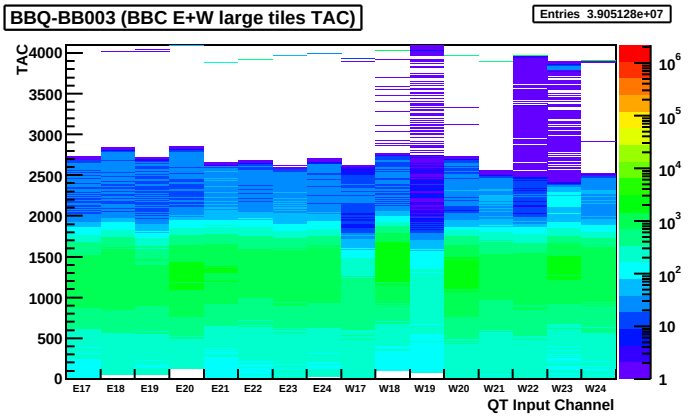

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.905128e+07
ADC
QT Input Channel

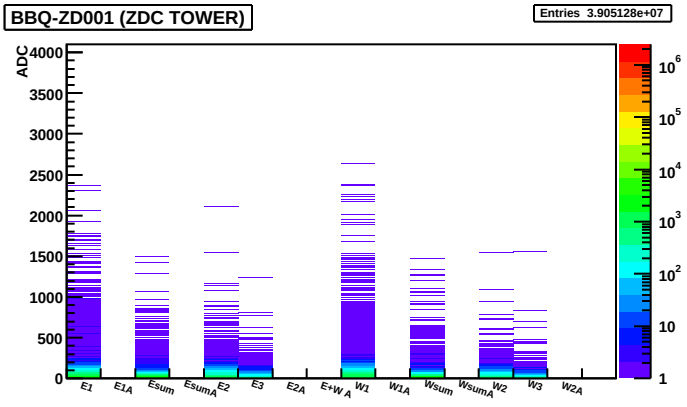

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.905128e+07
TAC
QT Input Channel

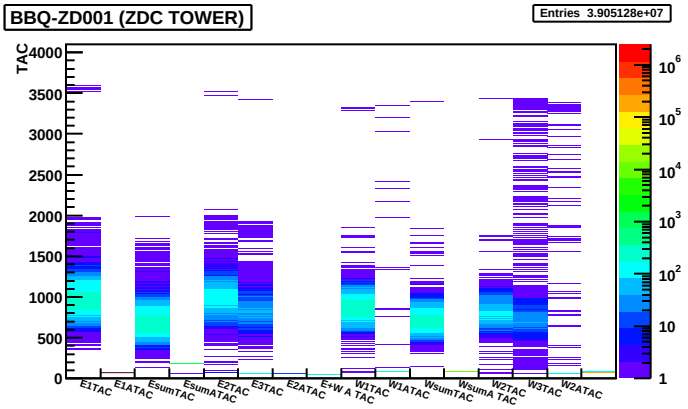

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.905128e+07
ADC
QT Input Channel

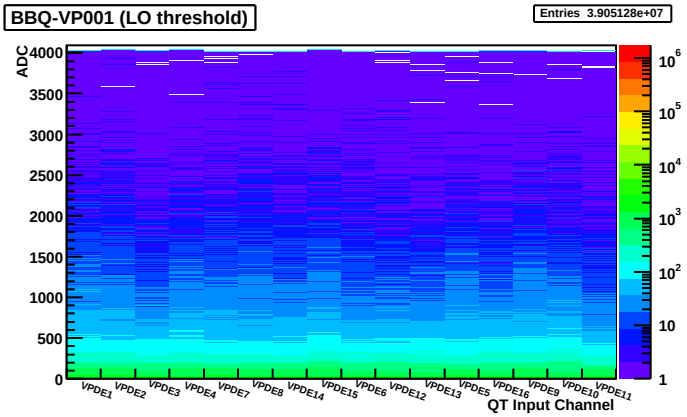

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.905128e+07
TAC
QT Input Channel

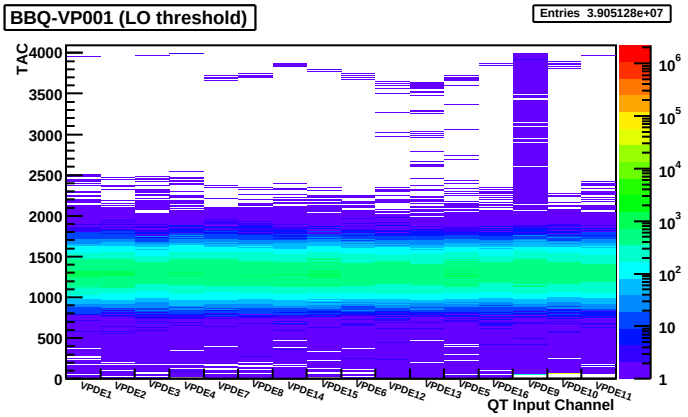

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.905128e+07
ADC
QT Input Channel

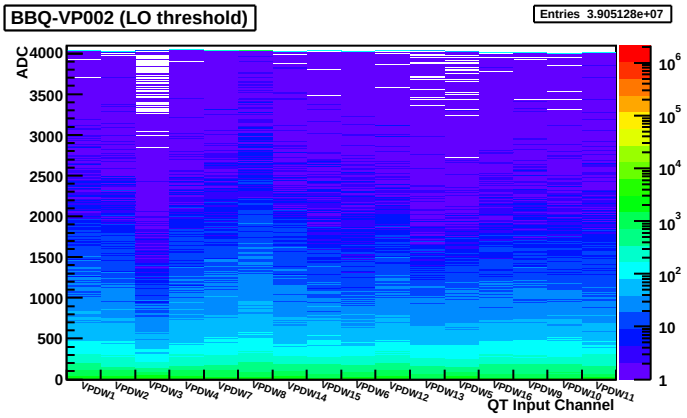

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.905128e+07
TAC
QT Input Channel

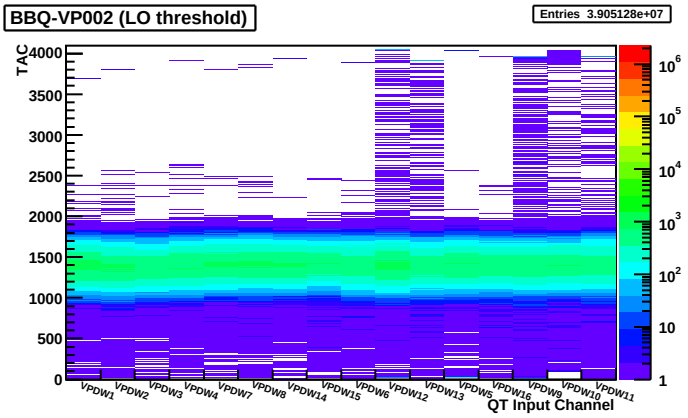

BBQ-ZD001 (ZDC TOWER)
Entries 3.905128e+07
ADC

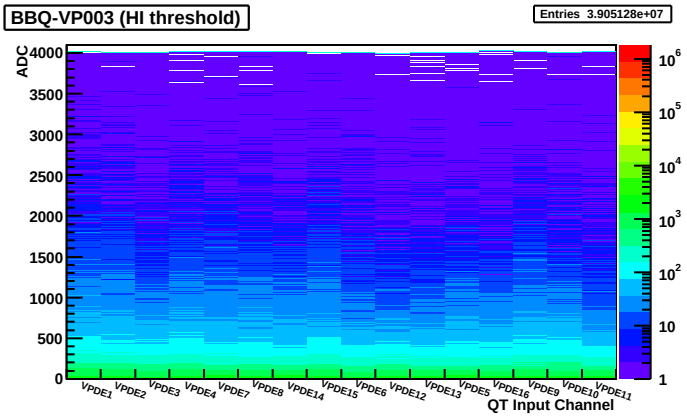

BBQ-ZD001 (ZDC TOWER)
Entries 3.905128e+07
TAC

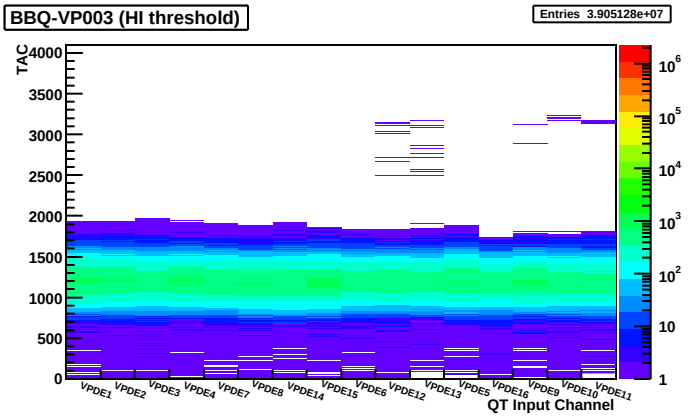

BBQ-VP001 (LO threshold)
Entries 3.905128e+07
ADC
QT Input Channel

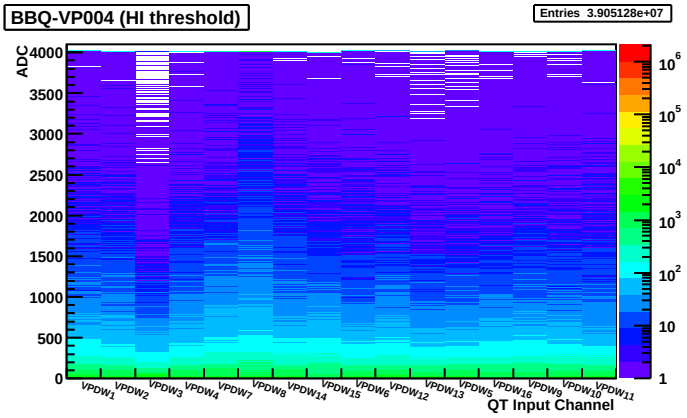

BBQ-VP001 (LO threshold)
Entries 3.905128e+07
TAC
QT Input Channel

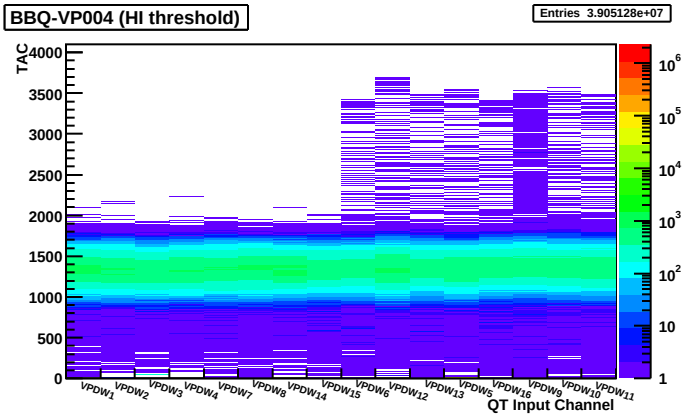

BBQ-VP002 (LO threshold)
Entries 3.905128e+07
ADC
QT Input Channel

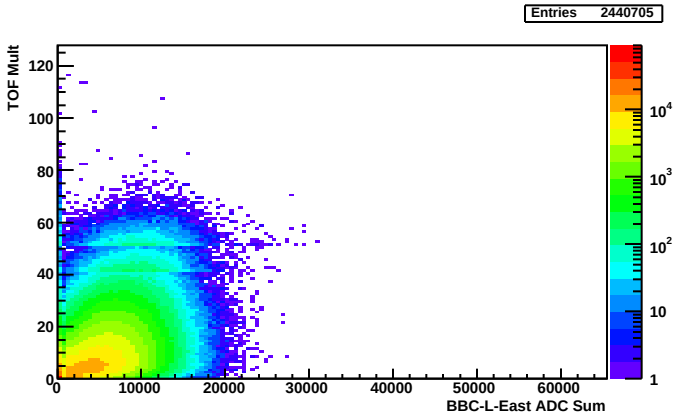

BBQ-VP002 (LO threshold)
Entries 3.905128e+07
TAC
QT Input Channel

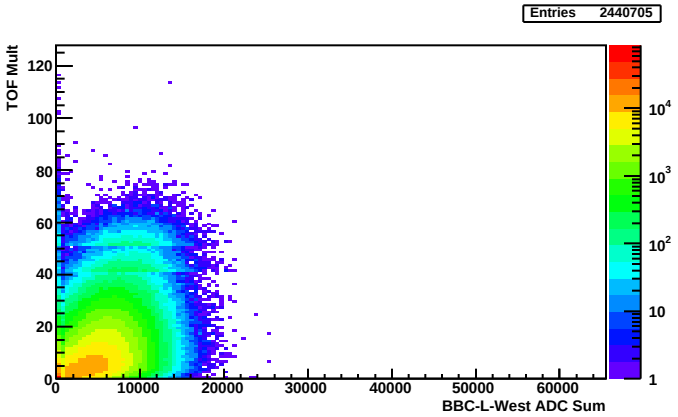

BBQ-VP003 (HI threshold)
Entries 3.905128e+07
ADC
QT Input Channel

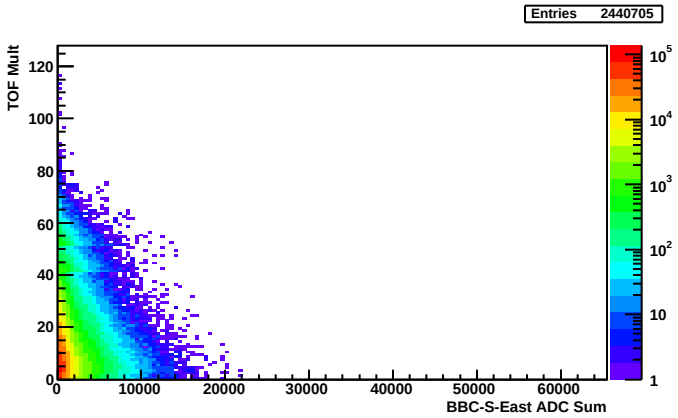

BBQ-VP003 (HI threshold)
Entries 3.905128e+07
TAC
QT Input Channel

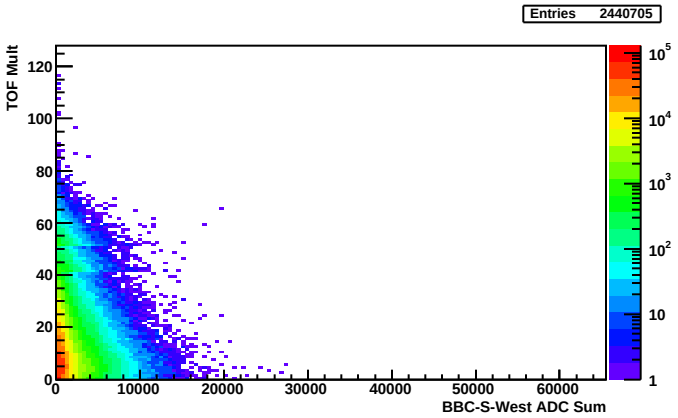

BBQ-VP004 (HI threshold)
Entries 3.905128e+07
ADC
QT Input Channel

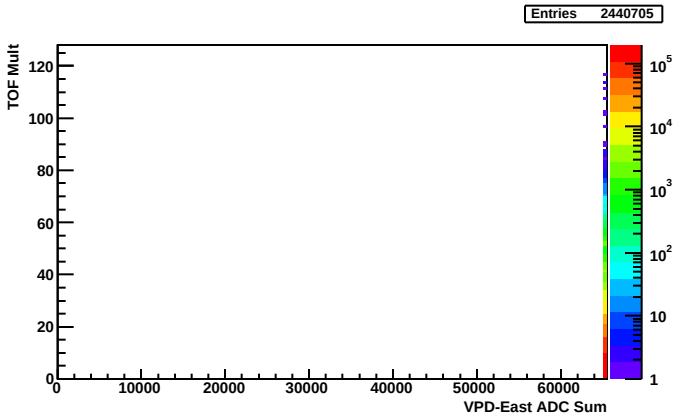

BBQ-VP004 (HI threshold)
Entries 3.905128e+07
TAC
QT Input Channel

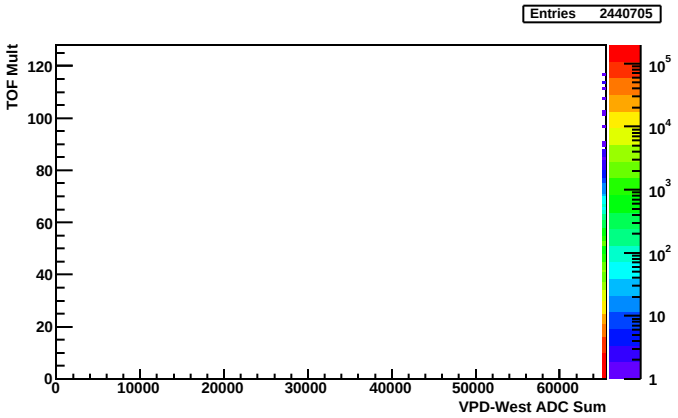

Entries 2440705
TOF Mult
BBC-L-East ADC Sum

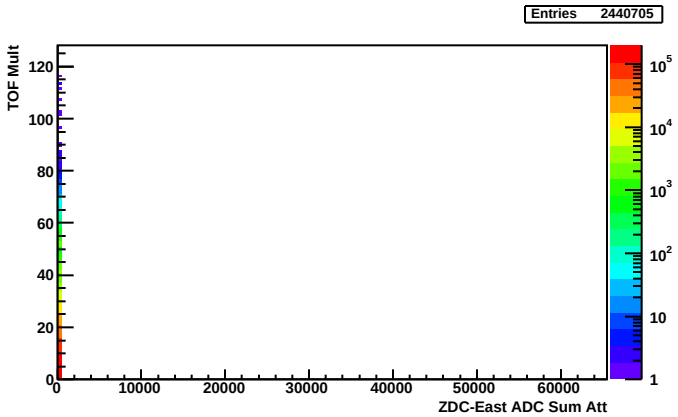

Entries 2440705
TOF Mult
BBC-L-West ADC Sum

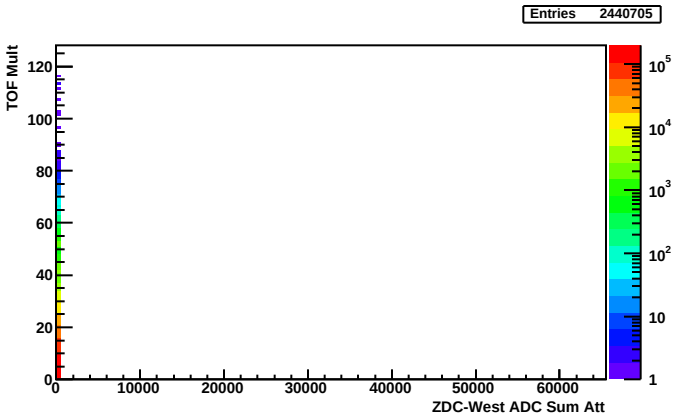

Entries 2440705
TOF Mult
BBC-S-East ADC Sum

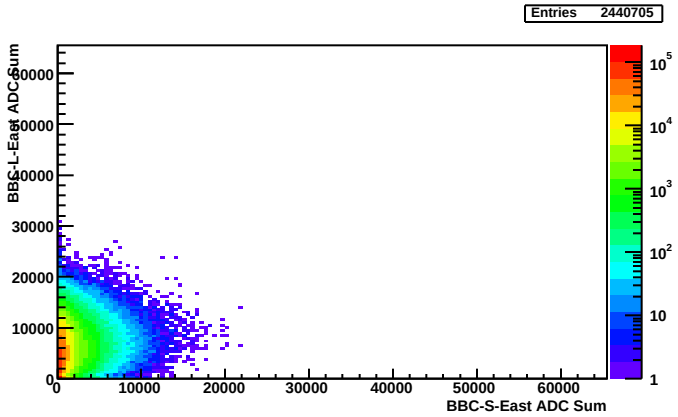

Entries 2440705
TOF Mult
BBC-S-West ADC Sum

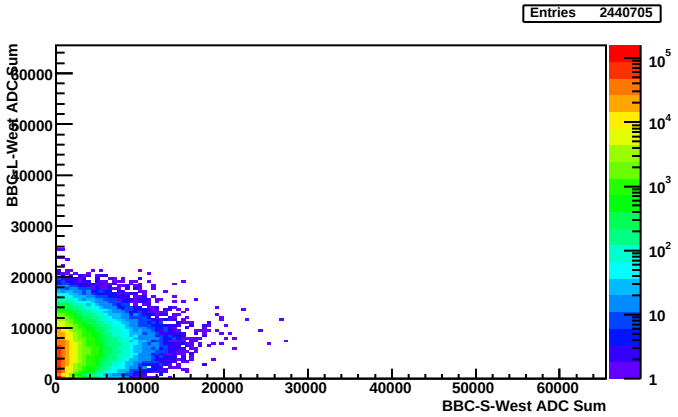

Entries 2440705
TOF Mult
VPD-East ADC Sum

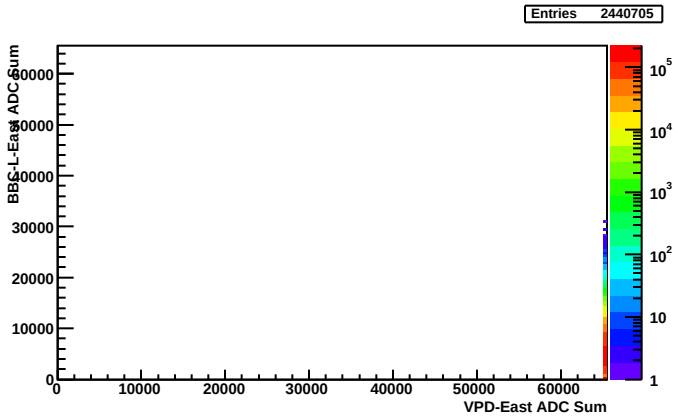

Entries 2440705
TOF Mult
VPD-West ADC Sum

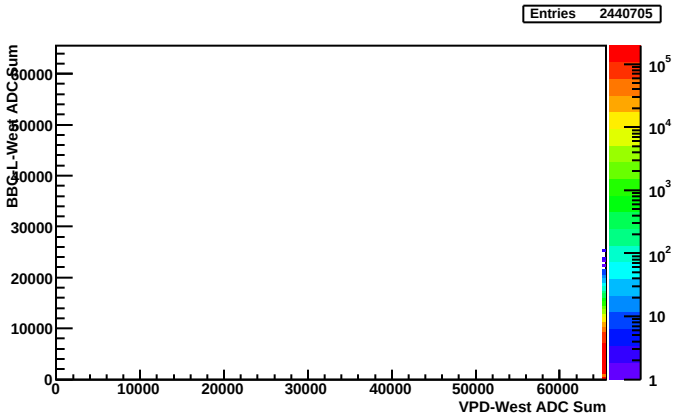

Entries 2440705
TOF Mult
ZDC-East ADC Sum Att

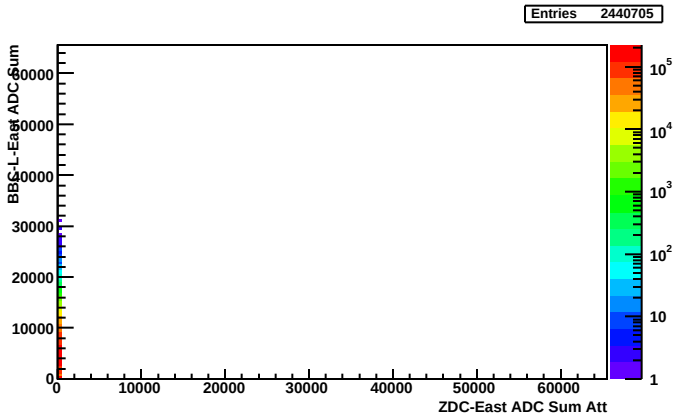

Entries 2440705
TOF Mult
ZDC-West ADC Sum Att

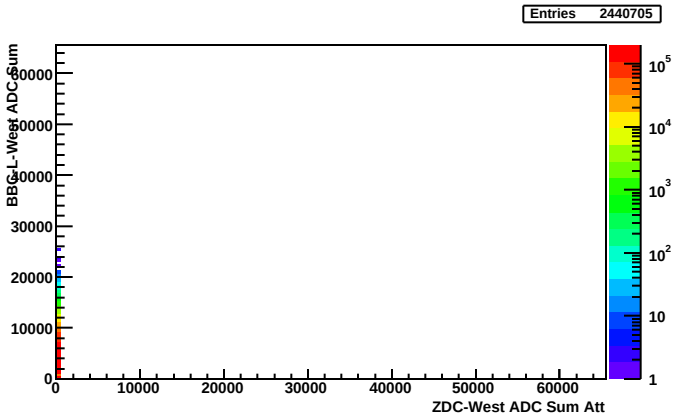

Entries 2440705
BBC-L-East ADC Sum
BBC-S-East ADC Sum

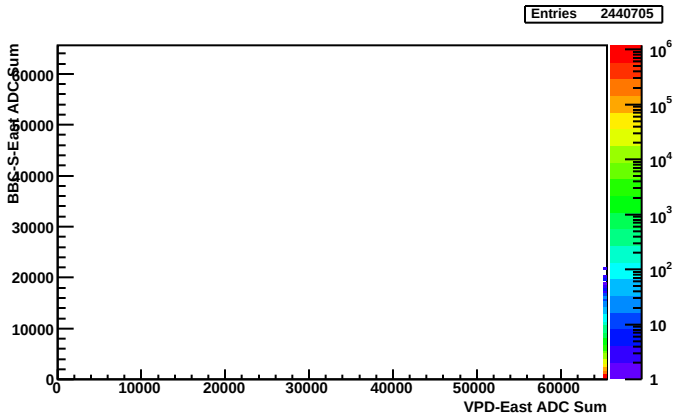

Entries 2440705
BBC-L-West ADC Sum
BBC-S-West ADC Sum

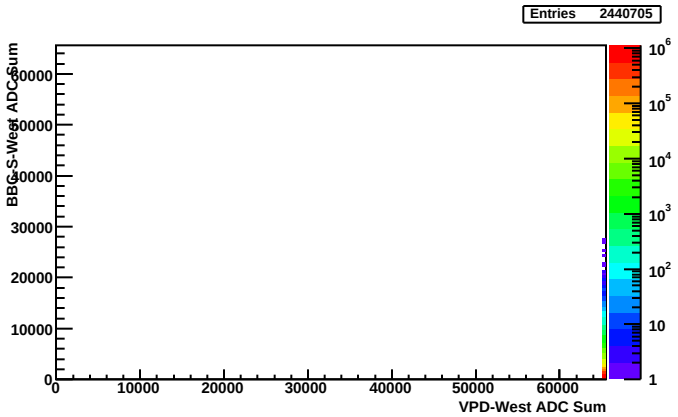

Entries 2440705
BBC-L-East ADC Sum
VPD-East ADC Sum

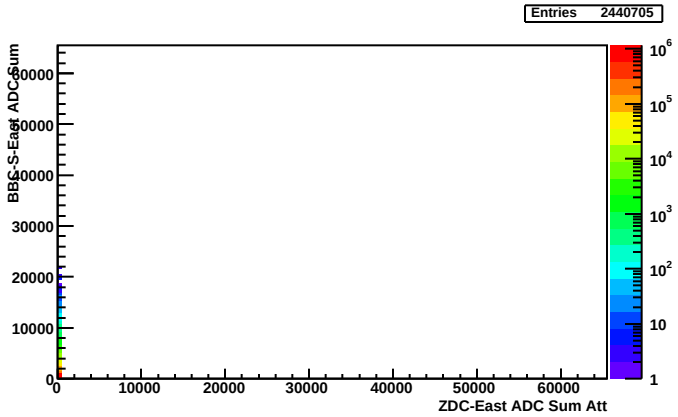

Entries 2440705
BBC-L-West ADC Sum
VPD-West ADC Sum

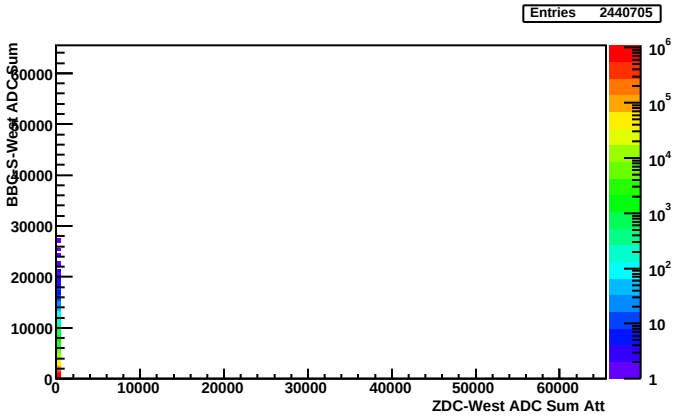

Entries 2440705
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

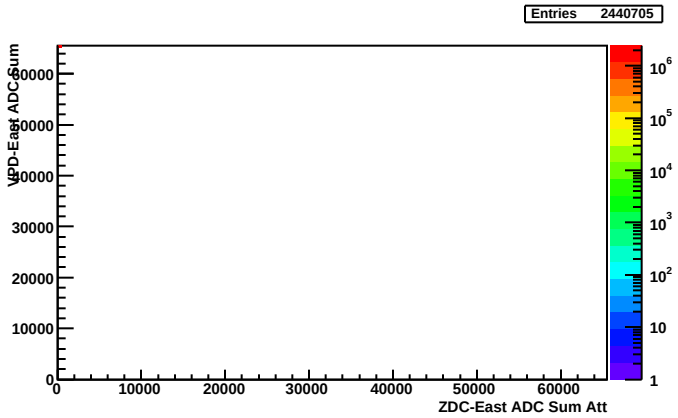

Entries 2440705
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 2440705
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 2440705
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 2440705
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 2440705
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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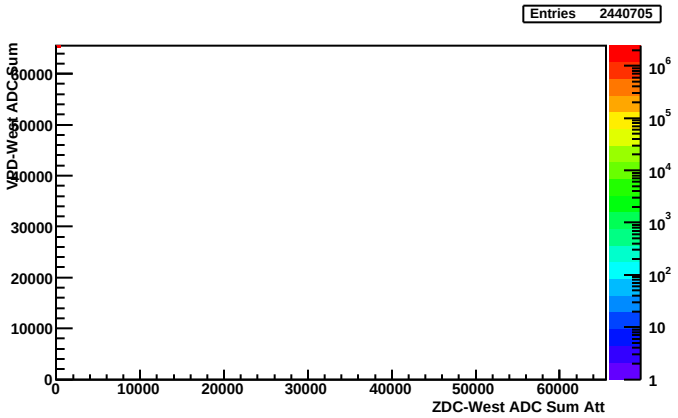
VPD-East ADC Sum
ZDC-East ADC Sum Att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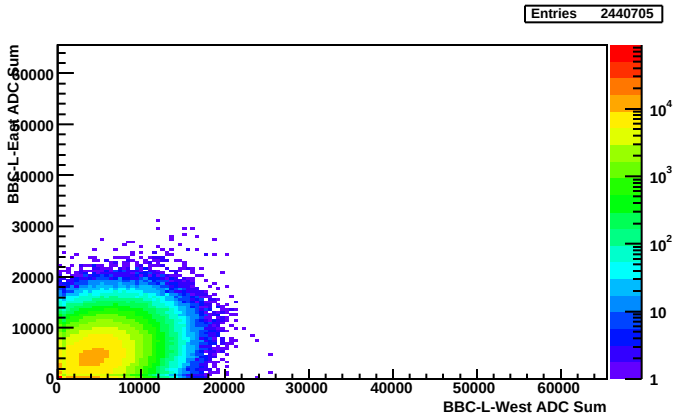
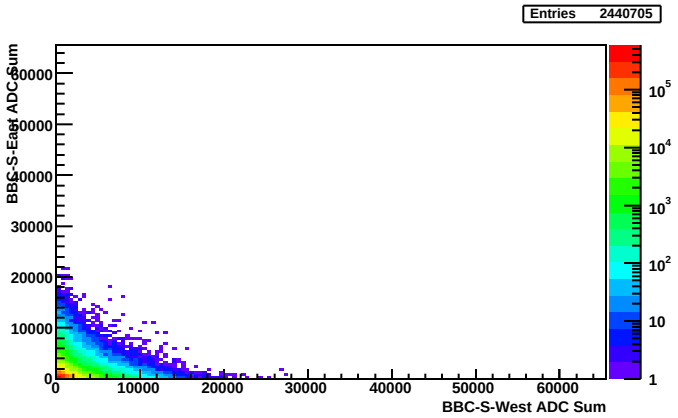
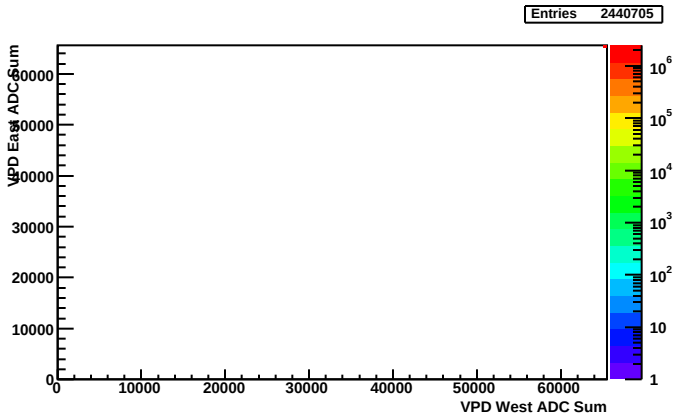
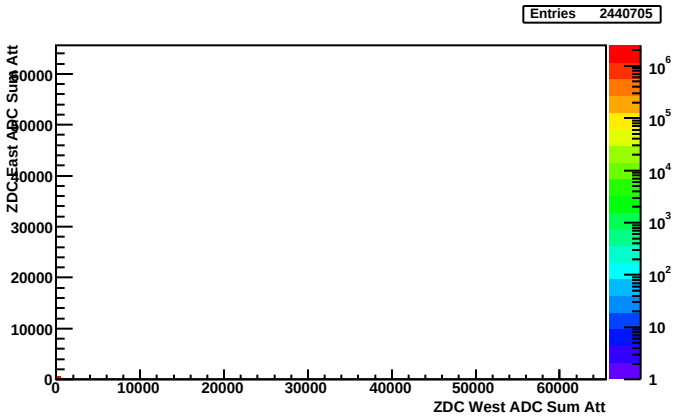
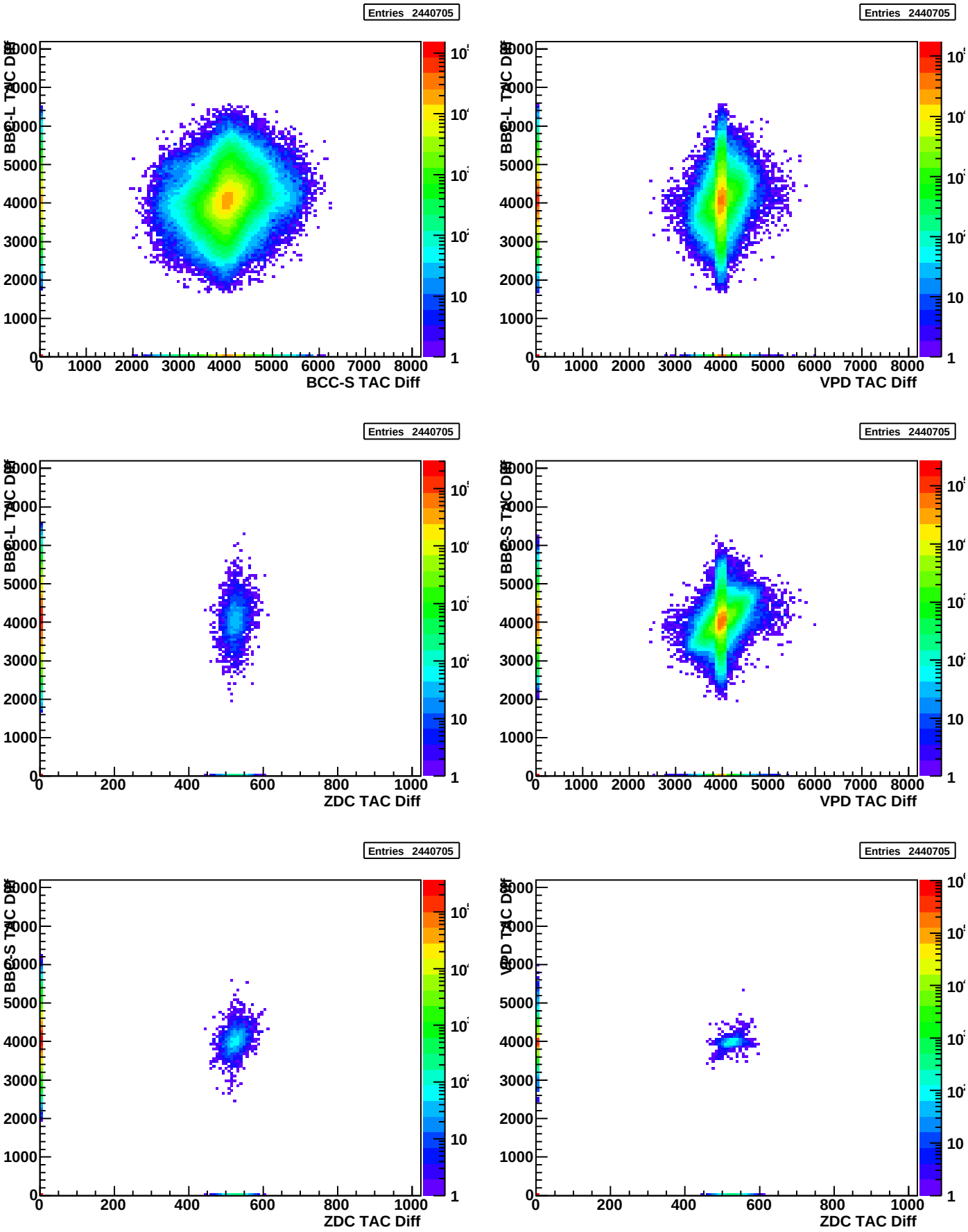
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 2440705
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 2440705
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 2440705
VPD East ADC Sum
VPD West ADC Sum


Entries 2440705
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 2440705
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2440705
BBC-L TAC Diff
VPD TAC Diff
Entries 2440705
BBC-L TAC Diff
ZDC TAC Diff
Entries 2440705
BBC-S TAC Diff
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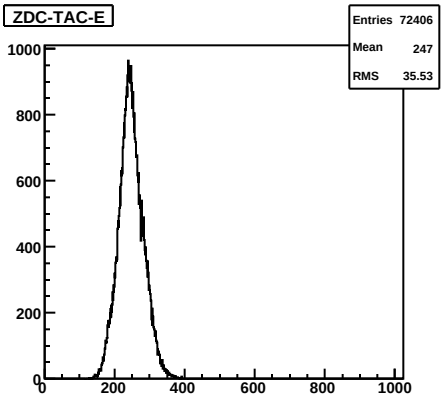
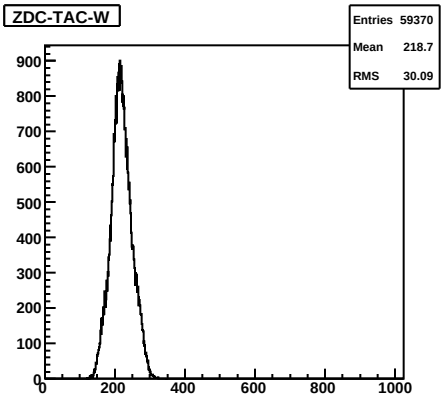
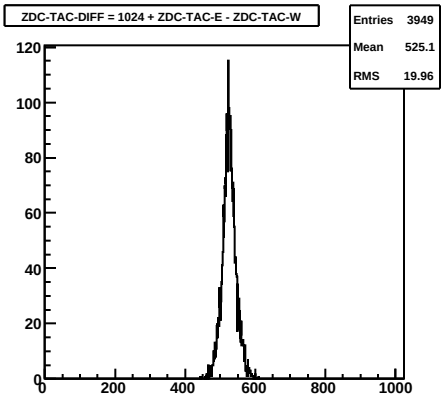
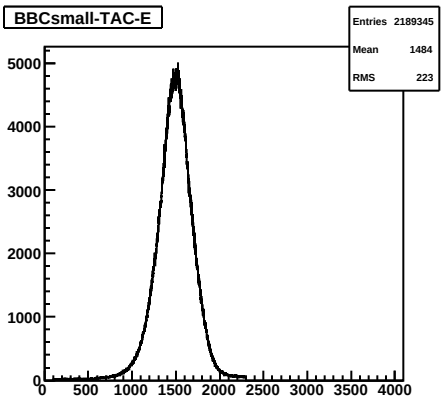
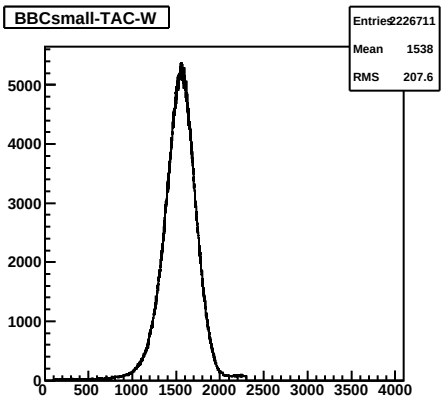
Entries 2440705
BBC-S TAC Diff
ZDC TAC Diff
Entries 2440705
VPD TAC Diff
ZDC TAC Diff

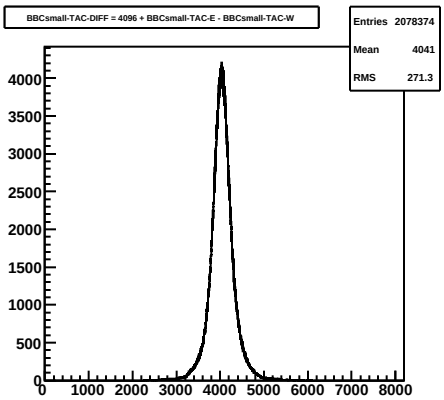

ZDC-TAC-E
Entries 72406
Mean 247
RMS 35.53

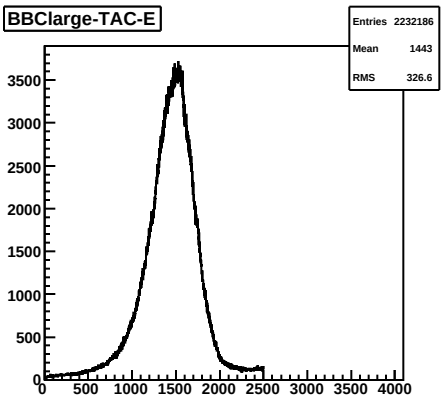

ZDC-TAC-W
Entries 59370
Mean 218.7
RMS 30.09

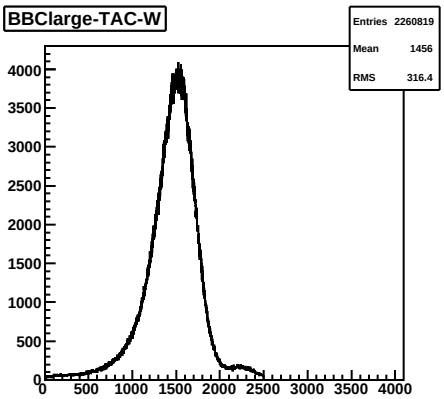

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3949
Mean 525.1
RMS 19.96

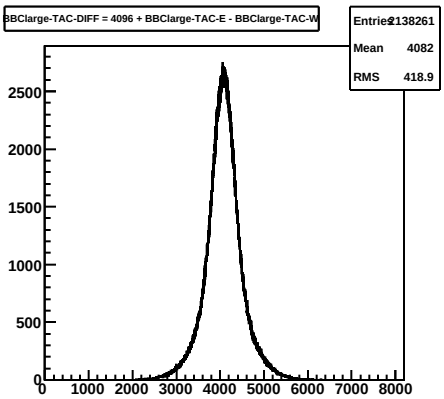

BBCsmall-TAC-E
Entries 2189345
Mean 1484
RMS 223

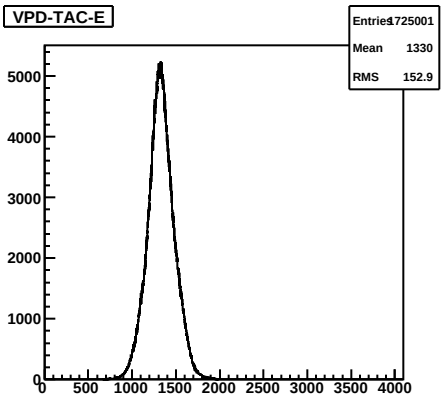

BBCsmall-TAC-W
Entries 2226711
Mean 1538
RMS 207.6

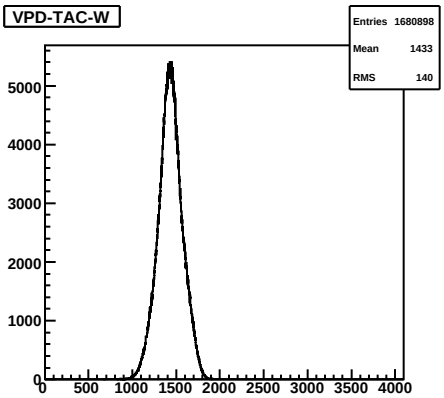

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2078374
Mean 4041
RMS 271.3

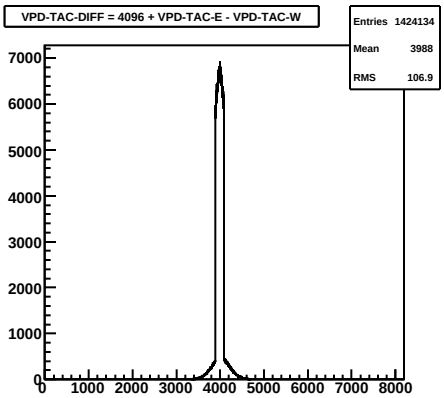

BBClarge-TAC-E
Entries 2232186
Mean 1443
RMS 326.6

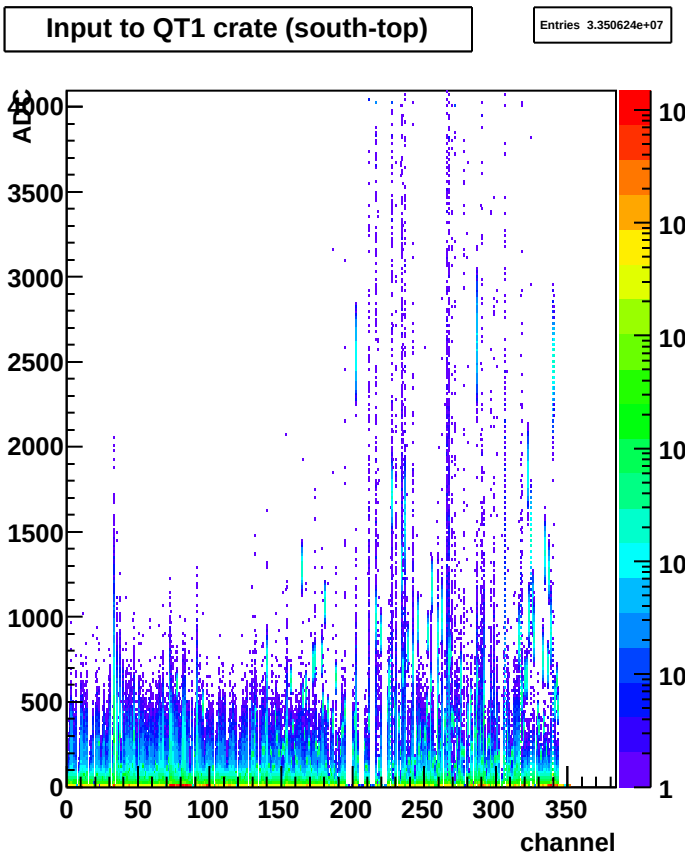

BBClarge-TAC-W
Entries 2260819
Mean 1456
RMS 316.4

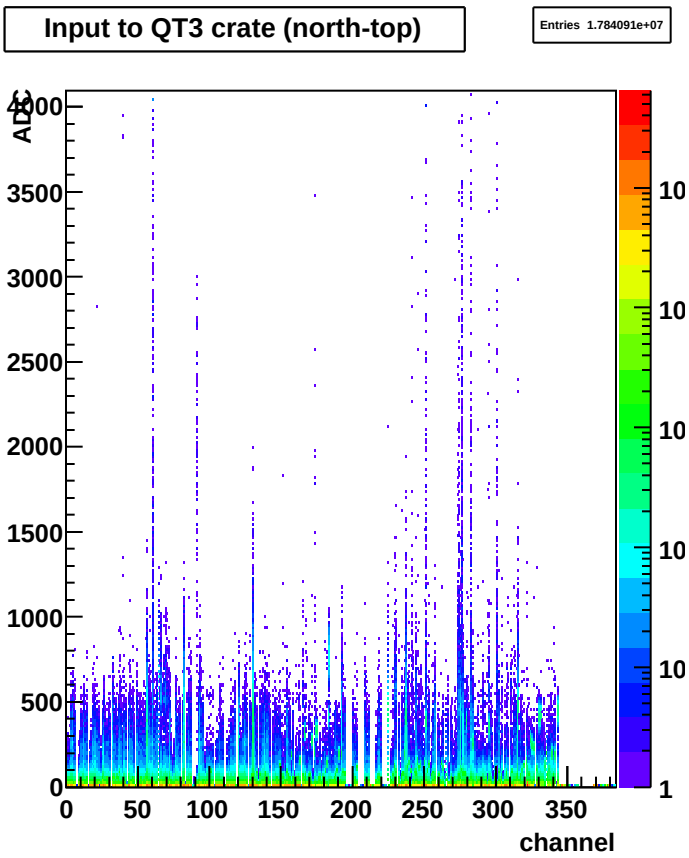

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2138261
Mean 4082
RMS 418.9

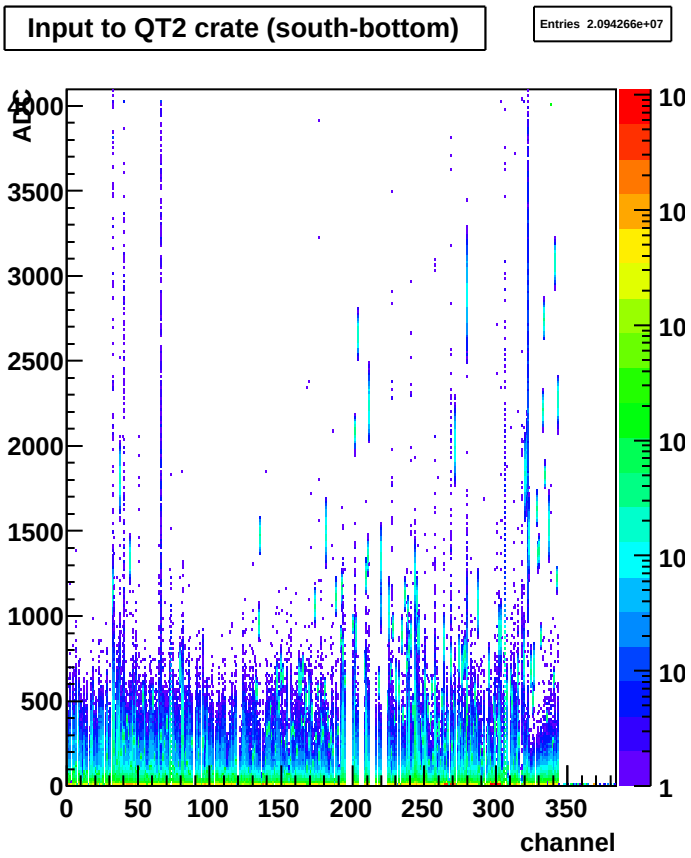

VPD-TAC-E
Entries 1725001
Mean 1330
RMS 152.9

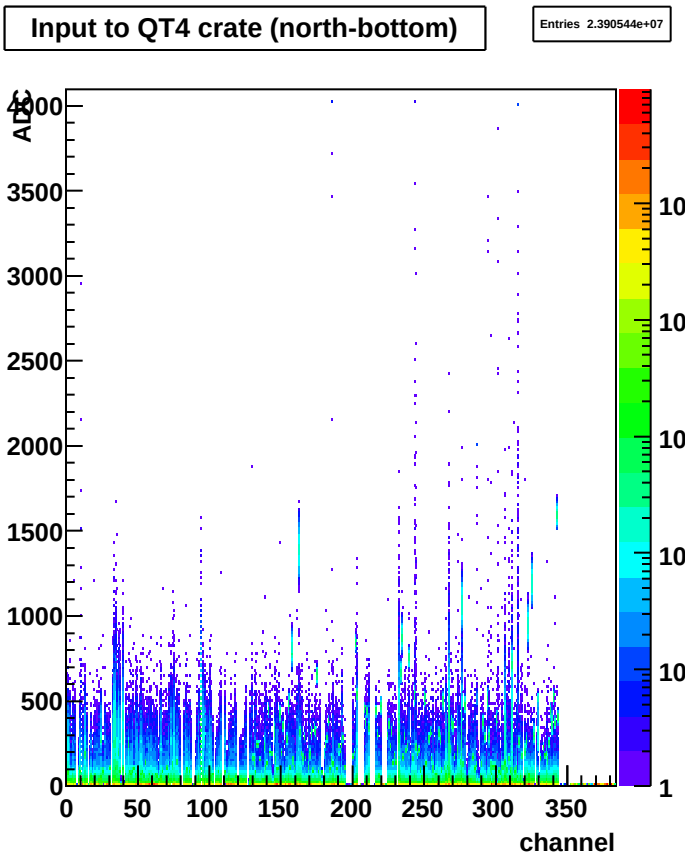

VPD-TAC-W
Entries 1680898
Mean 1433
RMS 140

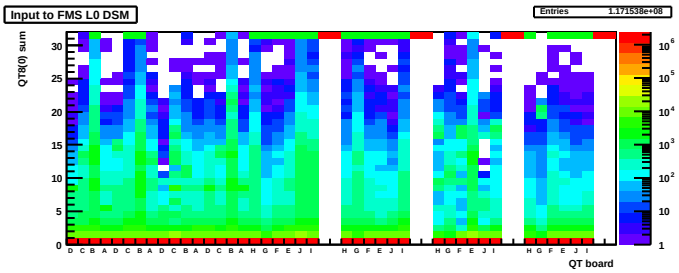

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1424134
Mean 3988
RMS 106.9

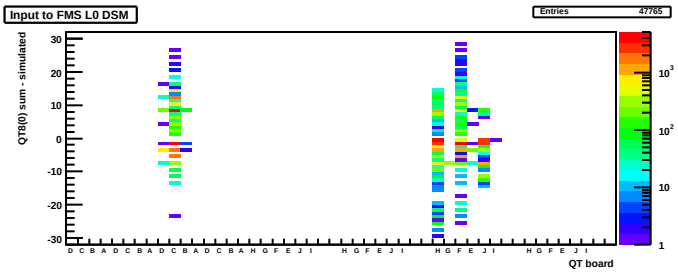

Input to QT1 crate (south-top)
Entries 3.350624e+07
ADC
channel

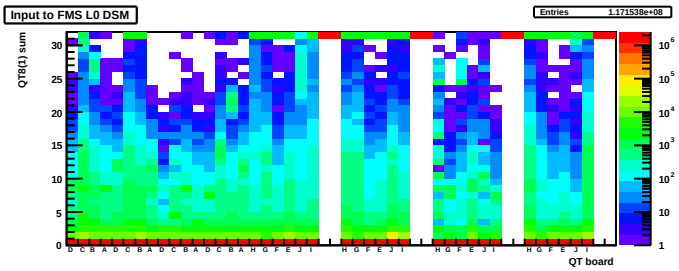

Input to QT3 crate (north-top)
Entries 1.784091e+07
ADC
channel

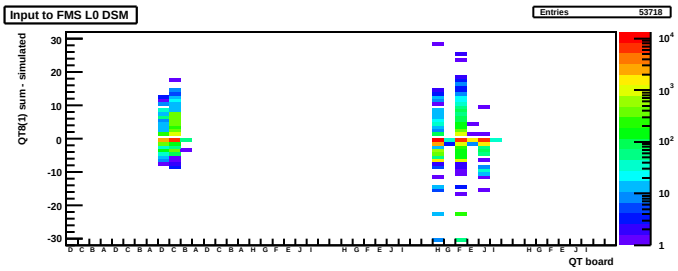

Input to QT2 crate (south-bottom)
Entries 2.094266e+07
ADC
channel

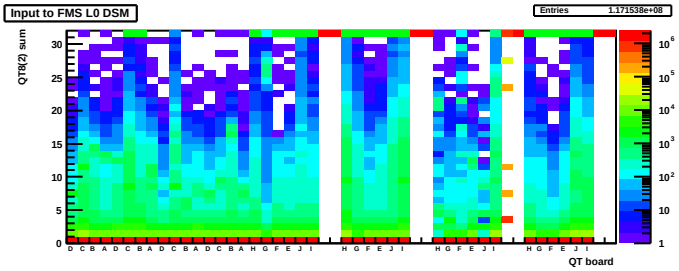

Input to QT4 crate (north-bottom)
Entries 2.390544e+07
ADC
channel

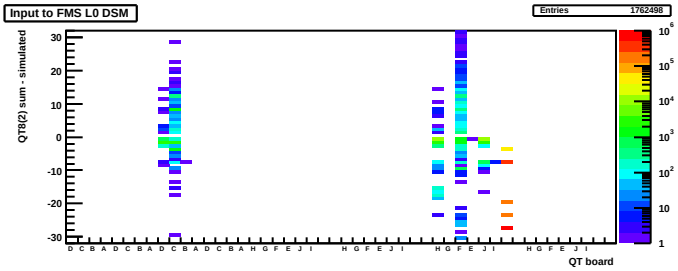

Input to FMS L0 DSM
Entries 1.171538e+08
QT8(0) sum
QT board

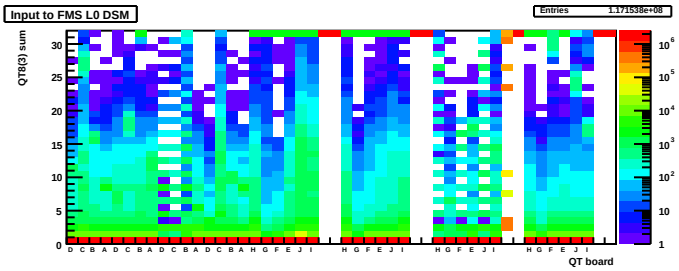

Input to FMS L0 DSM
Entries 47765
QT8(0) sum - simulated
QT board

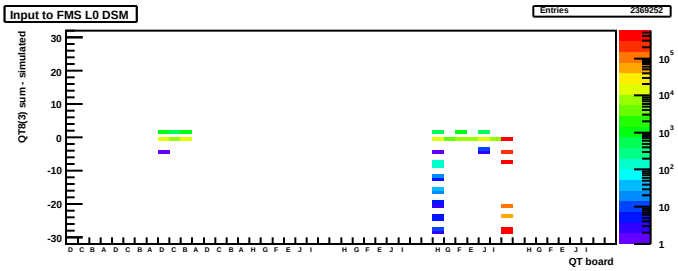

Input to FMS L0 DSM
Entries 1.171538e+08
QT8(1) sum
QT board

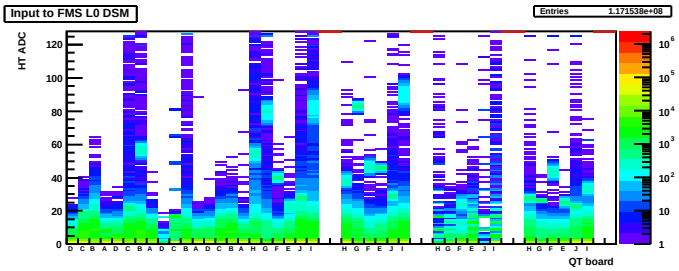

Input to FMS L0 DSM
Entries 53718
QT8(1) sum - simulated
QT board

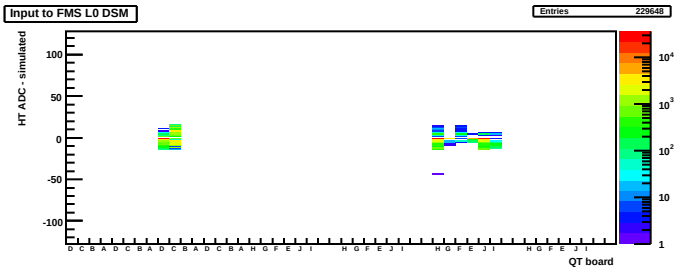

Input to FMS L0 DSM
Entries 1.171538e+08
QT8(2) sum
QT board

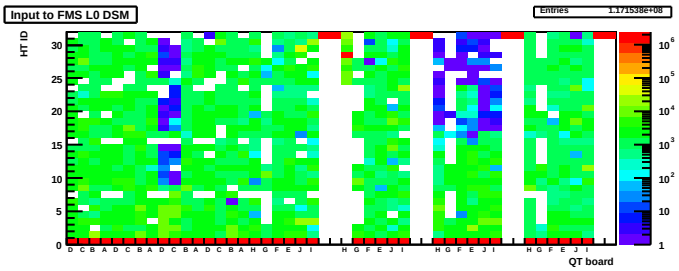

Input to FMS L0 DSM
Entries 1762498
QT8(2) sum - simulated
QT board

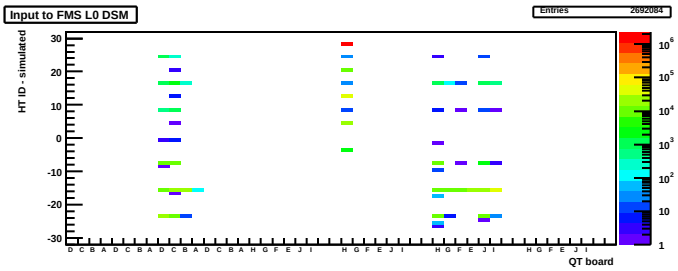

Input to FMS L0 DSM
Entries 1.171538e+08
QT8(3) sum
QT board

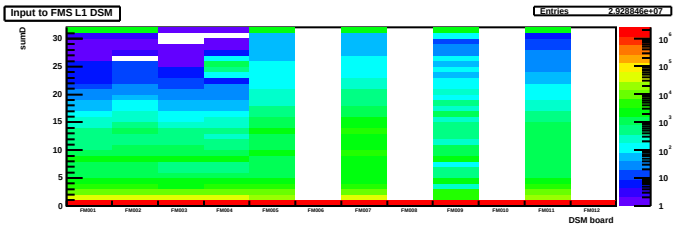

Input to FMS L0 DSM
Entries 2369252
QT8(3) sum - simulated
QT board

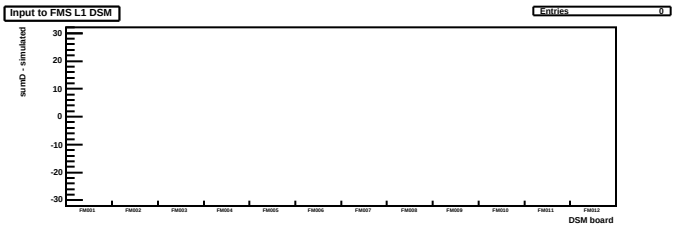

Input to FMS L0 DSM
Entries 1.171538e+08
HT ADC
QT board

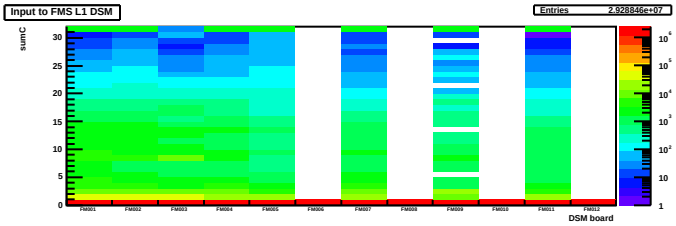

Input to FMS L0 DSM
Entries 229648
HT ADC - simulated
QT board

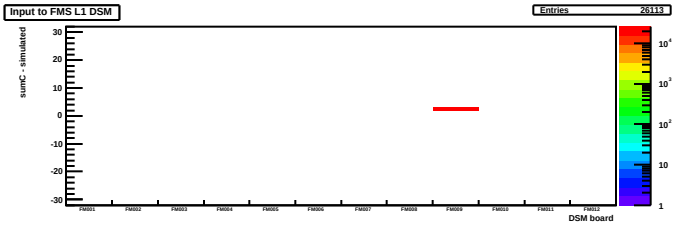

Input to FMS L0 DSM
Entries 1.171538e+08
HT ID
QT board

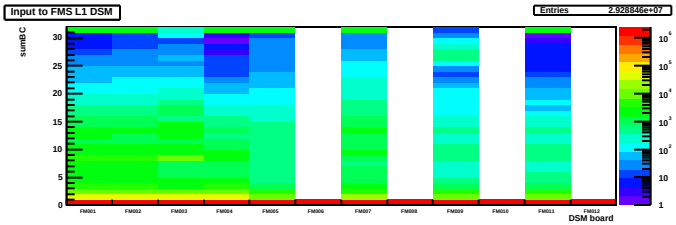

Input to FMS L0 DSM
Entries 2692084
HT ID - simulated
QT board

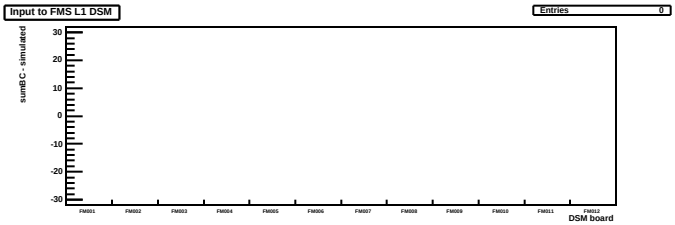

Input to FMS L1 DSM
Entries 2.928846e+07
sumD
DSM board

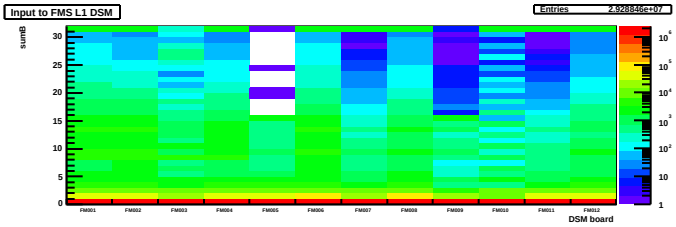

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

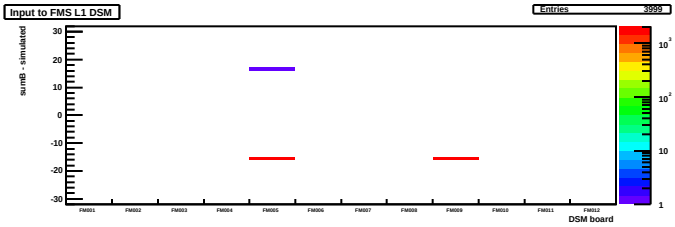

Input to FMS L1 DSM
Entries 2.928846e+07
sumC
DSM board

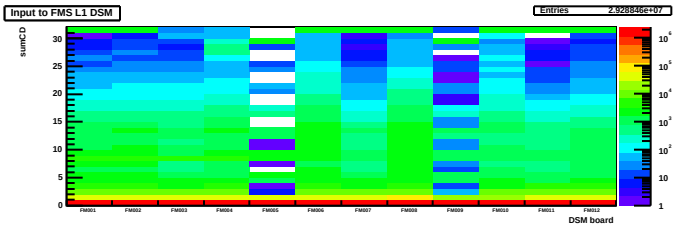

Input to FMS L1 DSM
Entries 26113
sumC - simulated
DSM board

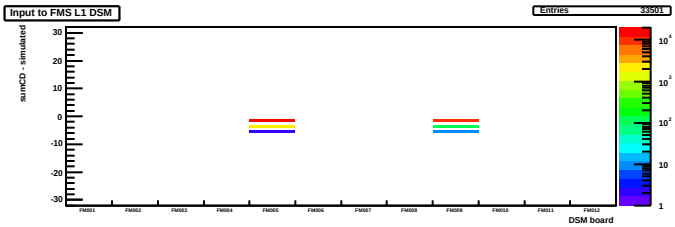

Input to FMS L1 DSM
Entries 2.928846e+07
sumBC
DSM board

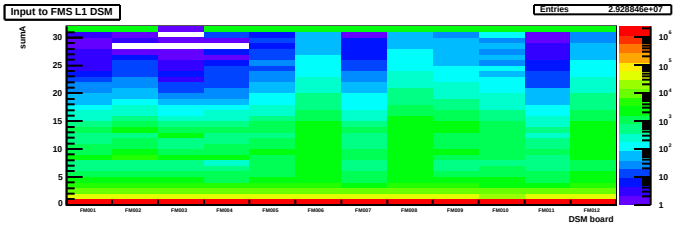

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

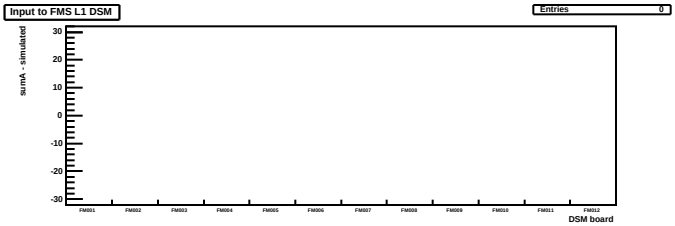

Input to FMS L1 DSM
Entries 2.928846e+07
sumB
DSM board

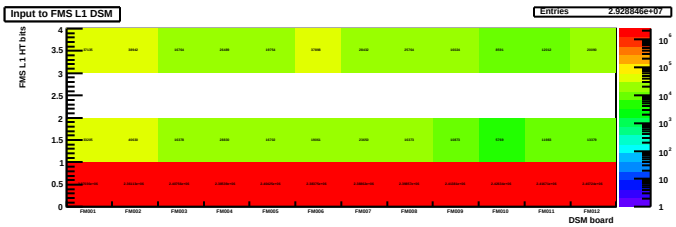

Input to FMS L1 DSM
Entries 3999
sumB - simulated
DSM board

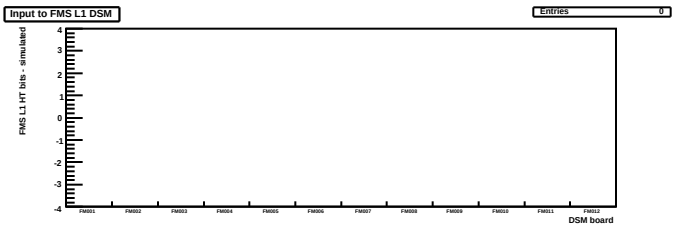

Input to FMS L1 DSM
Entries 2.928846e+07
sumCD
DSM board

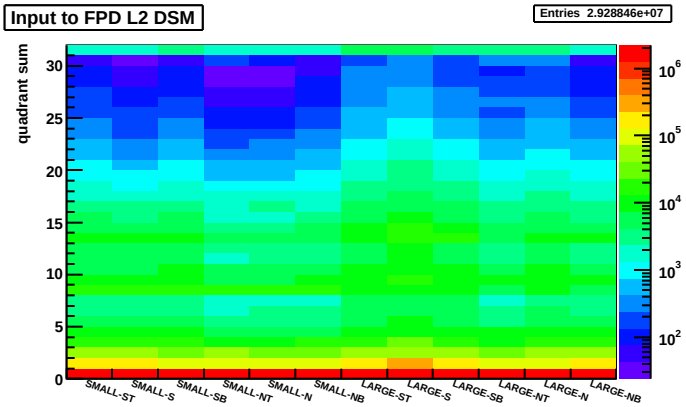

Input to FMS L1 DSM
Entries 33501
sumCD - simulated
DSM board

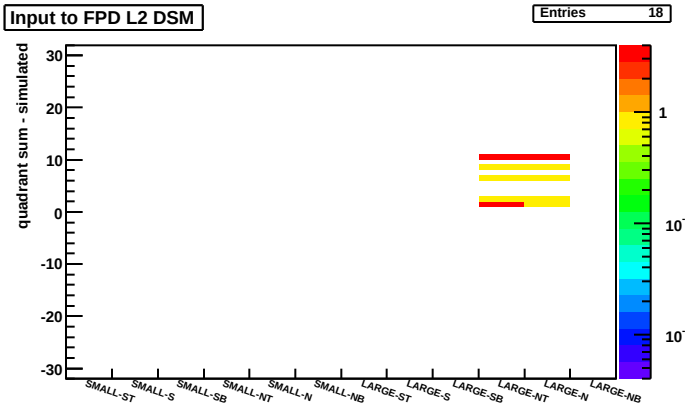

Input to FMS L1 DSM
Entries 2.928846e+07
sumA
DSM board

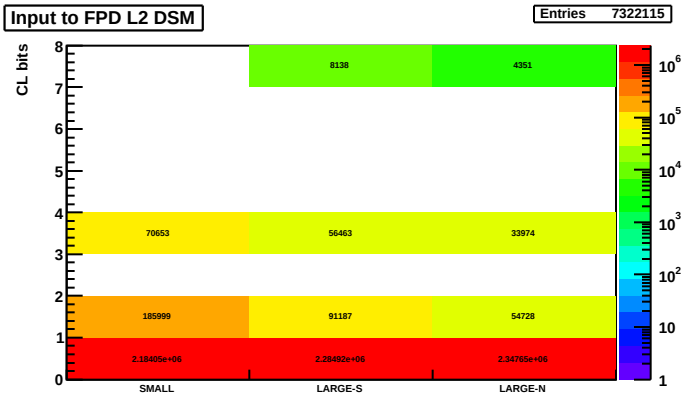

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

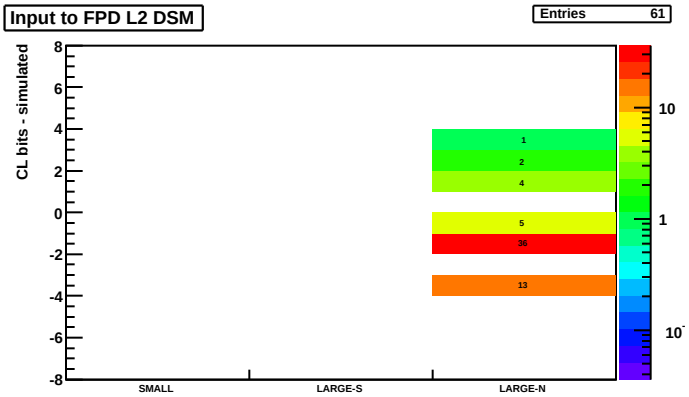

Input to FMS L1 DSM
Entries 2.928846e+07
FMS L1 HT bits
DSM board

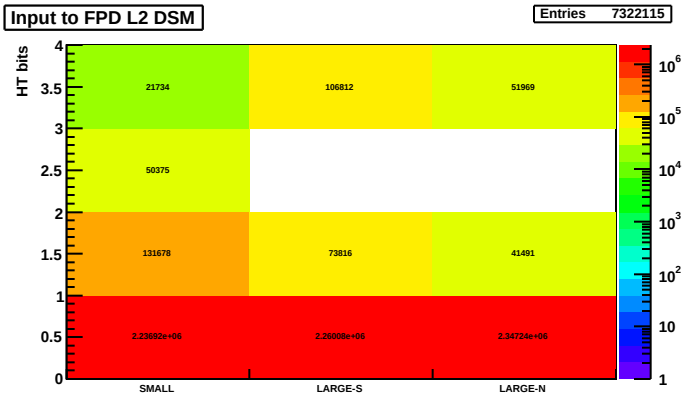

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

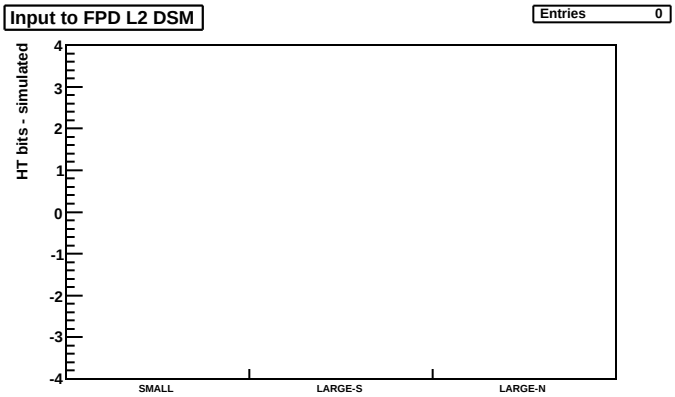

Input to FPD L2 DSM
Entries 2.928846e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

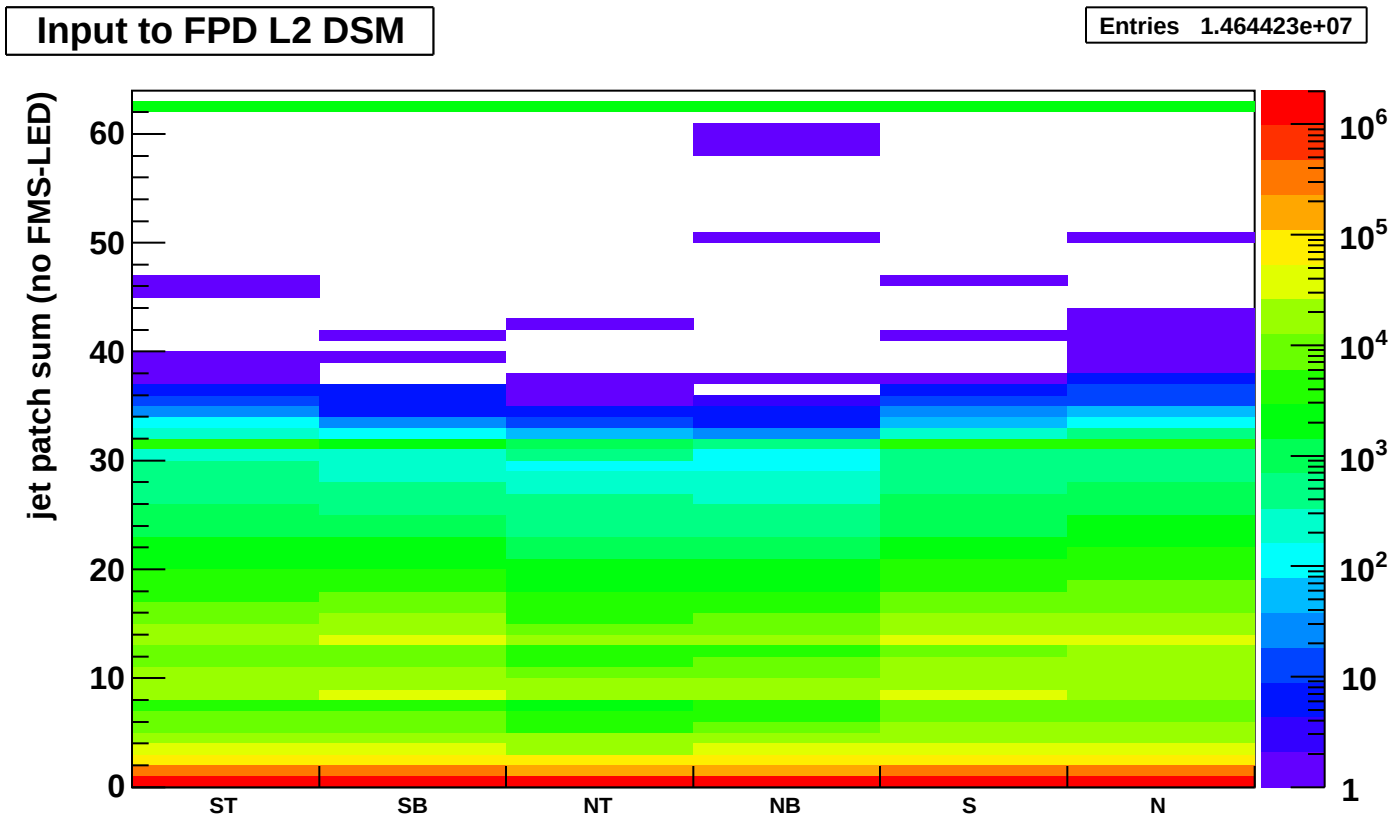

Input to FPD L2 DSM
Entries 18
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

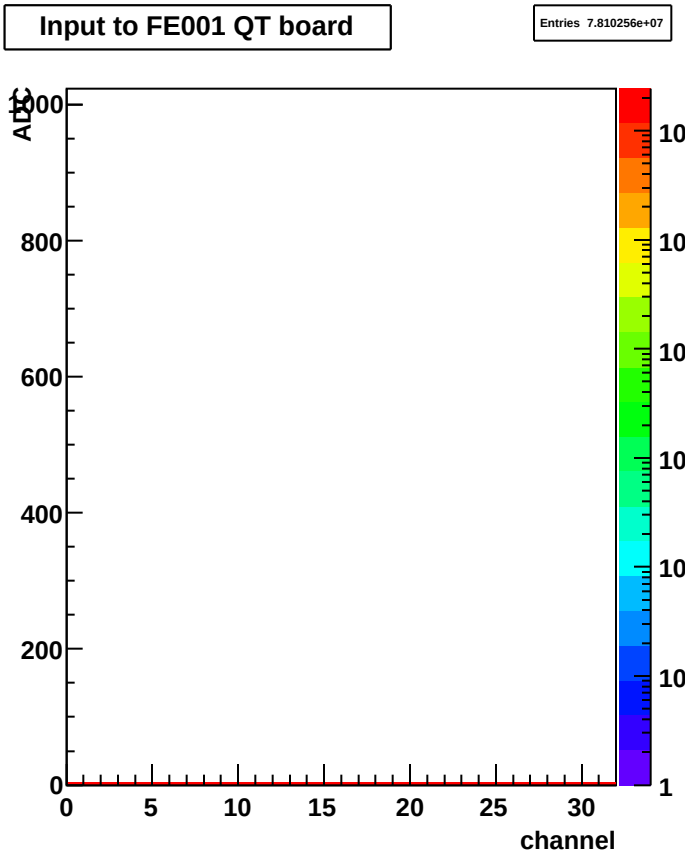

Input to FPD L2 DSM
Entries 7322115
CL bits
8138
4351
70653
56463
33974
185999
91187
54728
2.18405e+06
2.28492e+06
2.34765e+06
SMALL
LARGE-S
LARGE-N

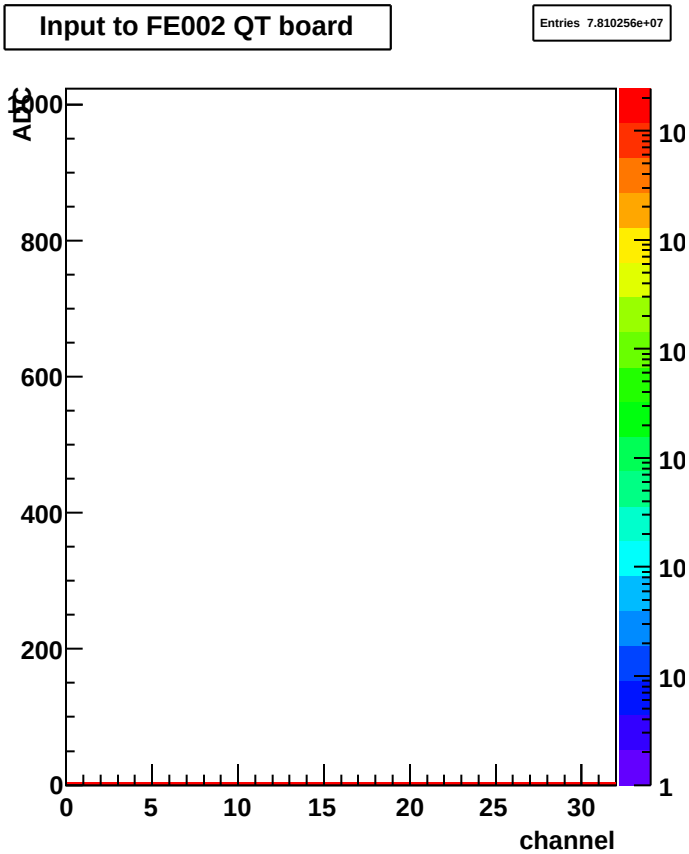

Input to FPD L2 DSM
Entries 61
CL bits - simulated
1
2
4
5
36
13
SMALL
LARGE-S
LARGE-N

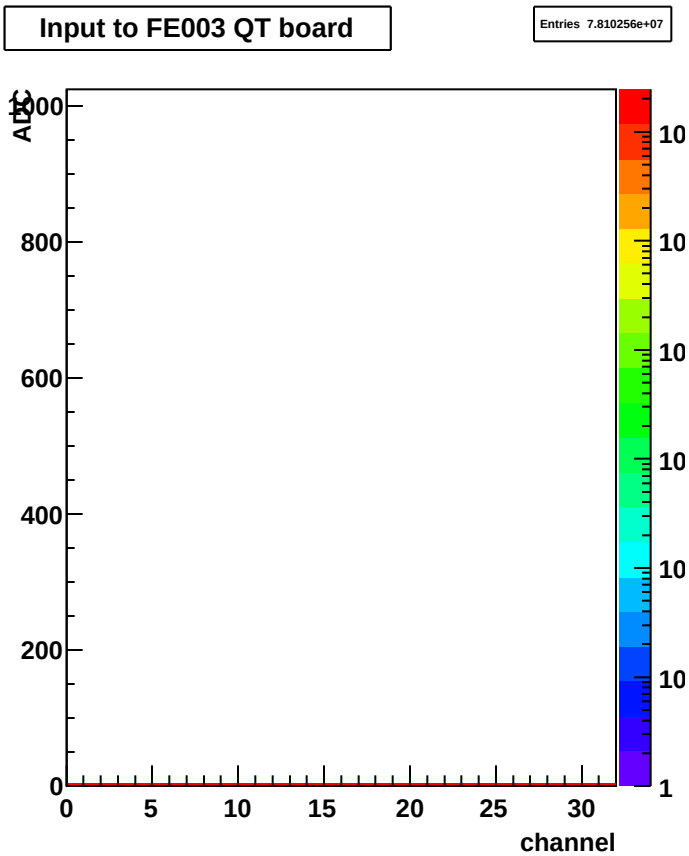

Input to FPD L2 DSM
Entries 7322115
HT bits
21734
106812
51969
50375
131678
73816
41491
2.23692e+06
2.26008e+06
2.34724e+06
SMALL
LARGE-S
LARGE-N

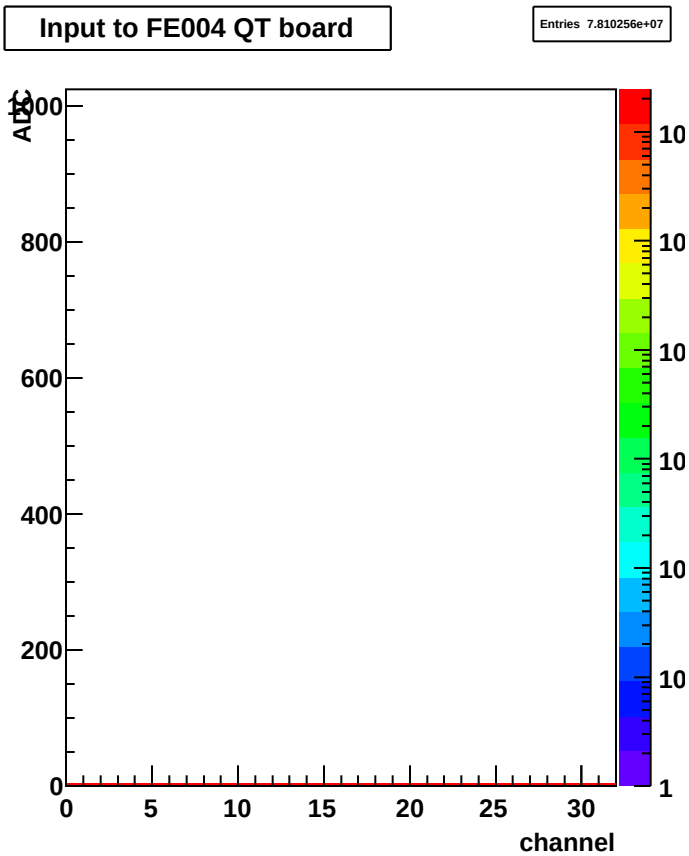

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

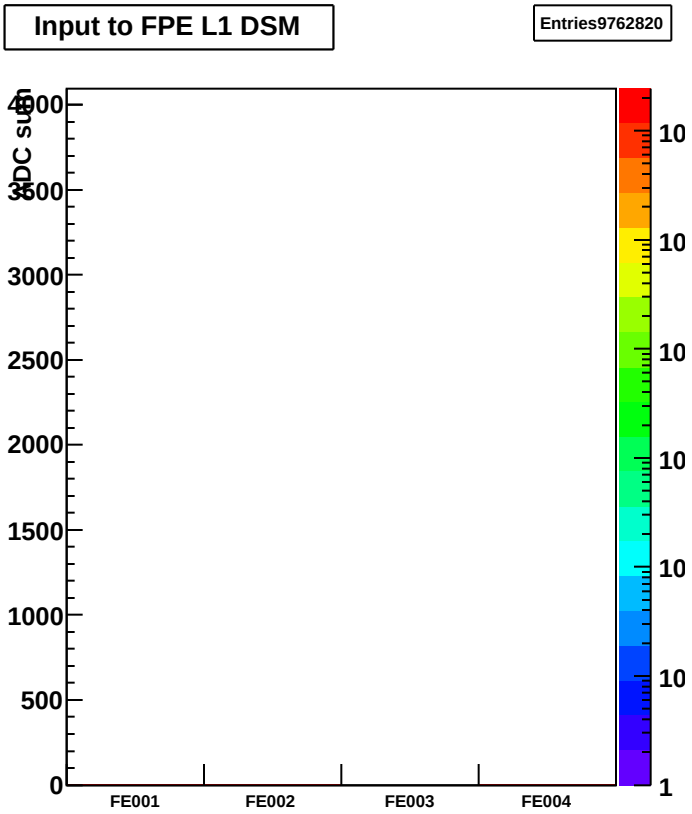

Input to FPD L2 DSM
Entries 1.464423e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

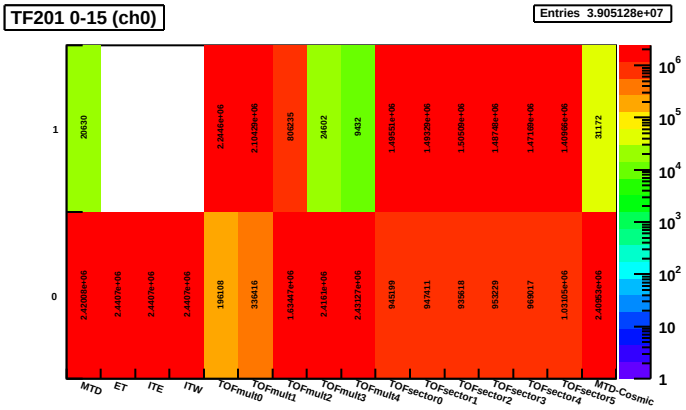

Input to FE001 QT board
Entries 7.810256e+07
ADC
channel

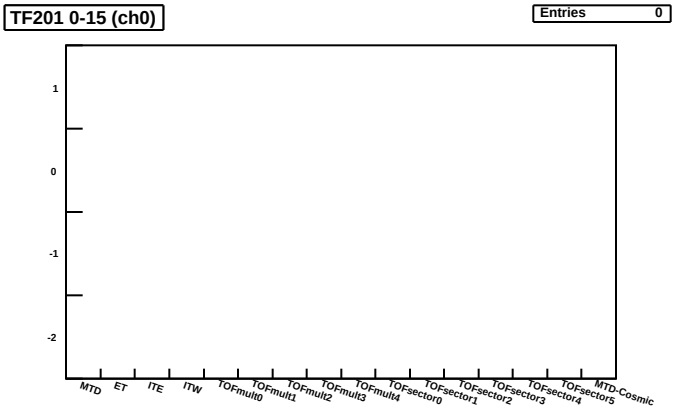

Input to FE002 QT board
Entries 7.810256e+07
ADC
channel

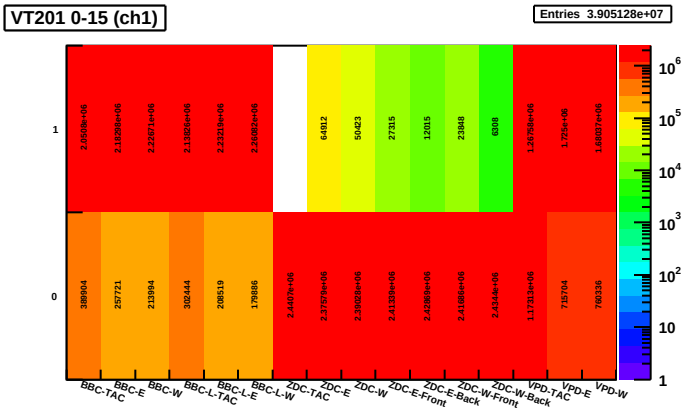

Input to FE003 QT board
Entries 7.810256e+07
ADC
channel

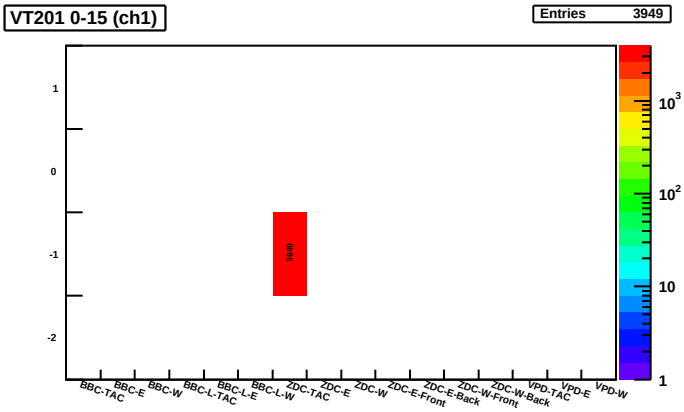

Input to FE004 QT board
Entries 7.810256e+07
ADC
channel


Input to FPE L1 DSM
Entries9762820
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 3.905128e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 3.905128e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W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Entries 3949
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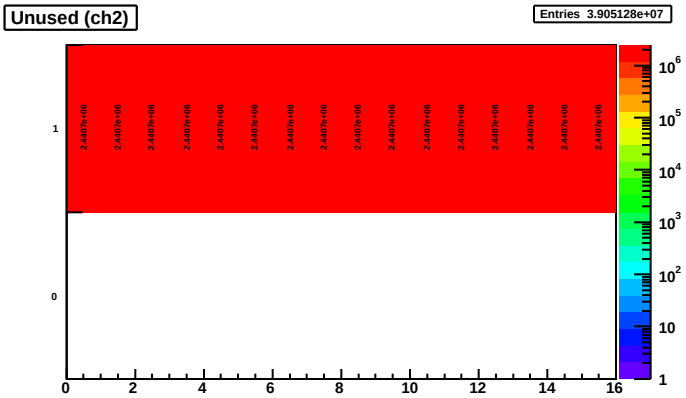
Unused (ch2)
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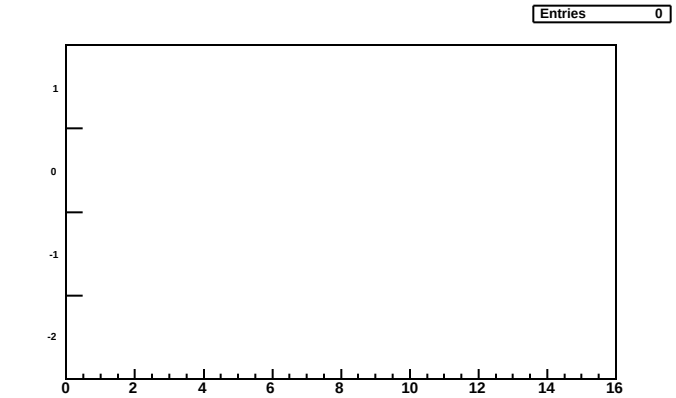
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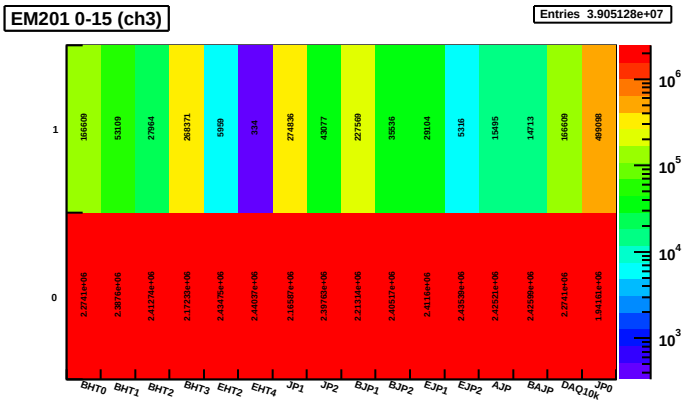
EM201 0-15 (ch3)
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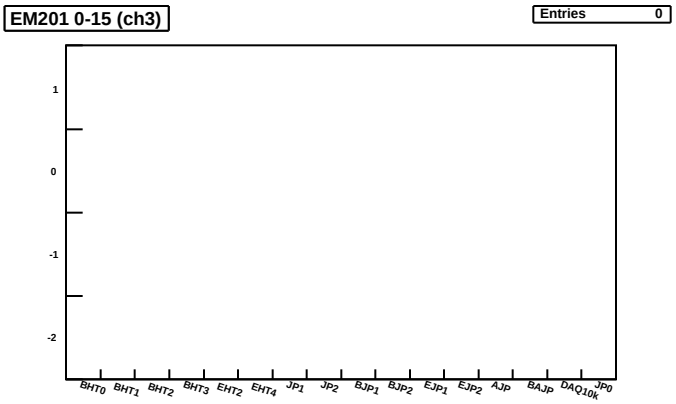
EM201 0-15 (ch3)
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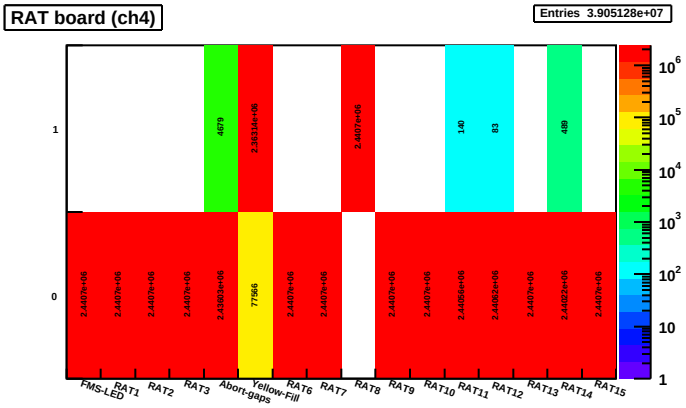
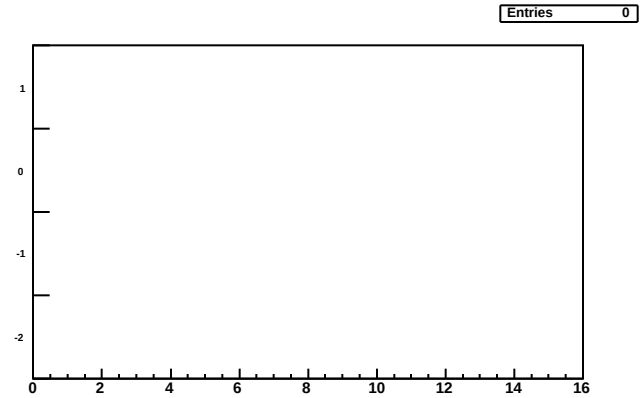
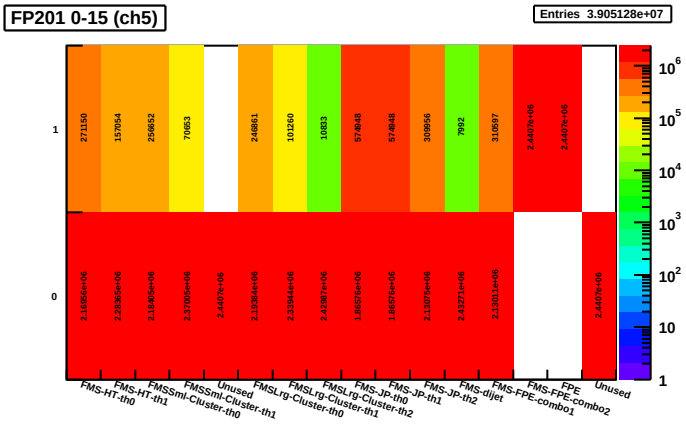
RAT board (ch4)





FP201 0-15 (ch5)



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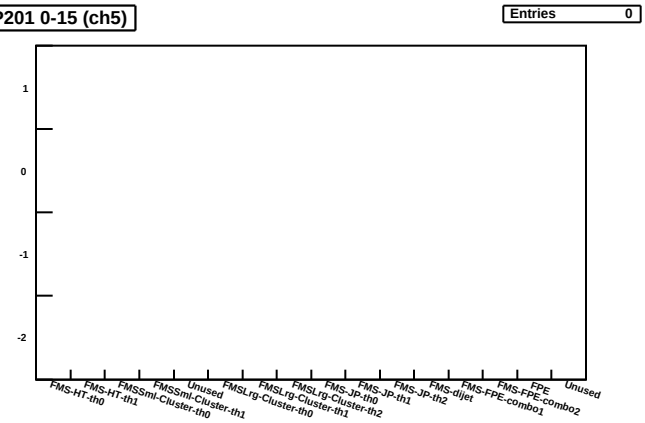
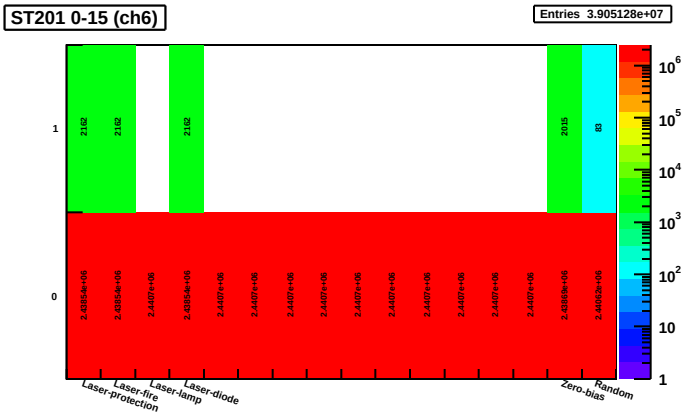
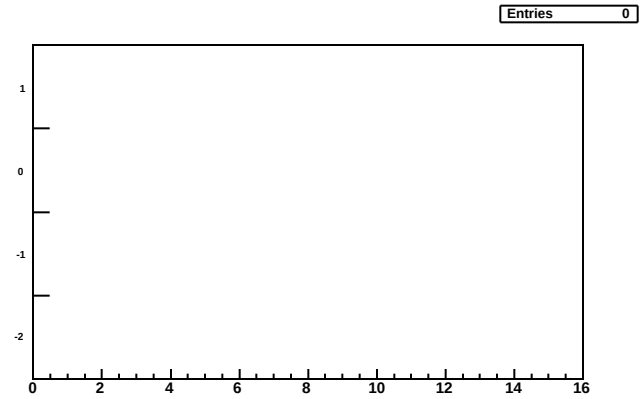
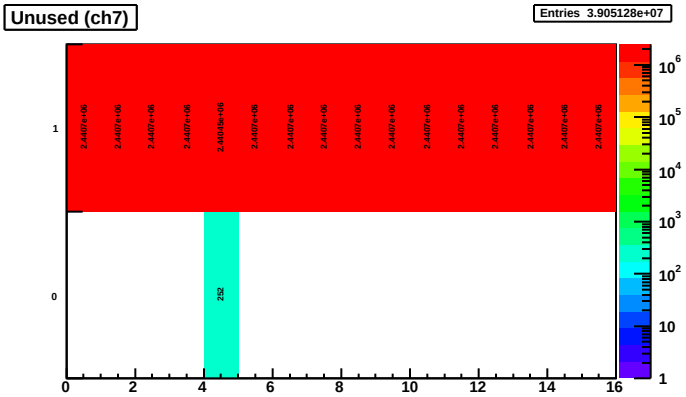
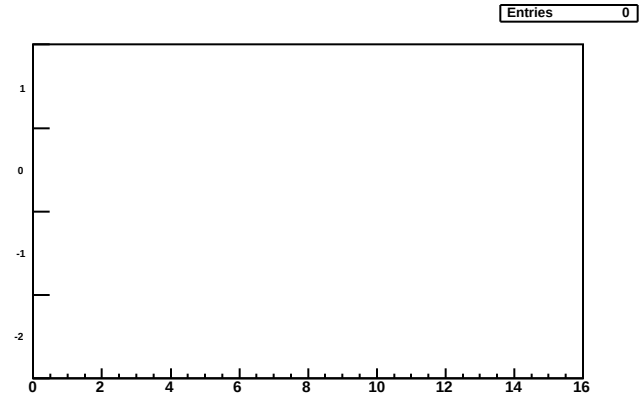


ST201 0-15 (ch6)





Unused (ch7)