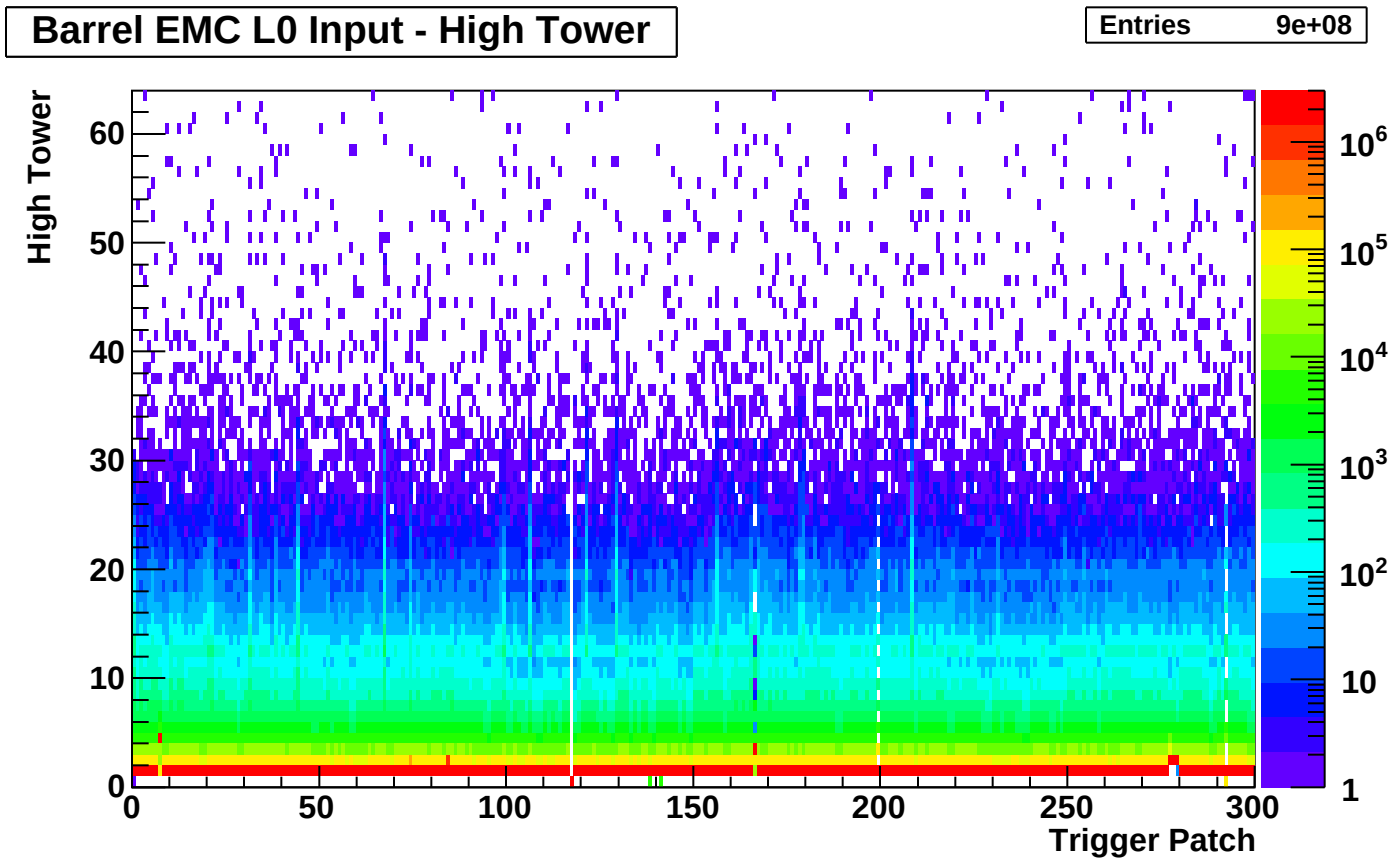
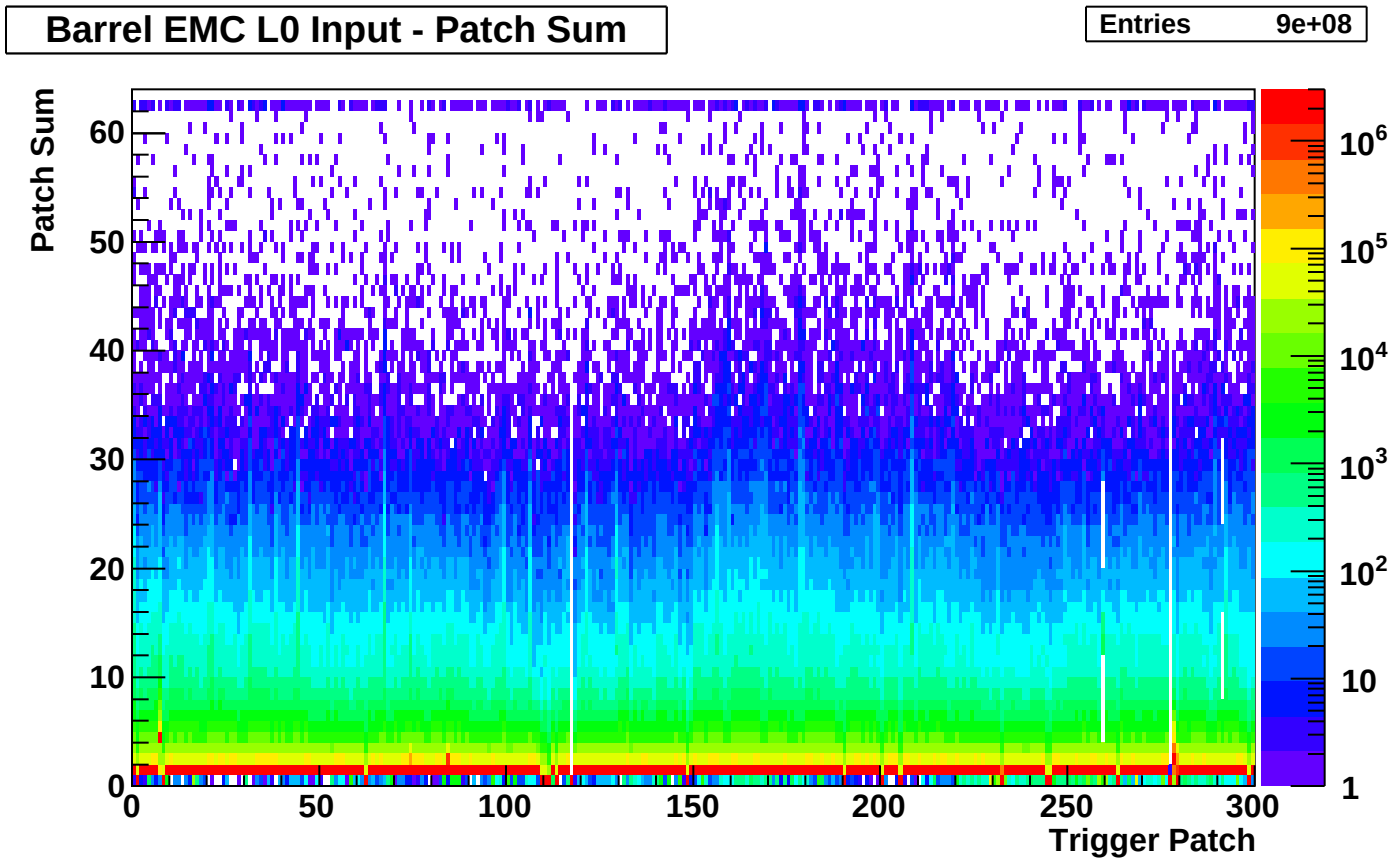
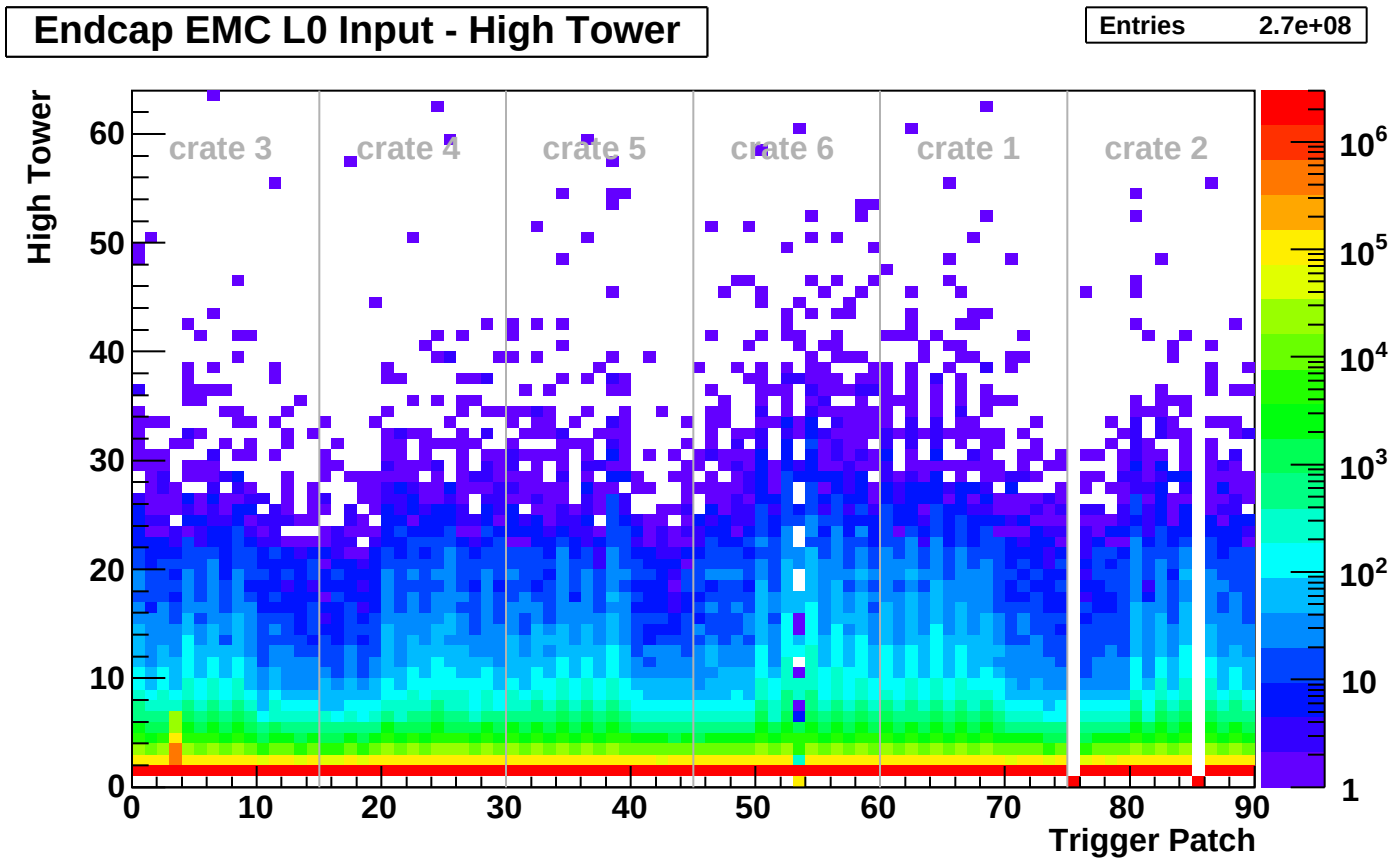
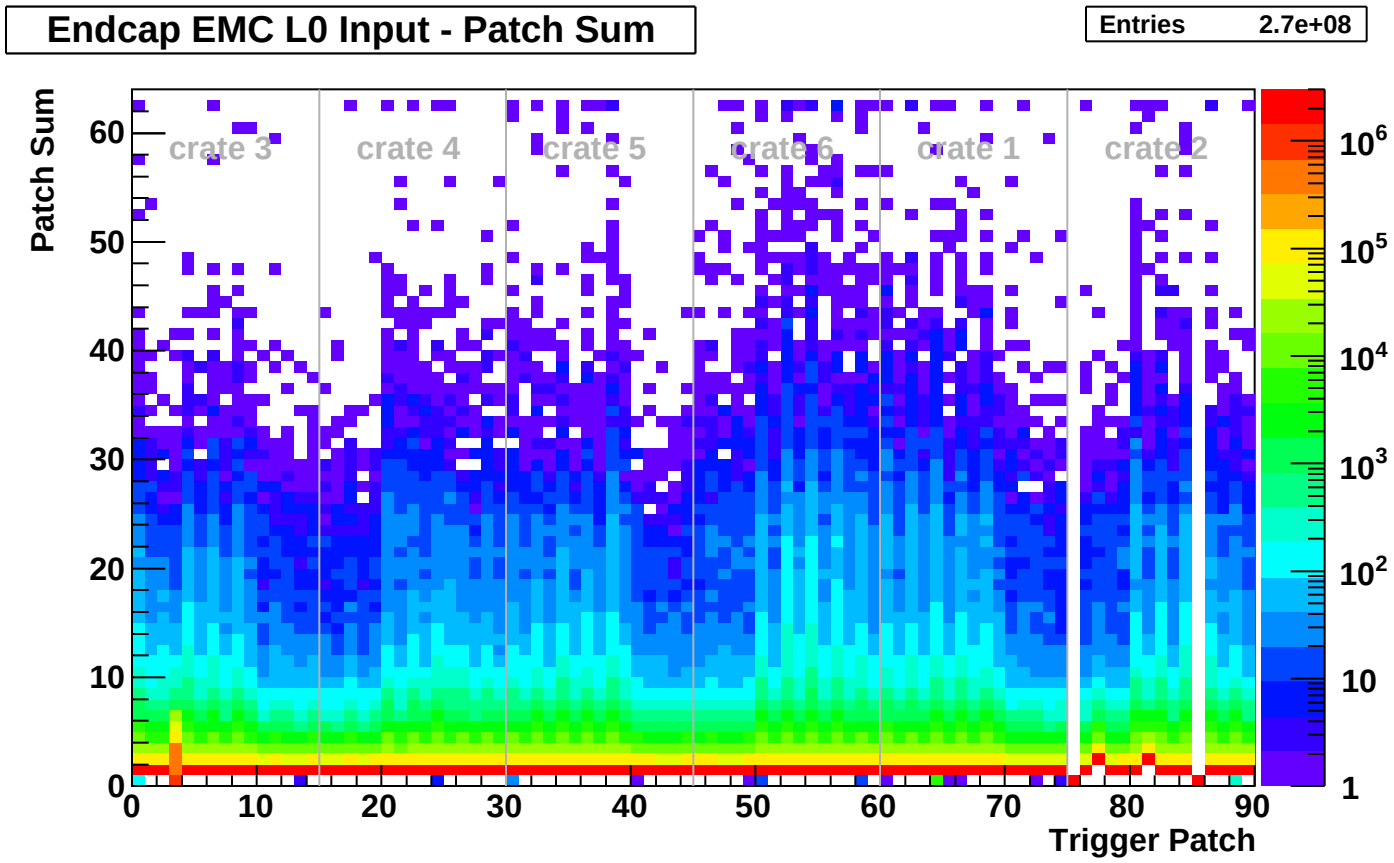
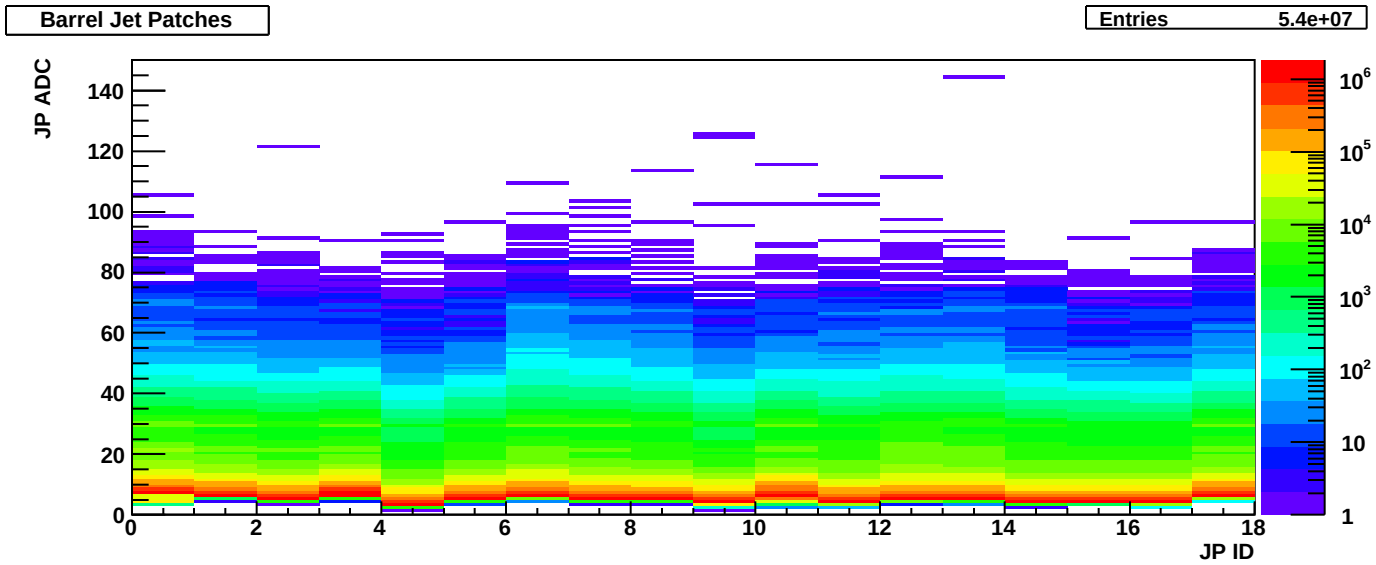
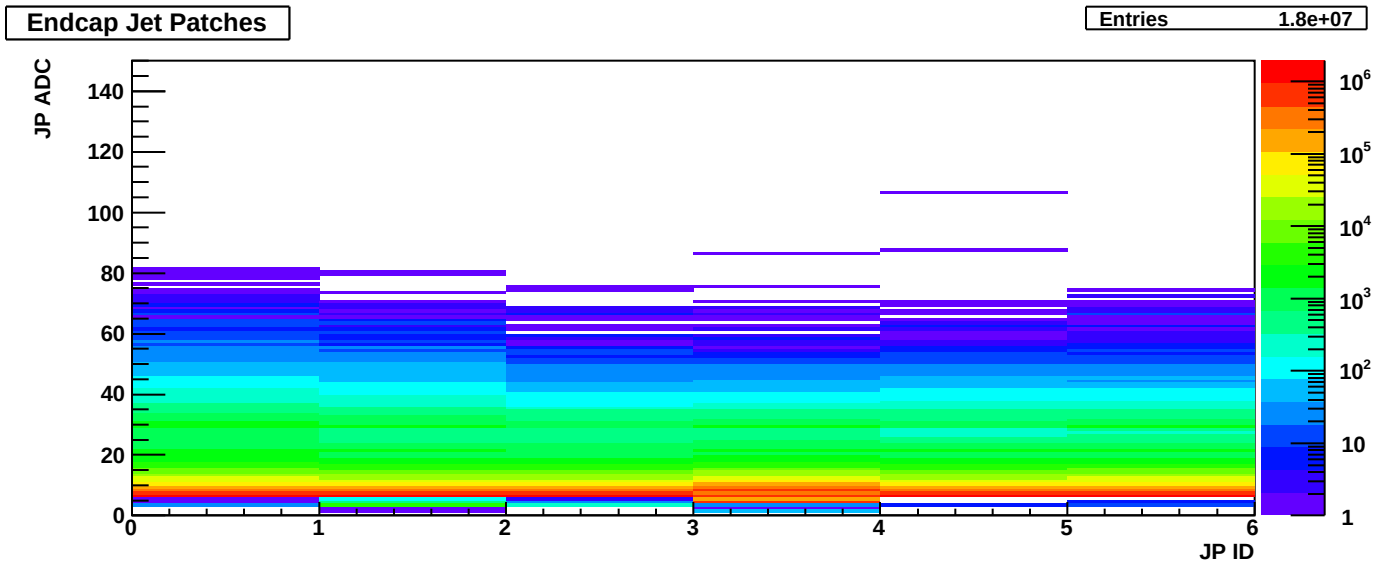

Barrel EMC L0 Input - High Tower
Entries 9e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

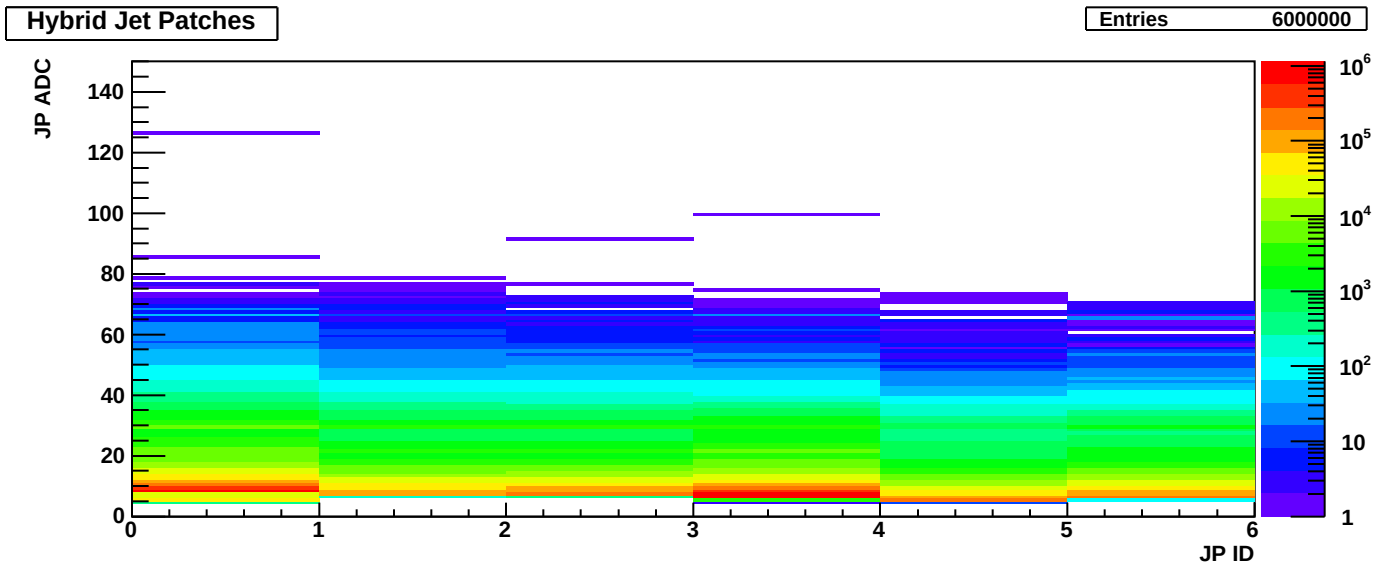

Barrel EMC L0 Input - Patch Sum
Entries 9e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

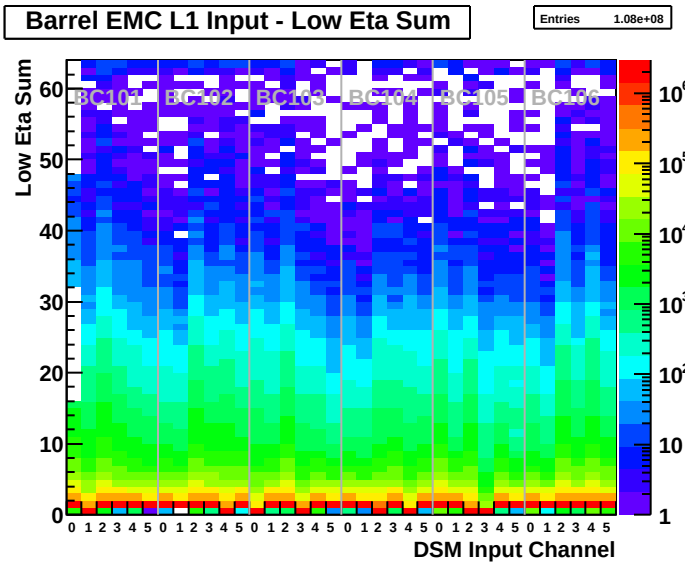

Endcap EMC L0 Input - High Tower
Entries 2.7e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

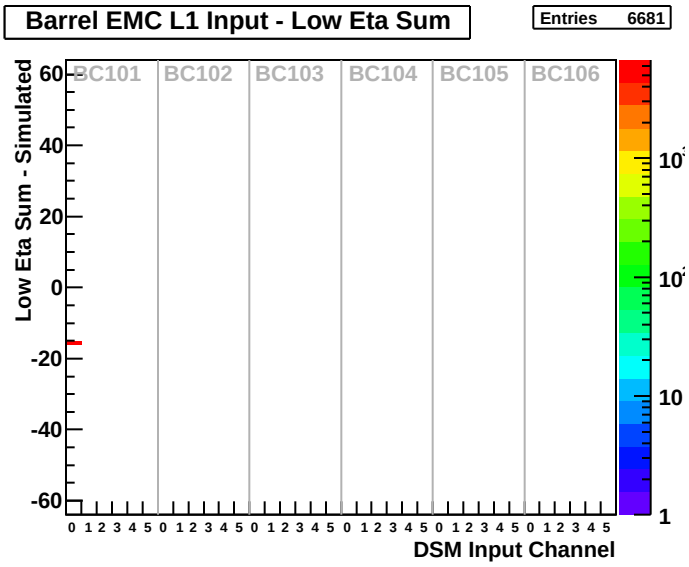

Endcap EMC L0 Input - Patch Sum
Entries 2.7e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

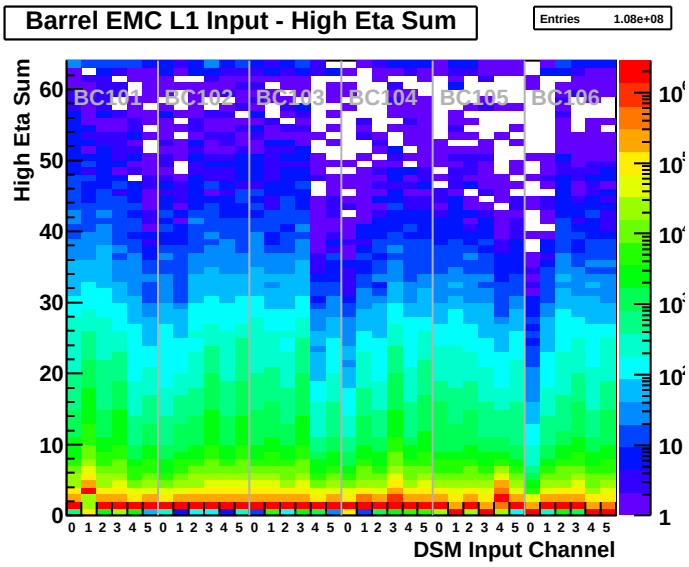

Barrel Jet Patches
Entries 5.4e+07
JP ADC
JP ID

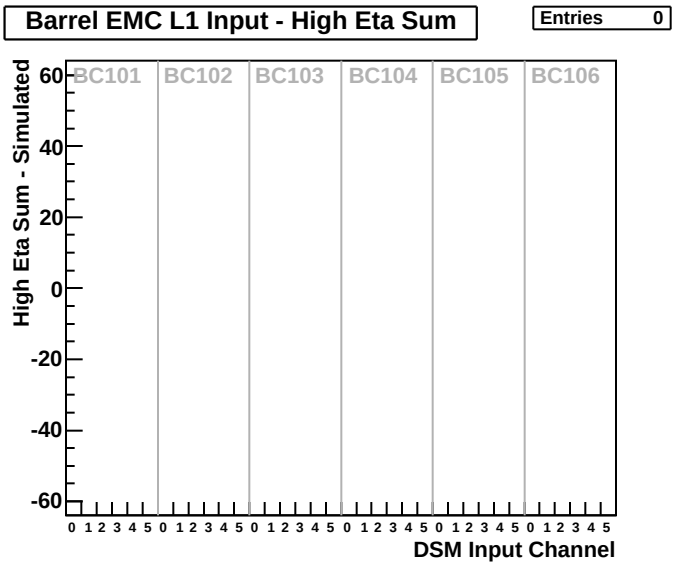

Endcap Jet Patches
Entries 1.8e+07
JP ADC
JP ID

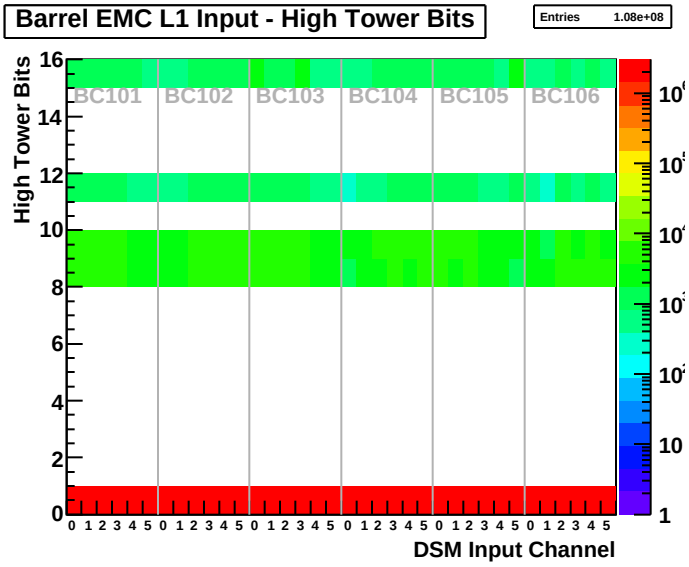

Hybrid Jet Patches
Entries 6000000
JP ADC
JP ID

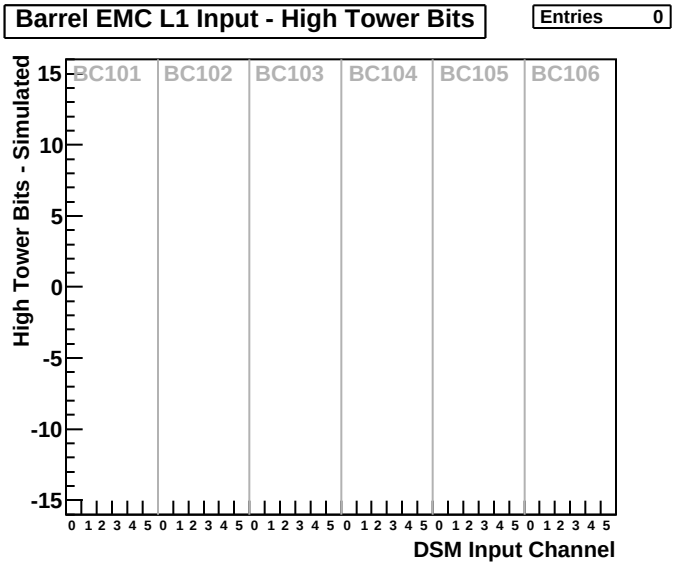

Barrel EMC L1 Input - Low Eta Sum
Entries 1.08e+08
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

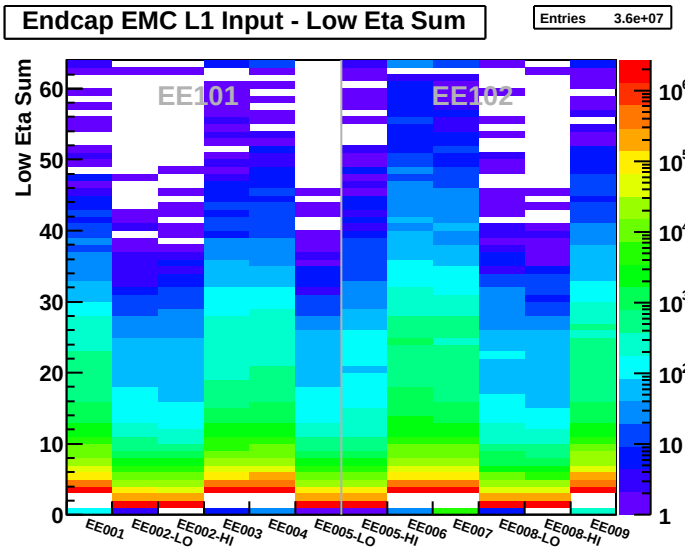

Barrel EMC L1 Input - Low Eta Sum
Entries 6681
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

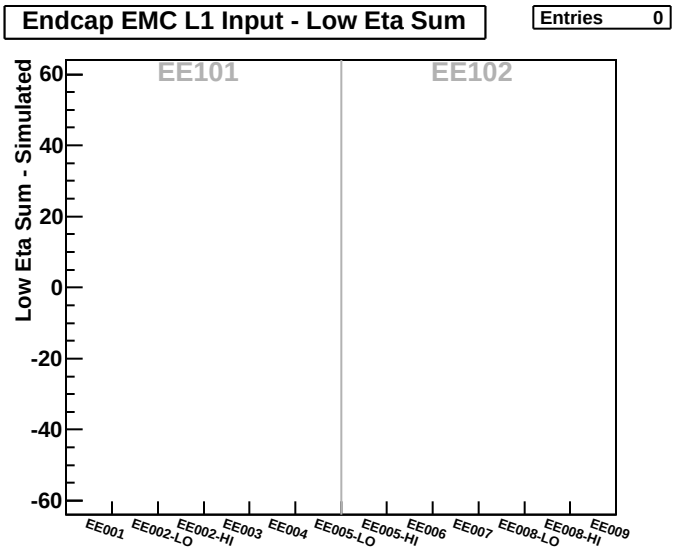

Barrel EMC L1 Input - High Eta Sum
Entries 1.08e+08
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

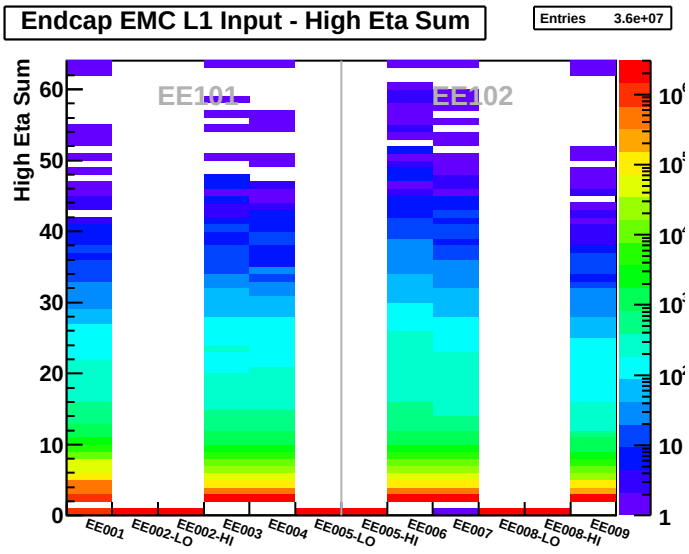

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

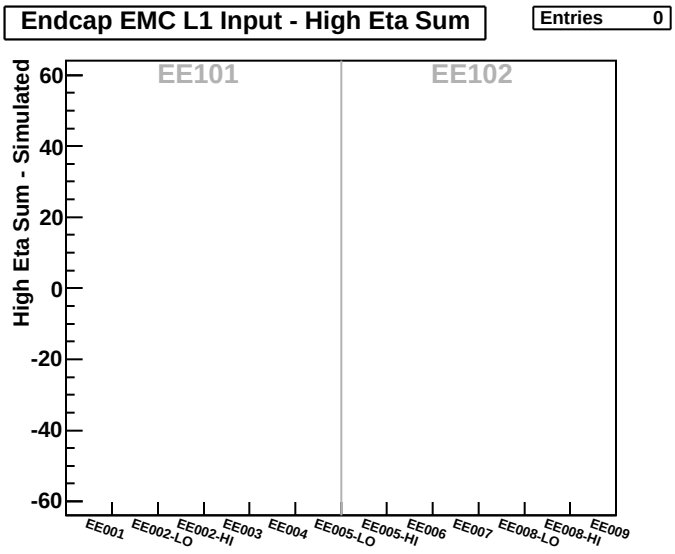

Barrel EMC L1 Input - High Tower Bits
Entries 1.08e+08
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

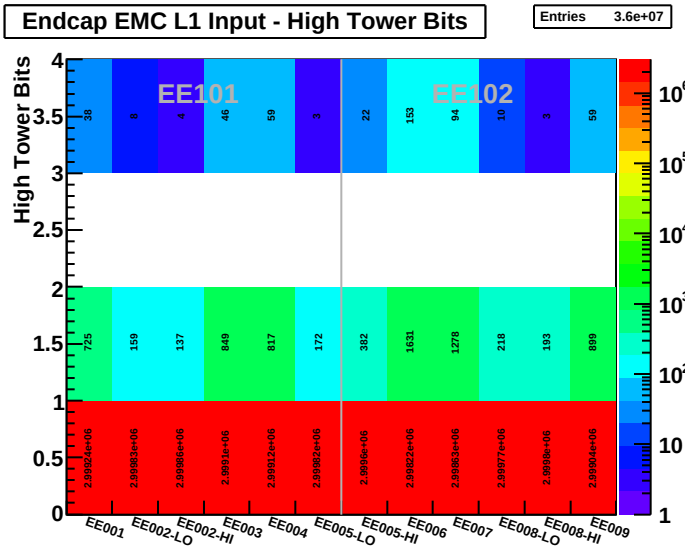

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

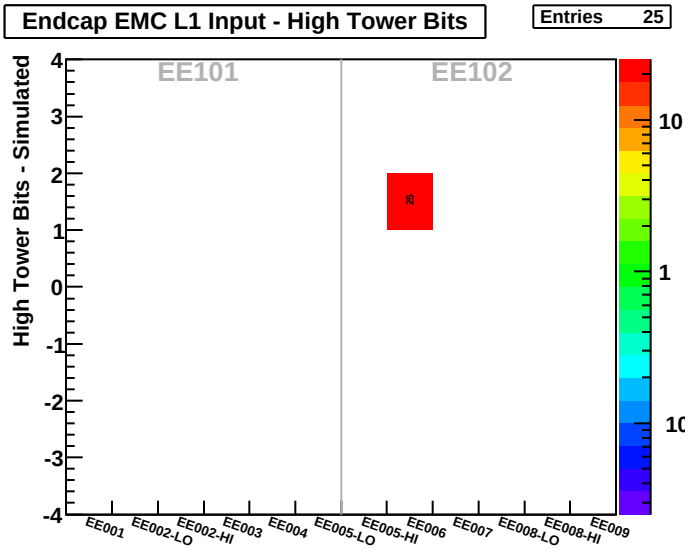

Endcap EMC L1 Input - Low Eta Sum
Entries 3.6e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

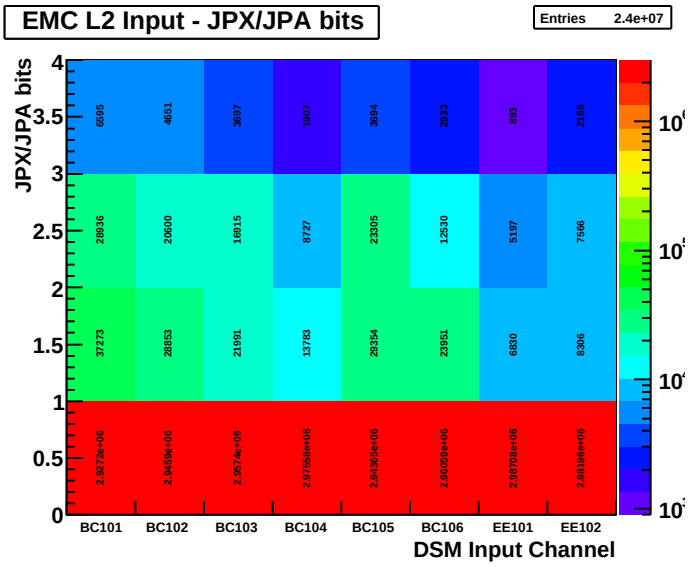

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

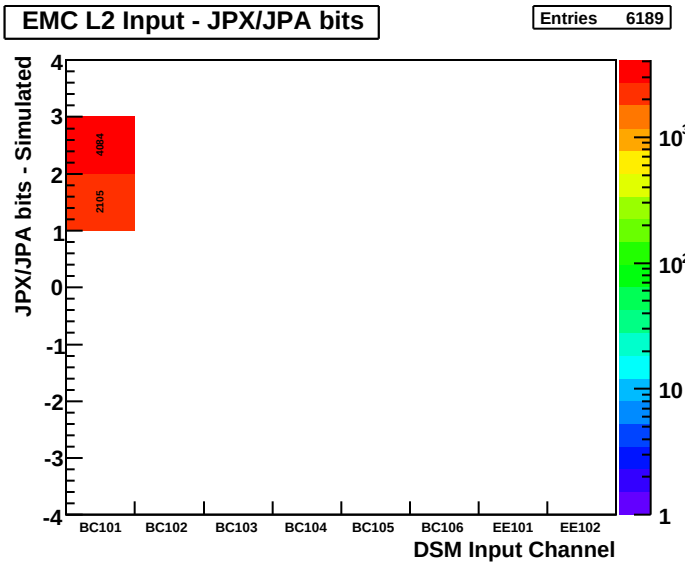

Endcap EMC L1 Input - High Eta Sum
Entries 3.6e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

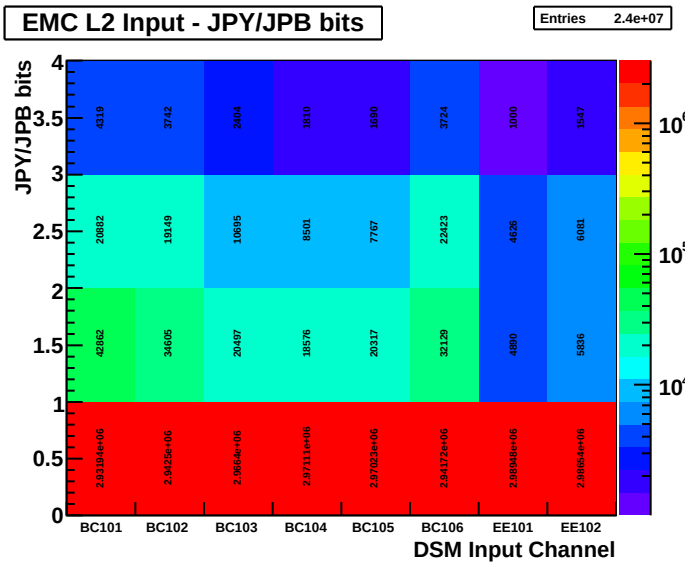

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

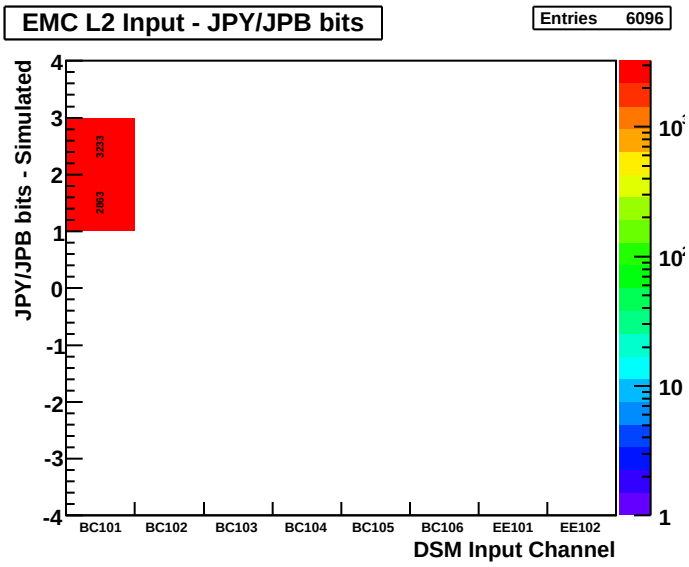

Endcap EMC L1 Input - High Tower Bits
Entries 3.6e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

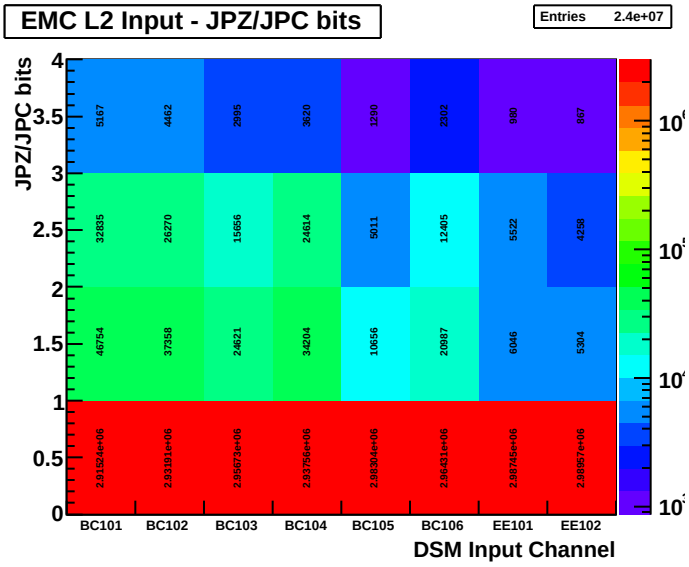

Endcap EMC L1 Input - High Tower Bits
Entries 25
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

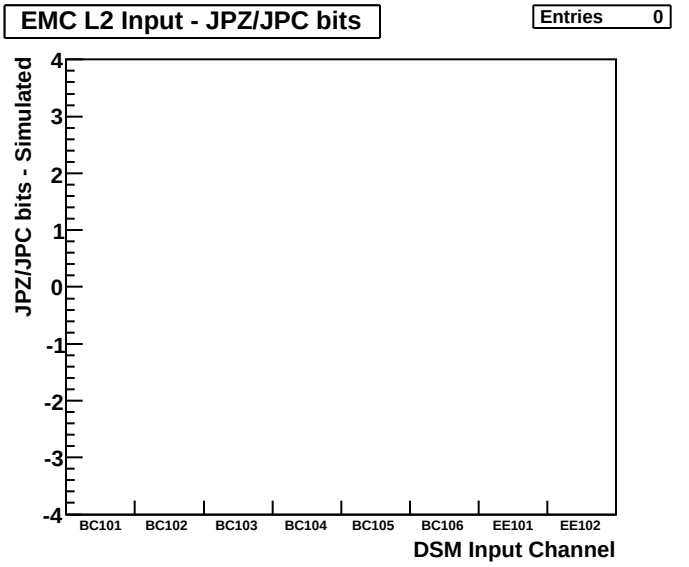

EMC L2 Input - JPX/JPA bits
Entries 2.4e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

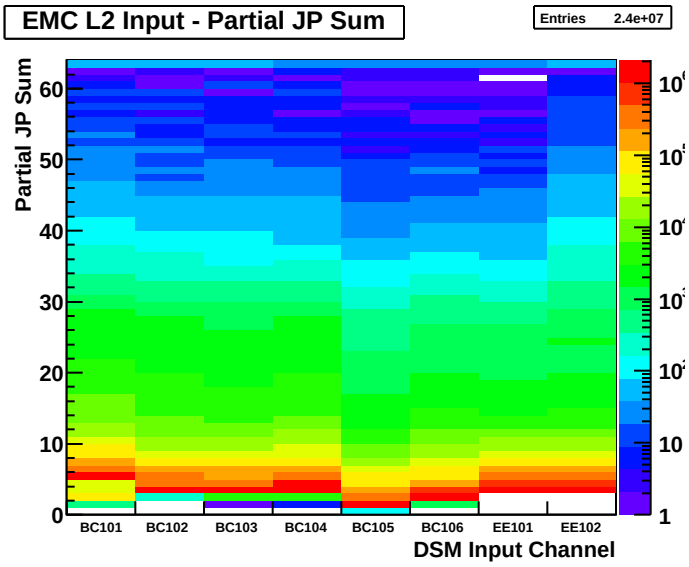

EMC L2 Input - JPX/JPA bits
Entries 6189
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

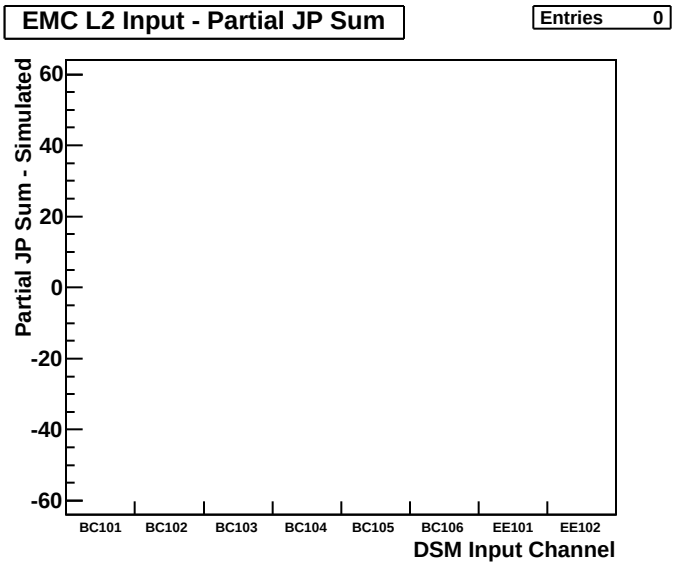

EMC L2 Input - JPY/JPB bits
Entries 2.4e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

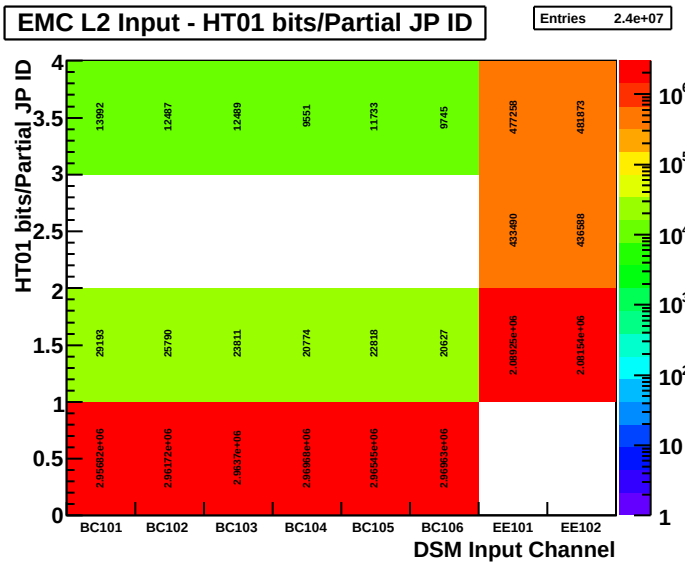

EMC L2 Input - JPY/JPB bits
Entries 6096
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

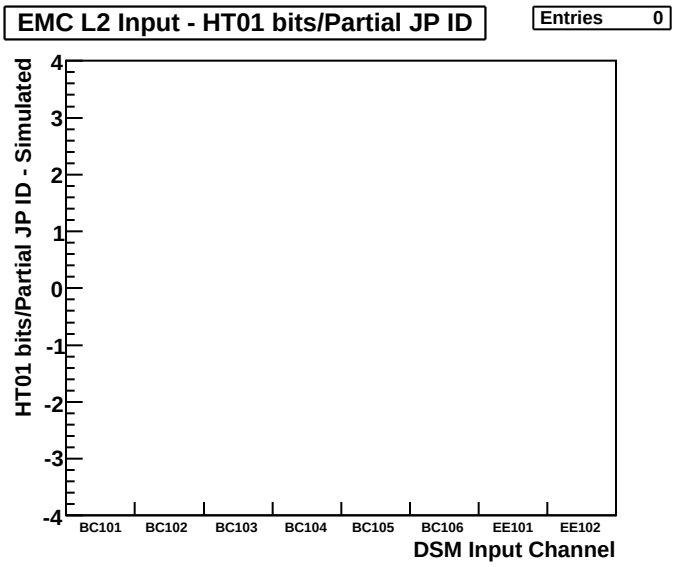

EMC L2 Input - JPZ/JPC bits
Entries 2.4e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

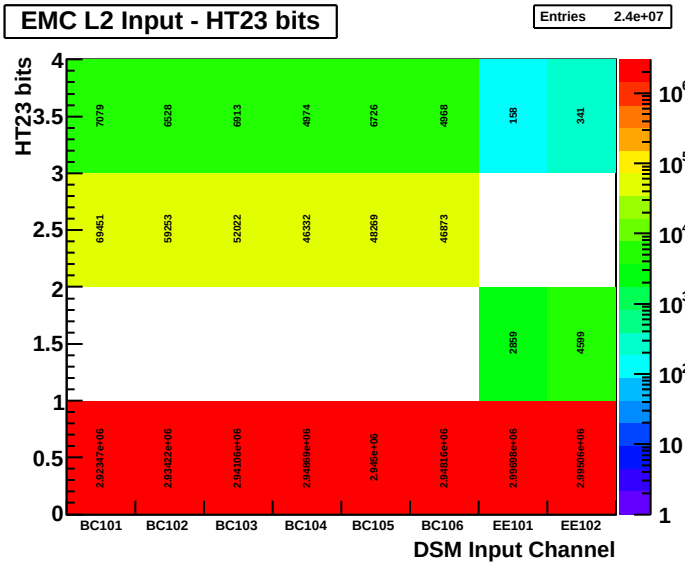

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

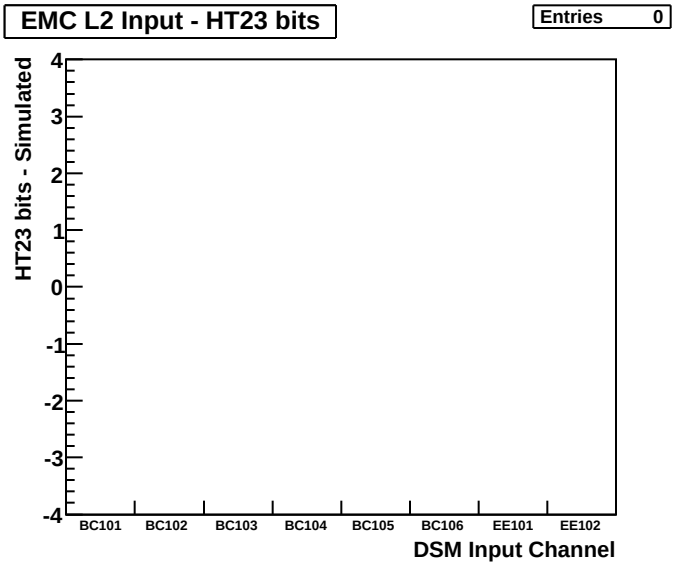

EMC L2 Input - Partial JP Sum
Entries 2.4e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

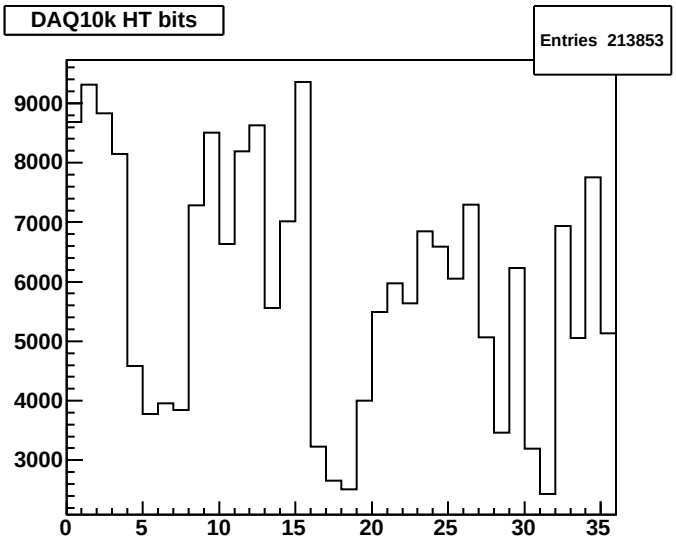

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

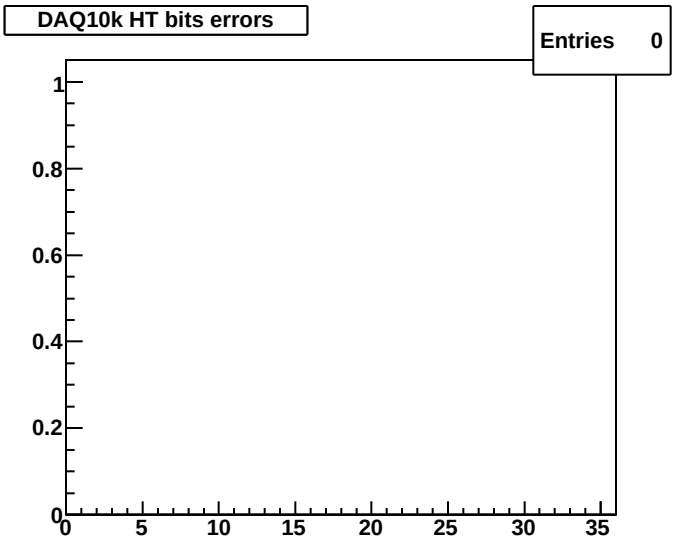

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2.4e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

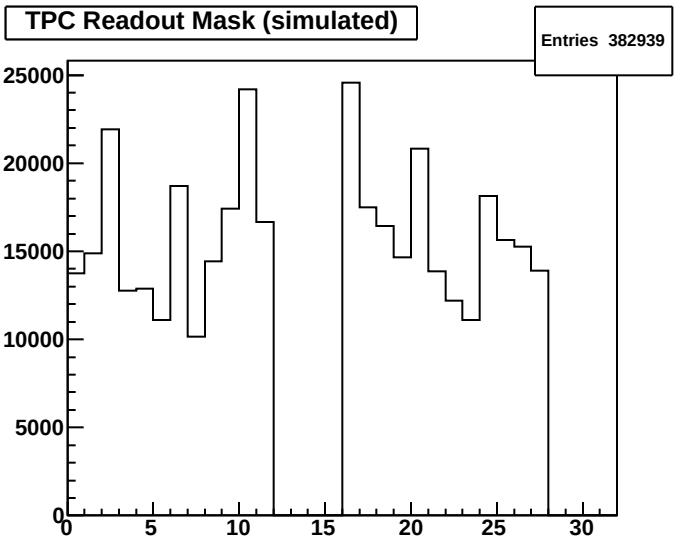

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

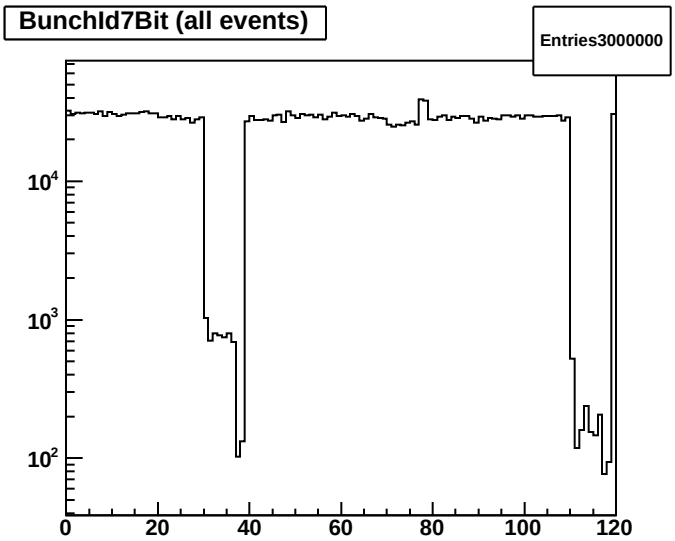

EMC L2 Input - HT23 bits
Entries 2.4e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

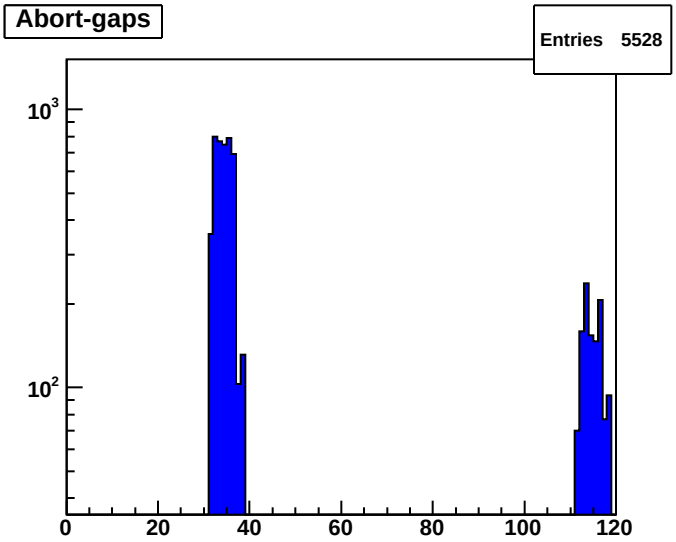

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

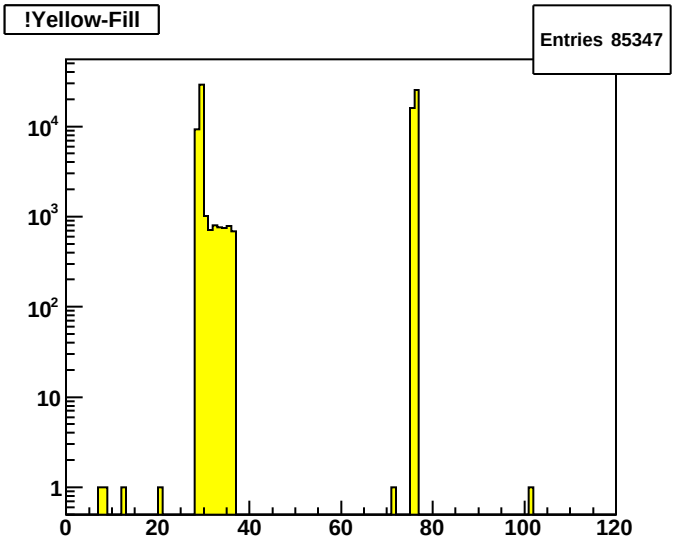

DAQ10k HT bits
Entries 213853

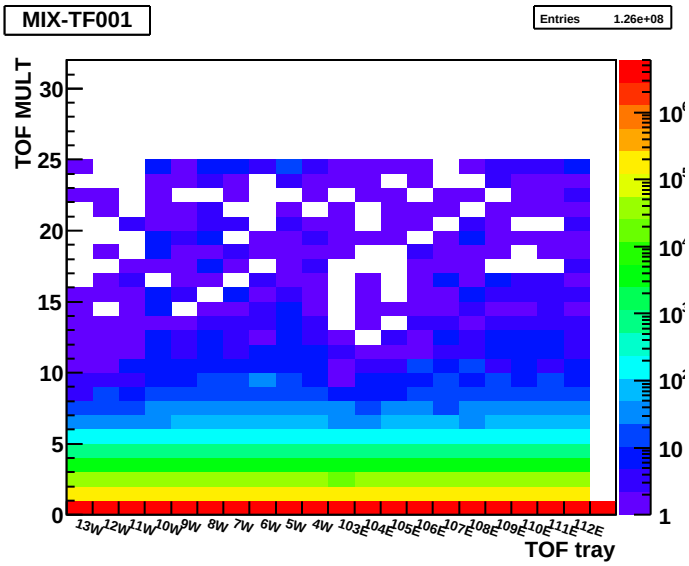

DAQ10k HT bits errors
Entries 0

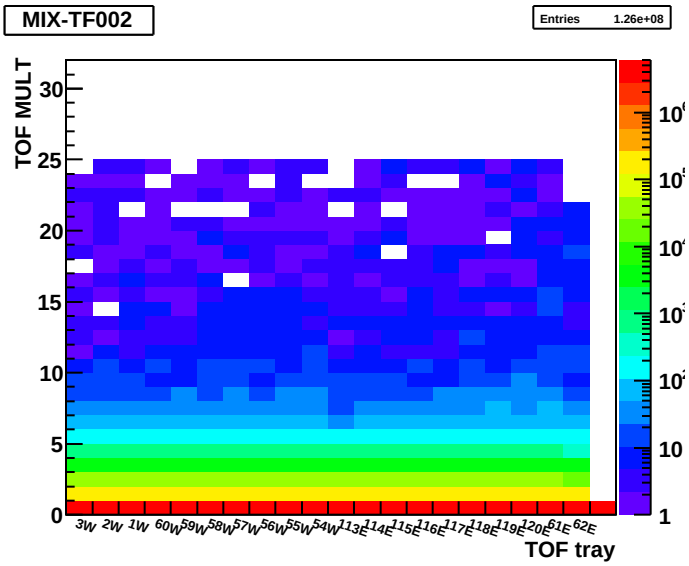

TPC Readout Mask (simulated)
Entries 382939

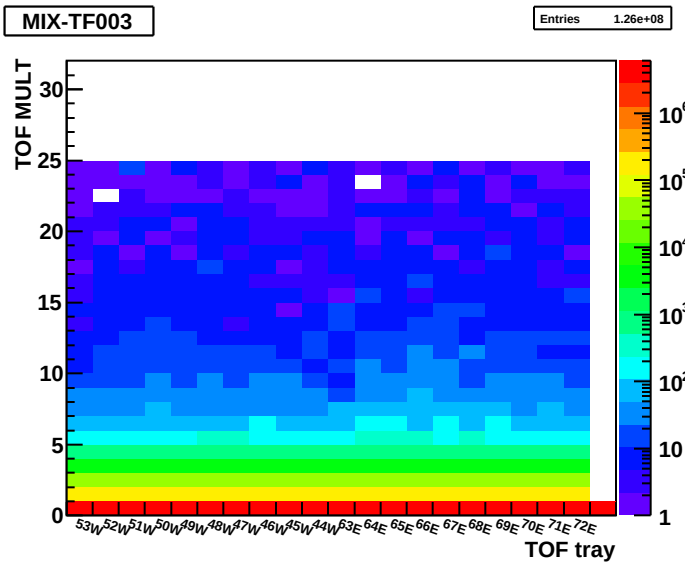

BunchId7Bit (all events)
Entries3000000

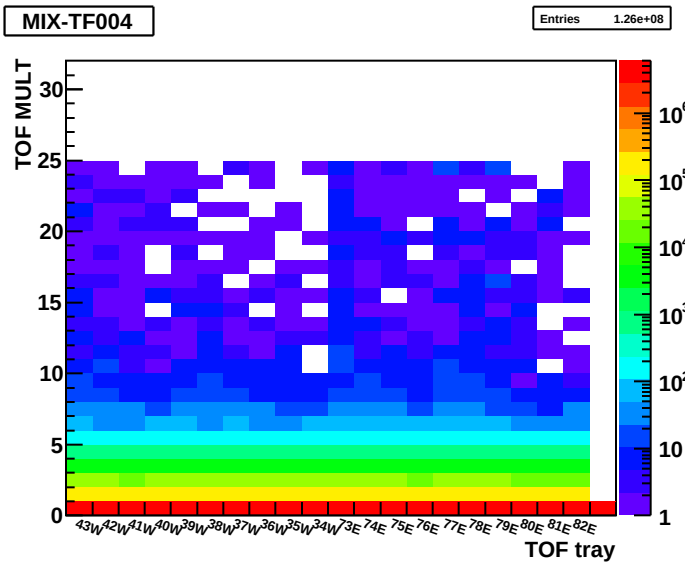

Abort-gaps
Entries 5528

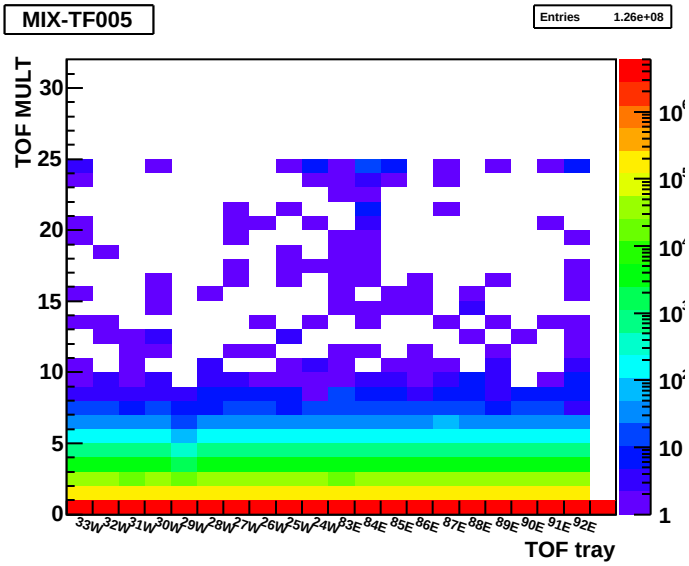

!Yellow-Fill
Entries 85347

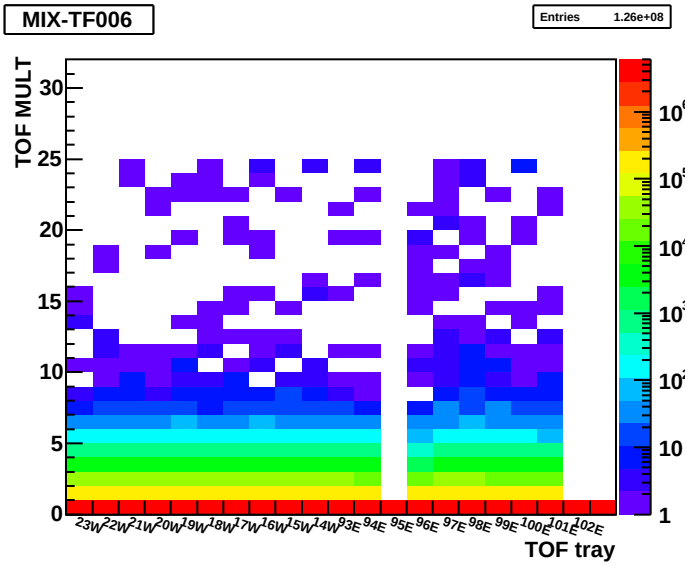

MIX-TF001
Entries 1.26e+08
TOF MULT
TOF tray

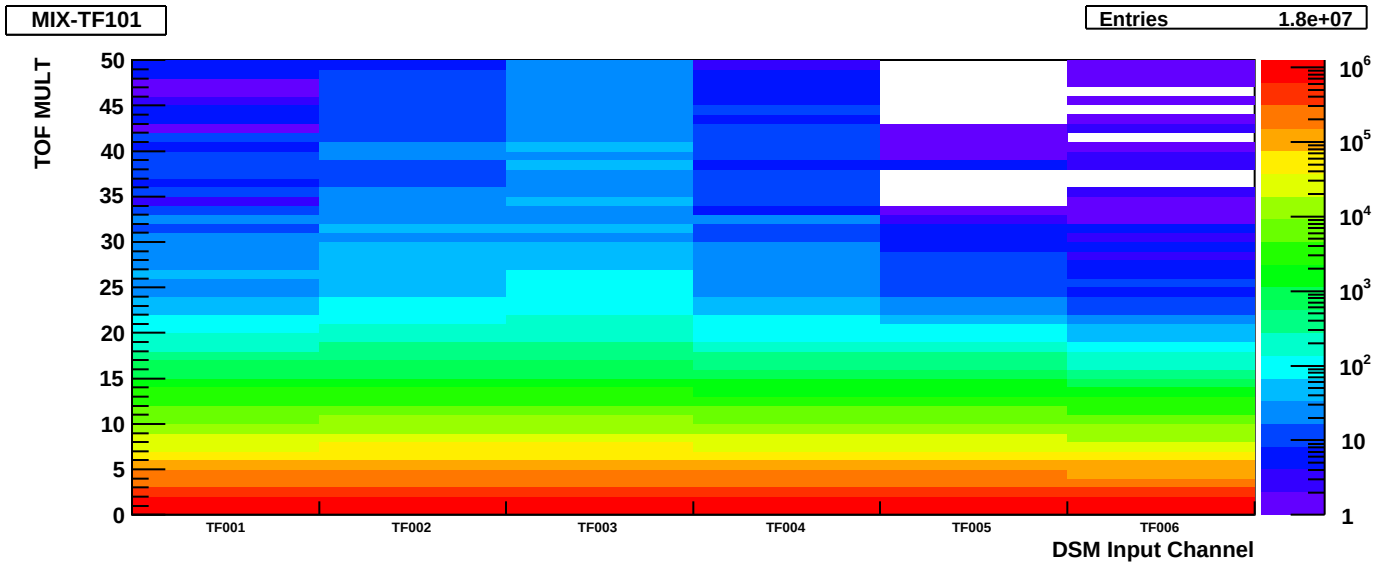

MIX-TF002
Entries 1.26e+08
TOF MULT
TOF tray

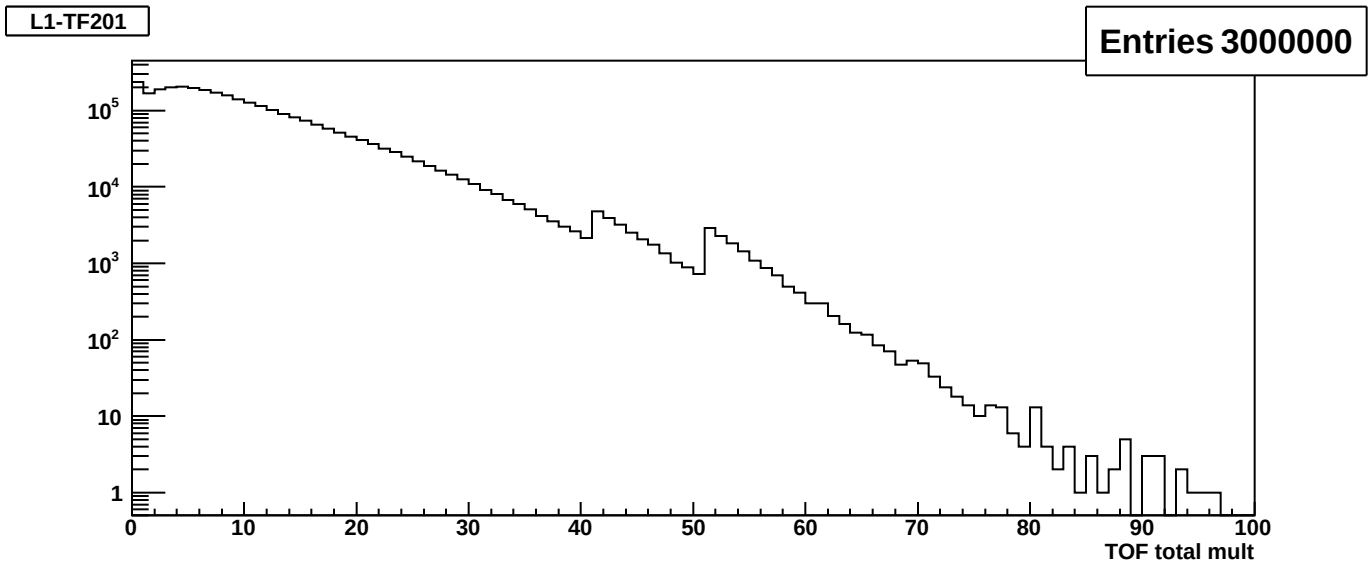

MIX-TF003
Entries 1.26e+08
TOF MULT
TOF tray

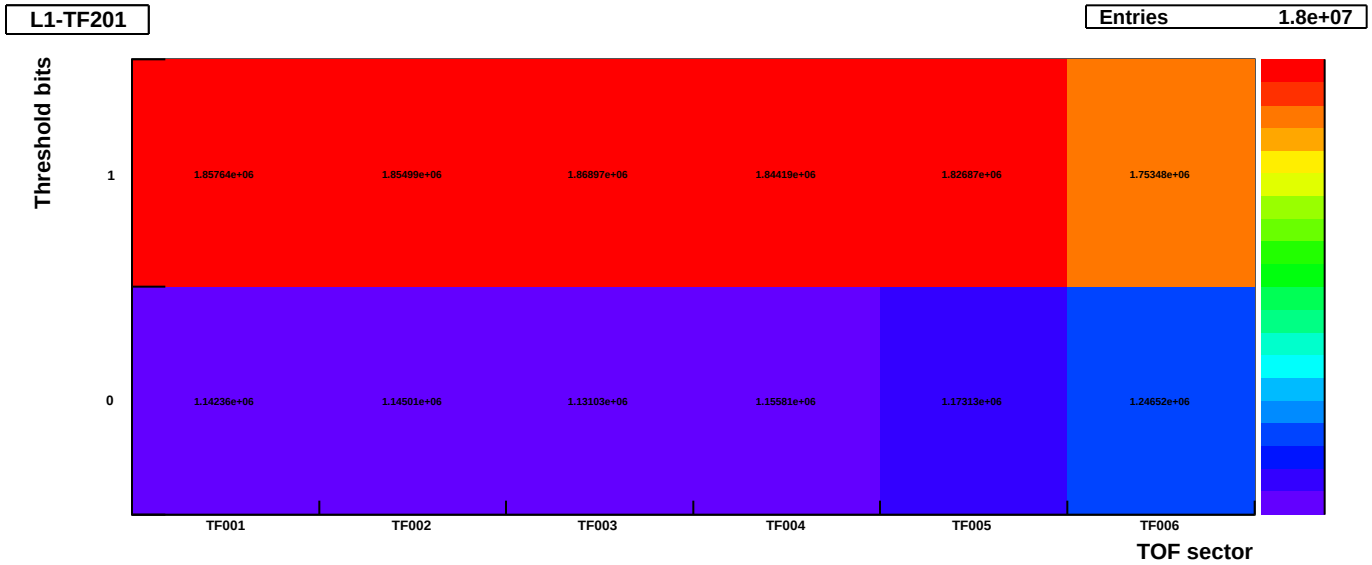

MIX-TF004
Entries 1.26e+08
TOF MULT
TOF tray

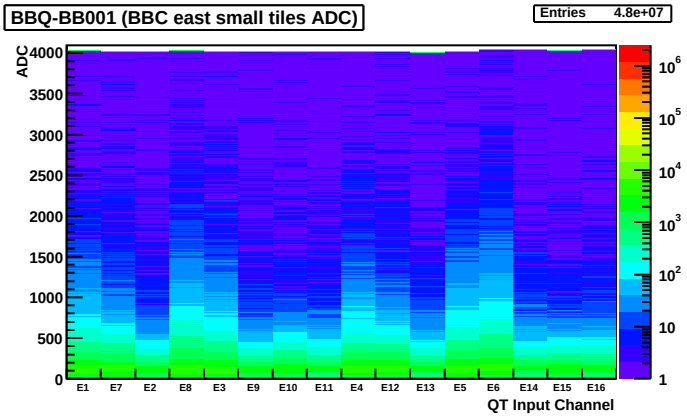

MIX-TF005
Entries 1.26e+08
TOF MULT
TOF tray

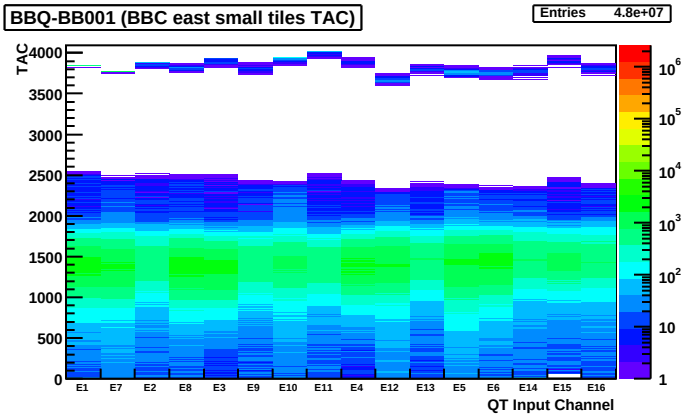

MIX-TF006
Entries 1.26e+08
TOF MULT
TOF tray

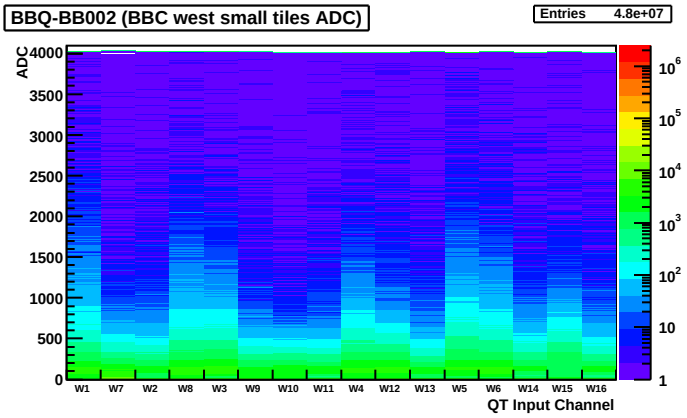

MIX-TF101
Entries 1.8e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^6
10^5
10^4
10^3
10^2
10
1

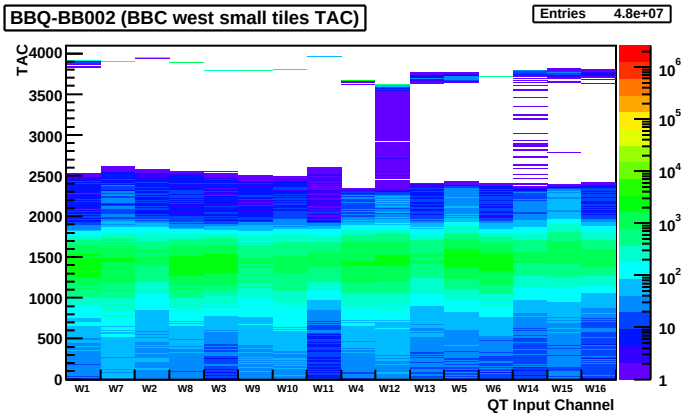

L1-TF201
Entries 3000000
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

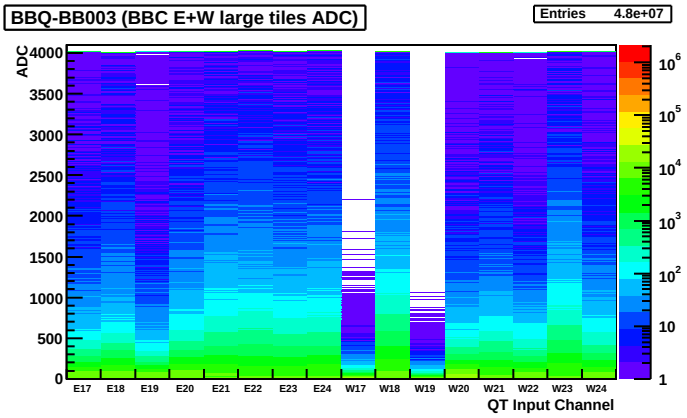

L1-TF201
Entries 1.8e+07
Threshold bits
1
0
1.85764e+06
1.85499e+06
1.86897e+06
1.84419e+06
1.82687e+06
1.75348e+06
1.14236e+06
1.14501e+06
1.13103e+06
1.15581e+06
1.17313e+06
1.24652e+06
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

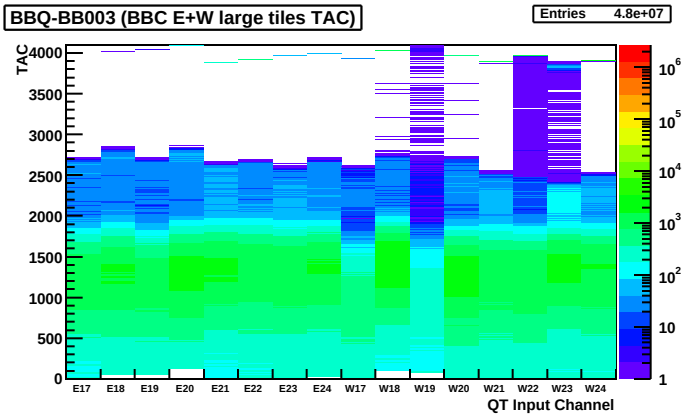

BBQ-BB001 (BBC east small tiles ADC)
Entries 4.8e+07
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

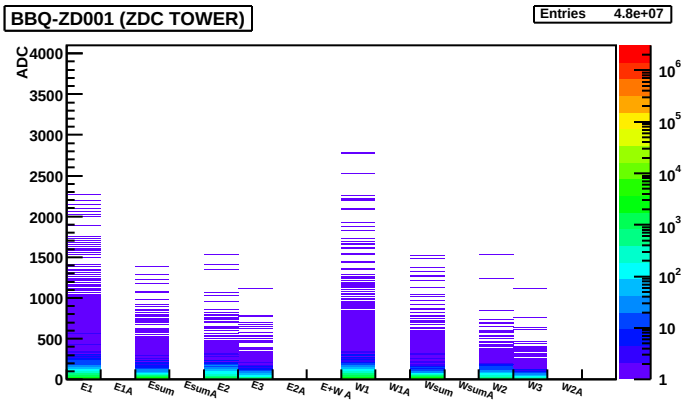

BBQ-BB001 (BBC east small tiles TAC)
Entries 4.8e+07
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

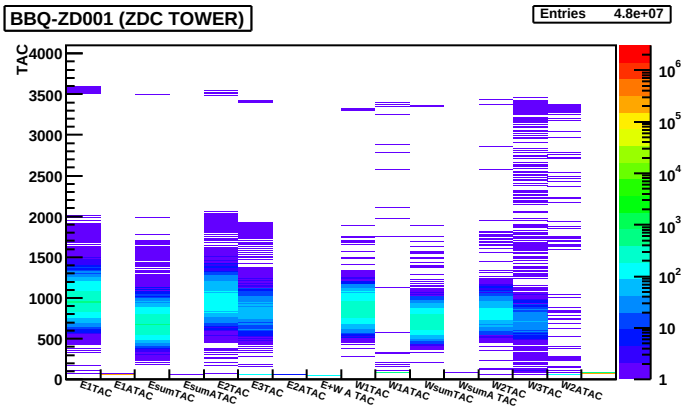

BBQ-BB002 (BBC west small tiles ADC)
Entries 4.8e+07
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

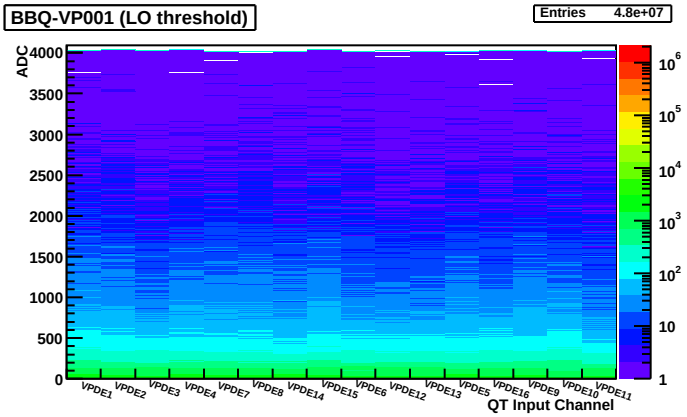

BBQ-BB002 (BBC west small tiles TAC)
Entries 4.8e+07
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

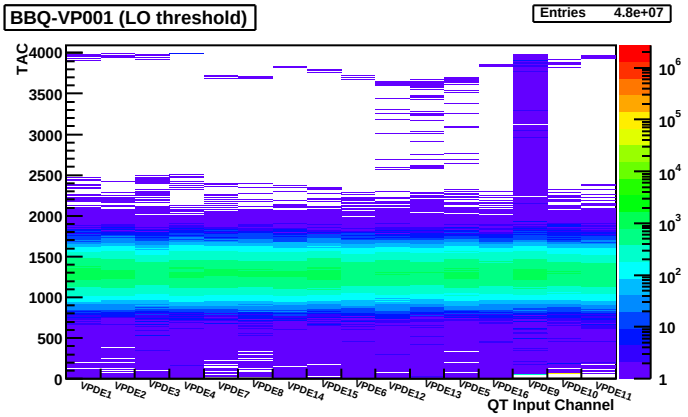

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4.8e+07
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

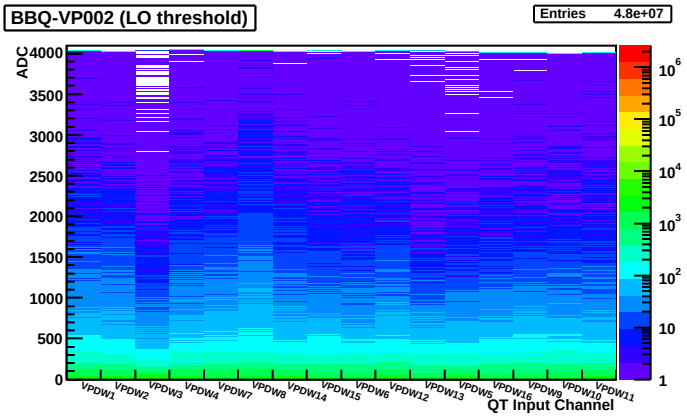

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4.8e+07
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

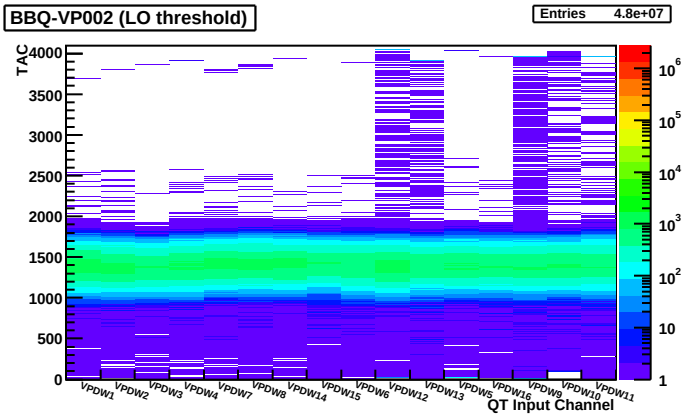

BBQ-ZD001 (ZDC TOWER)
Entries 4.8e+07
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

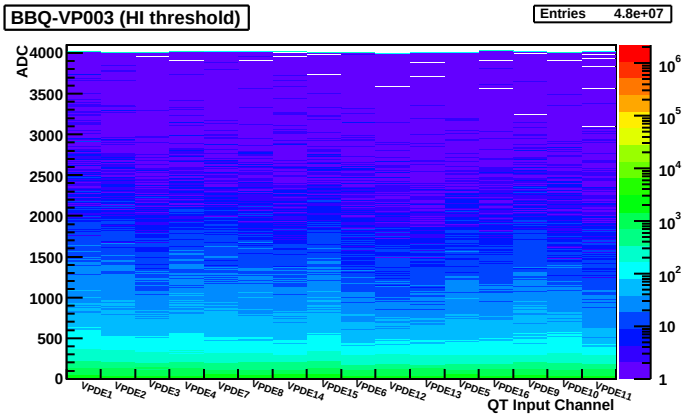

BBQ-ZD001 (ZDC TOWER)
Entries 4.8e+07
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

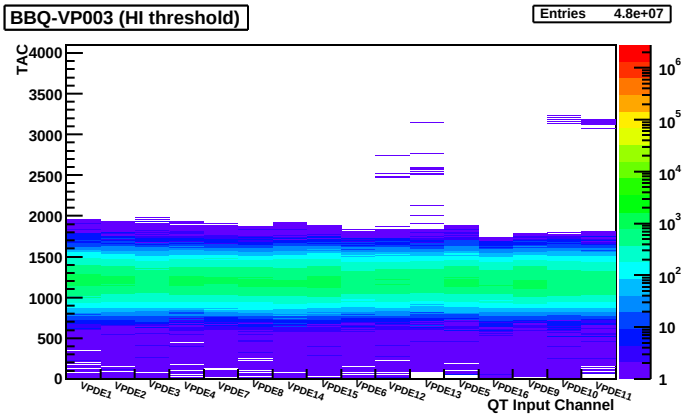

BBQ-VP001 (LO threshold)
Entries 4.8e+07
ADC
QT Input Channel

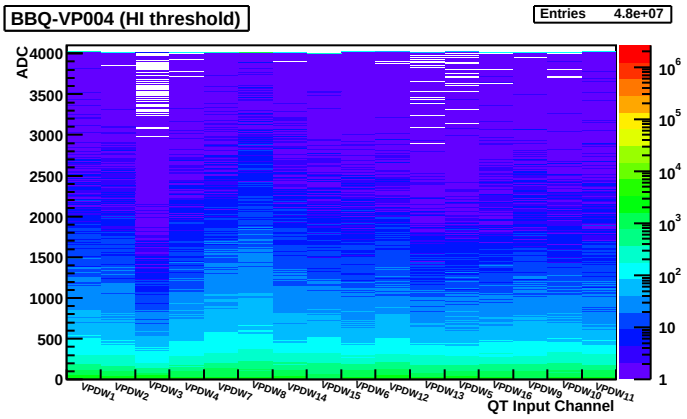

BBQ-VP001 (LO threshold)
Entries 4.8e+07
TAC
QT Input Channel

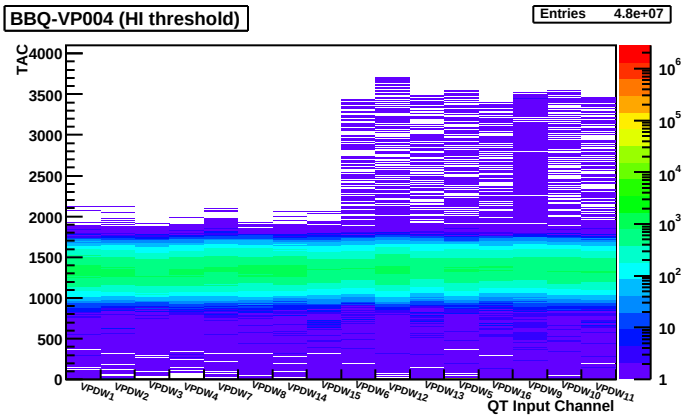

BBQ-VP002 (LO threshold)
Entries 4.8e+07
ADC
QT Input Channel

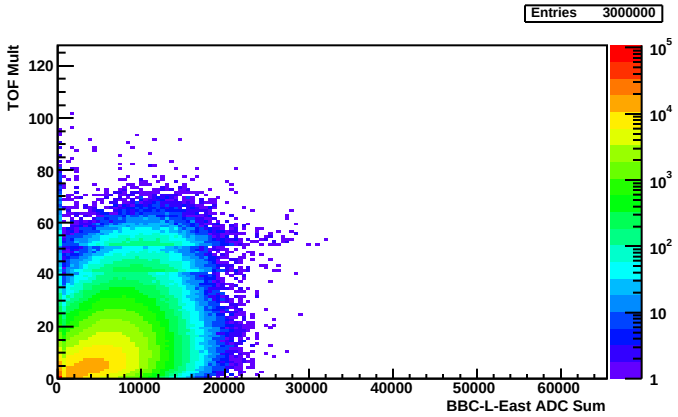

BBQ-VP002 (LO threshold)
Entries 4.8e+07
TAC
QT Input Channel

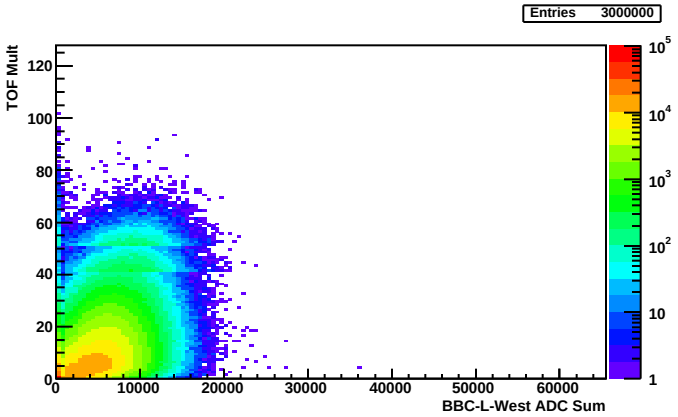

BBQ-VP003 (HI threshold)
Entries 4.8e+07
ADC
QT Input Channel

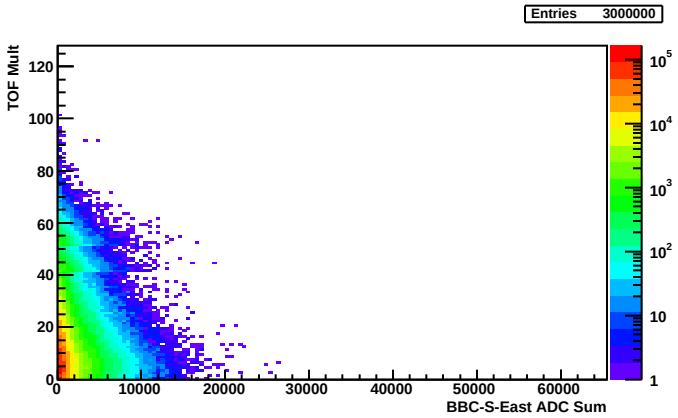

BBQ-VP003 (HI threshold)
Entries 4.8e+07
TAC
QT Input Channel

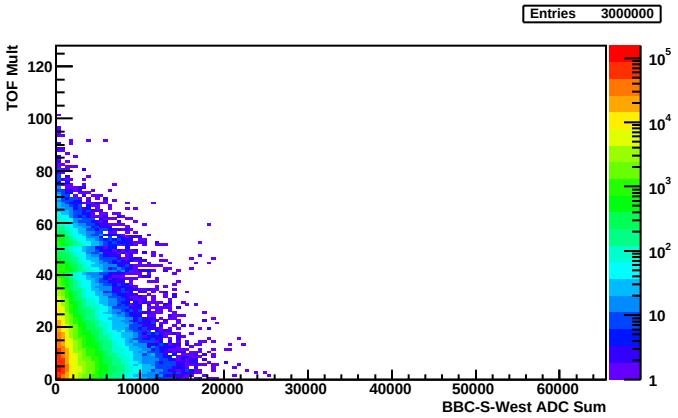

BBQ-VP004 (HI threshold)
Entries 4.8e+07
ADC
QT Input Channel

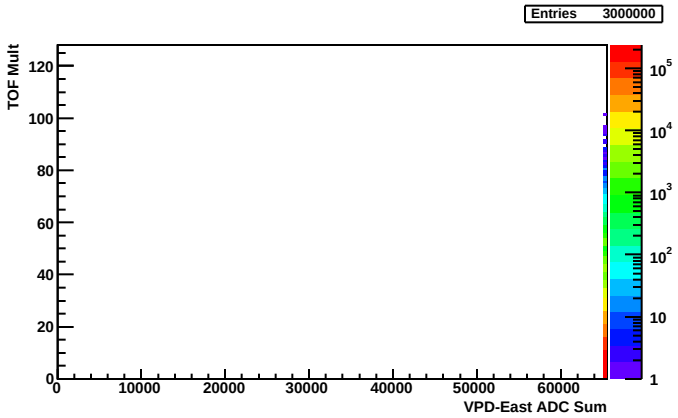

BBQ-VP004 (HI threshold)
Entries 4.8e+07
TAC
QT Input Channel

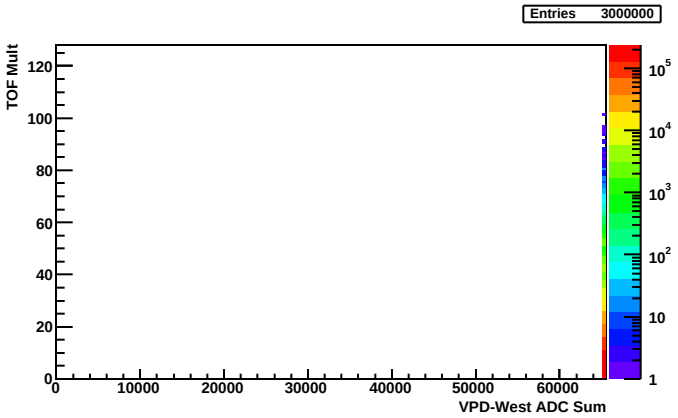

Entries 3000000
TOF Mult
BBC-L-East ADC Sum

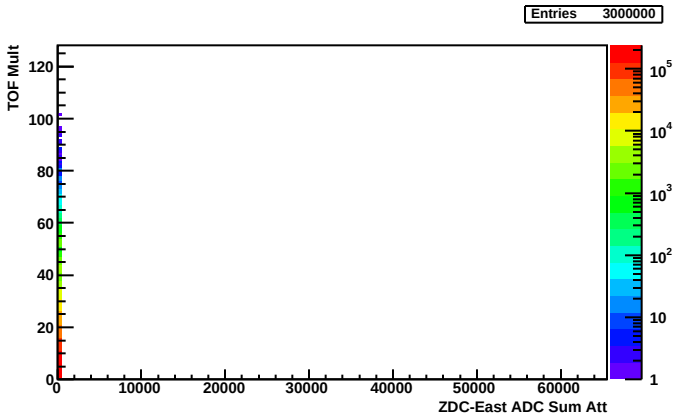

Entries 3000000
TOF Mult
BBC-L-West ADC Sum

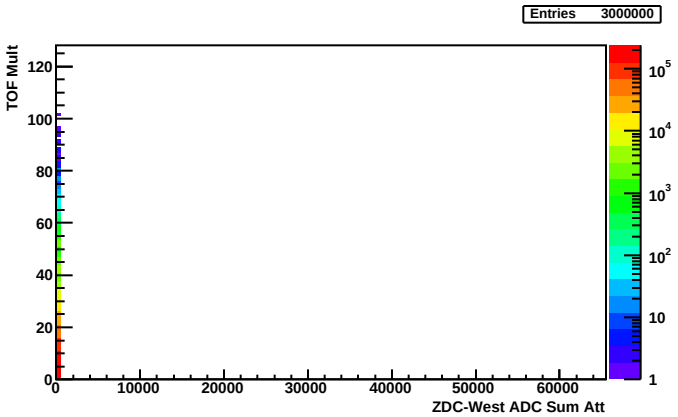

Entries 3000000
TOF Mult
BBC-S-East ADC Sum

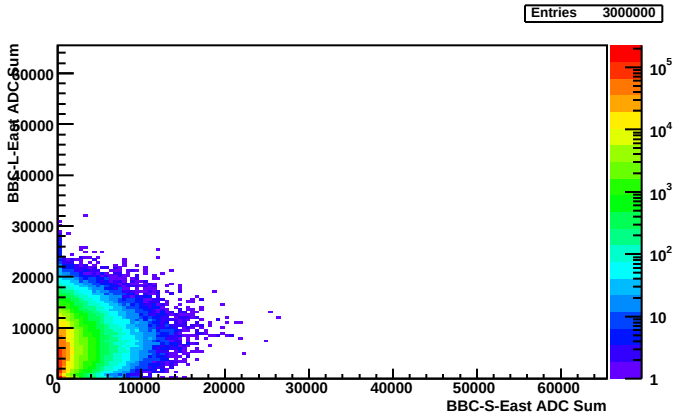

Entries 3000000
TOF Mult
BBC-S-West ADC Sum

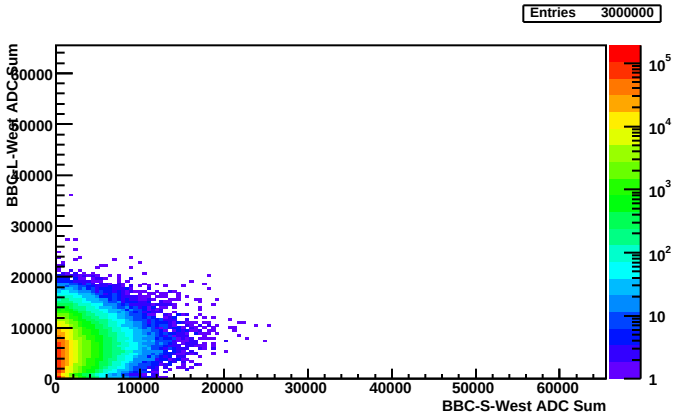

Entries 3000000
TOF Mult
VPD-East ADC Sum


Entries 3000000
TOF Mult
VPD-West ADC Sum


Entries 3000000
TOF Mult
ZDC-East ADC Sum Att


Entries 3000000
TOF Mult
ZDC-West ADC Sum Att


Entries 3000000
BBC-L-East ADC Sum
BBC-S-East ADC Sum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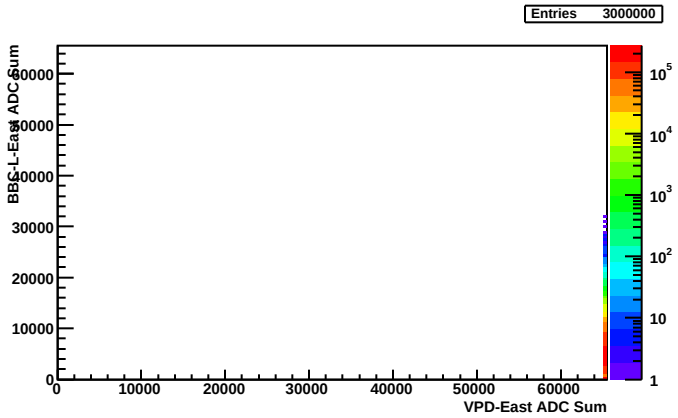
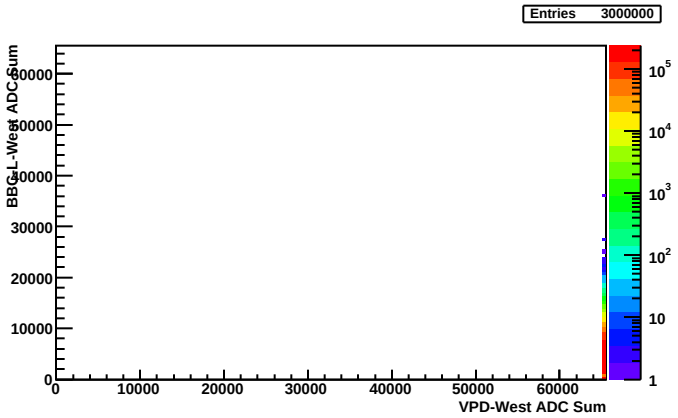
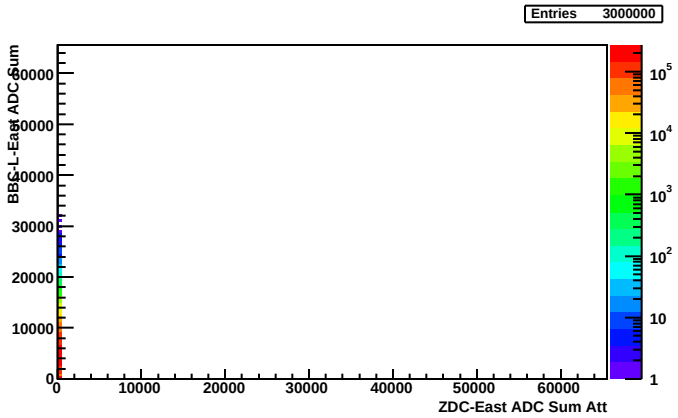
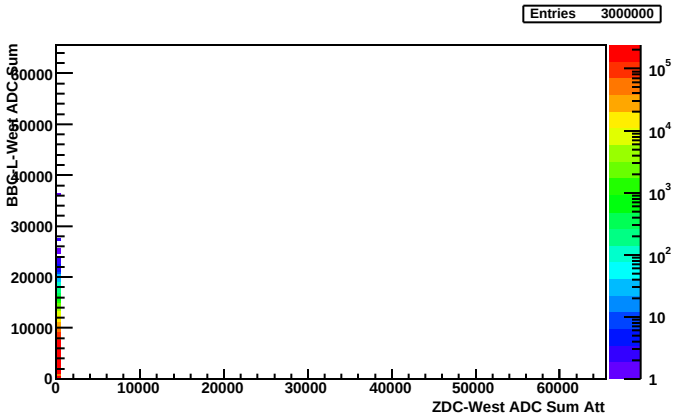
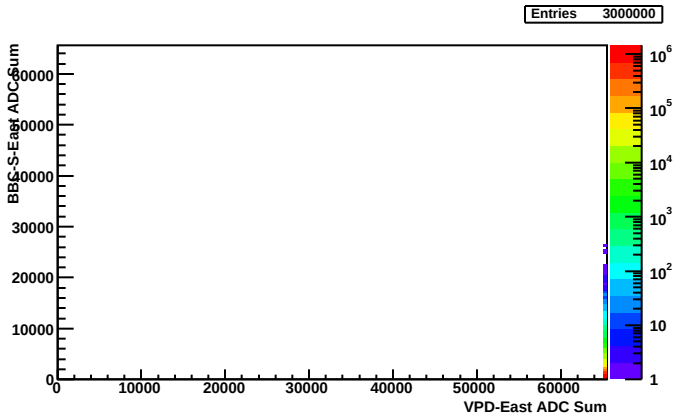
BBC-L-West ADC Sum
BBC-S-West ADC Sum

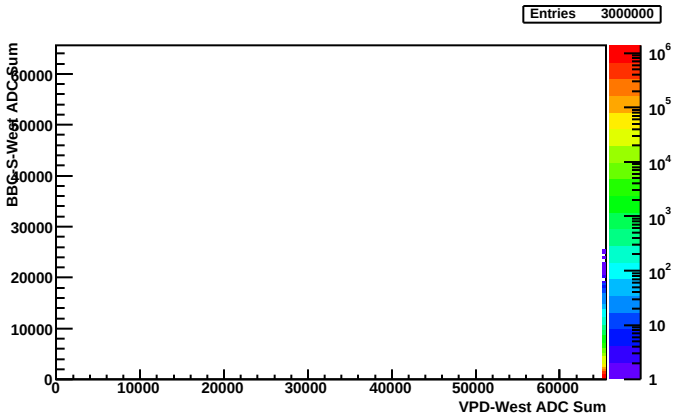

Entries 3000000
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 3000000
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 3000000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att


Entries 3000000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 3000000
BBC-S-East ADC Sum
VPD-East ADC Sum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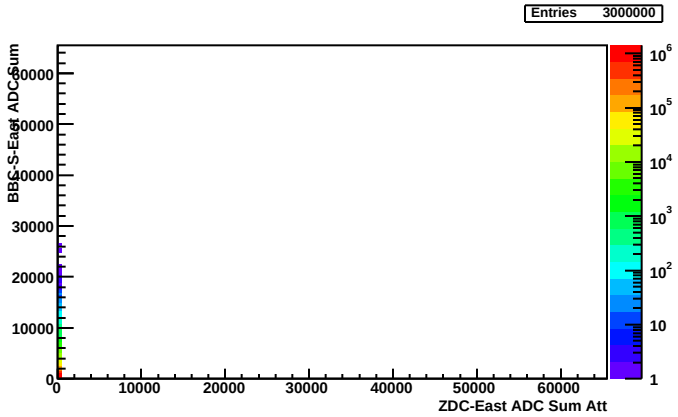
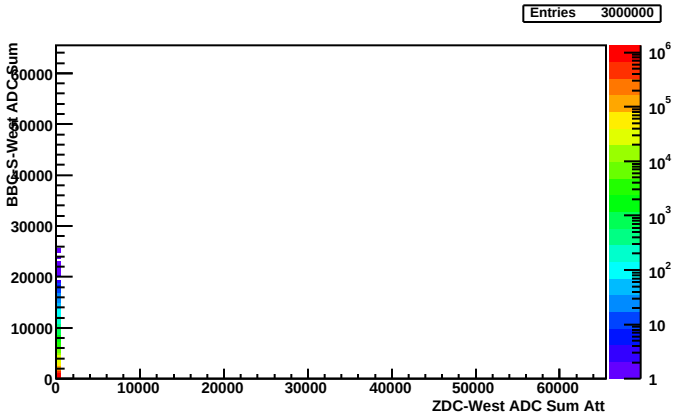
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 3000000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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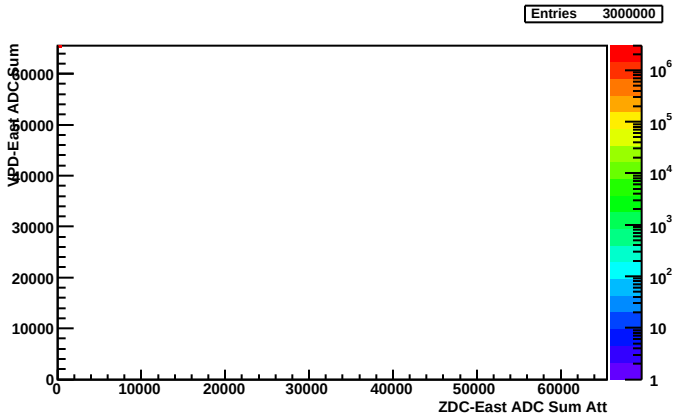
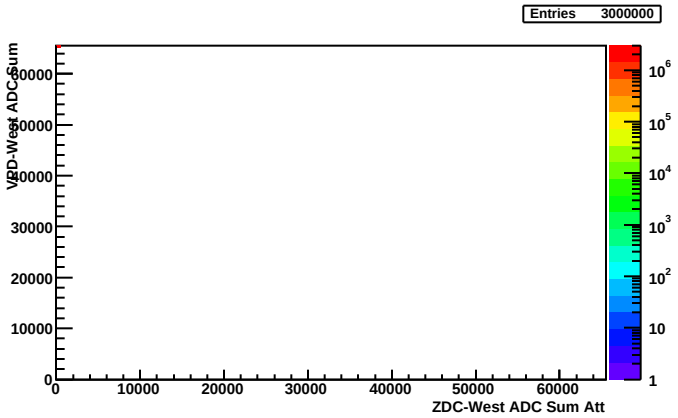
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 3000000
VPD-East ADC Sum
ZDC-East ADC Sum Att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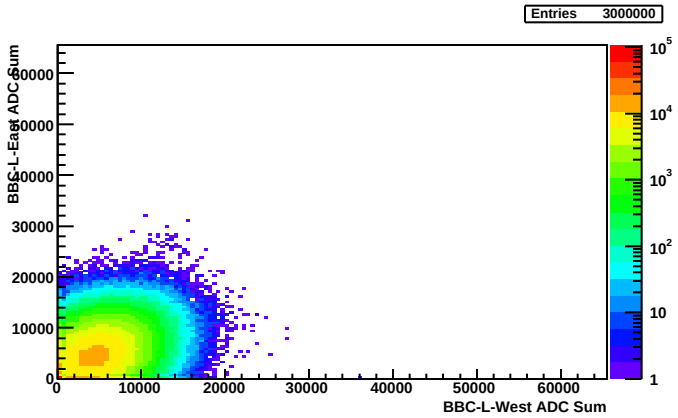
VPD-West ADC Sum
ZDC-West ADC Sum Att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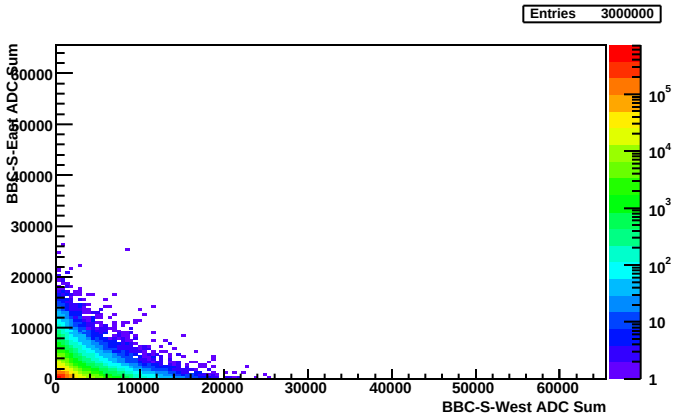
BBC-L-East ADC Sum
BBC-L-West ADC Sum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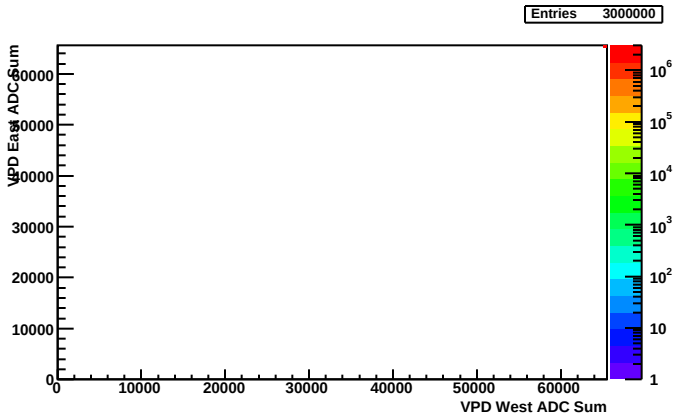
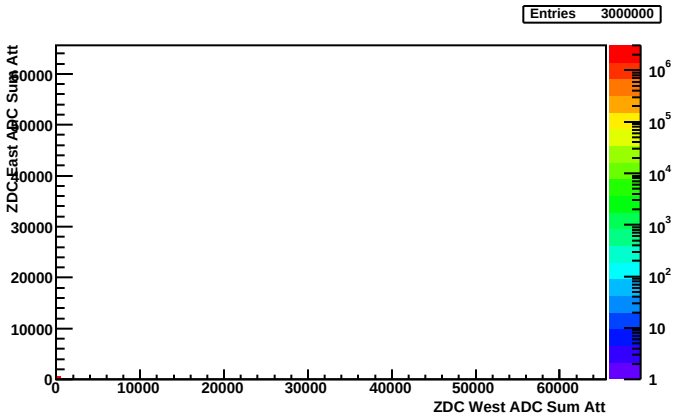
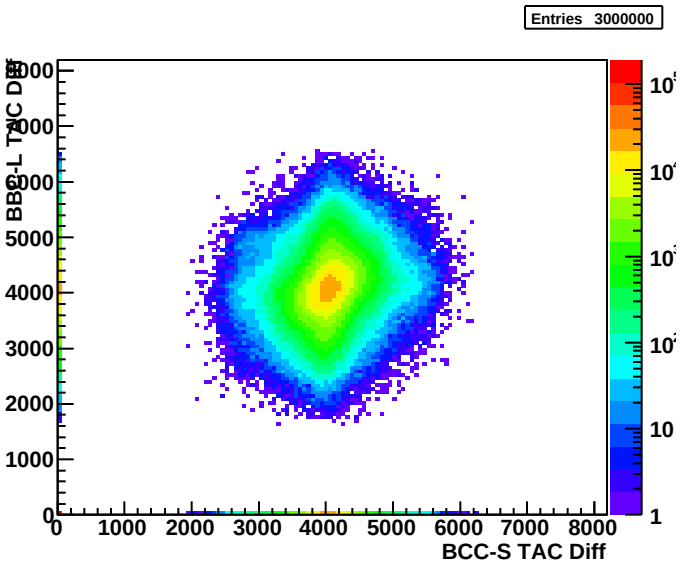
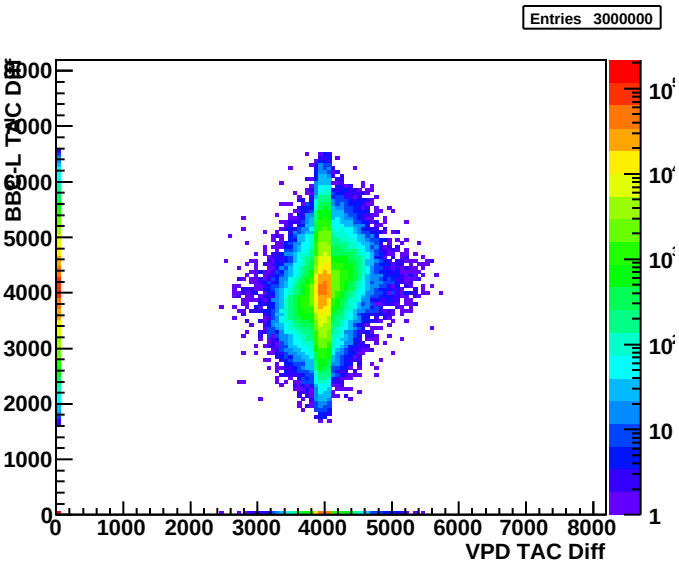
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 3000000
VPD East ADC Sum
VPD West ADC Sum


Entries 3000000
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 3000000
BBC-L TAC Diff
BCC-S TAC Diff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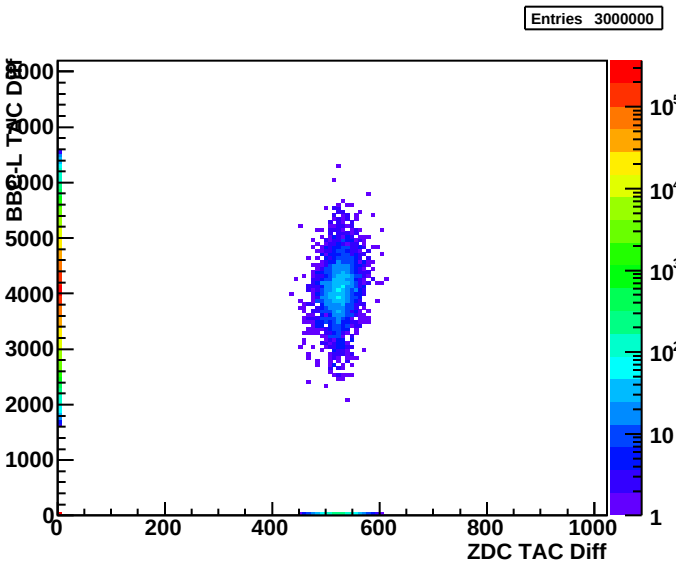
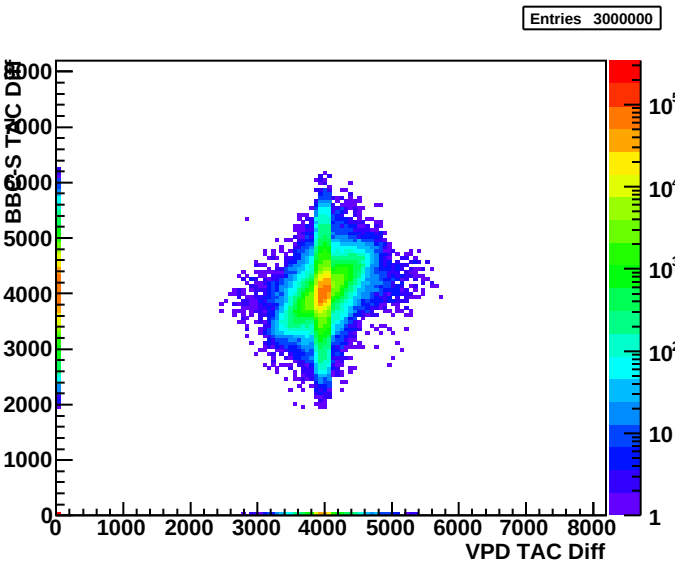
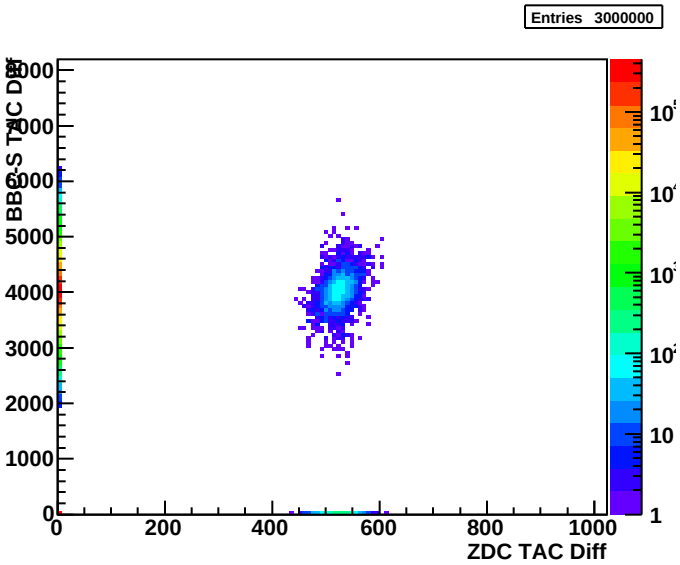
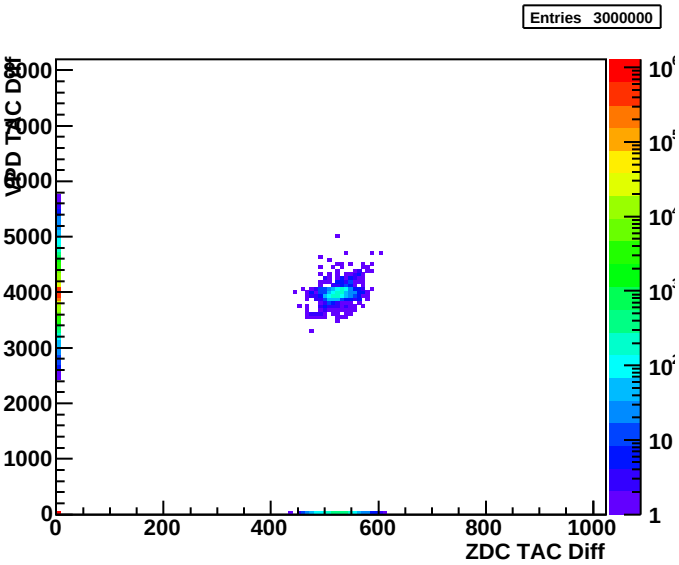
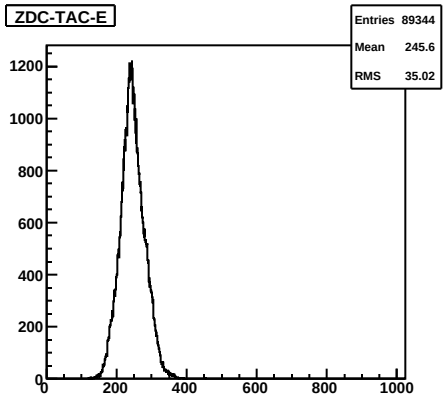
BBC-L TAC Diff
VPD TAC Diff

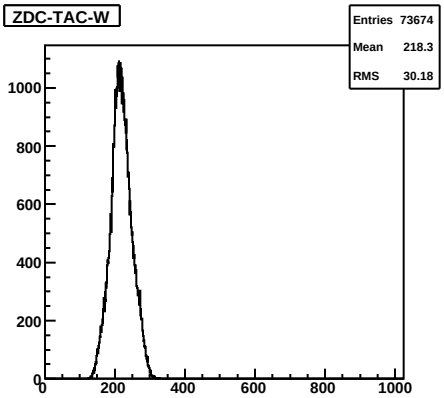

Entries 3000000
BBC-L TAC Diff
ZDC TAC Diff

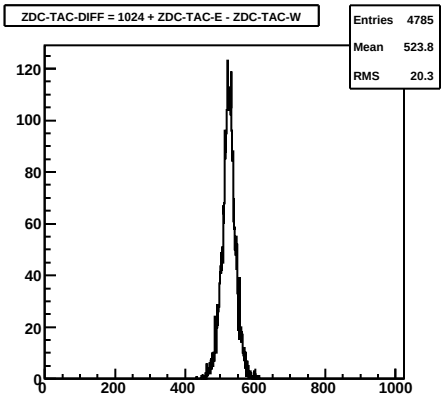

Entries 3000000
BBC-S TAC Diff
VPD TAC Diff

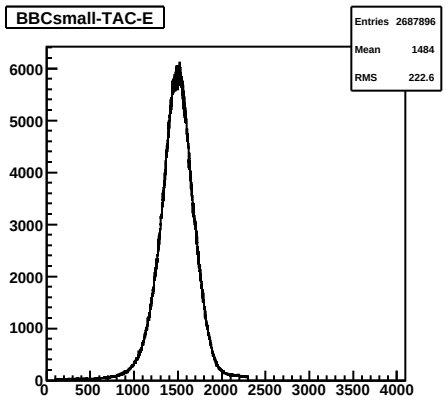

Entries 3000000
BBC-S TAC Diff
ZDC TAC Diff

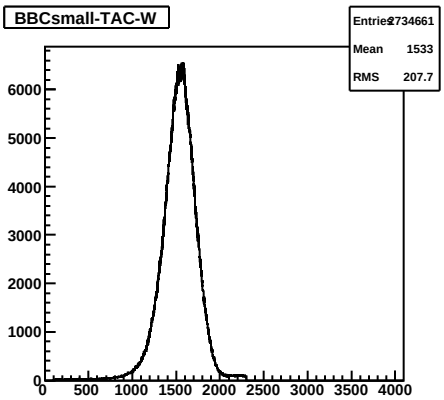

Entries 3000000
VPD TAC Diff
ZDC TAC Diff

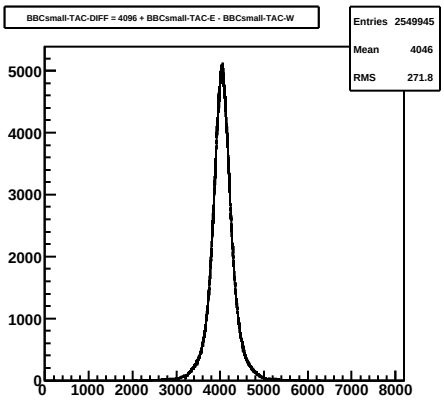

ZDC-TAC-E
Entries 89344
Mean 245.6
RMS 35.02

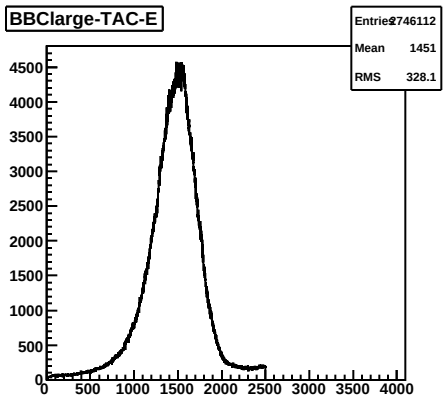

ZDC-TAC-W
Entries 73674
Mean 218.3
RMS 30.18

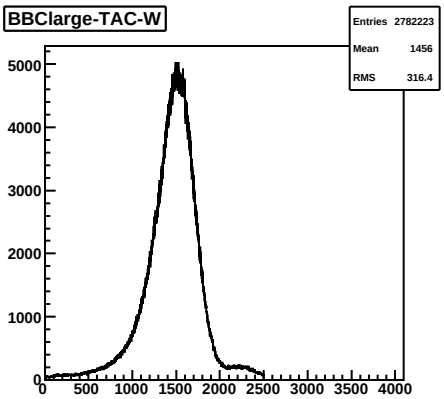

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 4785
Mean 523.8
RMS 20.3

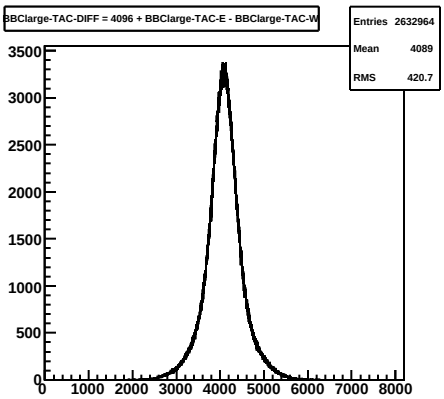

BBCsmall-TAC-E
Entries 2687896
Mean 1484
RMS 222.6

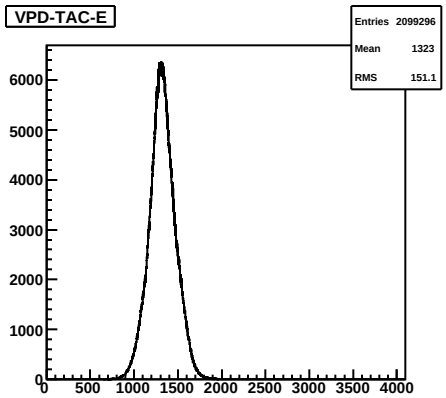

BBCsmall-TAC-W
Entries 2734661
Mean 1533
RMS 207.7

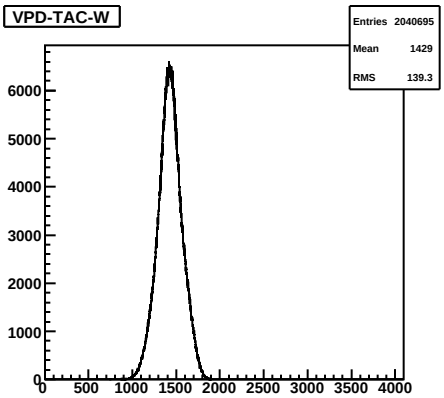

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2549945
Mean 4046
RMS 271.8

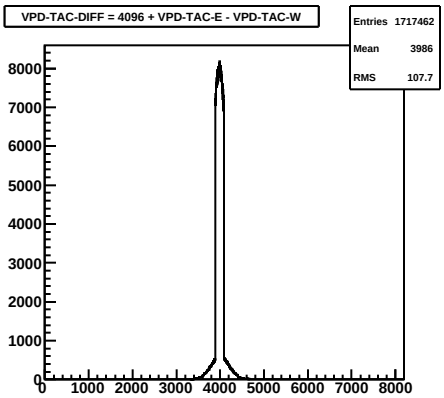

BBClarge-TAC-E
Entries 2746112
Mean 1451
RMS 328.1

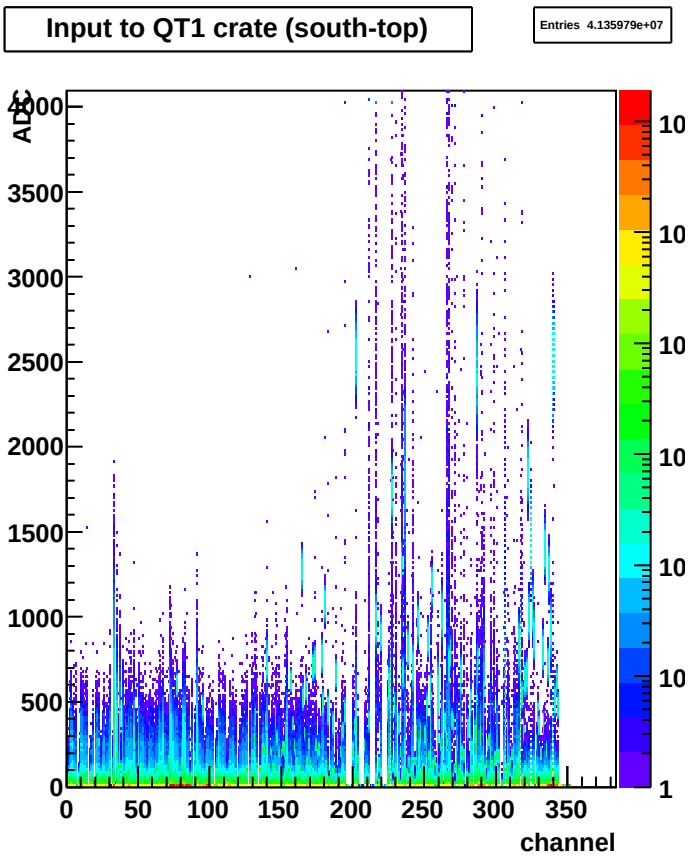

BBClarge-TAC-W
Entries 2782223
Mean 1456
RMS 316.4

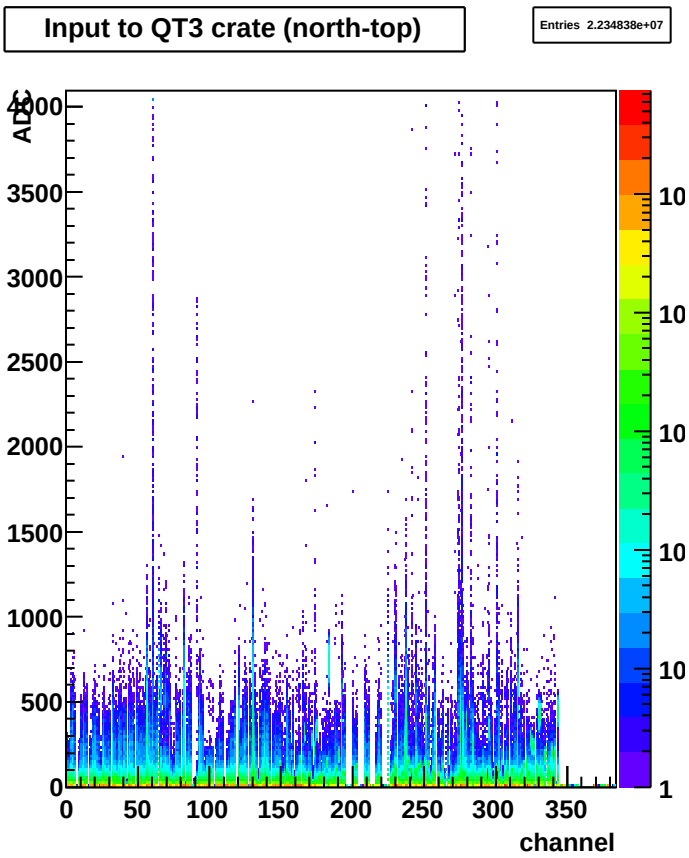

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2632964
Mean 4089
RMS 420.7

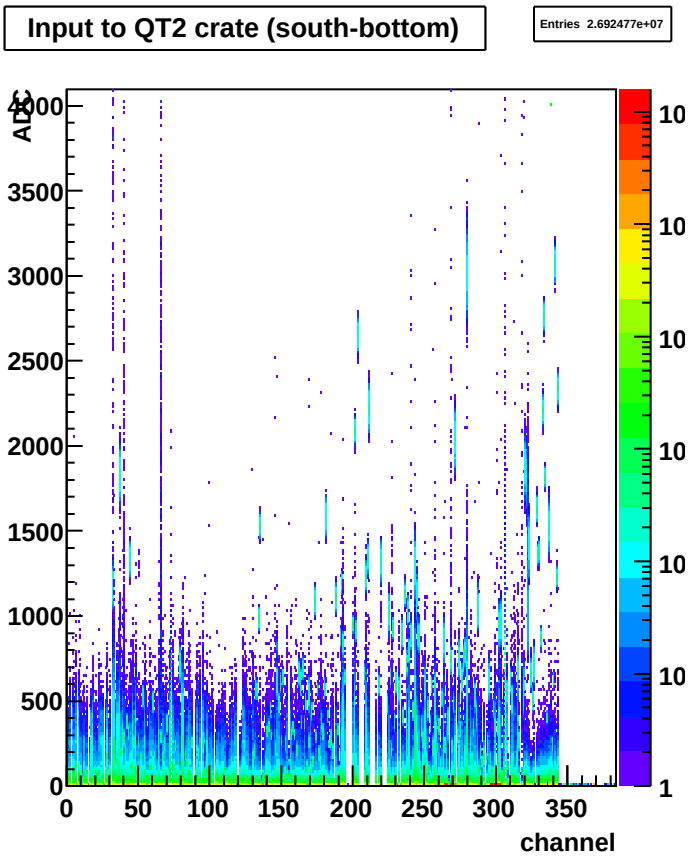

VPD-TAC-E
Entries 2099296
Mean 1323
RMS 151.1

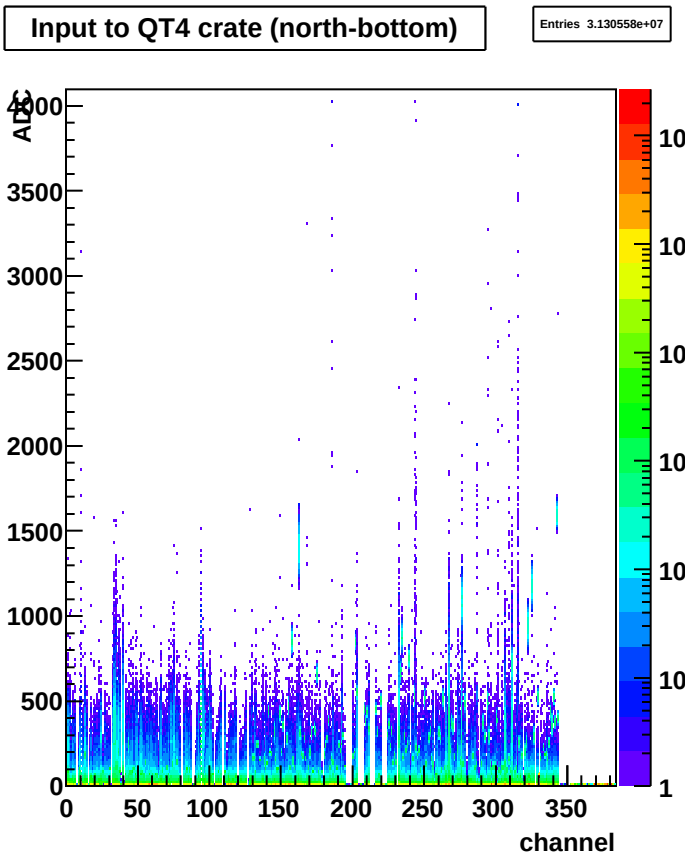

VPD-TAC-W
Entries 2040695
Mean 1429
RMS 139.3

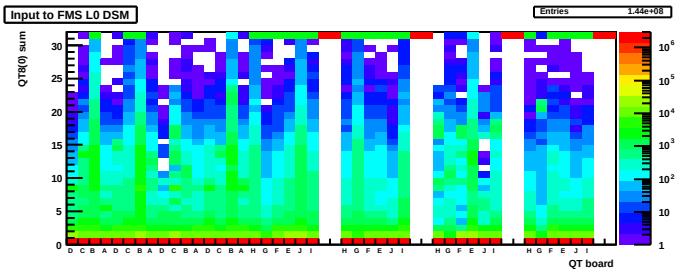

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1717462
Mean 3986
RMS 107.7

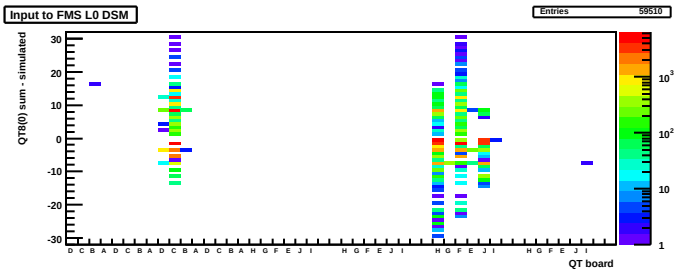

Input to QT1 crate (south-top)
Entries 4.135979e+07
ADC
channel

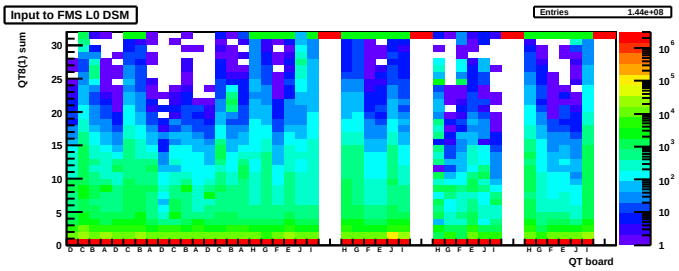

Input to QT3 crate (north-top)
Entries 2.234838e+07
ADC
channel

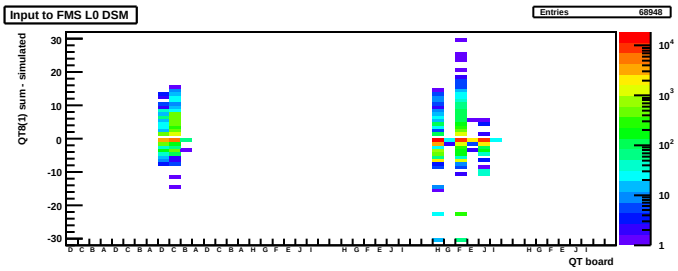

Input to QT2 crate (south-bottom)
Entries 2.692477e+07
ADC
channel

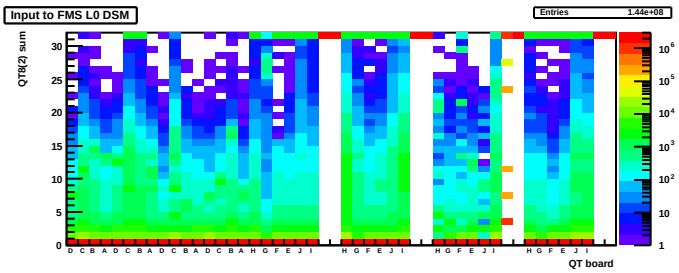

Input to QT4 crate (north-bottom)
Entries 3.130558e+07
ADC
channel

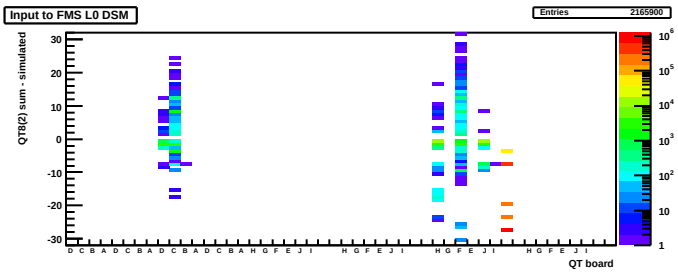

Input to FMS L0 DSM
Entries 1.44e+08
QT8(0) sum
QT board

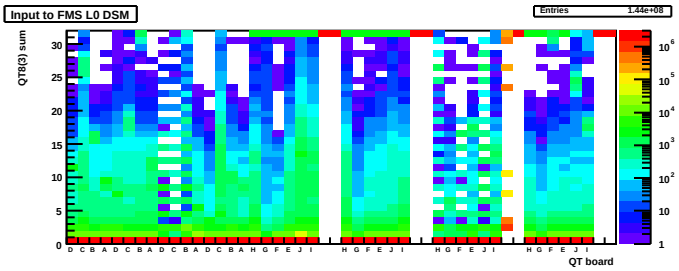

Input to FMS L0 DSM
Entries 59510
QT8(0) sum - simulated
QT board

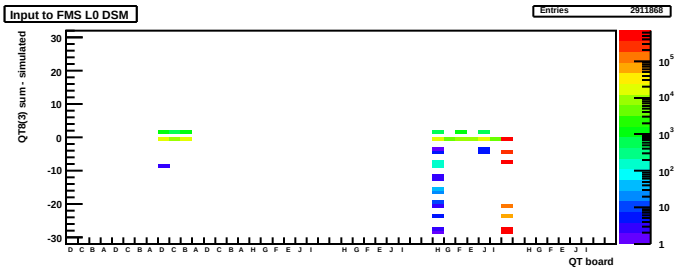

Input to FMS L0 DSM
Entries 1.44e+08
QT8(1) sum
QT board

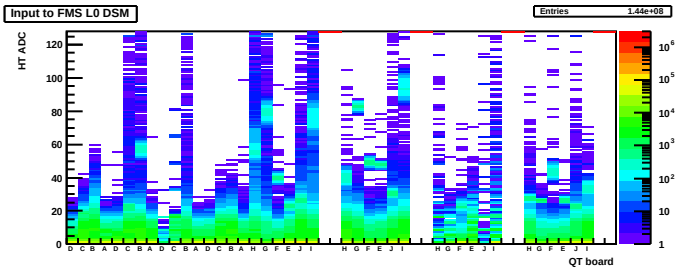

Input to FMS L0 DSM
Entries 68948
QT8(1) sum - simulated
QT board

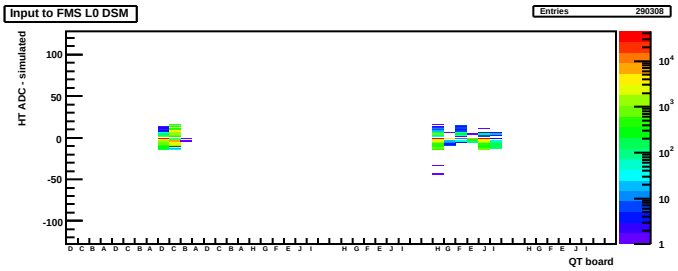

Input to FMS L0 DSM
Entries 1.44e+08
QT8(2) sum
QT board

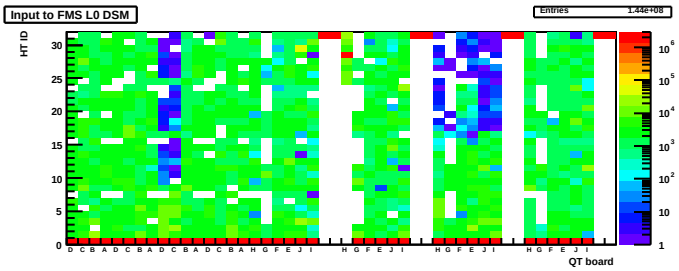

Input to FMS L0 DSM
Entries 2165900
QT8(2) sum - simulated
QT board

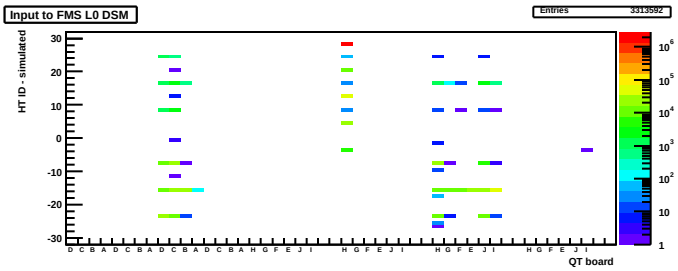

Input to FMS L0 DSM
Entries 1.44e+08
QT8(3) sum
QT board

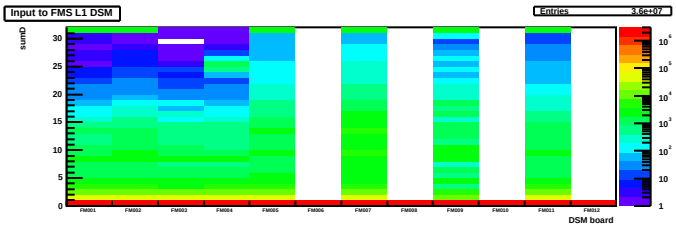

Input to FMS L0 DSM
Entries 2911868
QT8(3) sum - simulated
QT board

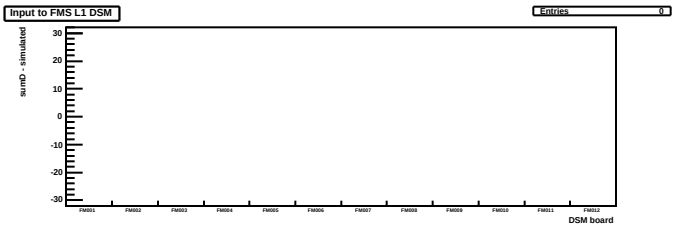

Input to FMS L0 DSM
Entries 1.44e+08
HT ADC
QT board

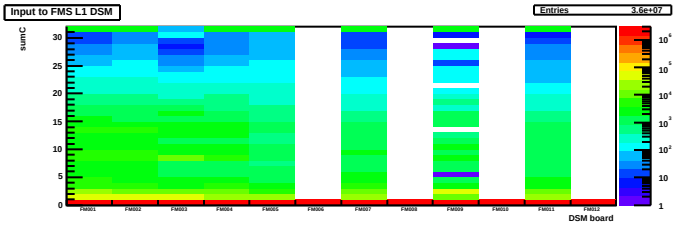

Input to FMS L0 DSM
Entries 290308
HT ADC - simulated
QT board

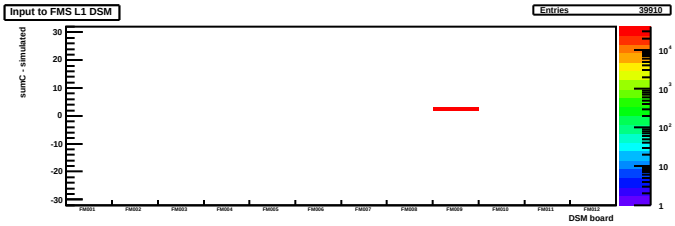

Input to FMS L0 DSM
Entries 1.44e+08
HT ID
QT board

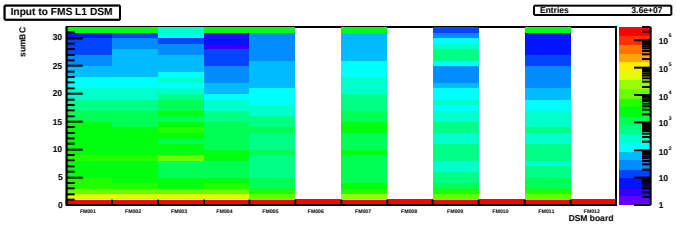

Input to FMS L0 DSM
Entries 3313592
HT ID - simulated
QT board

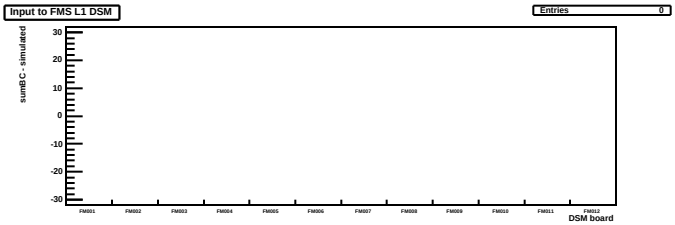

Input to FMS L1 DSM
Entries 3.6e+07
sumD
DSM board

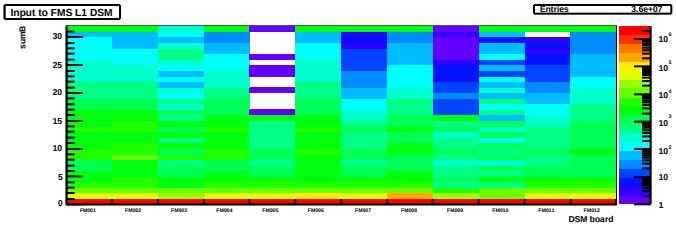

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

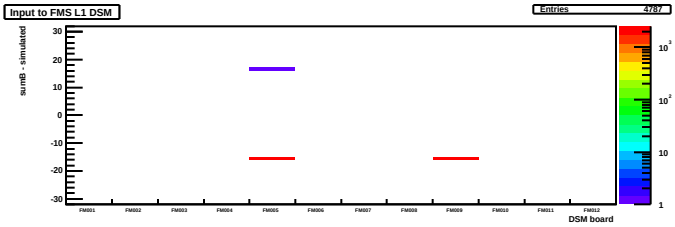

Input to FMS L1 DSM
Entries 3.6e+07
sumC
DSM board

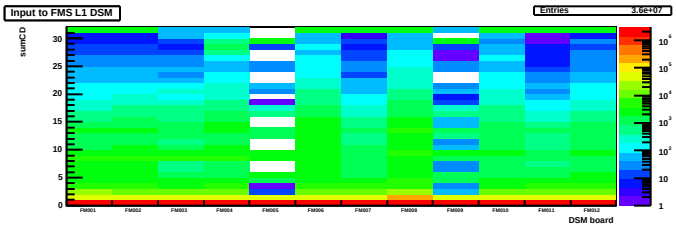

Input to FMS L1 DSM
Entries 39910
sumC - simulated
DSM board

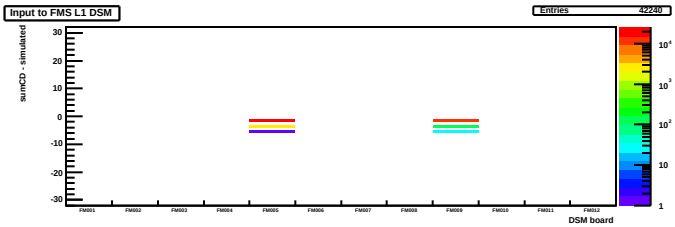

Input to FMS L1 DSM
Entries 3.6e+07
sumBC
DSM board

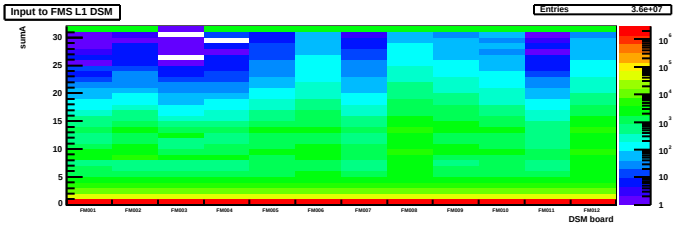

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

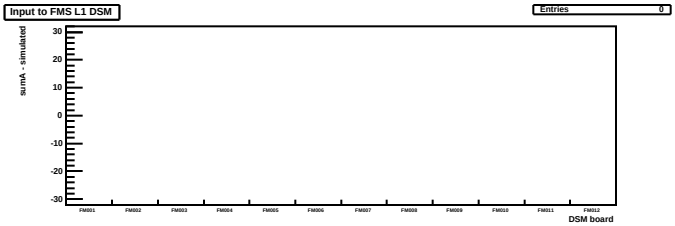

Input to FMS L1 DSM
Entries 3.6e+07
sumB
DSM board

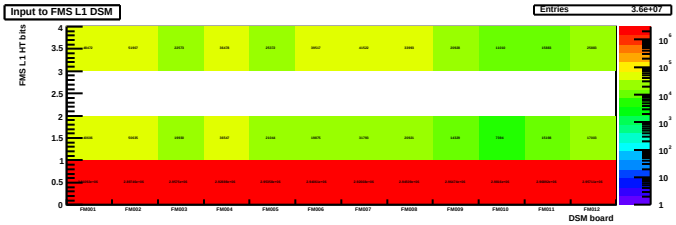

Input to FMS L1 DSM
Entries 4787
sumB - simulated
DSM board

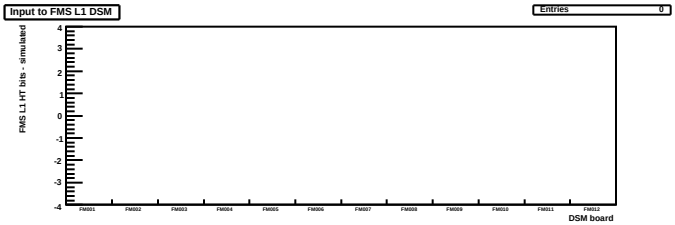

Input to FMS L1 DSM
Entries 3.6e+07
sumCD
DSM board

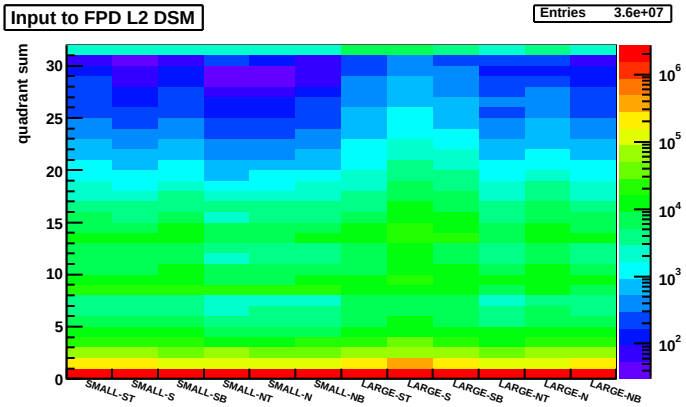

Input to FMS L1 DSM
Entries 42240
sumCD - simulated
DSM board

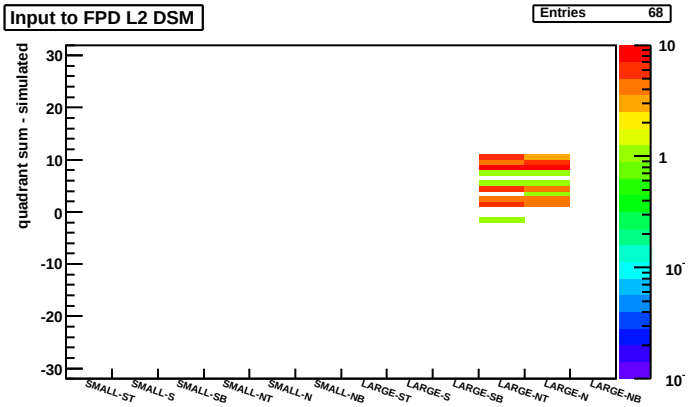

Input to FMS L1 DSM
Entries 3.6e+07
sumA
DSM board

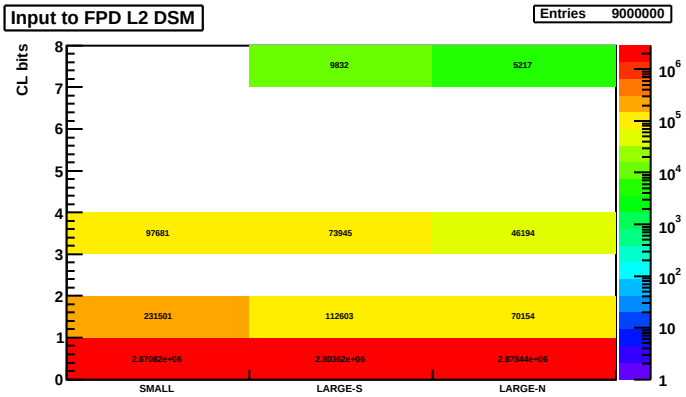

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

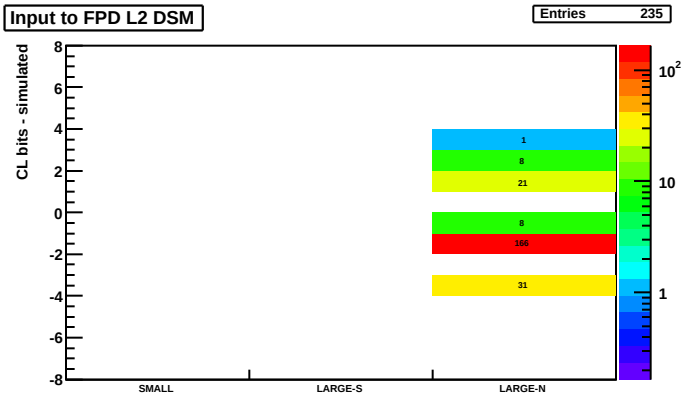

Input to FMS L1 DSM
Entries 3.6e+07
FMS L1 HT bits
DSM board

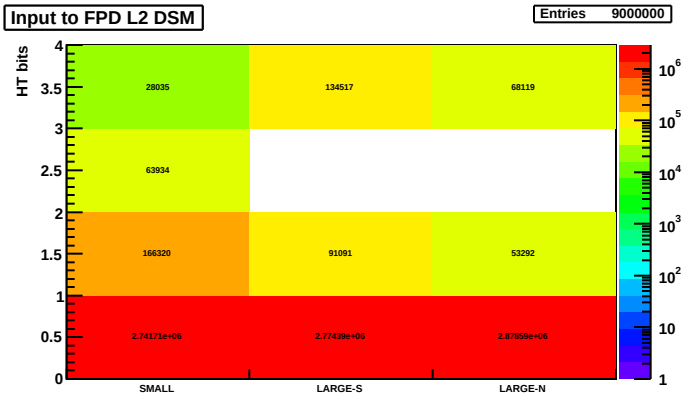

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

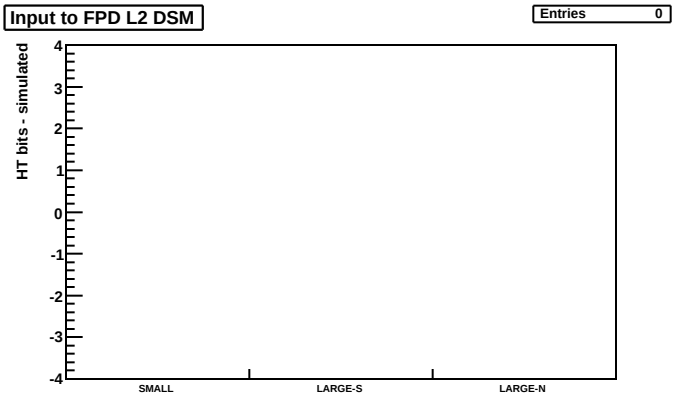

Input to FPD L2 DSM
Entries 3.6e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

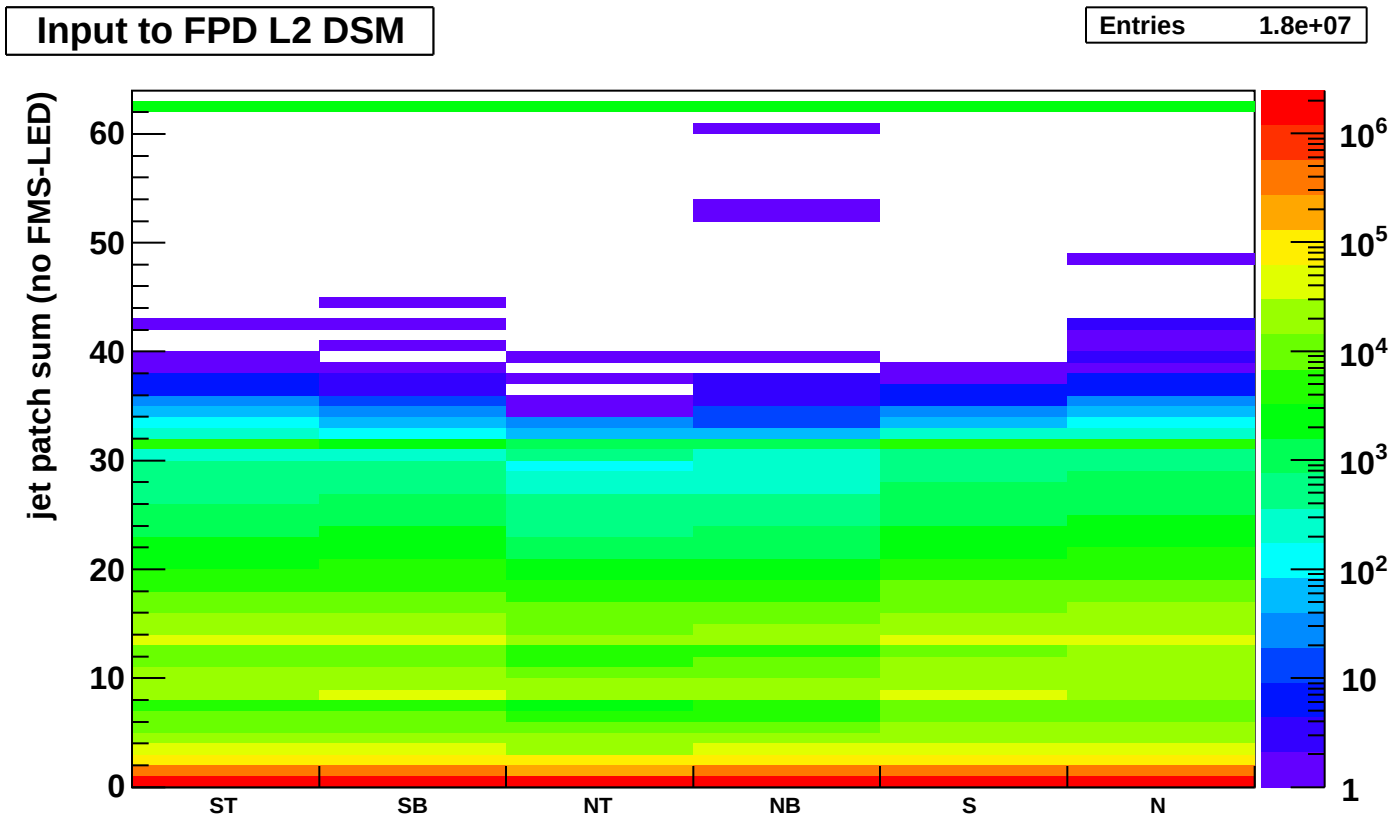

Input to FPD L2 DSM
Entries 68
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

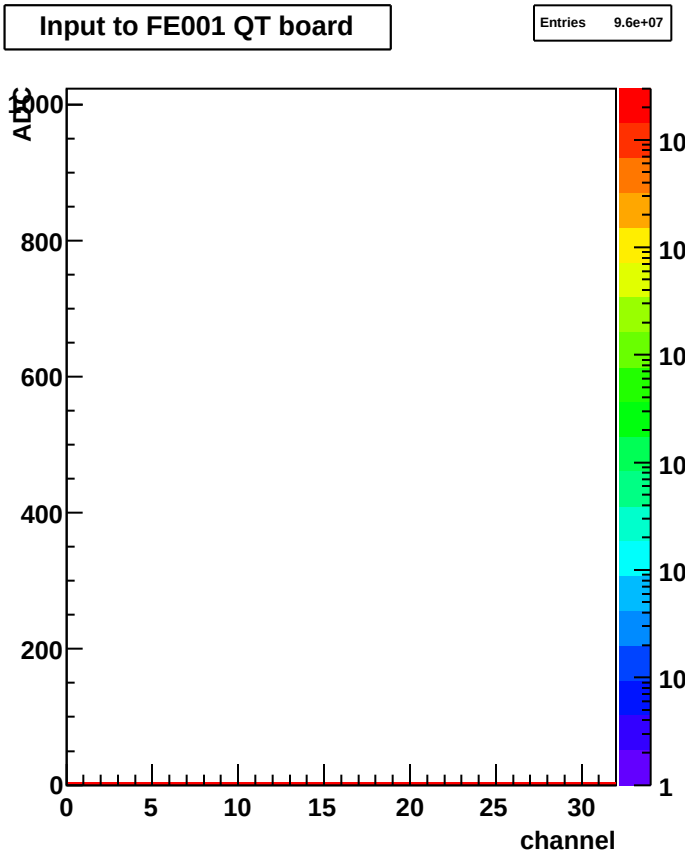

Input to FPD L2 DSM
Entries 9000000
CL bits
9832
5217
97681
73945
46194
231501
112603
70154
2.67082e+06
2.80362e+06
2.87844e+06
SMALL
LARGE-S
LARGE-N

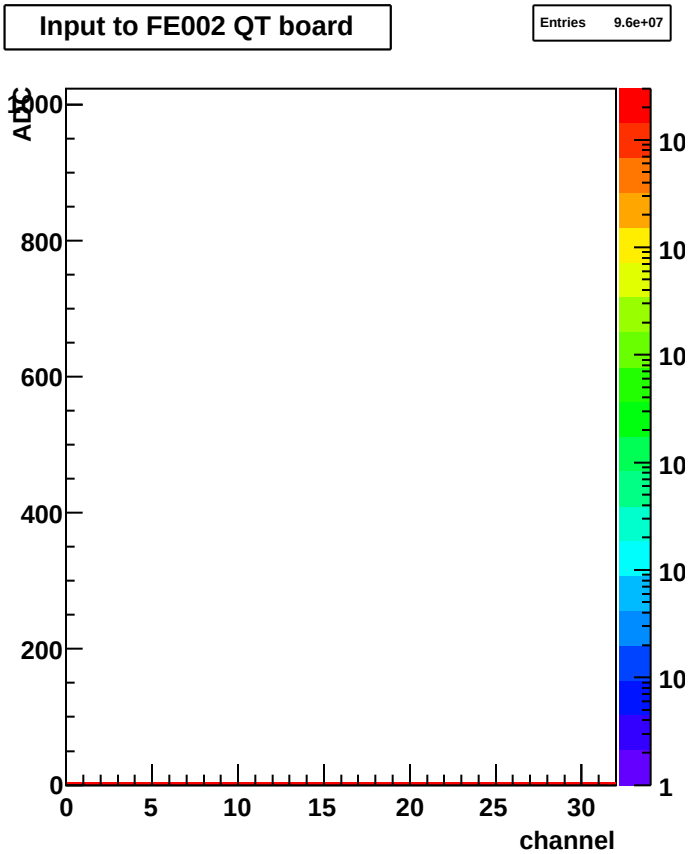

Input to FPD L2 DSM
Entries 235
CL bits - simulated
1
8
21
8
166
31
SMALL
LARGE-S
LARGE-N

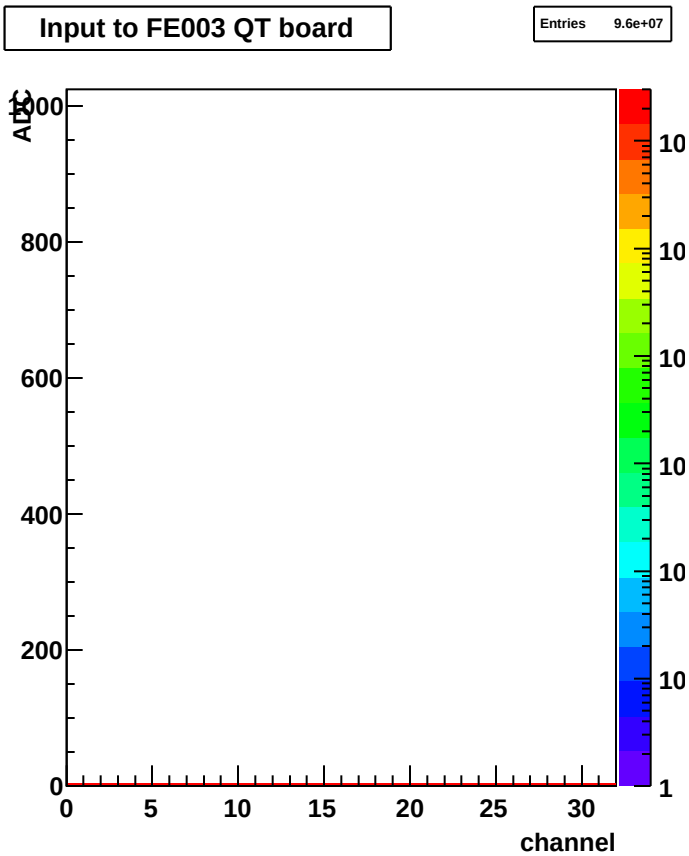

Input to FPD L2 DSM
Entries 9000000
HT bits
28035
134517
68119
63934
166320
91091
53292
2.74171e+06
2.77439e+06
2.87859e+06
SMALL
LARGE-S
LARGE-N

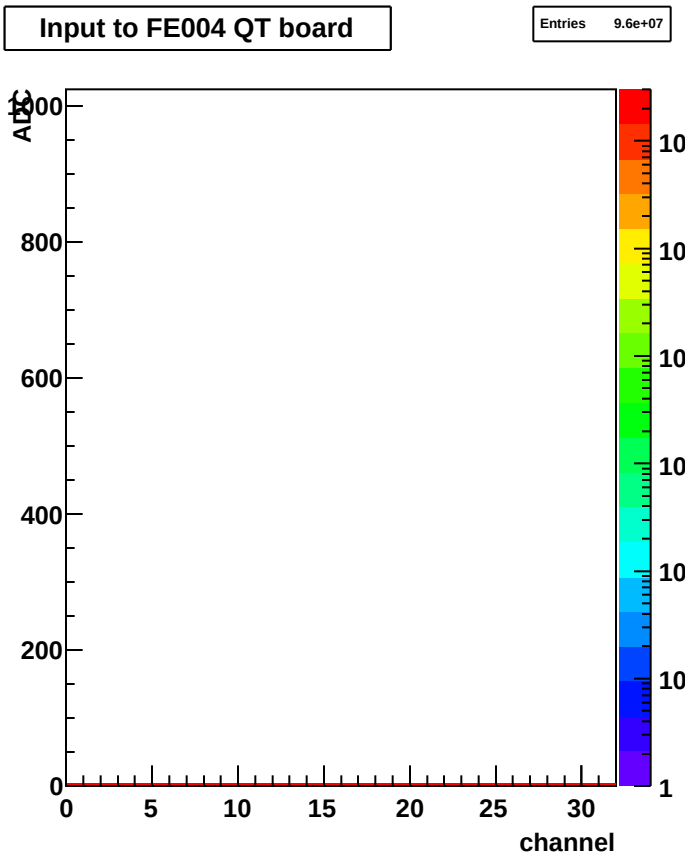

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

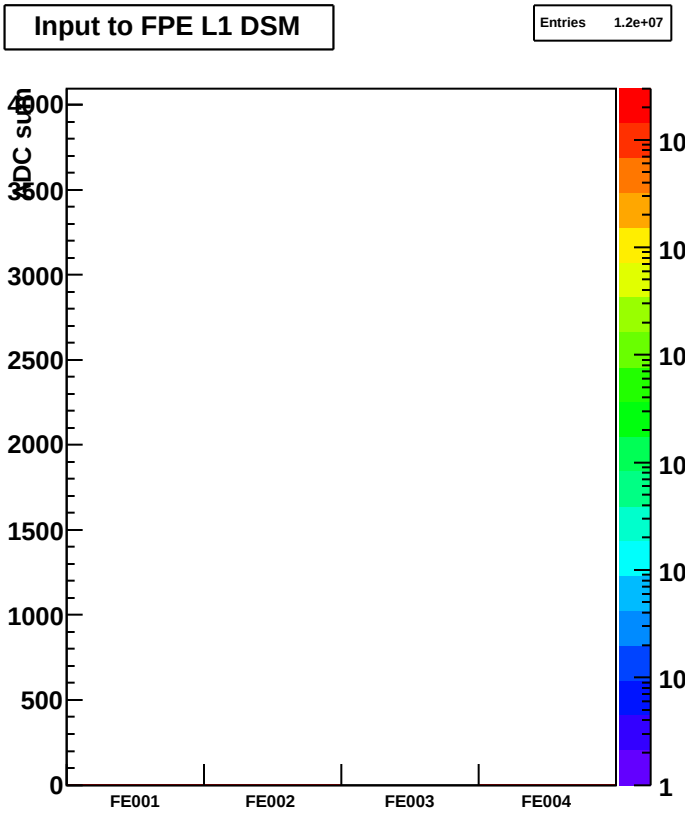

Input to FPD L2 DSM
Entries 1.8e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

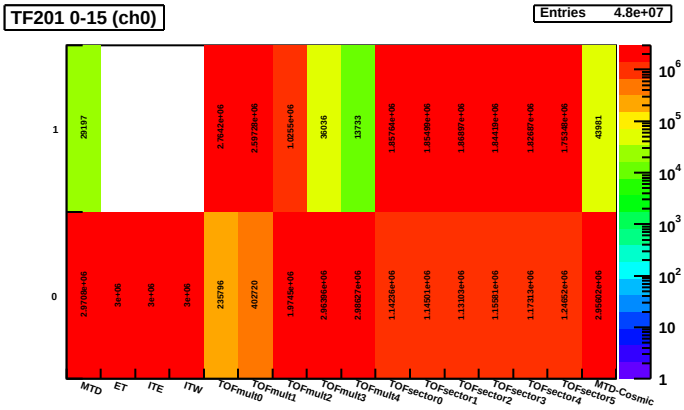

Input to FE001 QT board
Entries 9.6e+07
ADC
channel

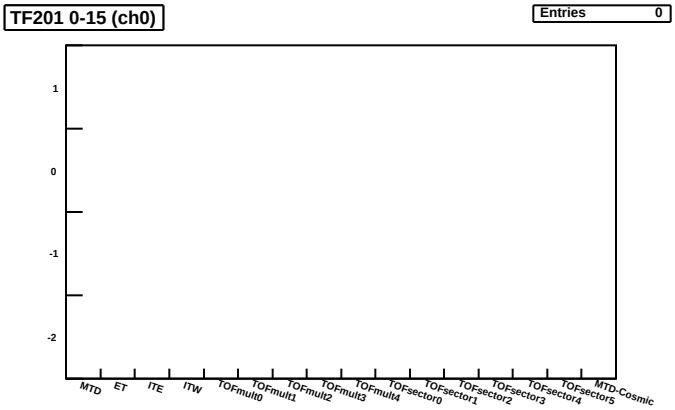

Input to FE002 QT board
Entries 9.6e+07
ADC
channel

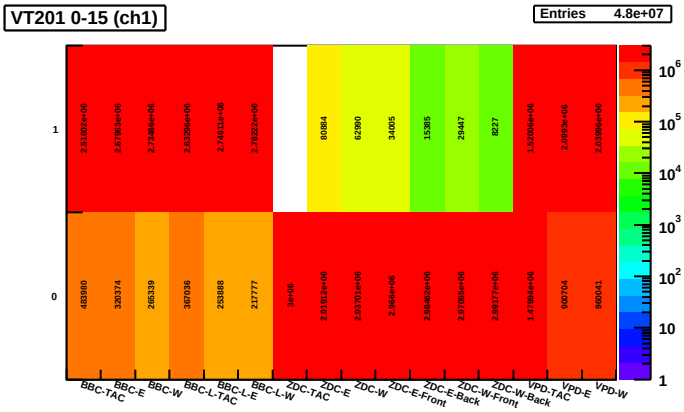

Input to FE003 QT board
Entries 9.6e+07
ADC
channel

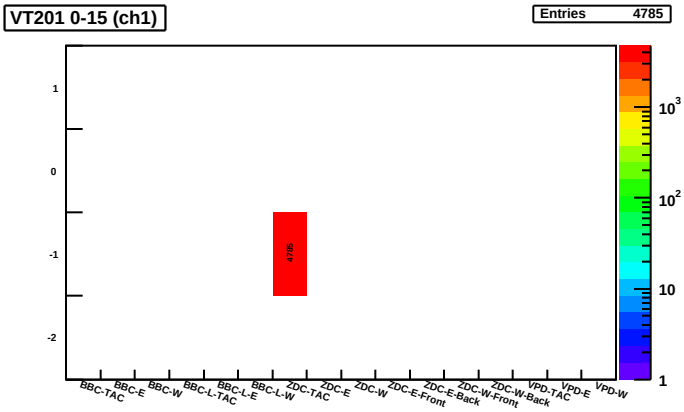

Input to FE004 QT board
Entries 9.6e+07
ADC
channel

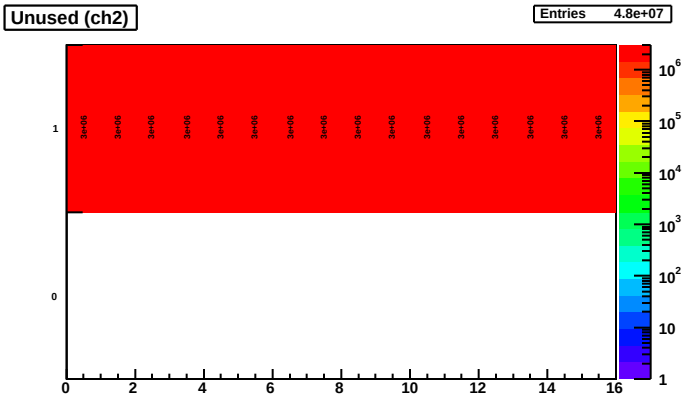

Input to FPE L1 DSM
Entries 1.2e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

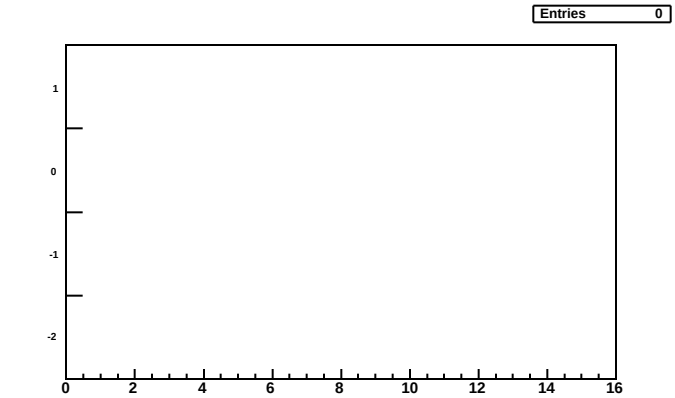

TF201 0-15 (ch0)
Entries 4.8e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

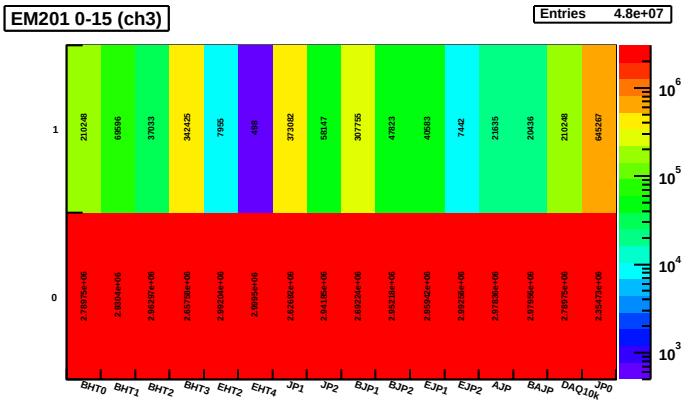

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

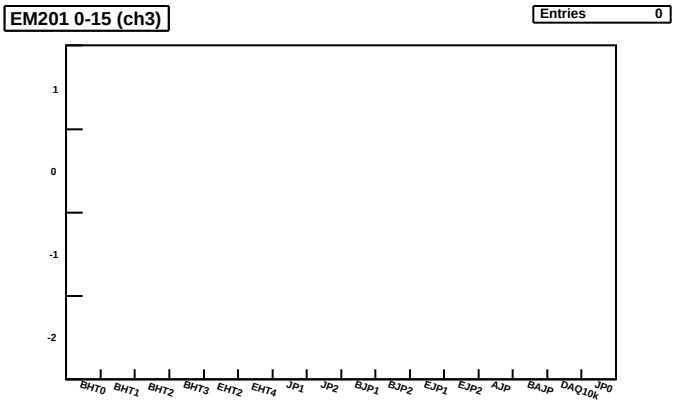

VT201 0-15 (ch1)
Entries 4.8e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 4785
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

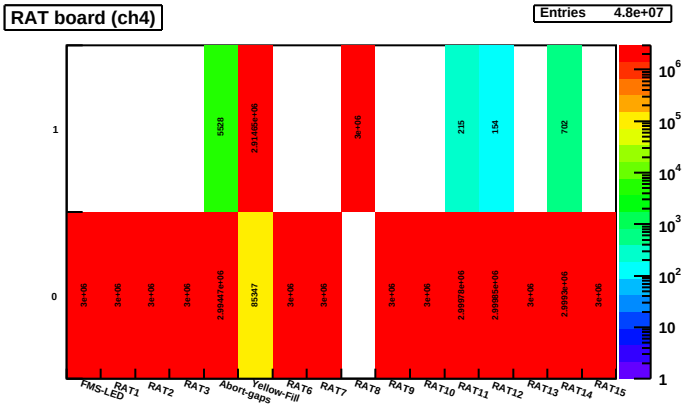

Unused (ch2)
Entries 4.8e+07

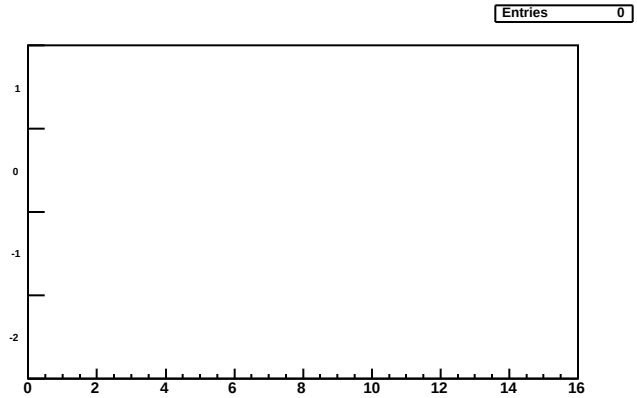

Entries 0


EM201 0-15 (ch3)
Entries 4.8e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

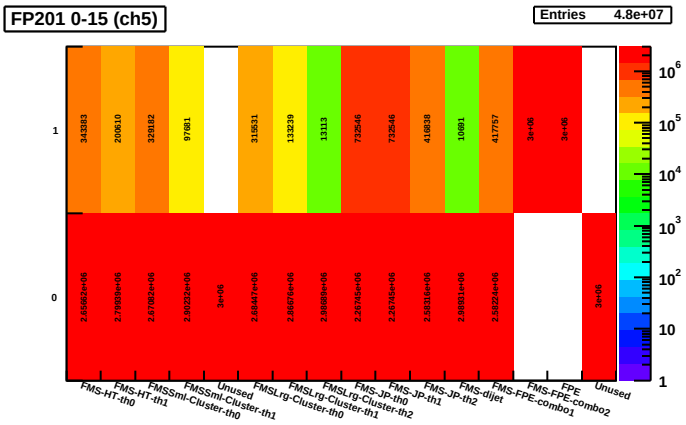

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

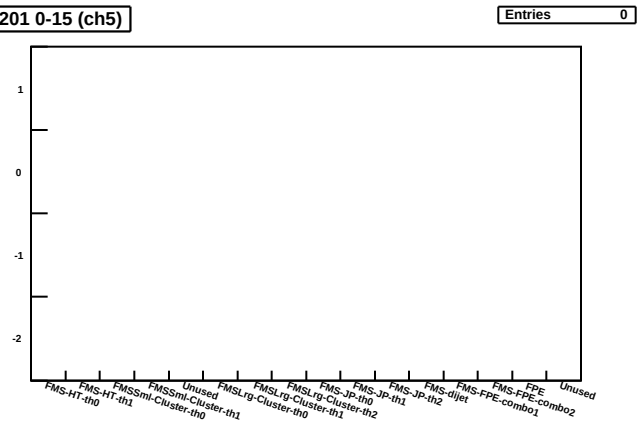




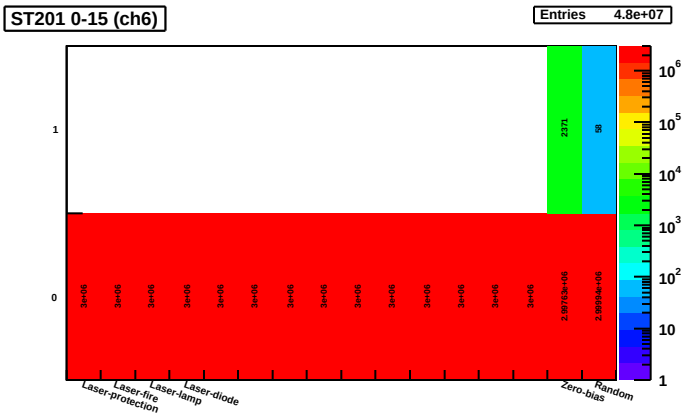
FP201 0-15 (ch5)

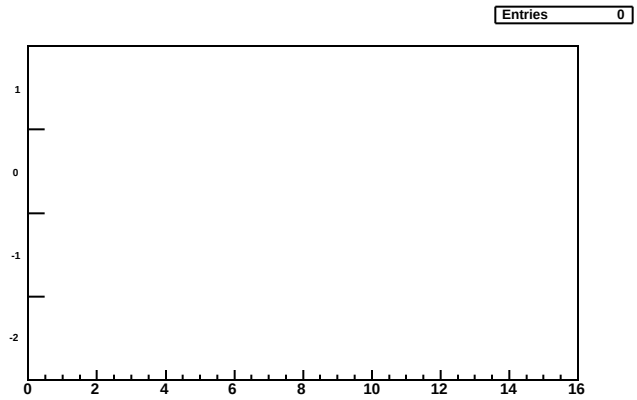
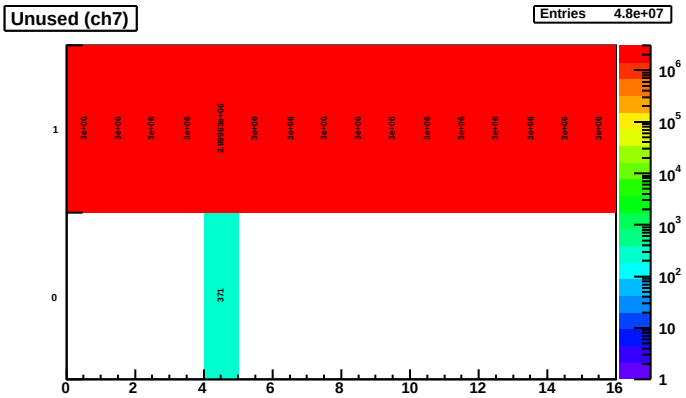
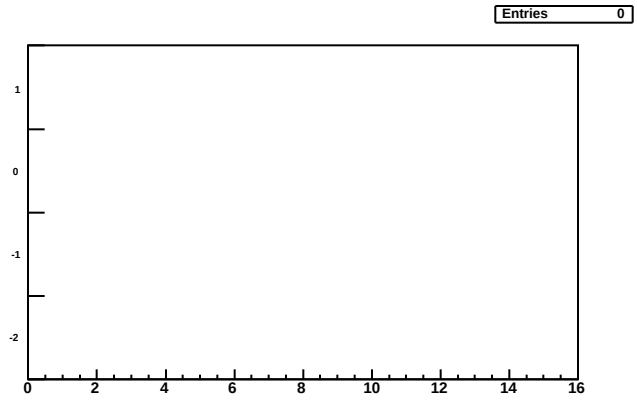


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)