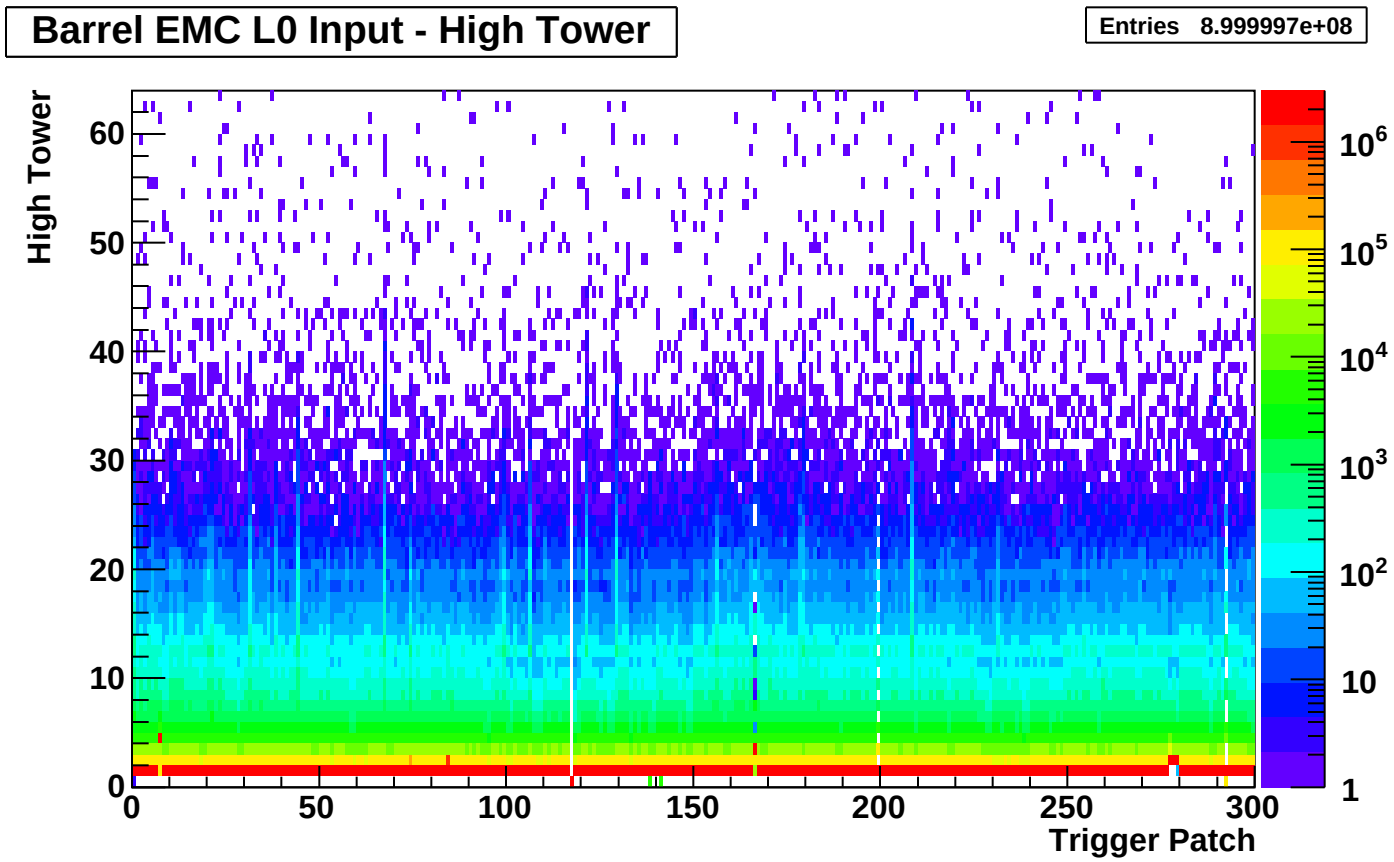
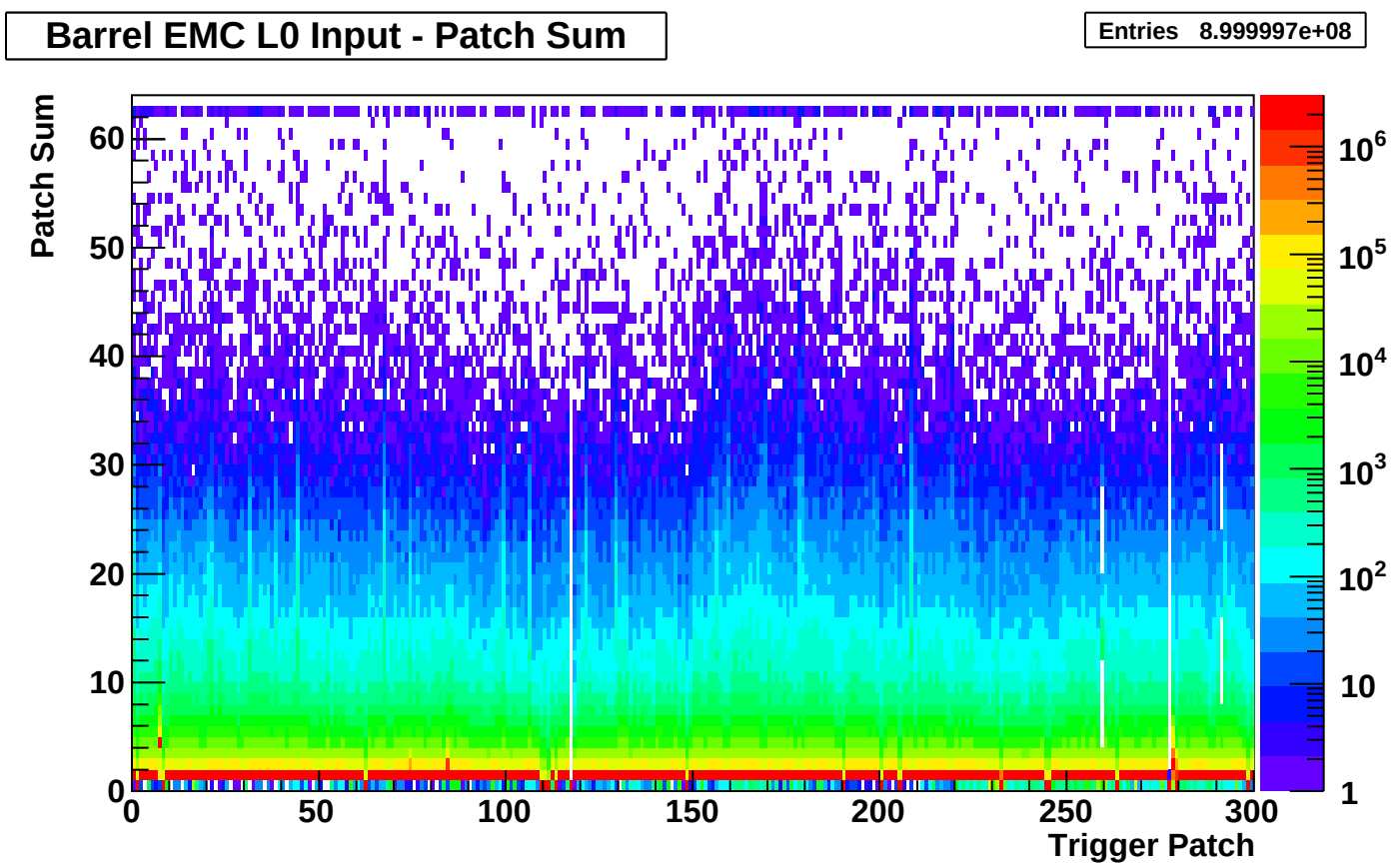
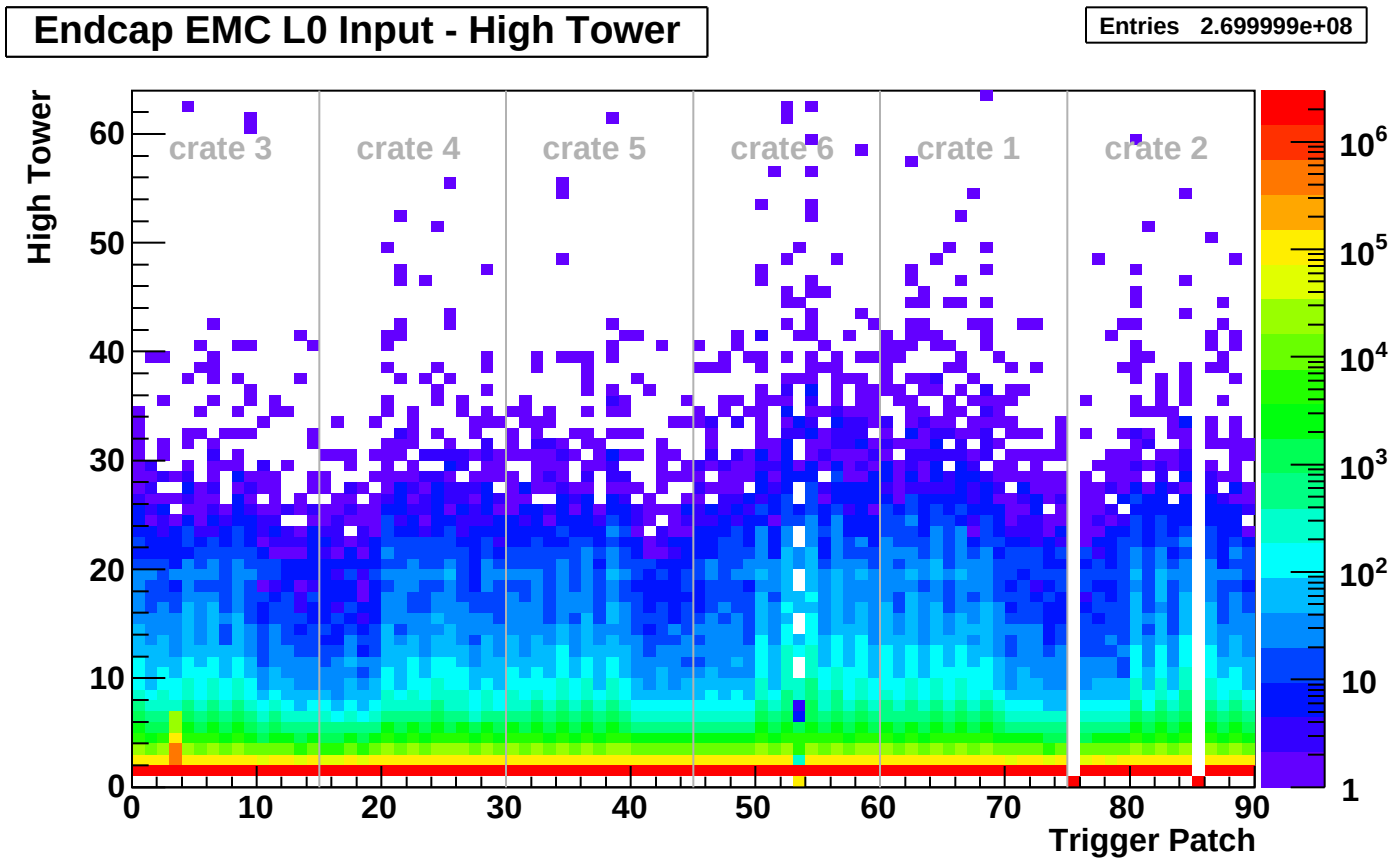
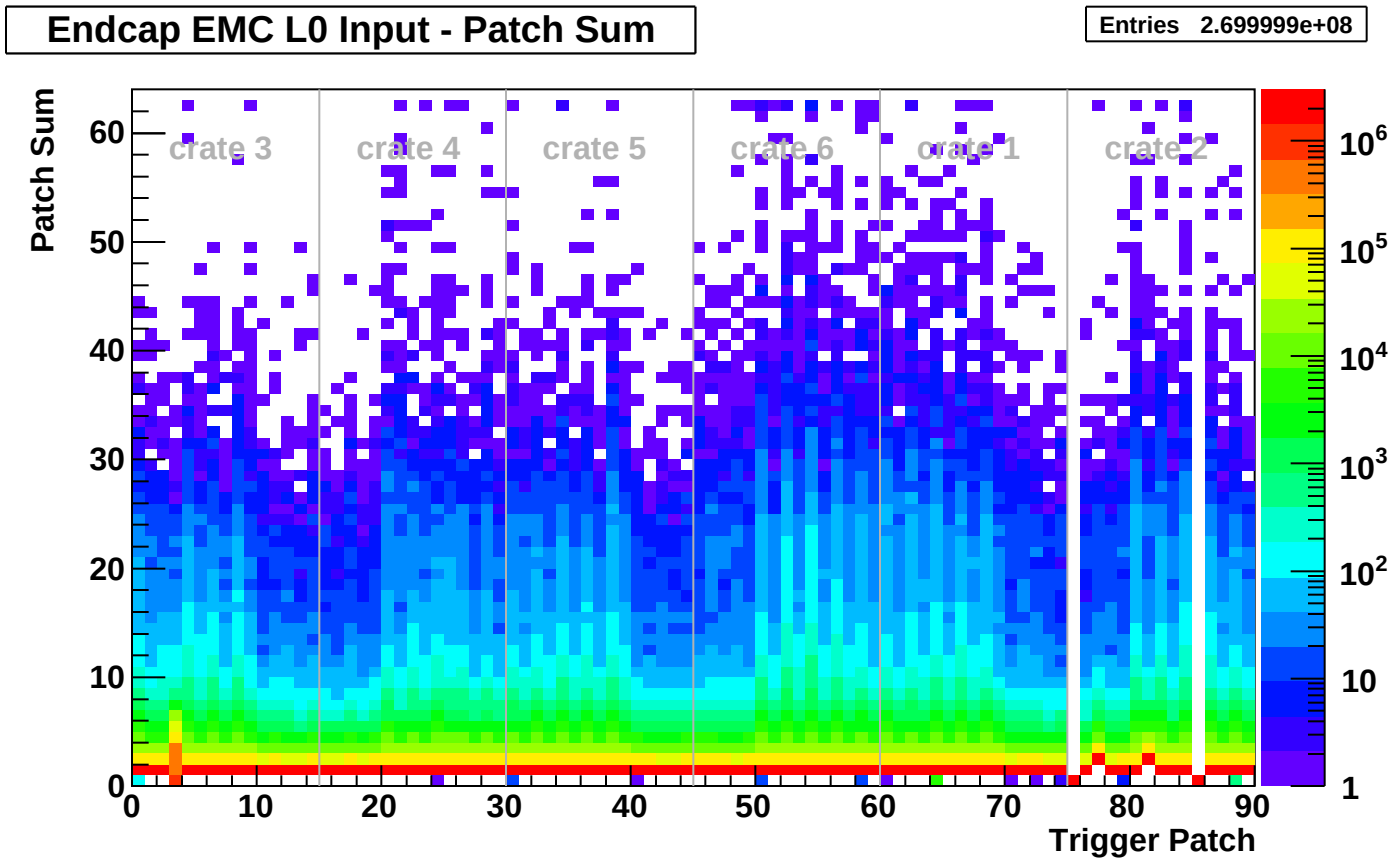
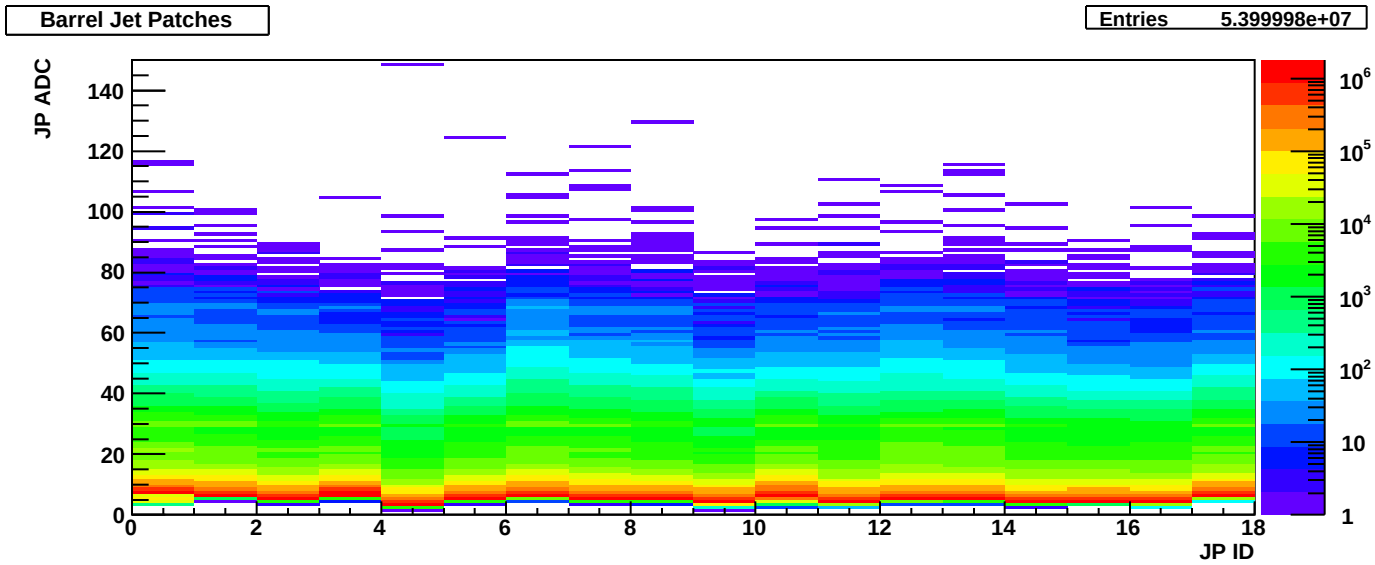
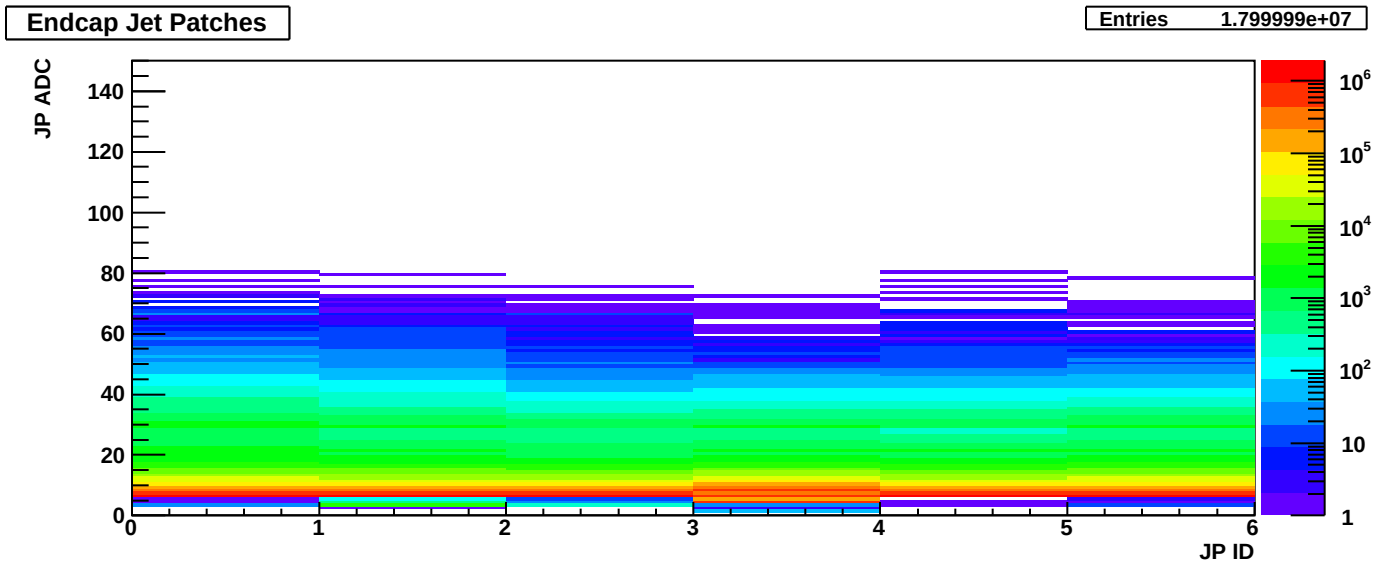

Barrel EMC L0 Input - High Tower
Entries 8.999997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

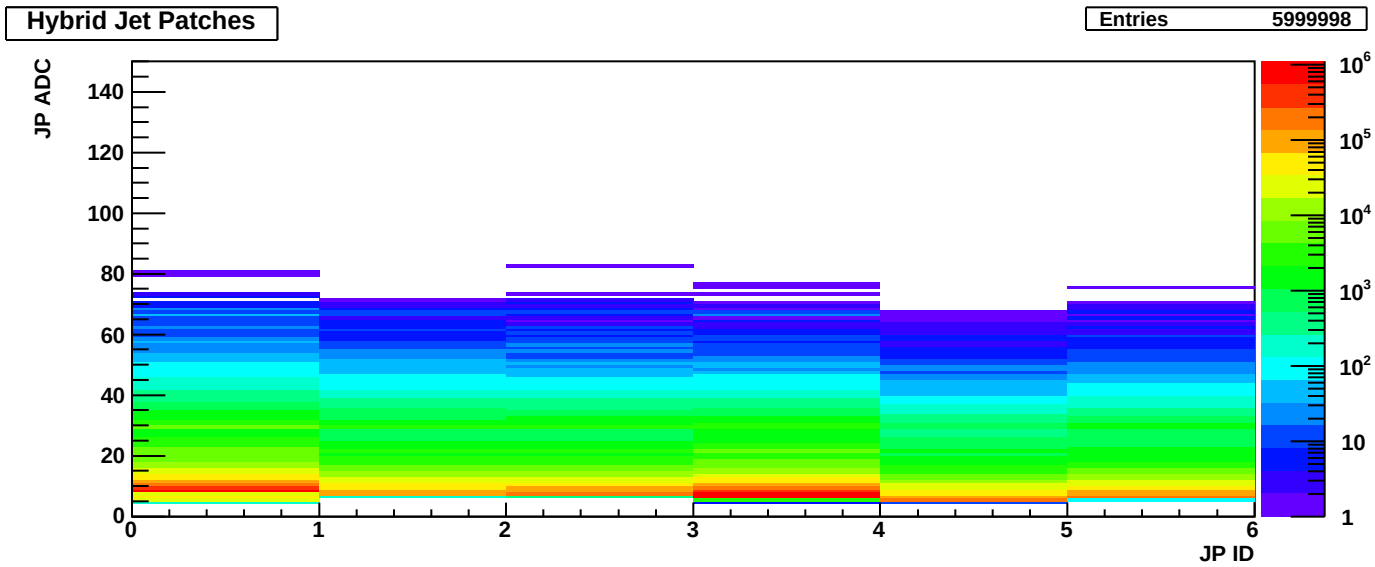

Barrel EMC L0 Input - Patch Sum
Entries 8.999997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

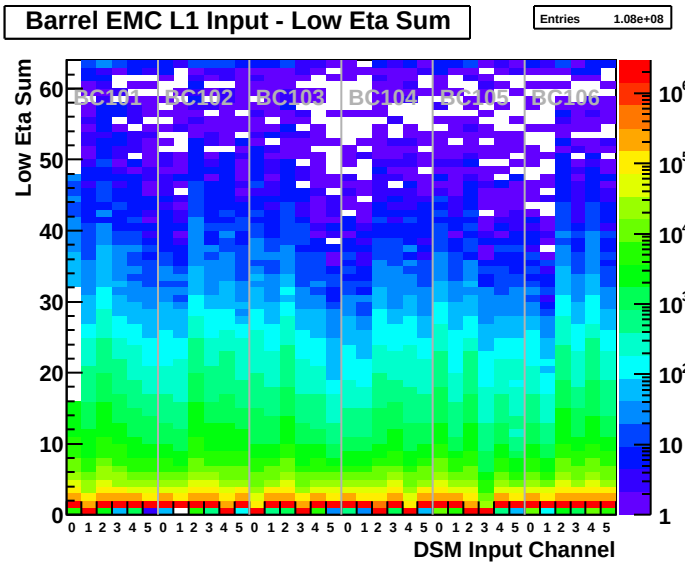

Endcap EMC L0 Input - High Tower
Entries 2.699999e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

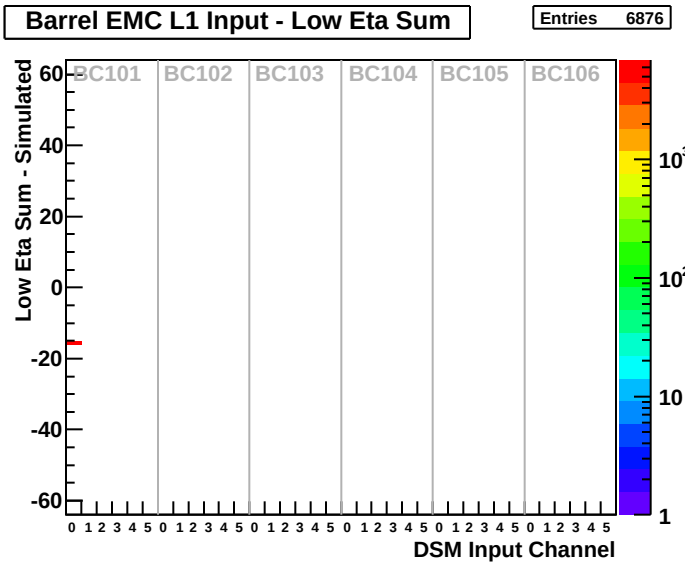

Endcap EMC L0 Input - Patch Sum
Entries 2.699999e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

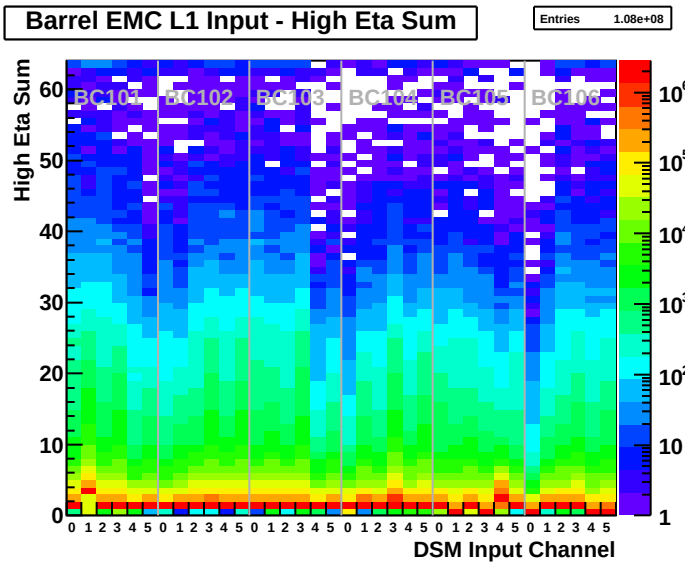

Barrel Jet Patches
Entries 5.399998e+07
JP ADC
JP ID

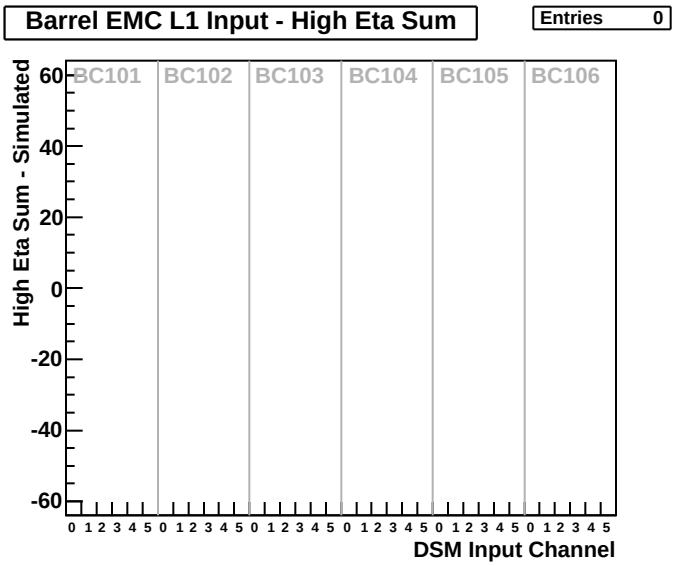

Endcap Jet Patches
Entries 1.799999e+07
JP ADC
JP ID

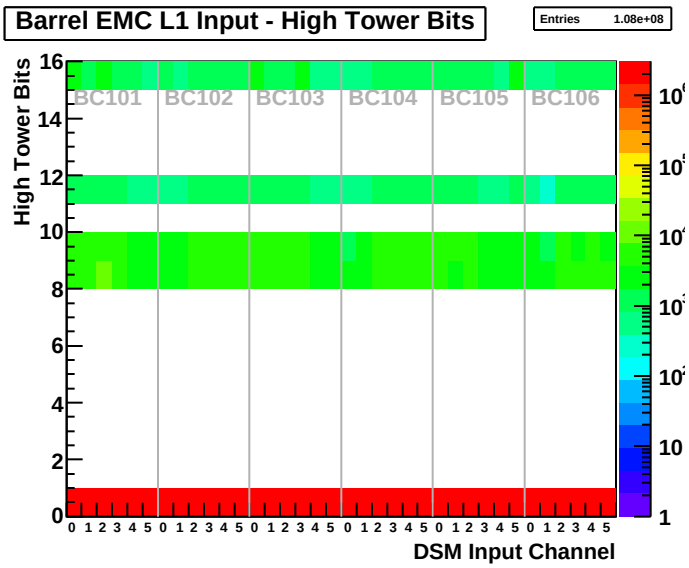

Hybrid Jet Patches
Entries 5999998
JP ADC
JP ID

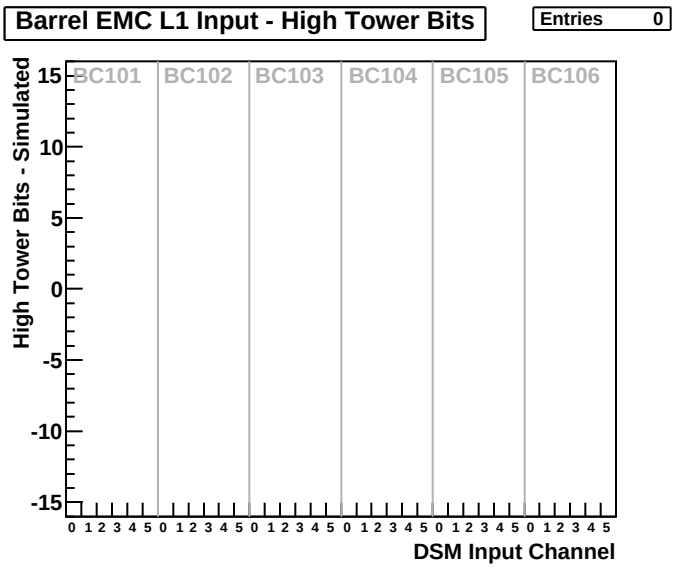

Barrel EMC L1 Input - Low Eta Sum
Entries 1.08e+08
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

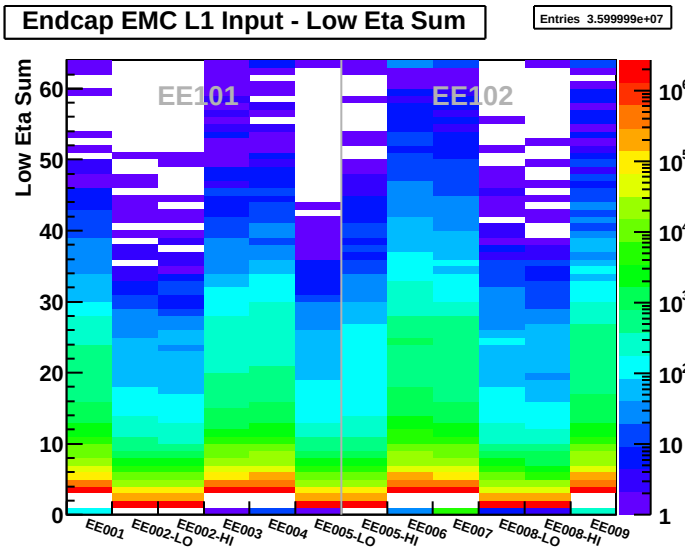

Barrel EMC L1 Input - Low Eta Sum
Entries 6876
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

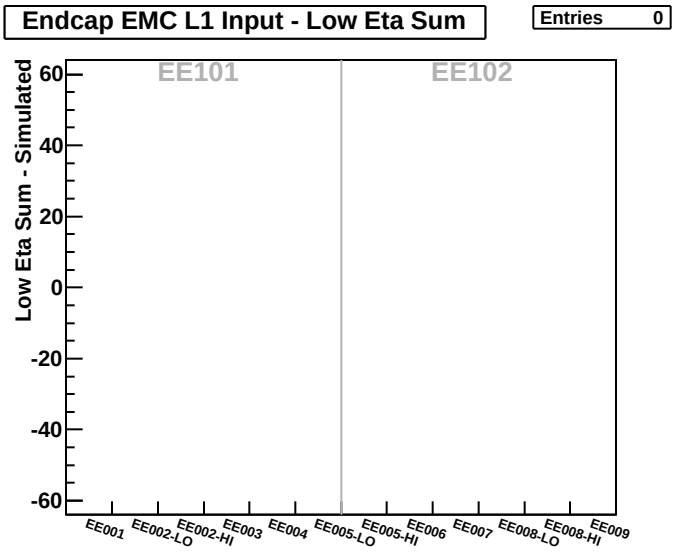

Barrel EMC L1 Input - High Eta Sum
Entries 1.08e+08
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

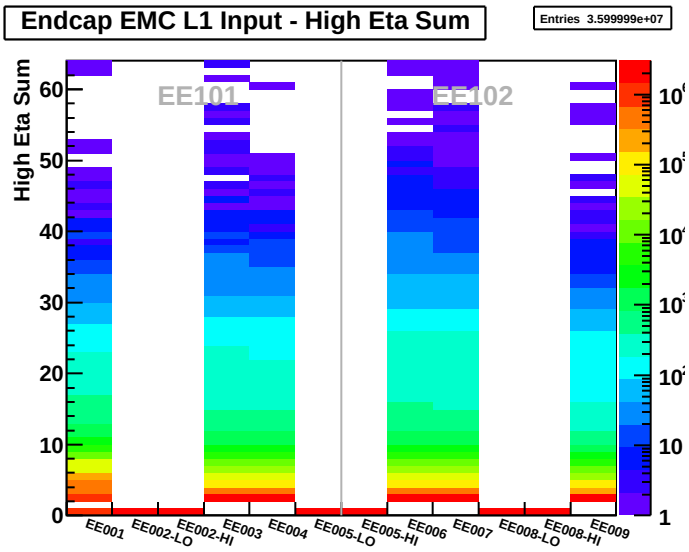

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

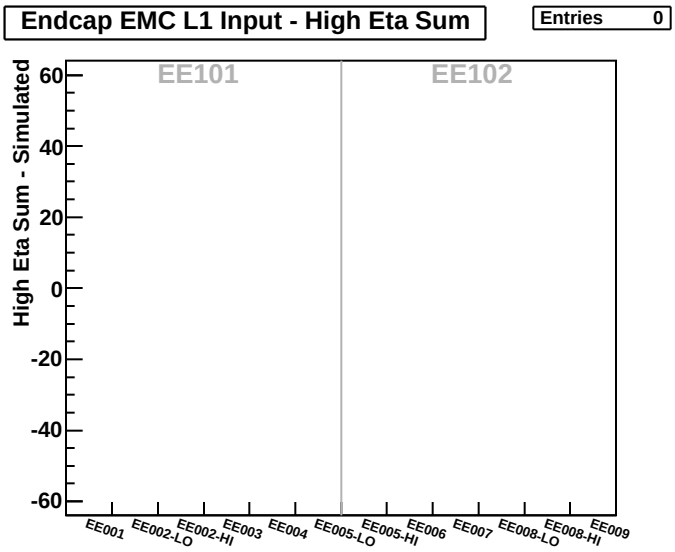

Barrel EMC L1 Input - High Tower Bits
Entries 1.08e+08
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

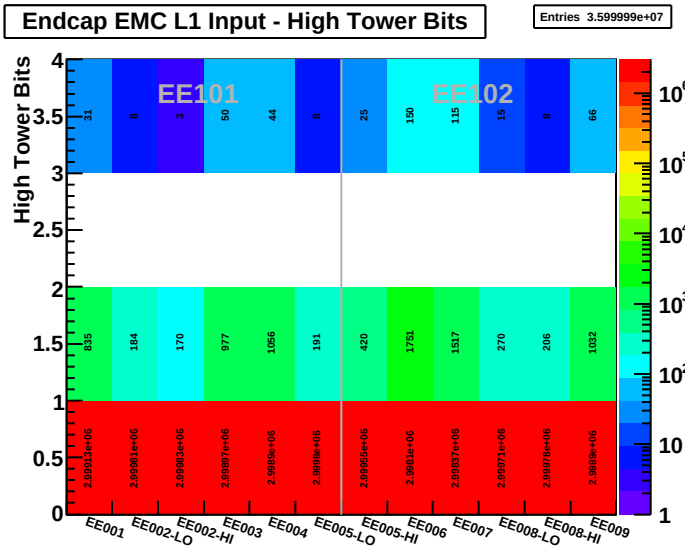

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

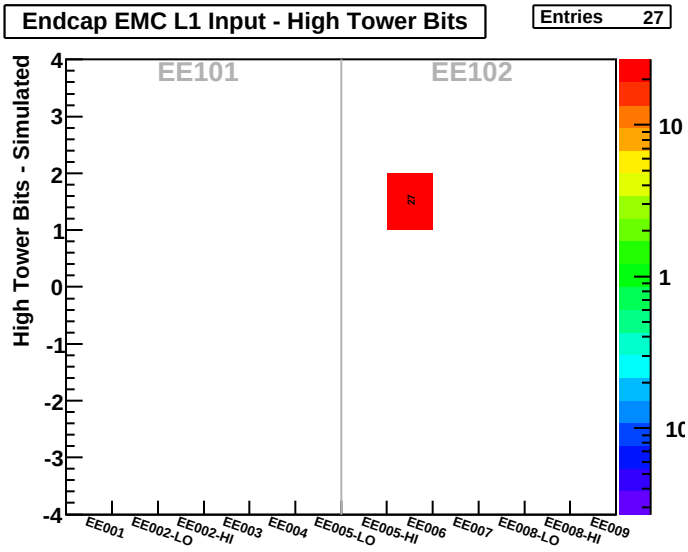

Endcap EMC L1 Input - Low Eta Sum
Entries 3.599999e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

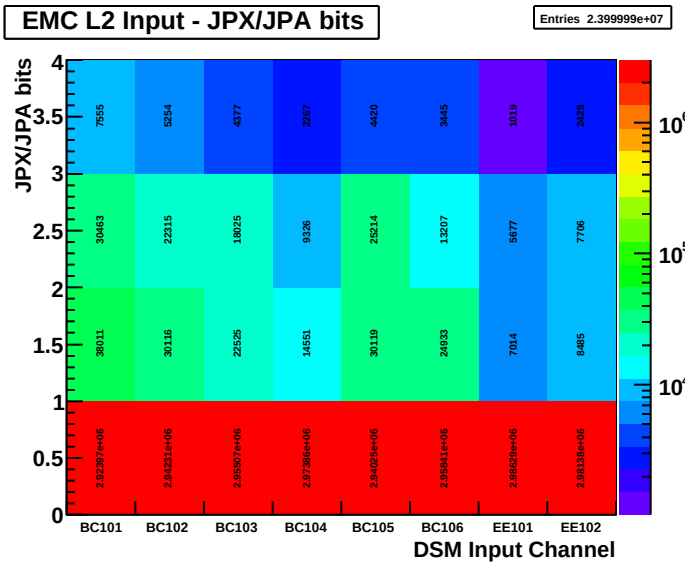

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

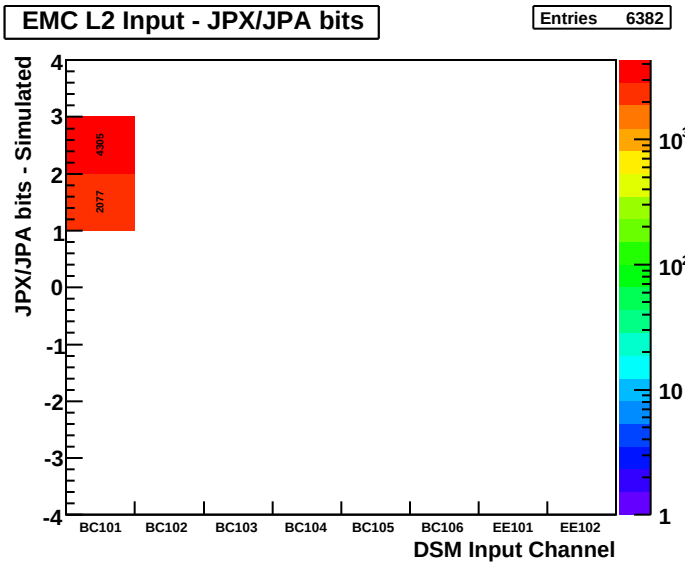

Endcap EMC L1 Input - High Eta Sum
Entries 3.599999e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

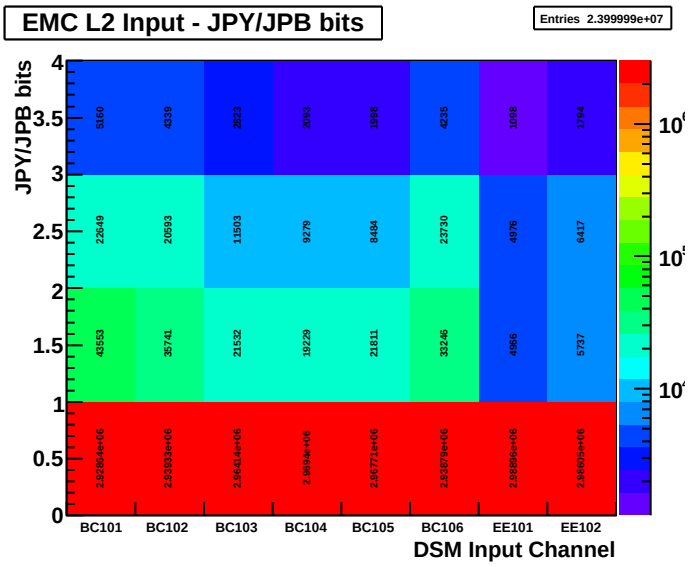

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

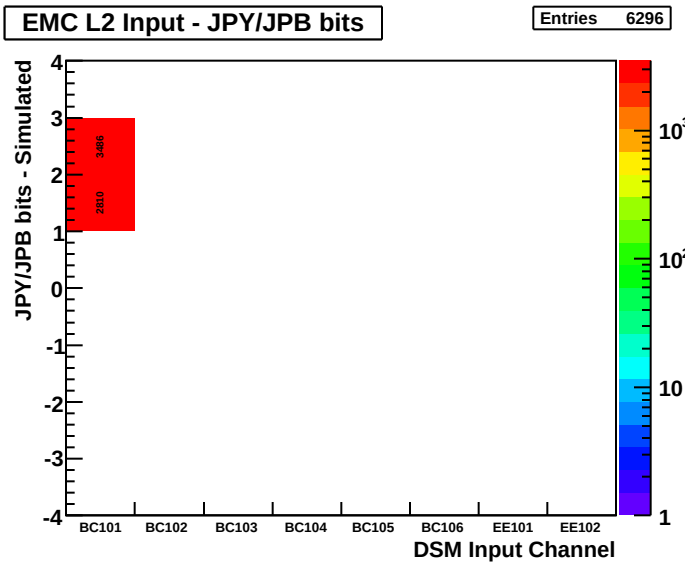

Endcap EMC L1 Input - High Tower Bits
Entries 3.599999e+07
High Tower Bits
EE101
EE102
31 8 3 50 44 8 25 150 115 15 8 66
835 184 170 977 1056 191 420 1751 1517 270 206 1032
2.99913e+06 2.99981e+06 2.99983e+06 2.99897e+06 2.9989e+06 2.9998e+06 2.99955e+06 2.9981e+06 2.99837e+06 2.99971e+06 2.99978e+06 2.9989e+06
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

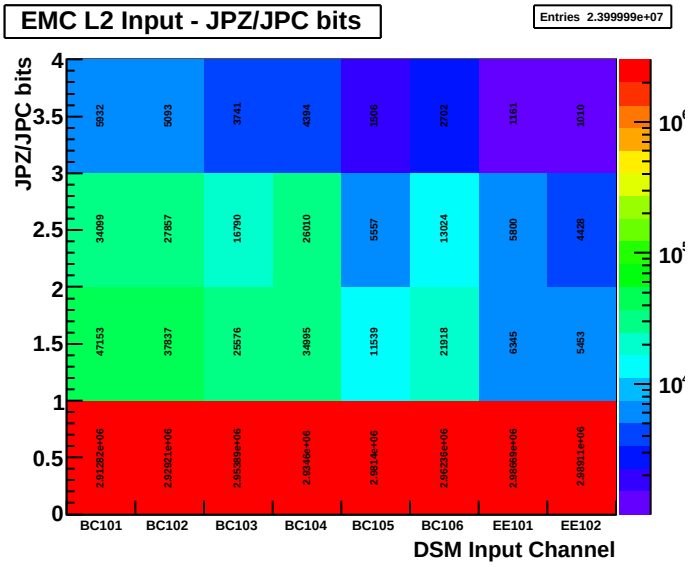

Endcap EMC L1 Input - High Tower Bits
Entries 27
High Tower Bits - Simulated
EE101
EE102
27
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

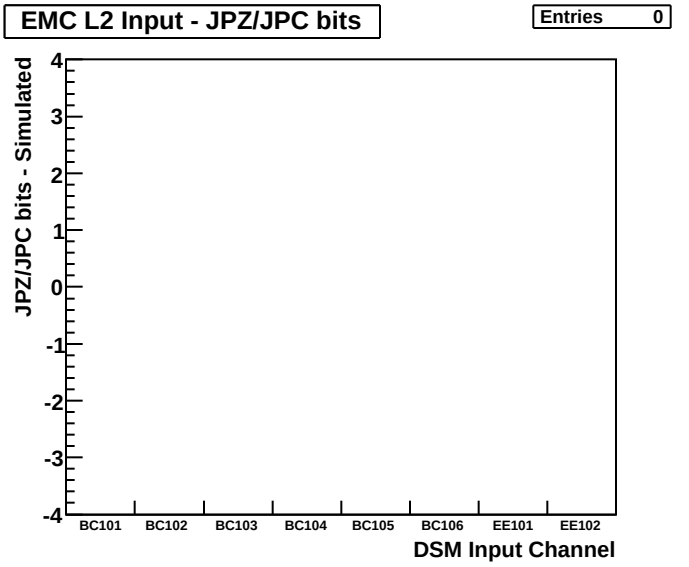

EMC L2 Input - JPX/JPA bits
Entries 2.399999e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
7555 5254 4377 2267 4420 3445 1019 2425
30463 22315 18025 9326 25214 13207 5677 7706
38011 30116 22525 14551 30119 24933 7014 8485
2.92397e+06 2.94231e+06 2.95507e+06 2.97386e+06 2.94025e+06 2.95941e+06 2.98629e+06 2.98138e+06

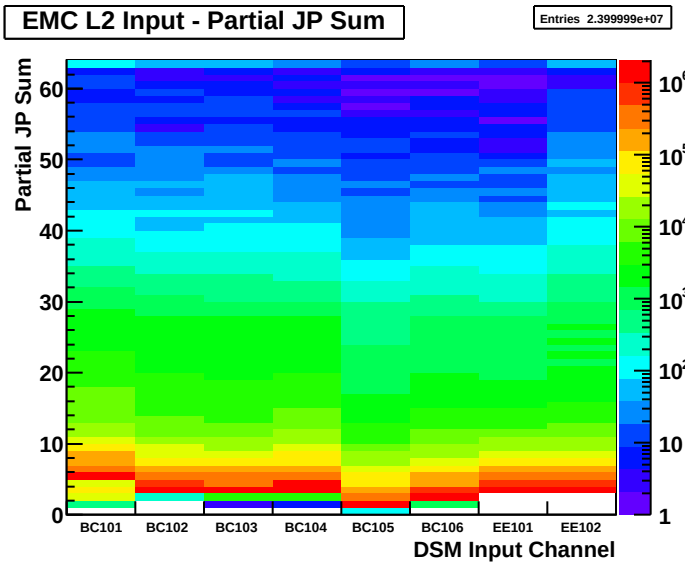

EMC L2 Input - JPX/JPA bits
Entries 6382
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4305
2077

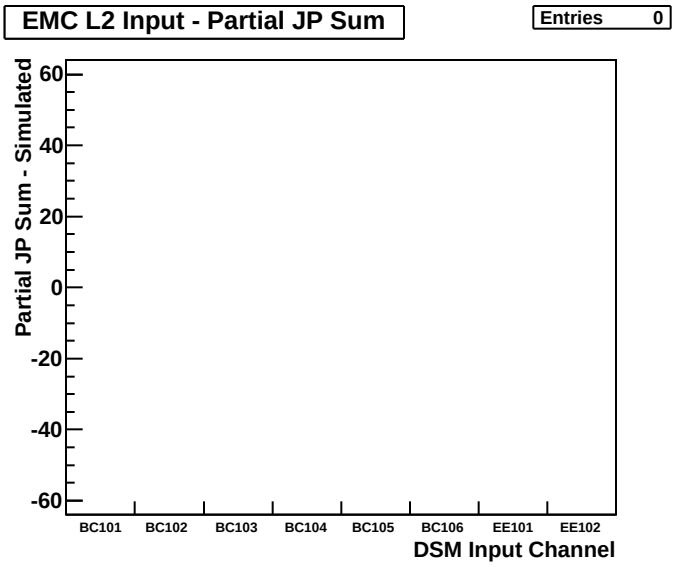

EMC L2 Input - JPY/JPB bits
Entries 2.399999e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5160 4339 2823 2093 1998 4235 1098 1794
22649 20593 11503 9279 8484 23730 4976 6417
43553 35741 21532 19229 21811 33246 4966 5737
2.92864e+06 2.93933e+06 2.96414e+06 2.96594e+06 2.96771e+06 2.93879e+06 2.98896e+06 2.98605e+06

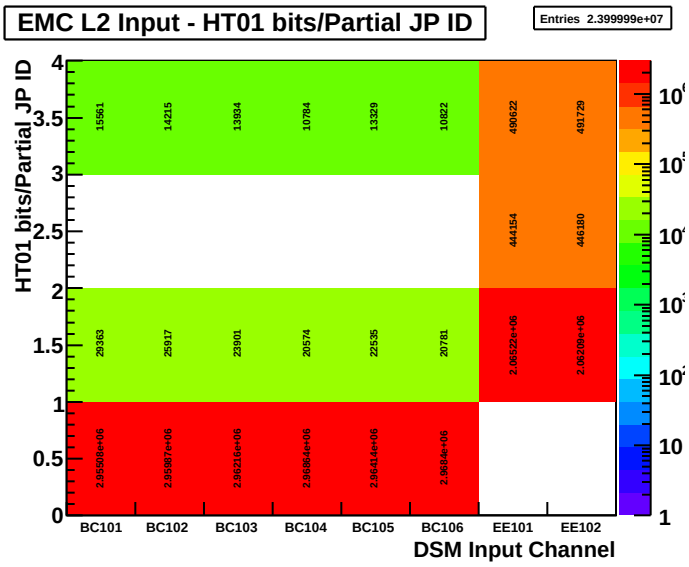

EMC L2 Input - JPY/JPB bits
Entries 6296
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3486
2810

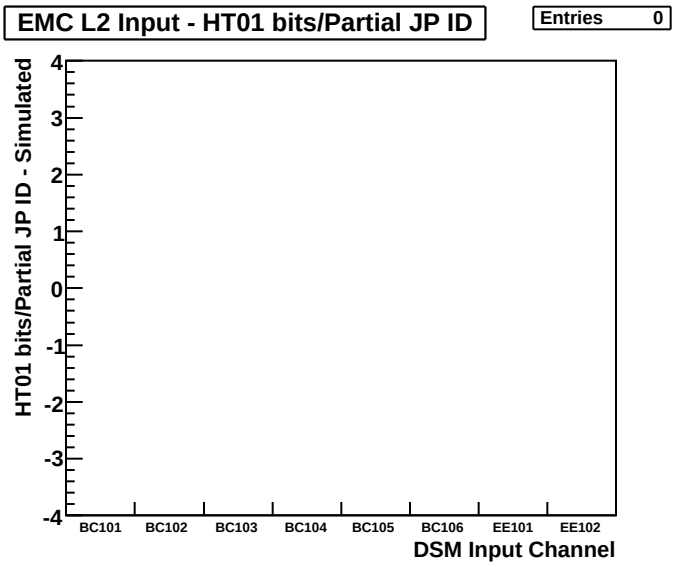

EMC L2 Input - JPZ/JPC bits
Entries 2.399999e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5932 5093 3741 4394 1506 2702 1161 1010
34099 27857 16790 26010 5557 13024 5800 4428
47153 37837 25576 34095 11539 21918 6345 5463
2.91282e+06 2.92321e+06 2.95389e+06 2.9346e+06 2.9814e+06 2.96236e+06 2.98669e+06 2.98931e+06

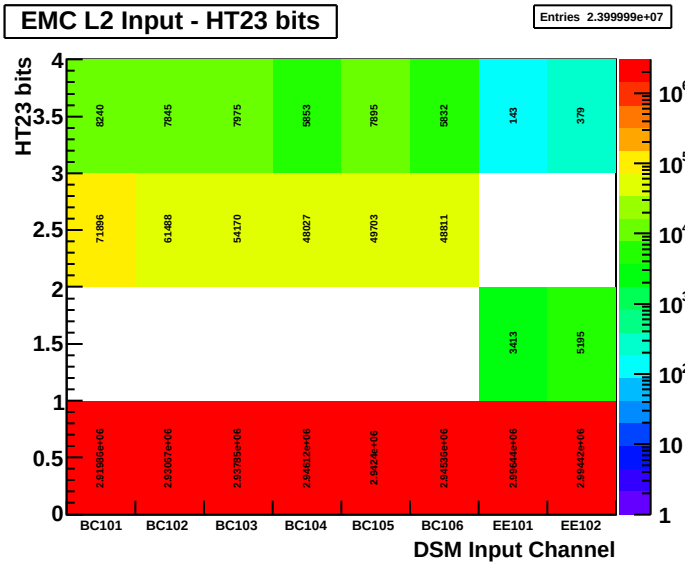

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

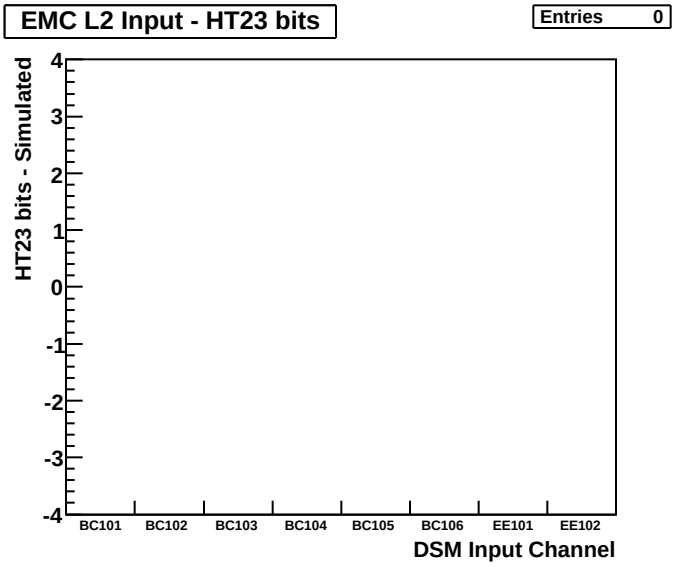

EMC L2 Input - Partial JP Sum
Entries 2.399999e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

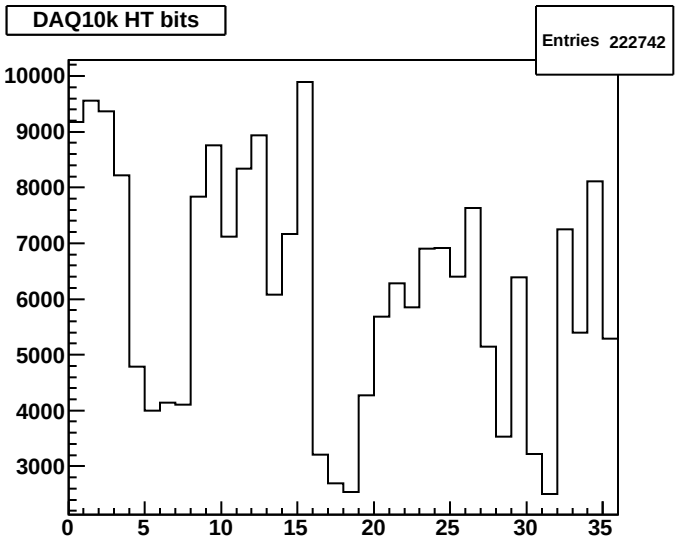

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

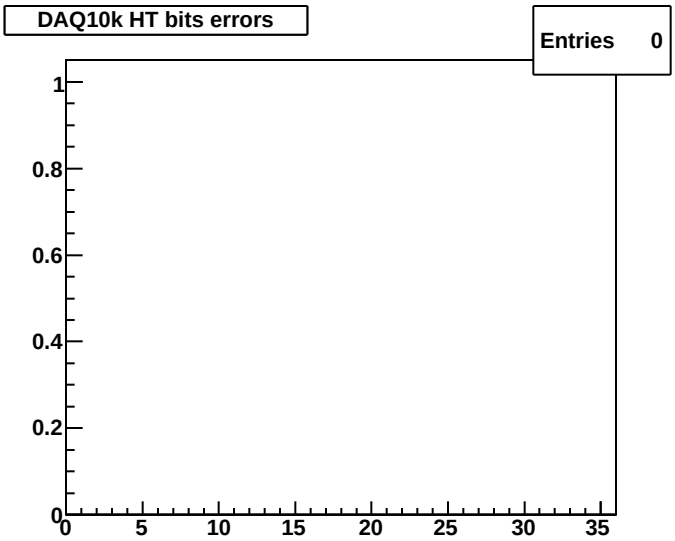

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2.399999e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

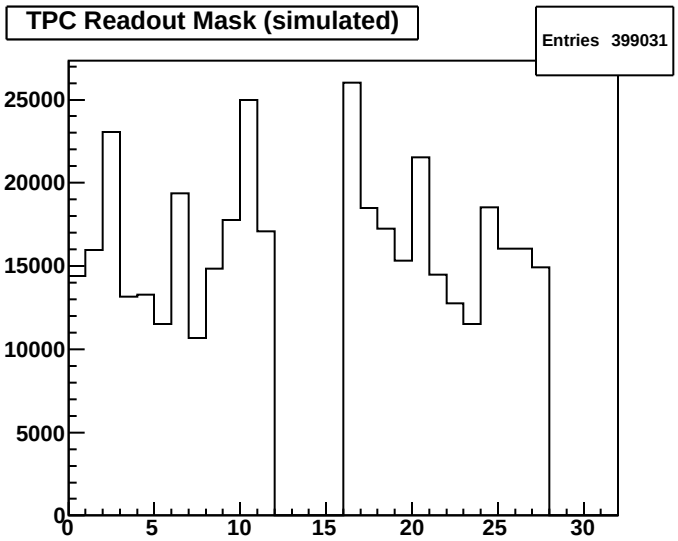

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

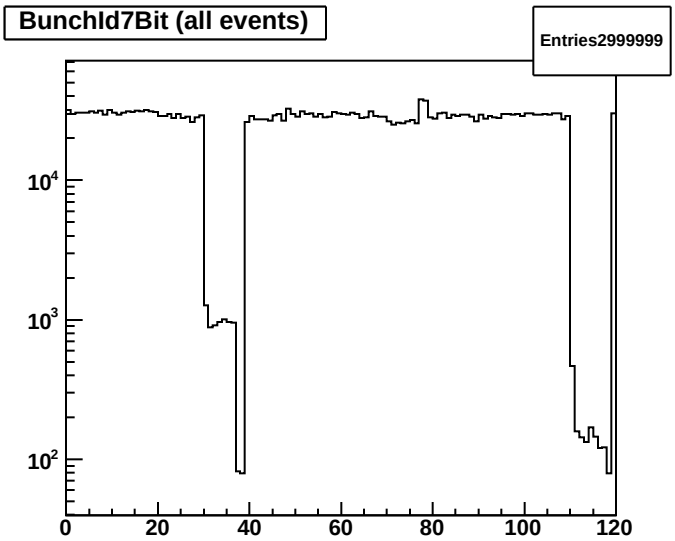

EMC L2 Input - HT23 bits
Entries 2.399999e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

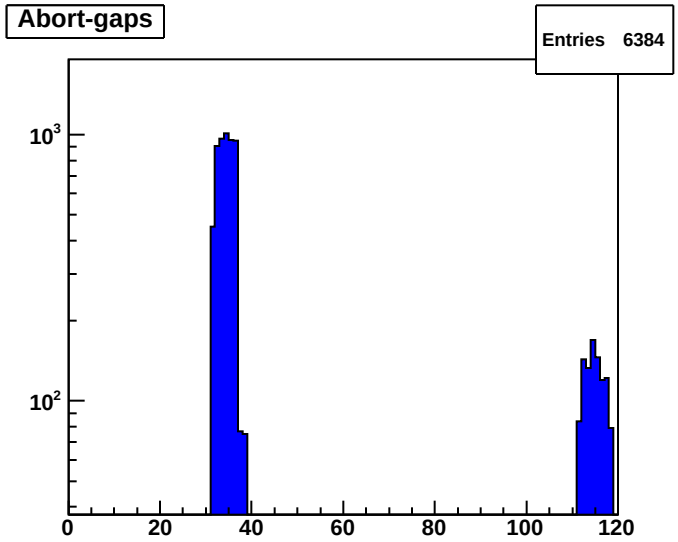

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

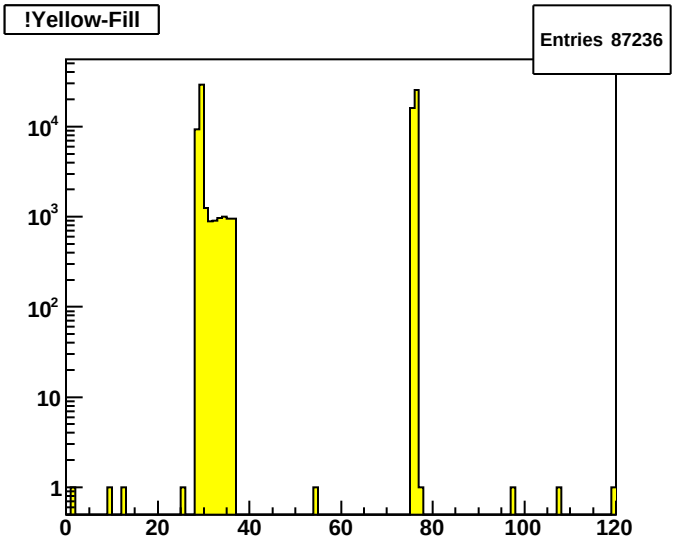

DAQ10k HT bits
Entries 222742

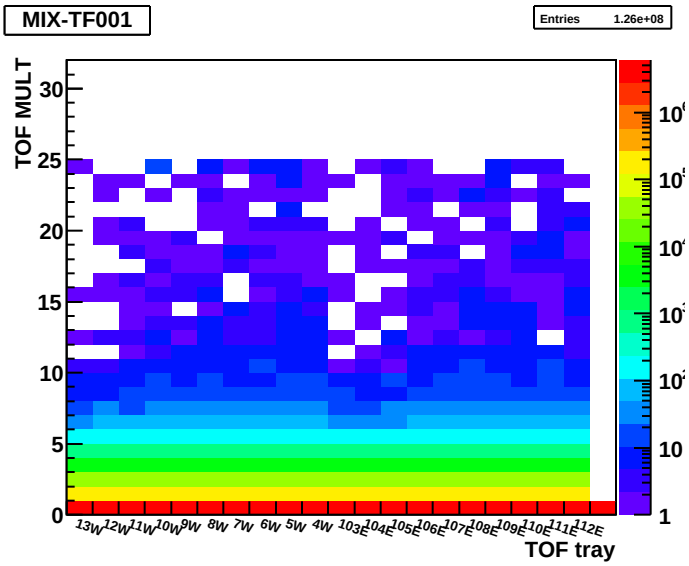

DAQ10k HT bits errors
Entries 0

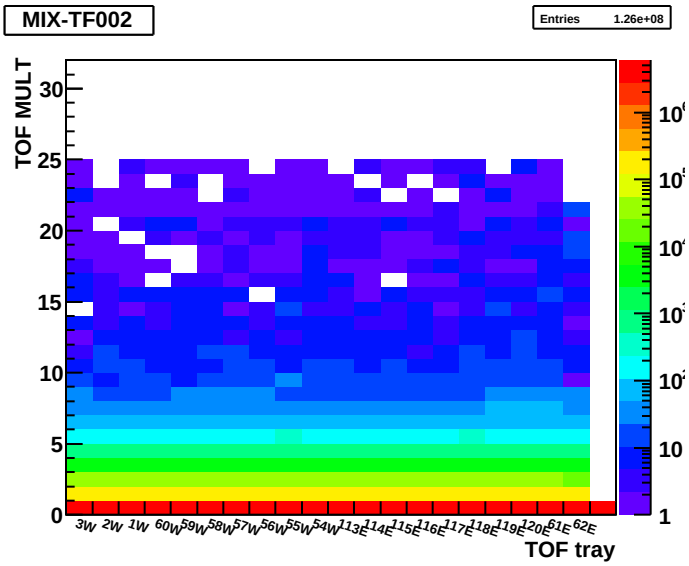

TPC Readout Mask (simulated)
Entries 399031

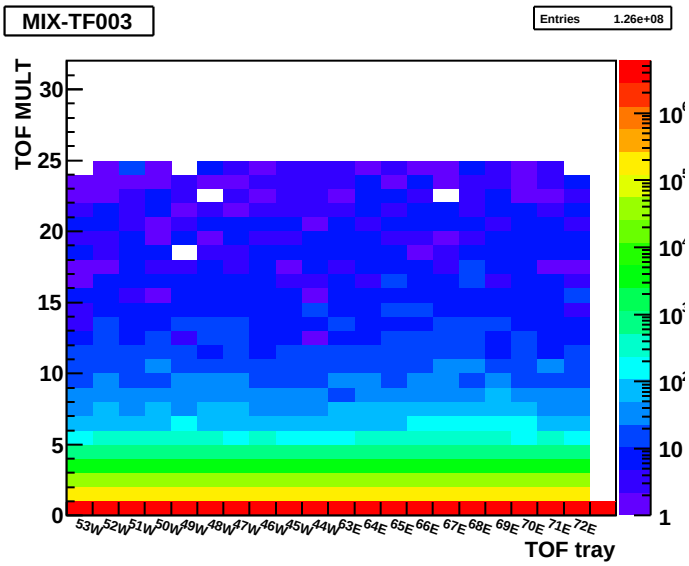

BunchId7Bit (all events)
Entries2999999

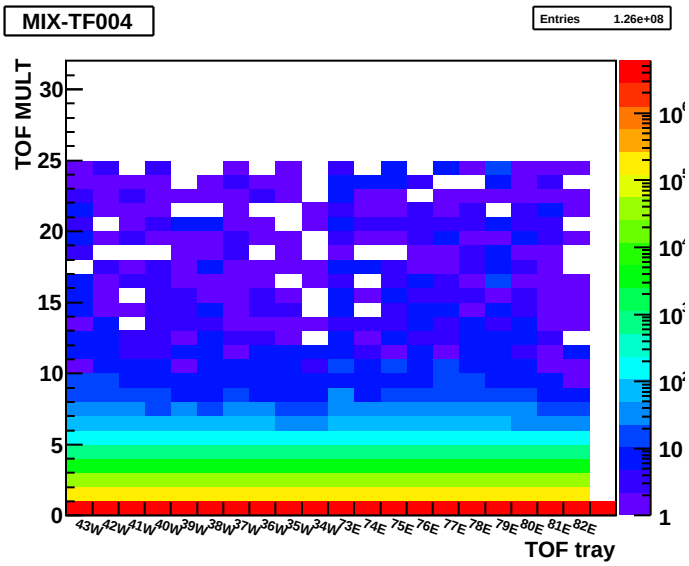

Abort-gaps
Entries 6384

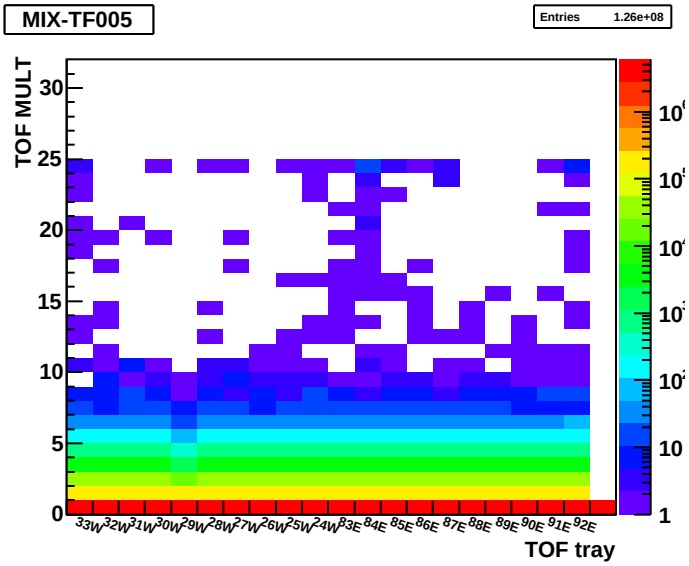

!Yellow-Fill
Entries 87236

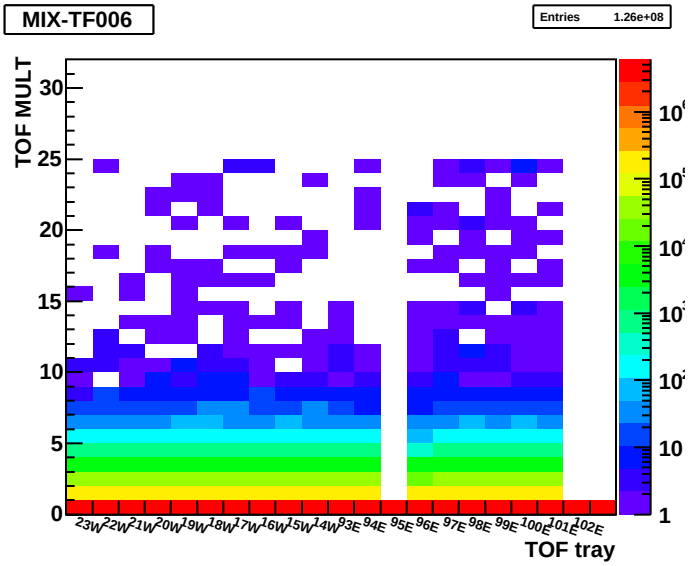

MIX-TF001
Entries 1.26e+08
TOF MULT
TOF tray

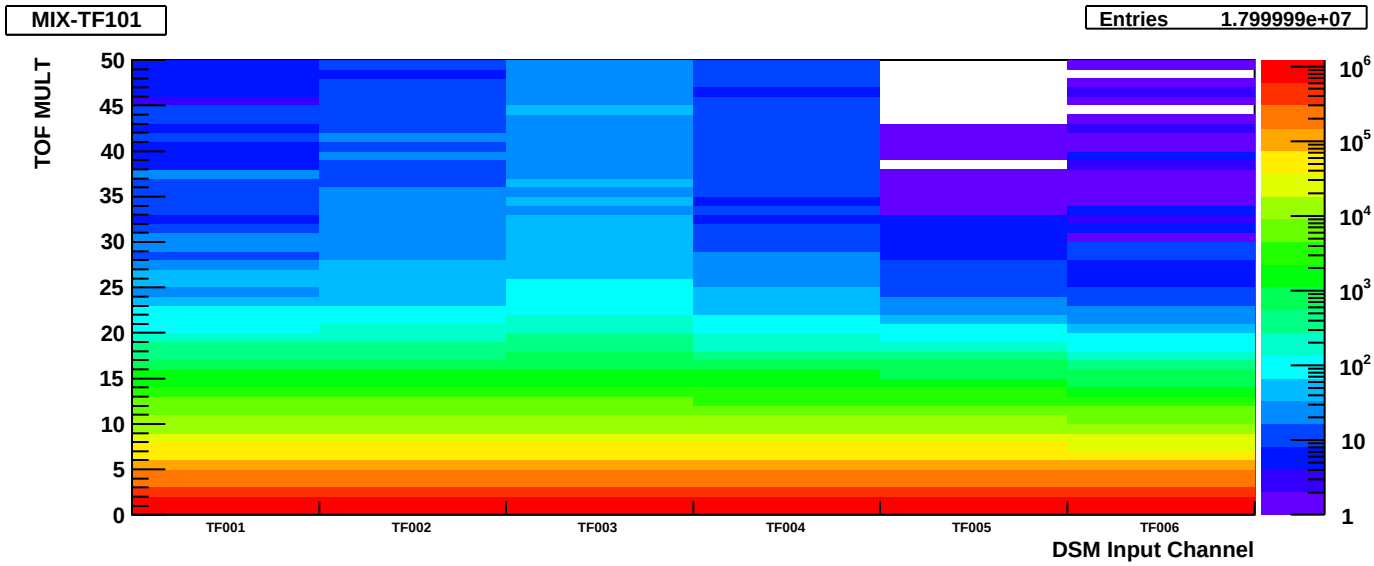

MIX-TF002
Entries 1.26e+08
TOF MULT
TOF tray

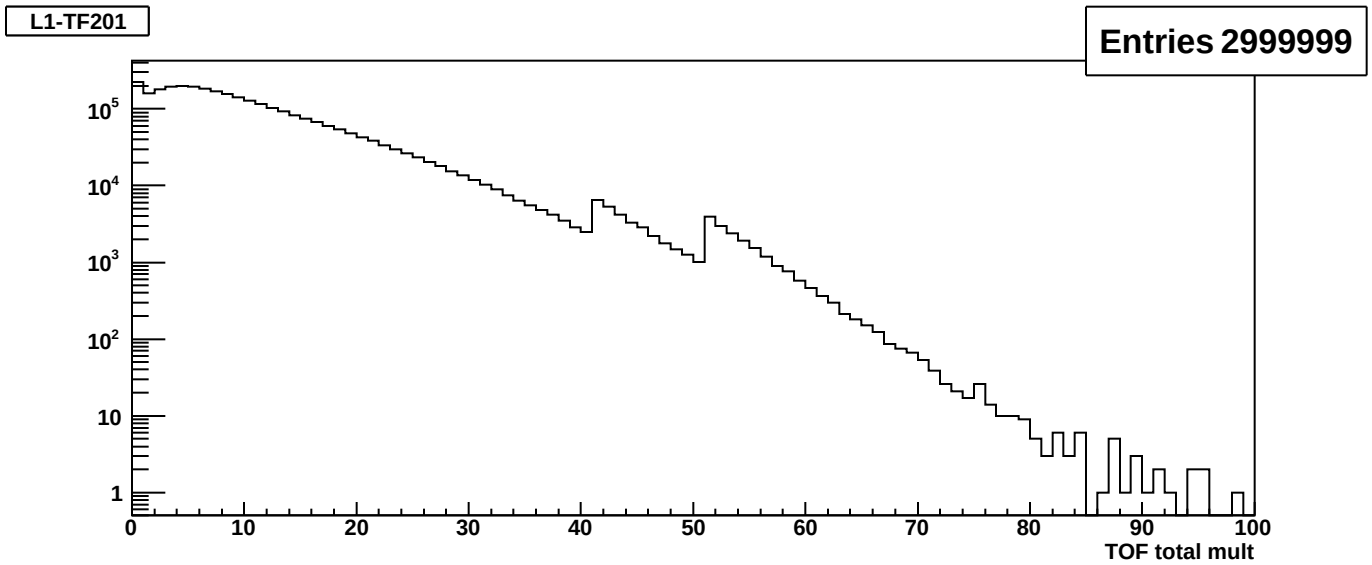

MIX-TF003
Entries 1.26e+08
TOF MULT
TOF tray

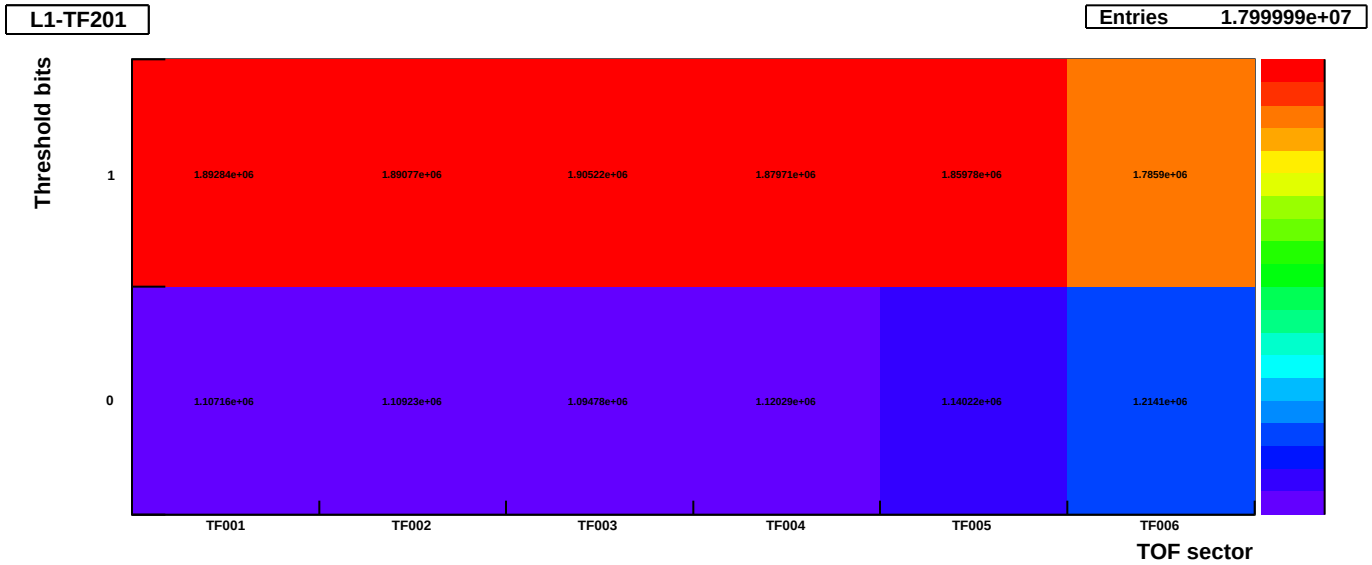

MIX-TF004
Entries 1.26e+08
TOF MULT
TOF tray

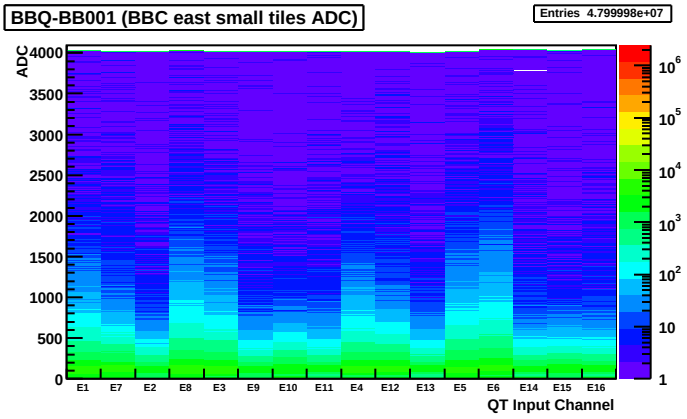

MIX-TF005
Entries 1.26e+08
TOF MULT
TOF tray

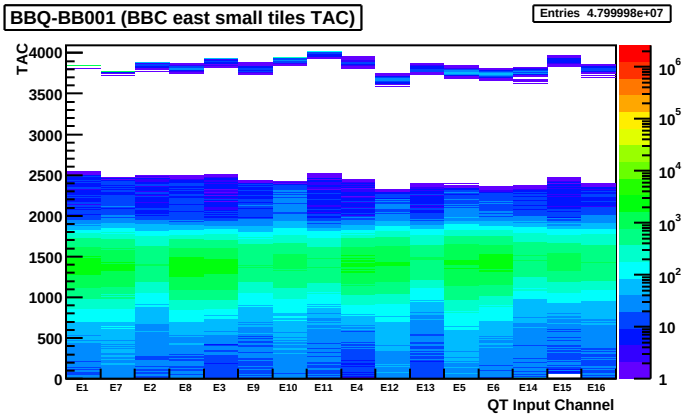

MIX-TF006
Entries 1.26e+08
TOF MULT
TOF tray

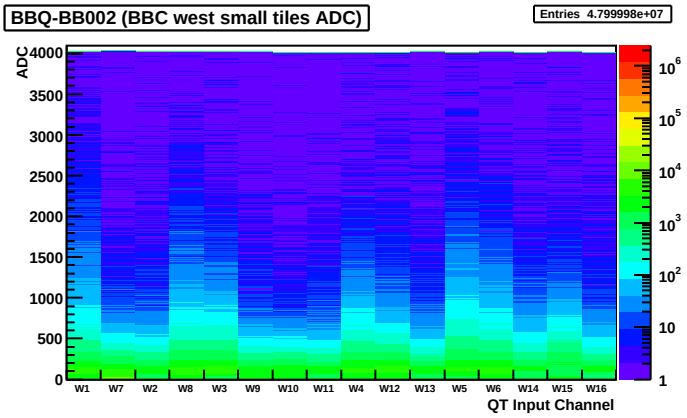

MIX-TF101
Entries 1.799999e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^6
10^5
10^4
10^3
10^2
10
1

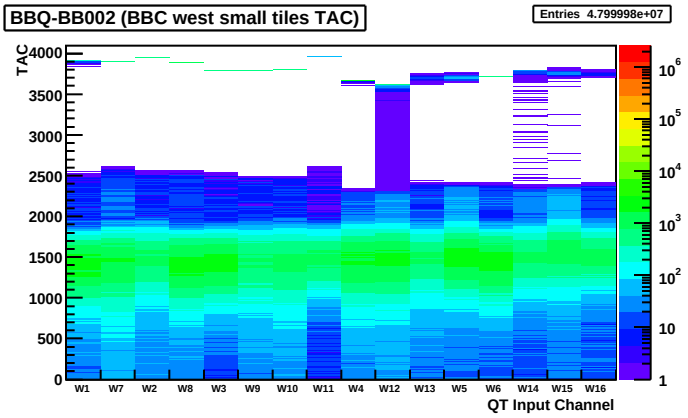

L1-TF201
Entries 2999999
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

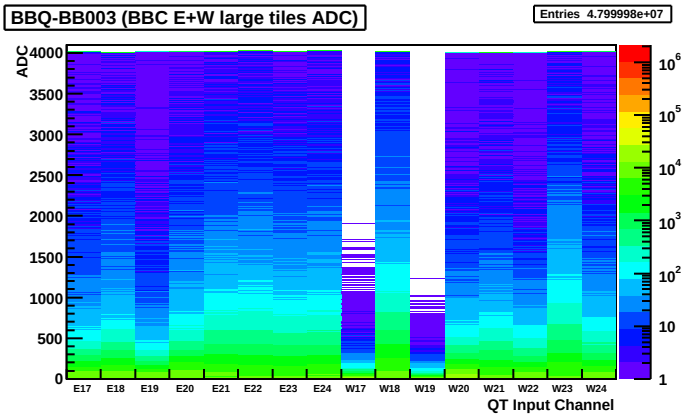

L1-TF201
Entries 1.799999e+07
Threshold bits
1
0
1.89284e+06
1.89077e+06
1.90522e+06
1.87971e+06
1.85978e+06
1.7859e+06
1.10716e+06
1.10923e+06
1.09478e+06
1.12029e+06
1.14022e+06
1.2141e+06
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

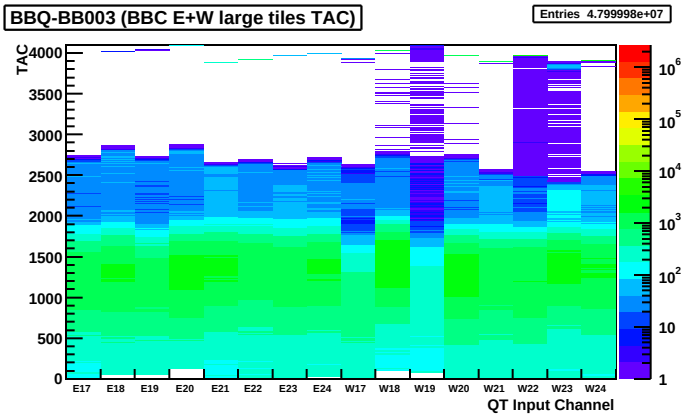

BBQ-BB001 (BBC east small tiles ADC)
Entries 4.799998e+07
ADC
QT Input Channel

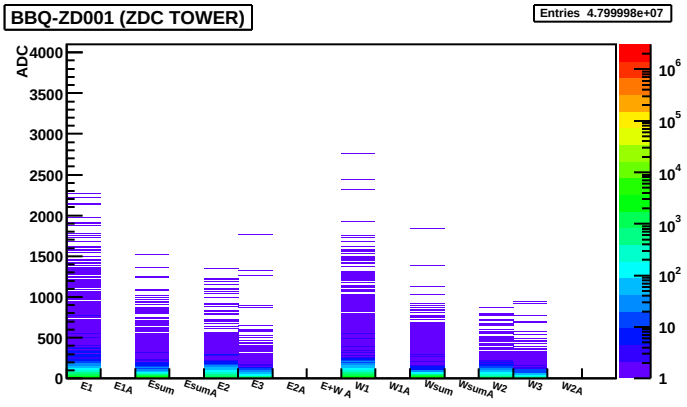

BBQ-BB001 (BBC east small tiles TAC)
Entries 4.799998e+07
TAC
QT Input Channel

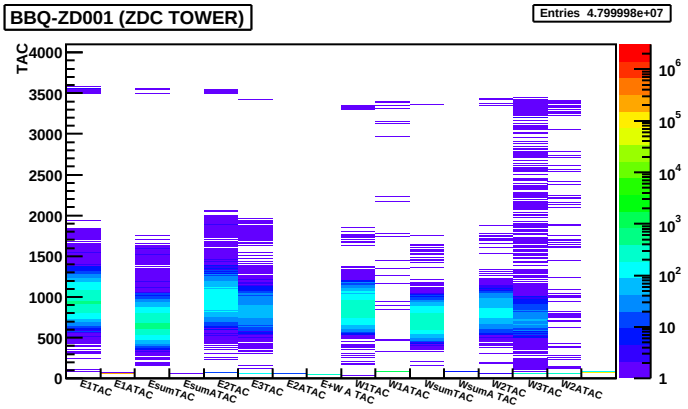

BBQ-BB002 (BBC west small tiles ADC)
Entries 4.799998e+07
ADC
QT Input Channel

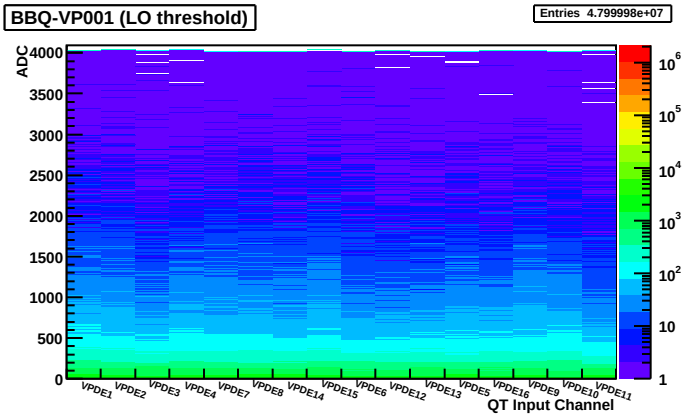

BBQ-BB002 (BBC west small tiles TAC)
Entries 4.799998e+07
TAC
QT Input Channel

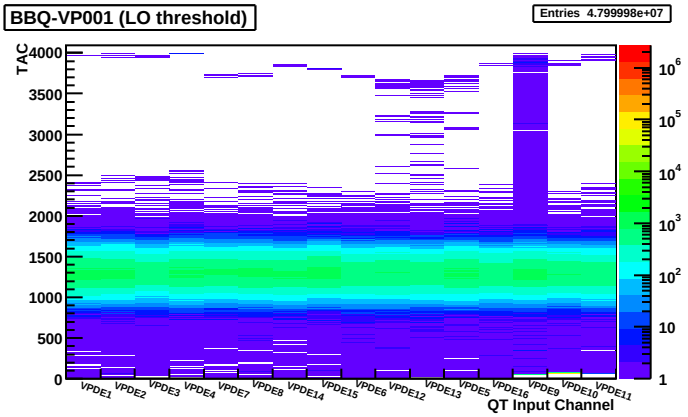

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4.799998e+07
ADC
QT Input Channel

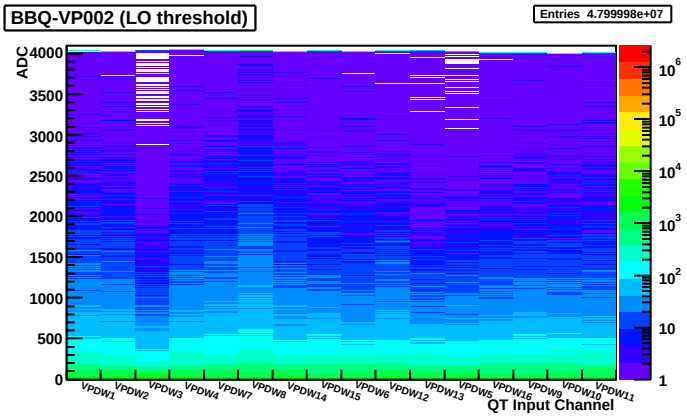

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4.799998e+07
TAC
QT Input Channel

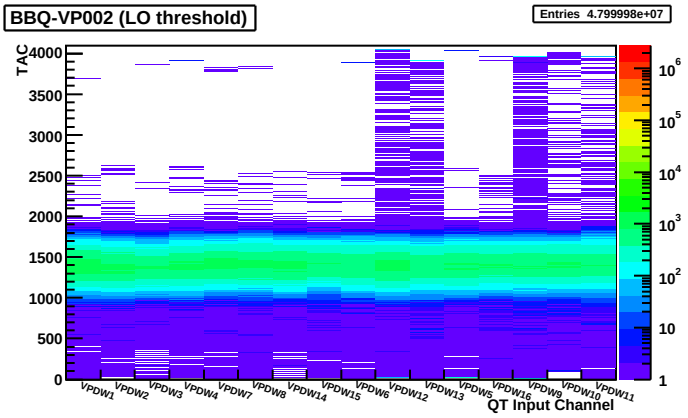

BBQ-ZD001 (ZDC TOWER)
Entries 4.799998e+07
ADC

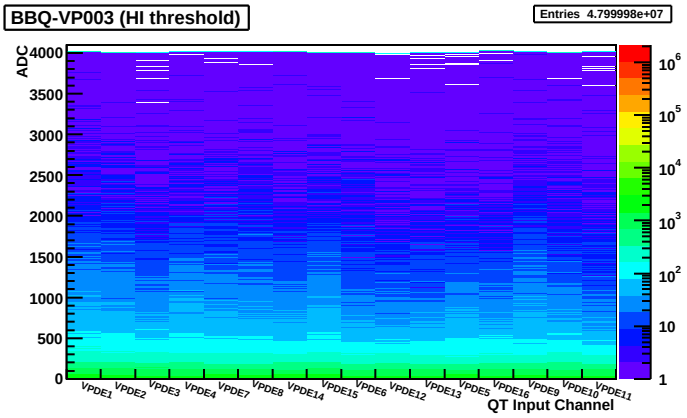

BBQ-ZD001 (ZDC TOWER)
Entries 4.799998e+07
TAC

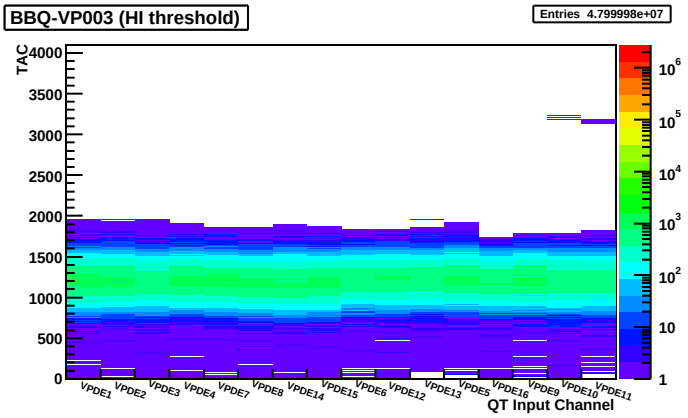

BBQ-VP001 (LO threshold)
Entries 4.799998e+07
ADC
QT Input Channel

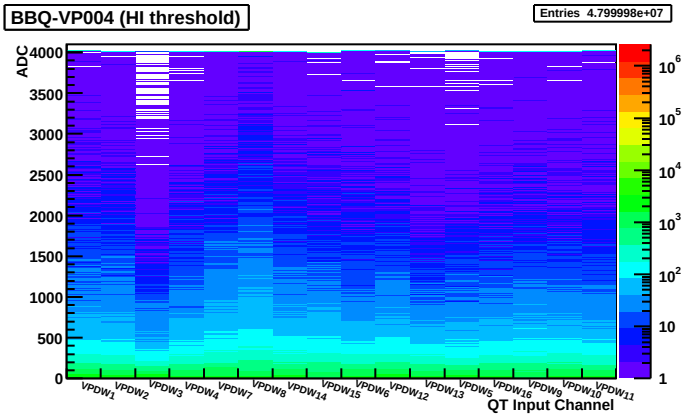

BBQ-VP001 (LO threshold)
Entries 4.799998e+07
TAC
QT Input Channel

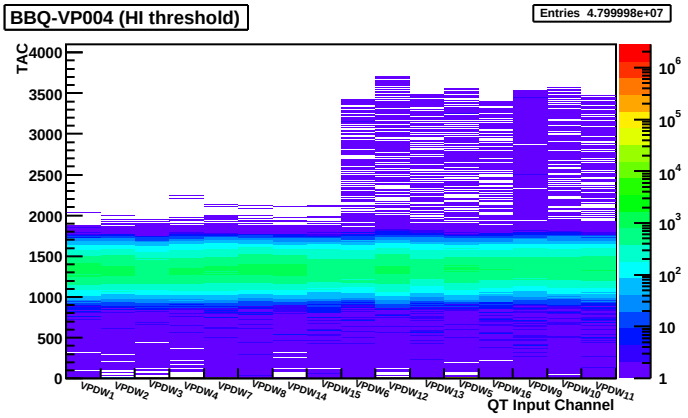

BBQ-VP002 (LO threshold)
Entries 4.799998e+07
ADC
QT Input Channel

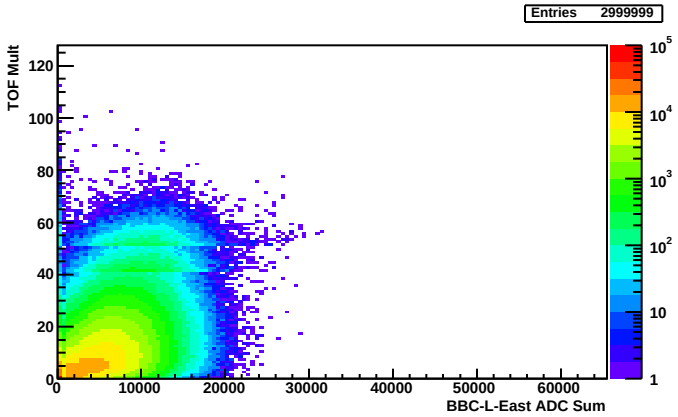

BBQ-VP002 (LO threshold)
Entries 4.799998e+07
TAC
QT Input Channel

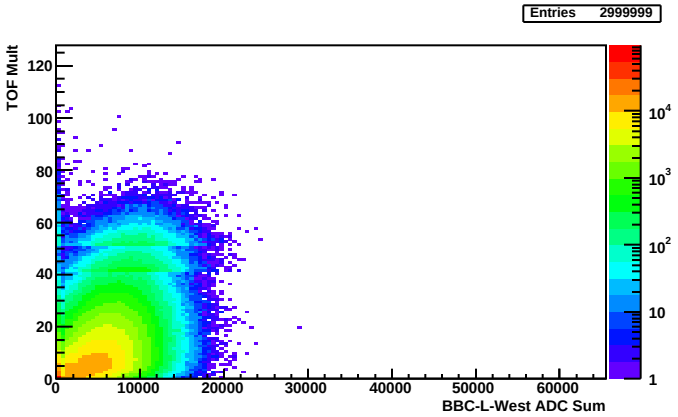

BBQ-VP003 (HI threshold)
Entries 4.799998e+07
ADC
QT Input Channel

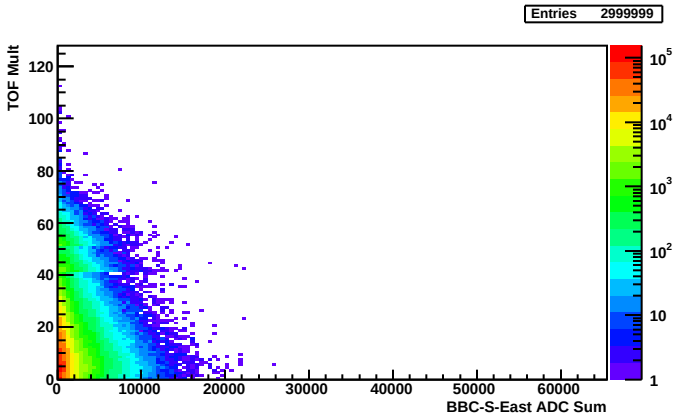

BBQ-VP003 (HI threshold)
Entries 4.799998e+07
TAC
QT Input Channel

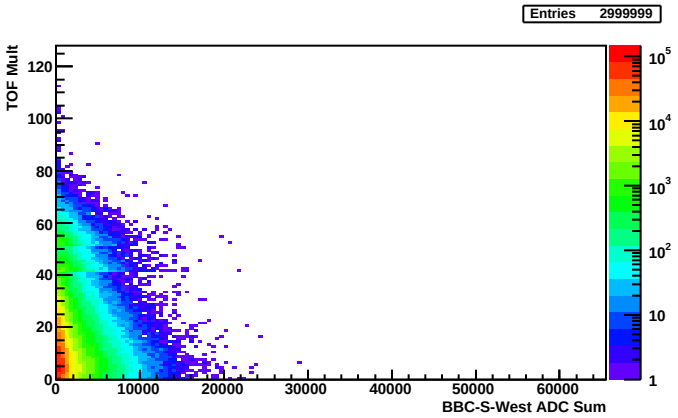

BBQ-VP004 (HI threshold)
Entries 4.799998e+07
ADC
QT Input Channel

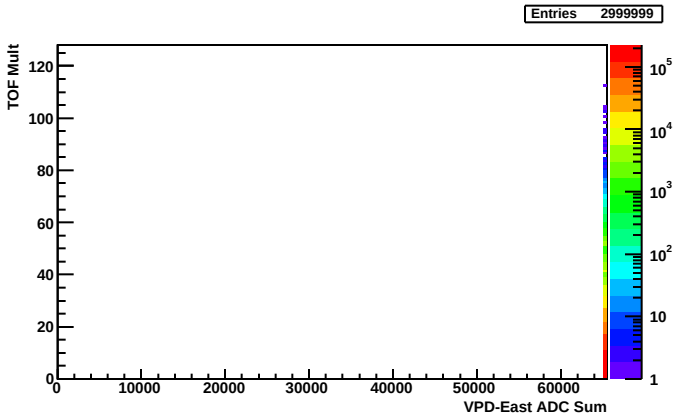

BBQ-VP004 (HI threshold)
Entries 4.799998e+07
TAC
QT Input Channel

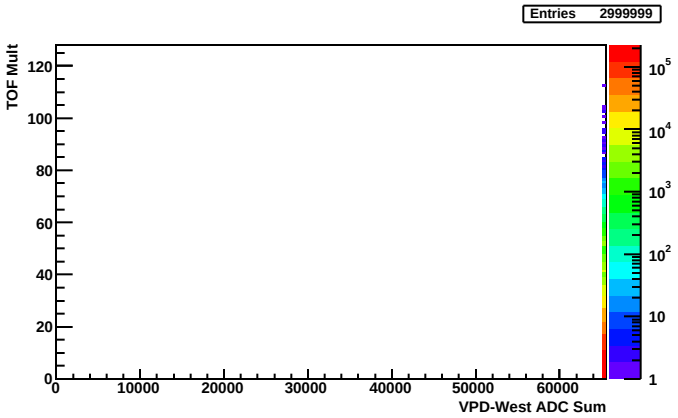

Entries 2999999
TOF Mult
BBC-L-East ADC Sum

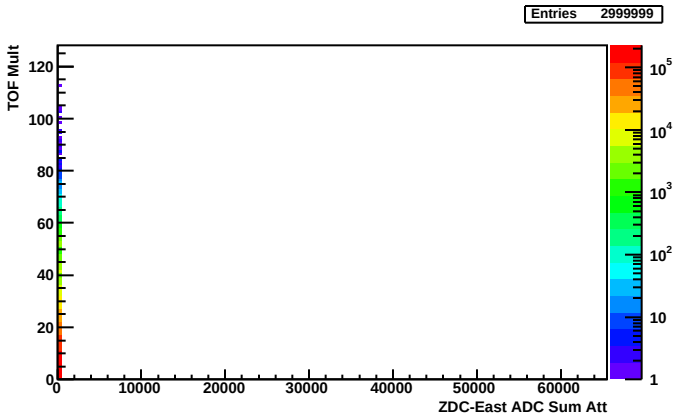

Entries 2999999
TOF Mult
BBC-L-West ADC Sum

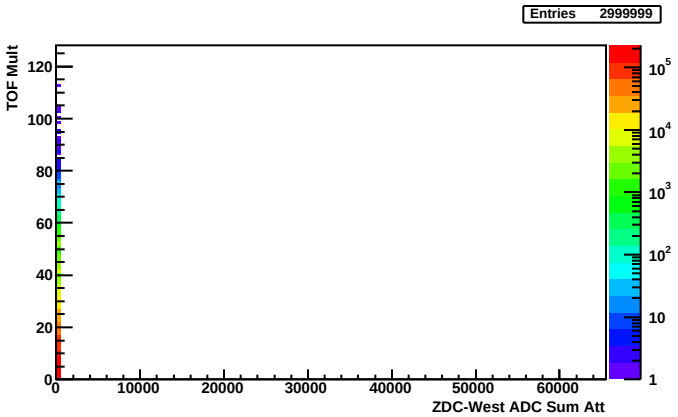

Entries 2999999
TOF Mult
BBC-S-East ADC Sum

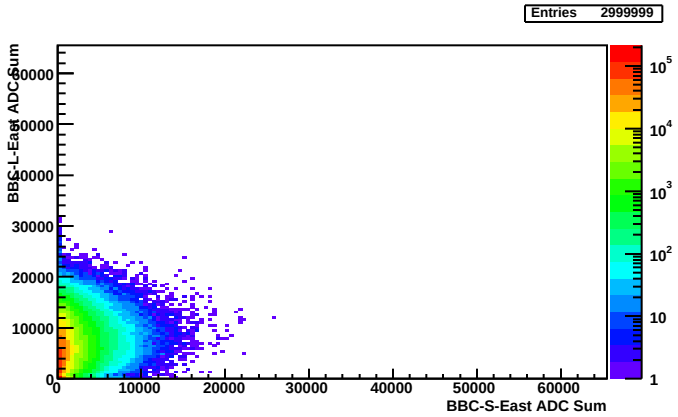

Entries 2999999
TOF Mult
BBC-S-West ADC Sum

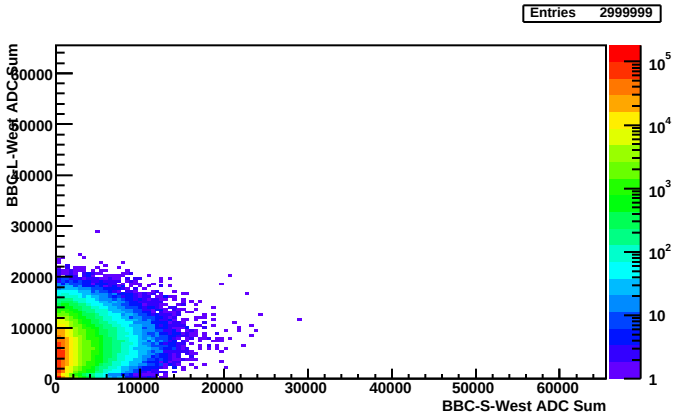

Entries 2999999
TOF Mult
VPD-East ADC Sum

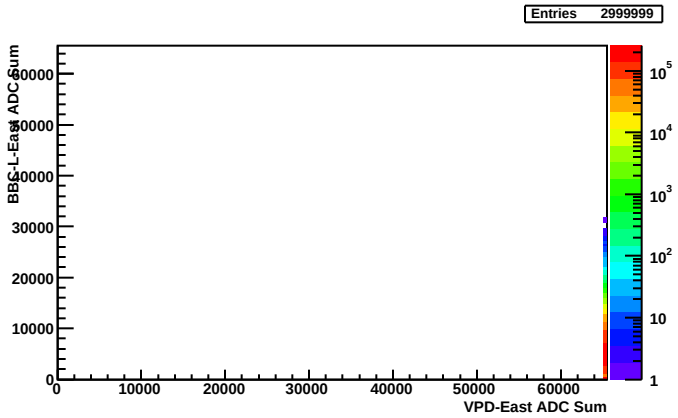

Entries 2999999
TOF Mult
VPD-West ADC Sum

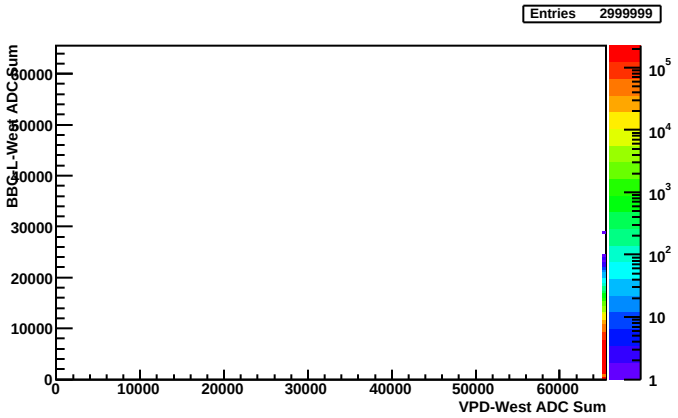

Entries 2999999
TOF Mult
ZDC-East ADC Sum Att

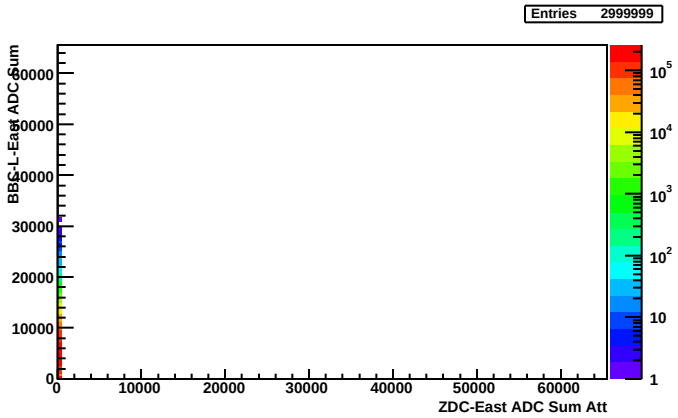

Entries 2999999
TOF Mult
ZDC-West ADC Sum Att

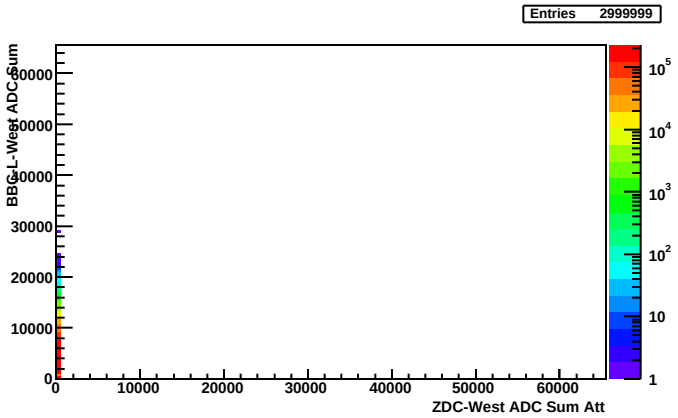

Entries 2999999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

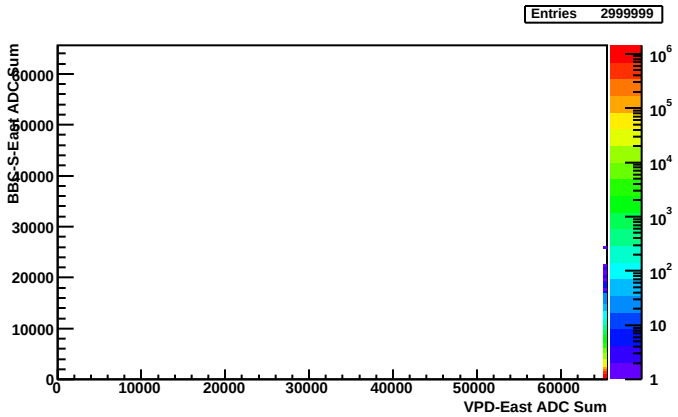

Entries 2999999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

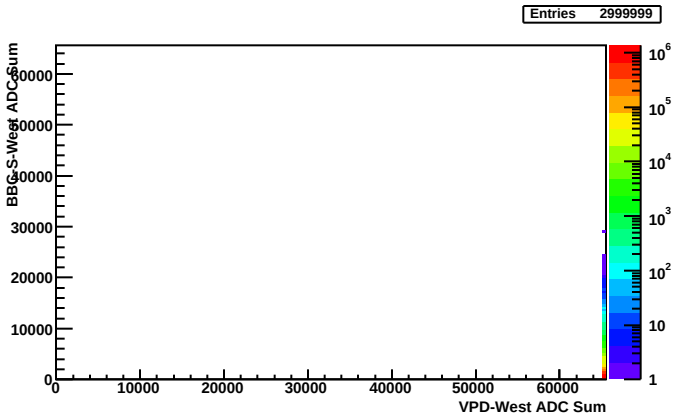

Entries 2999999
BBC-L-East ADC Sum
VPD-East ADC Sum

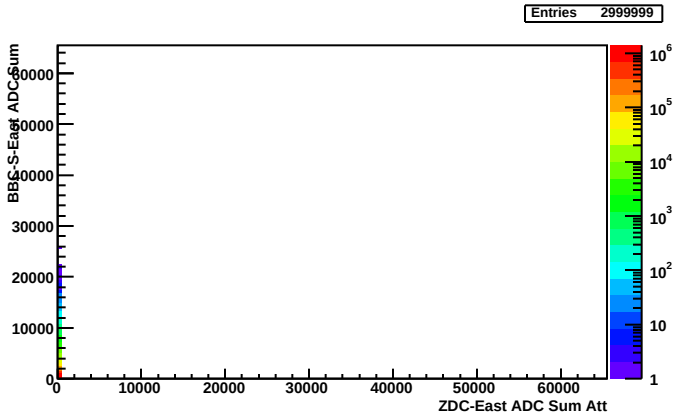

Entries 2999999
BBC-L-West ADC Sum
VPD-West ADC Sum

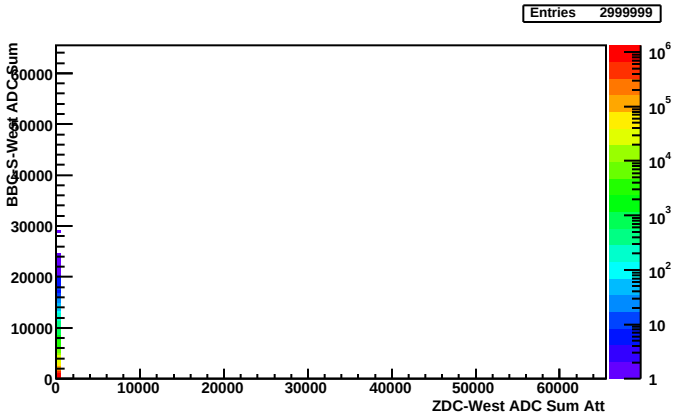

Entries 2999999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

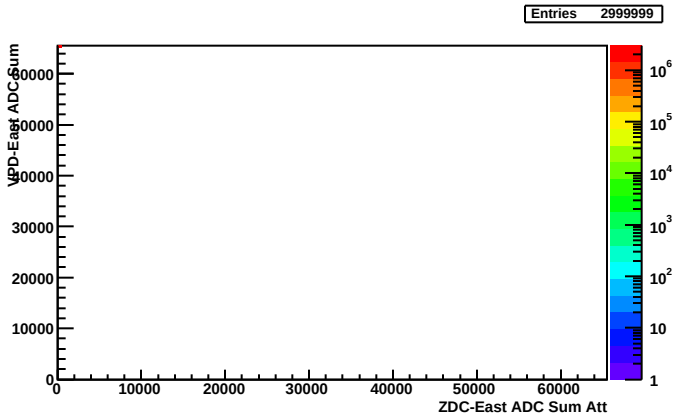

Entries 2999999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

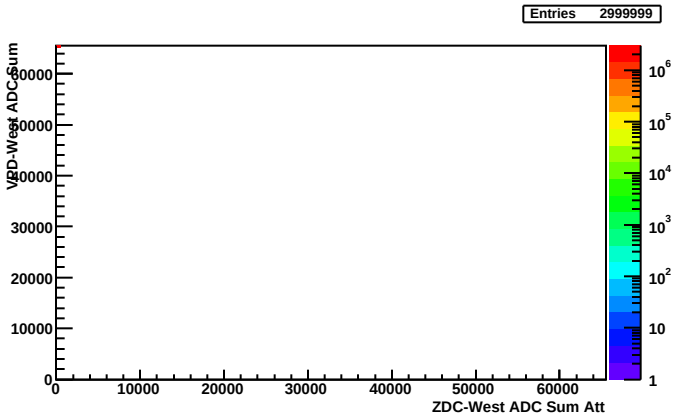

Entries 2999999
BBC-S-East ADC Sum
VPD-East ADC Sum

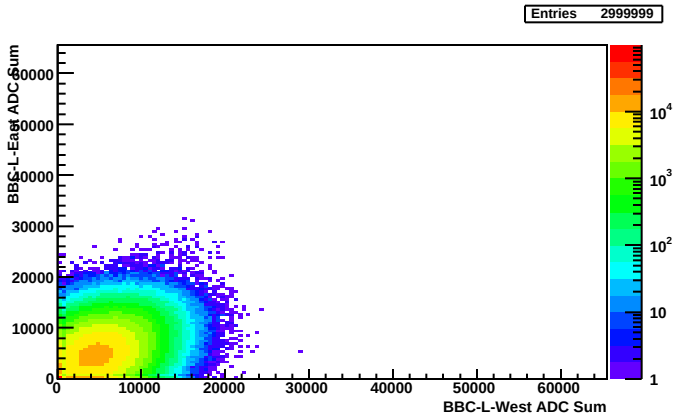

Entries 2999999
BBC-S-West ADC Sum
VPD-West ADC Sum

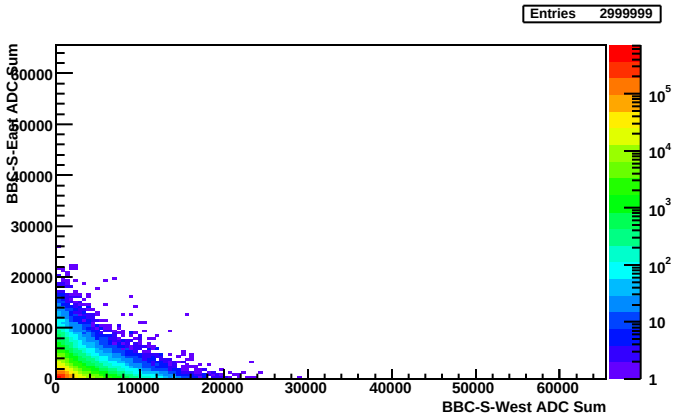

Entries 2999999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

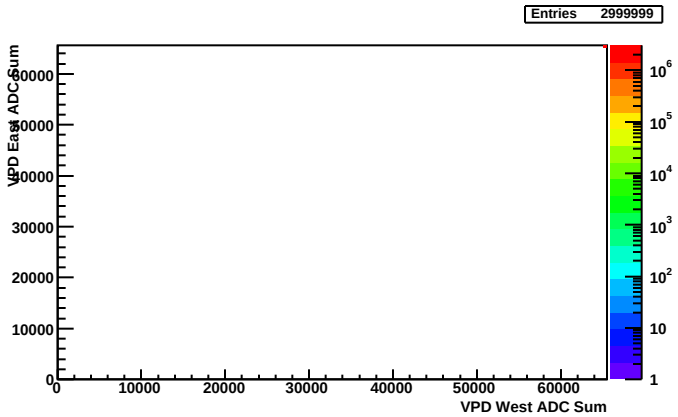

Entries 2999999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

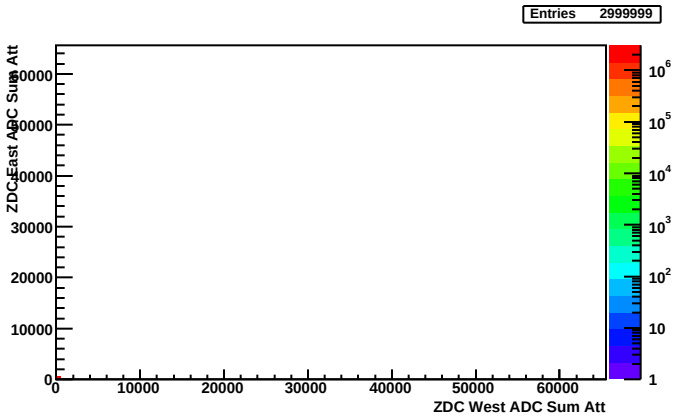

Entries 2999999
VPD-East ADC Sum
ZDC-East ADC Sum Att

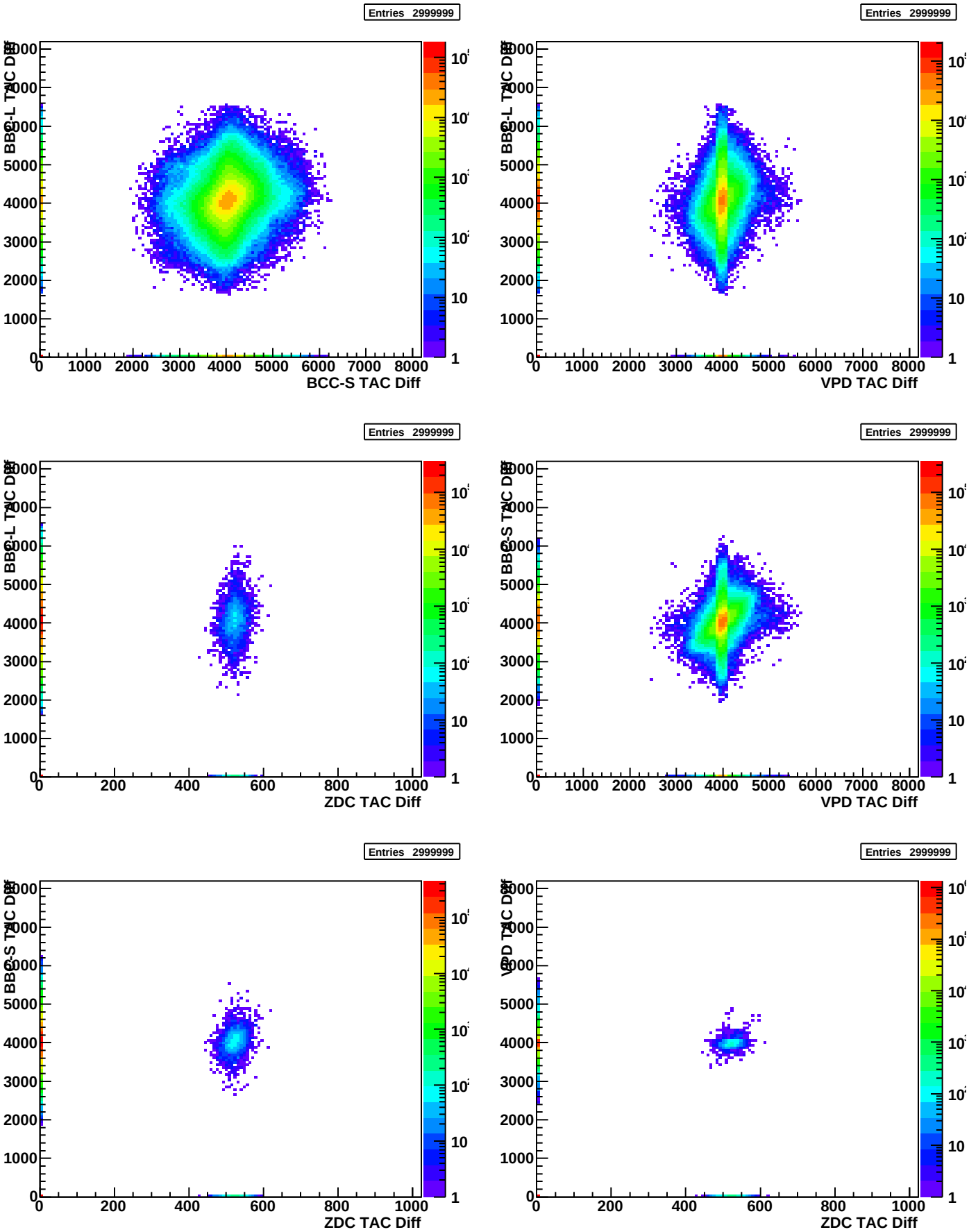

Entries 2999999
VPD-West ADC Sum
ZDC-West ADC Sum Att

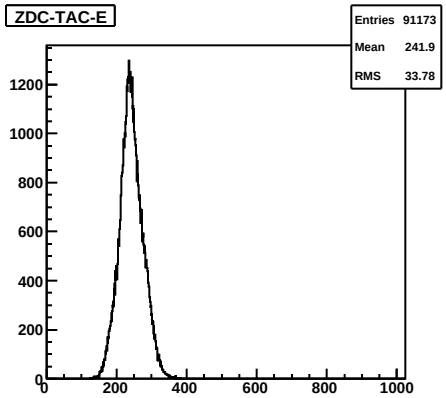

Entries 2999999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

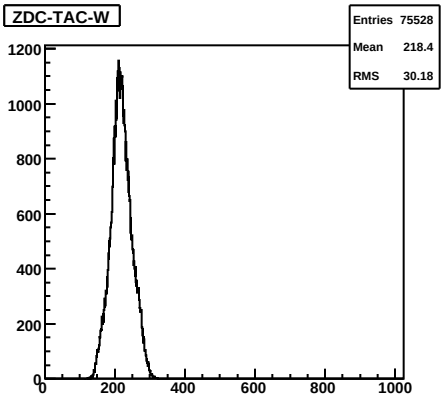

Entries 2999999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

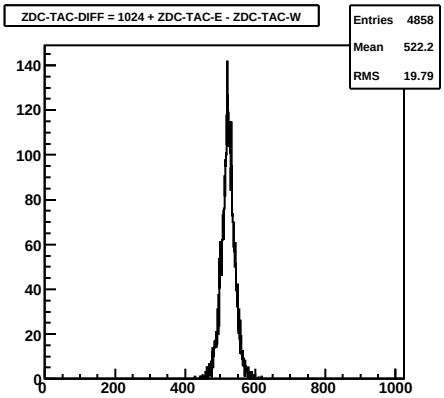

Entries 2999999
VPD East ADC Sum
VPD West ADC Sum

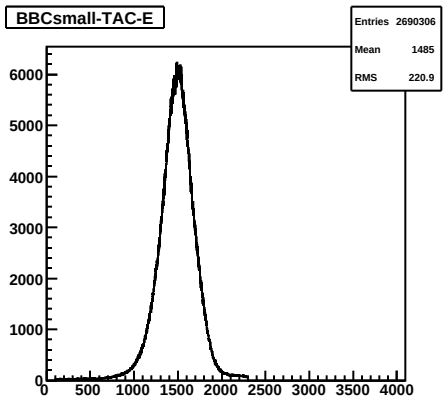

Entries 2999999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

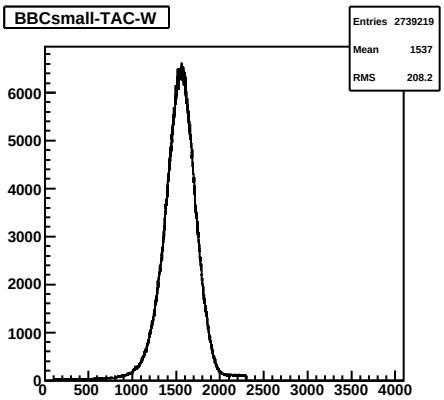

Entries 2999999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2999999
BBC-L TAC Diff
VPD TAC Diff
Entries 2999999
BBC-L TAC Diff
ZDC TAC Diff
Entries 2999999
BBC-S TAC Diff
VPD TAC Diff
Entries 2999999
BBC-S TAC Diff
ZDC TAC Diff
Entries 2999999
VPD TAC Diff
ZDC TAC Diff

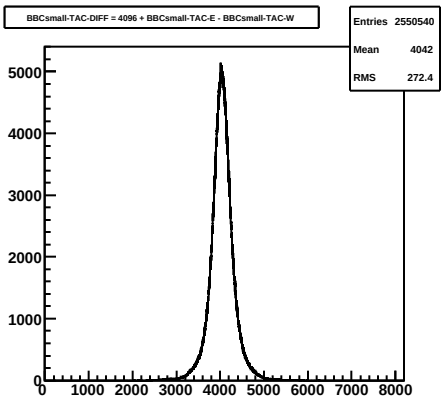

ZDC-TAC-E
Entries 91173
Mean 241.9
RMS 33.78

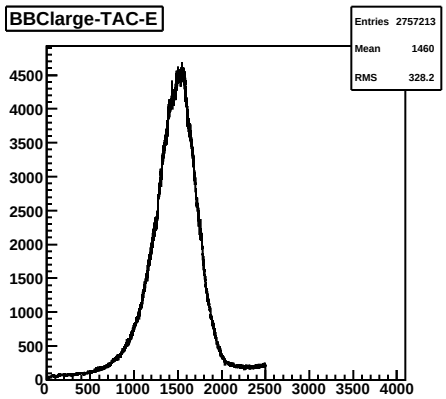

ZDC-TAC-W
Entries 75528
Mean 218.4
RMS 30.18

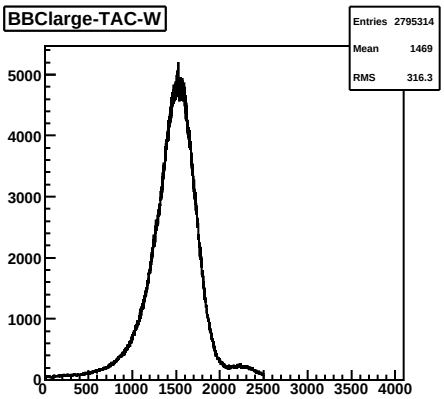

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 4858
Mean 522.2
RMS 19.79

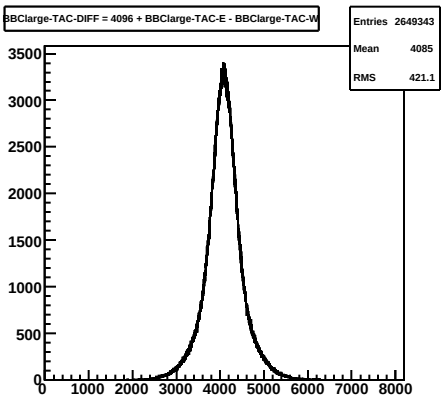

BBCsmall-TAC-E
Entries 2690306
Mean 1485
RMS 220.9

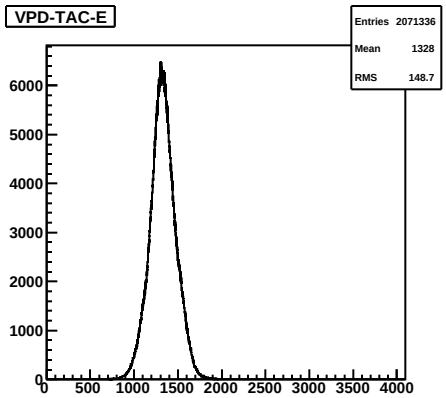

BBCsmall-TAC-W
Entries 2739219
Mean 1537
RMS 208.2

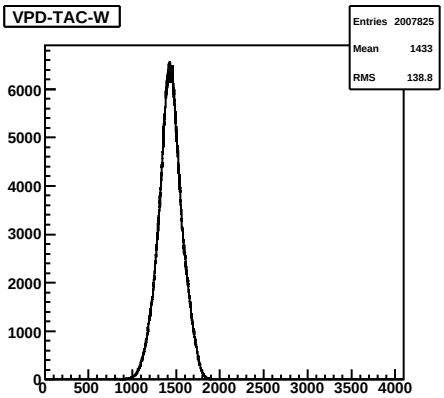

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2550540
Mean 4042
RMS 272.4

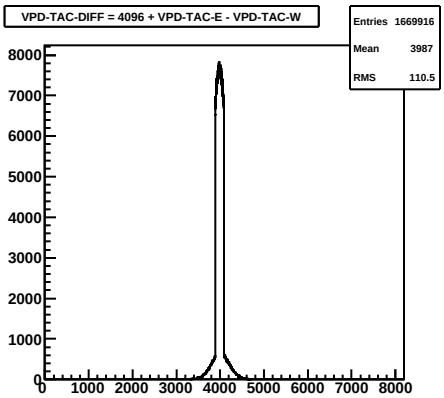

BBClarge-TAC-E
Entries 2757213
Mean 1460
RMS 328.2

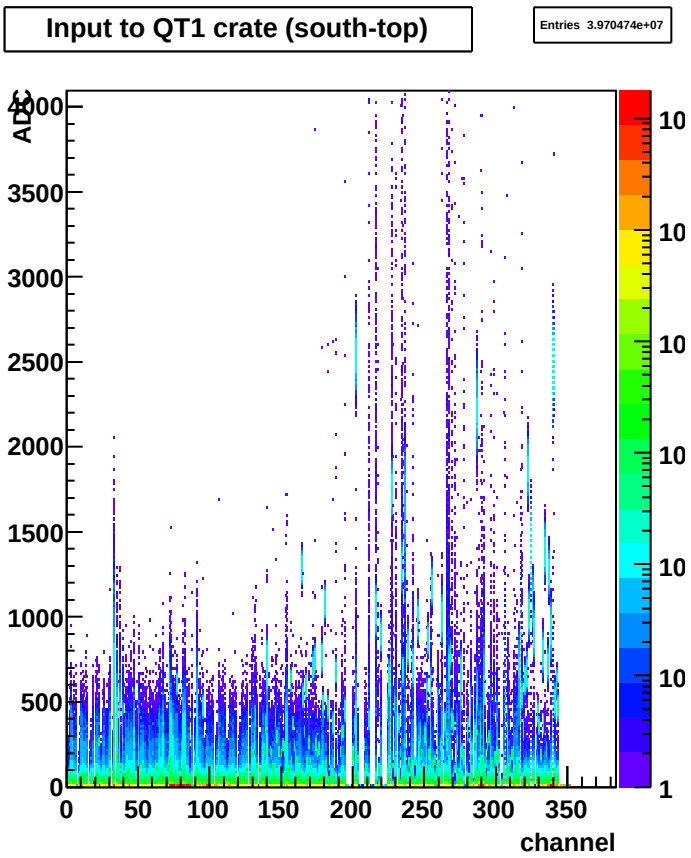

BBClarge-TAC-W
Entries 2795314
Mean 1469
RMS 316.3

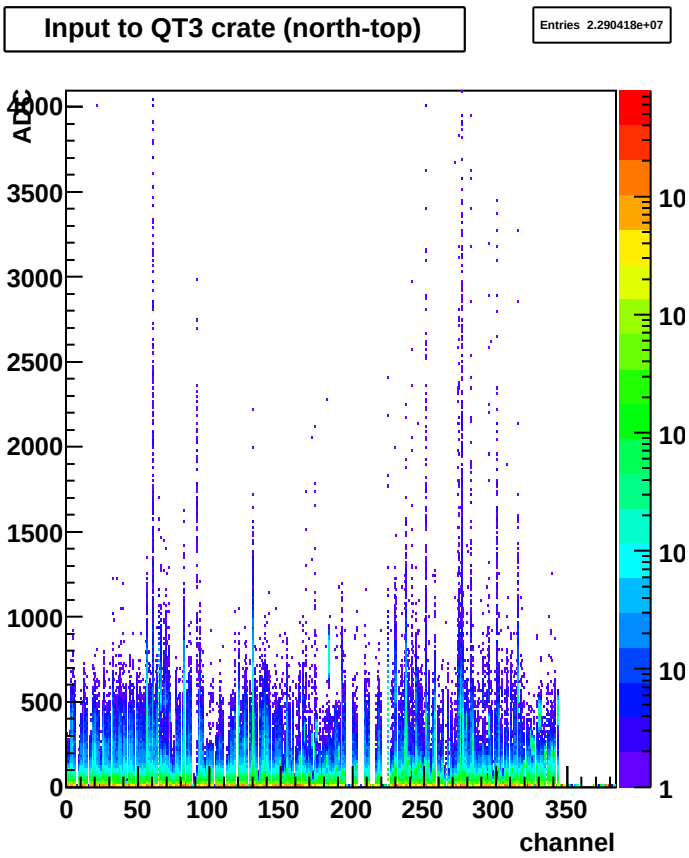

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2649343
Mean 4085
RMS 421.1

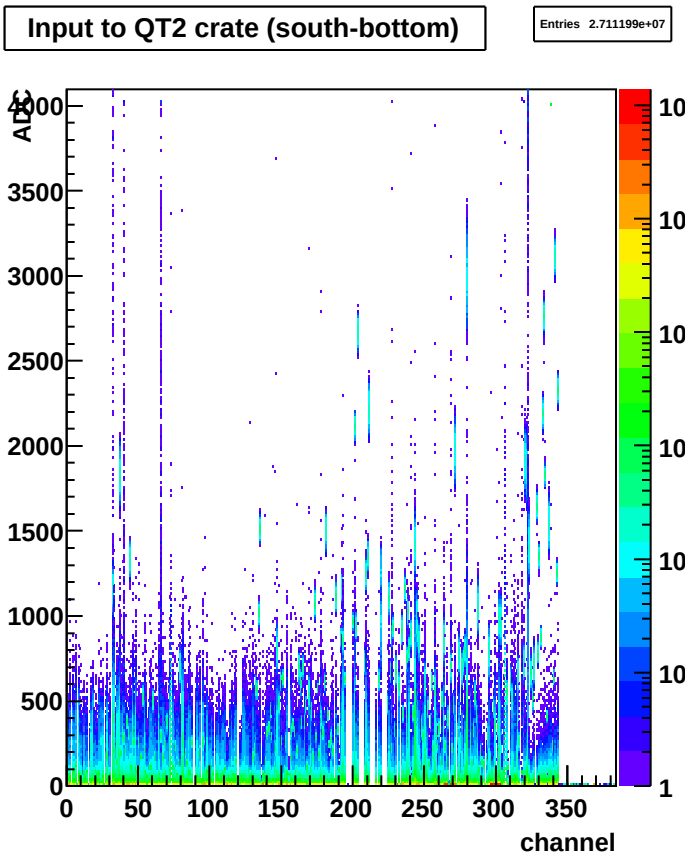

VPD-TAC-E
Entries 2071336
Mean 1328
RMS 148.7

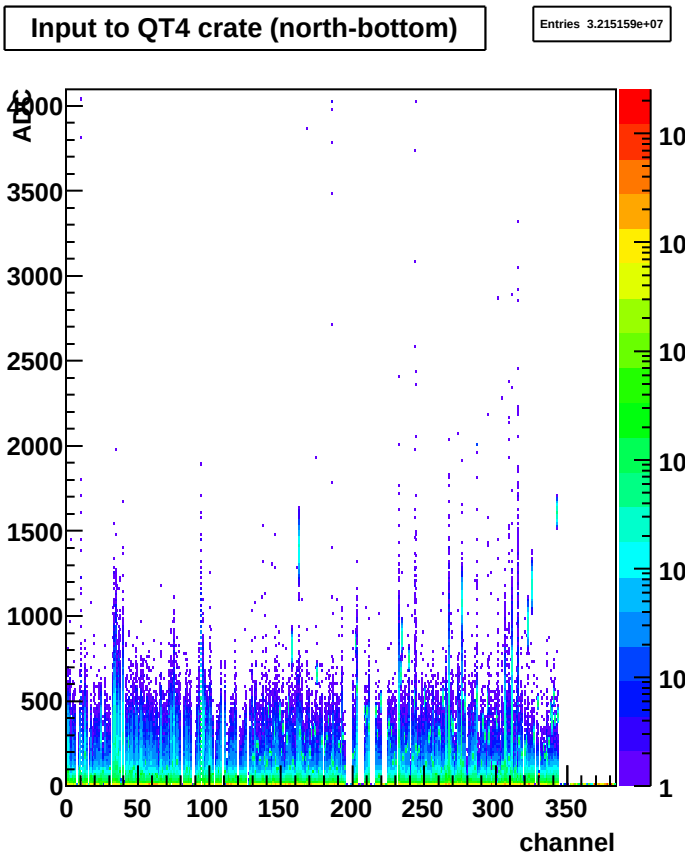

VPD-TAC-W
Entries 2007825
Mean 1433
RMS 138.8

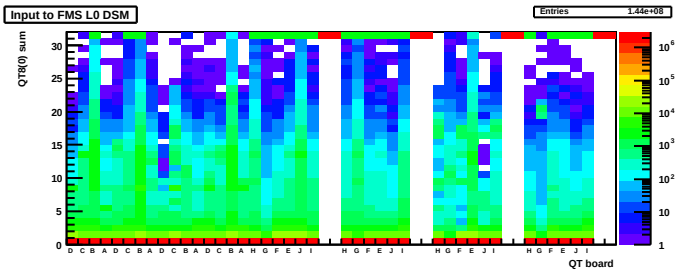

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1669916
Mean 3987
RMS 110.5

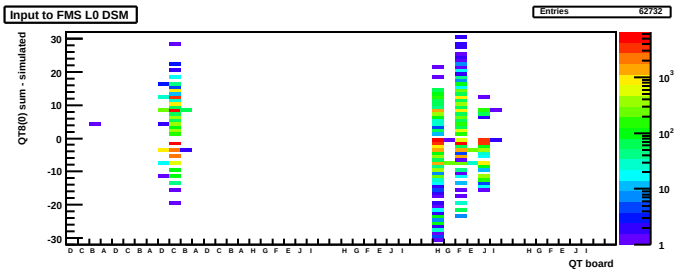

Input to QT1 crate (south-top)
Entries 3.970474e+07
ADC
channel

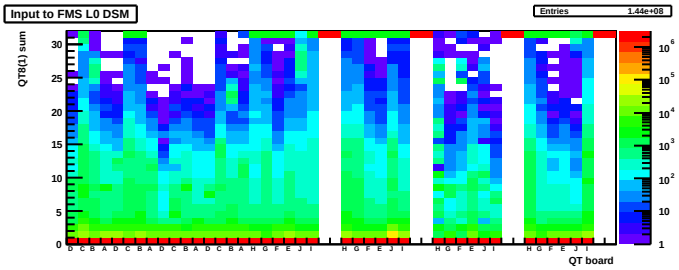

Input to QT3 crate (north-top)
Entries 2.290418e+07
ADC
channel

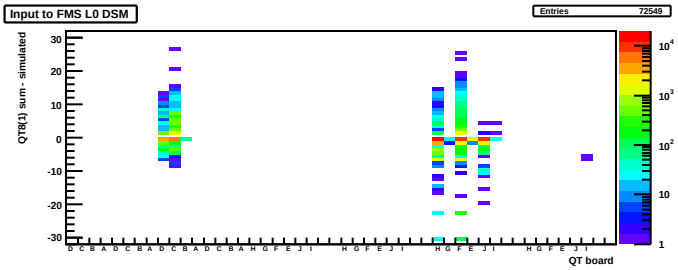

Input to QT2 crate (south-bottom)
Entries 2.711199e+07
ADC
channel

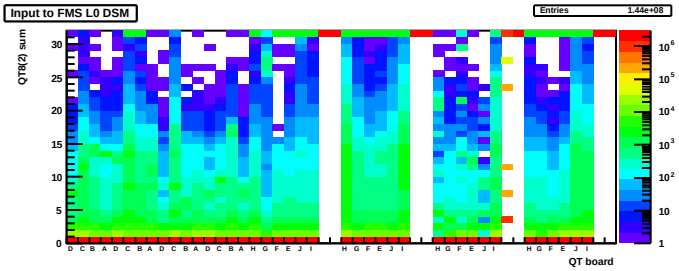

Input to QT4 crate (north-bottom)
Entries 3.215159e+07
ADC
channel

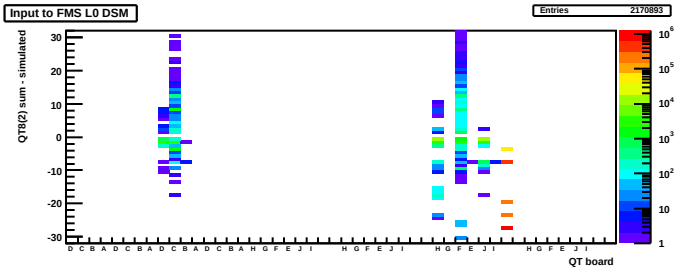

Input to FMS L0 DSM
Entries 1.44e+08
QT8(0) sum
QT board

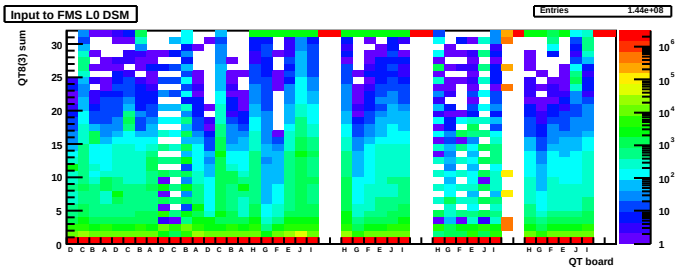

Input to FMS L0 DSM
Entries 62732
QT8(0) sum - simulated
QT board

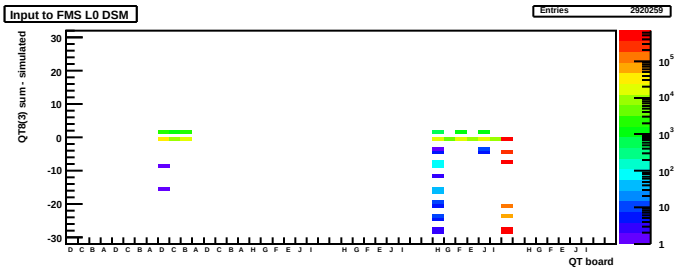

Input to FMS L0 DSM
Entries 1.44e+08
QT8(1) sum
QT board

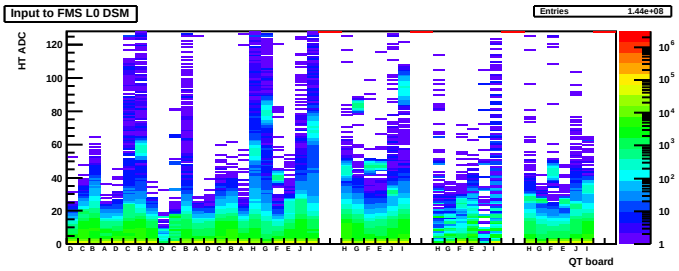

Input to FMS L0 DSM
Entries 72549
QT8(1) sum - simulated
QT board

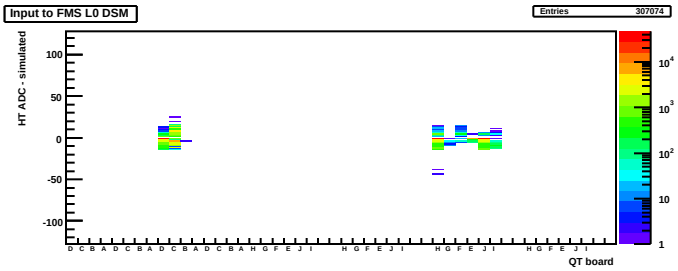

Input to FMS L0 DSM
Entries 1.44e+08
QT8(2) sum
QT board

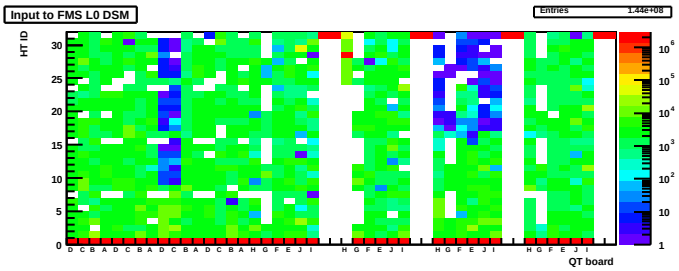

Input to FMS L0 DSM
Entries 2170893
QT8(2) sum - simulated
QT board

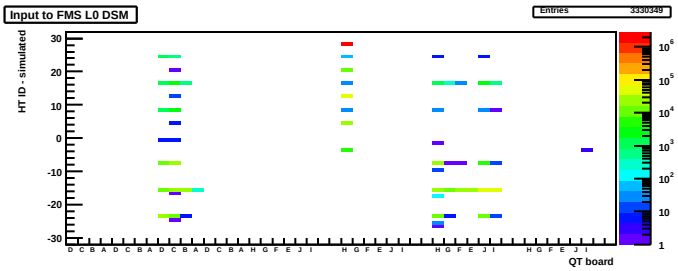

Input to FMS L0 DSM
Entries 1.44e+08
QT8(3) sum
QT board

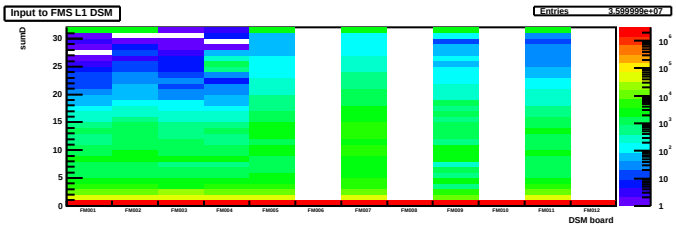

Input to FMS L0 DSM
Entries 2920259
QT8(3) sum - simulated
QT board

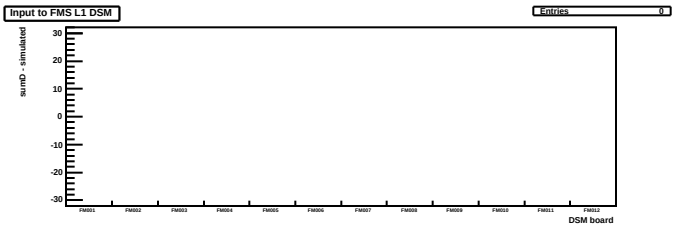

Input to FMS L0 DSM
Entries 1.44e+08
HT ADC
QT board

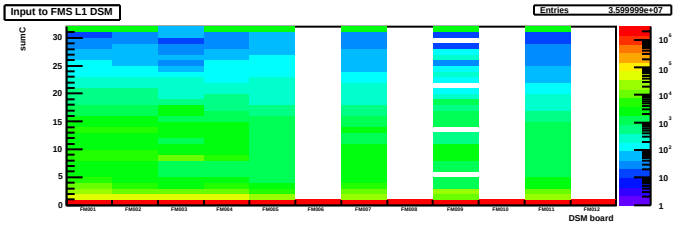

Input to FMS L0 DSM
Entries 307074
HT ADC - simulated
QT board

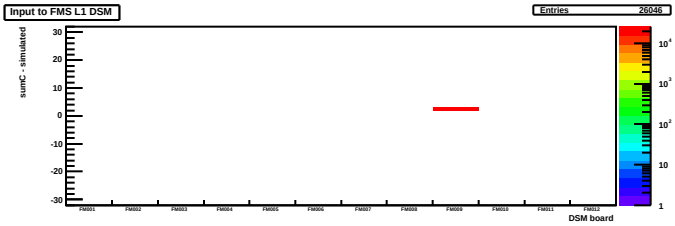

Input to FMS L0 DSM
Entries 1.44e+08
HT ID
QT board

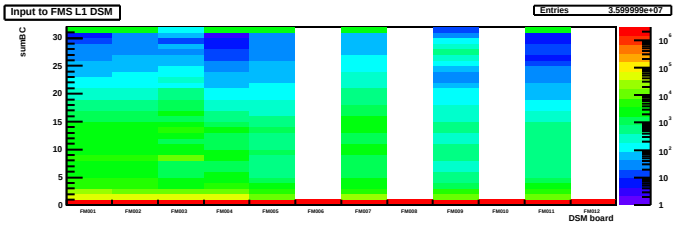

Input to FMS L0 DSM
Entries 3330349
HT ID - simulated
QT board

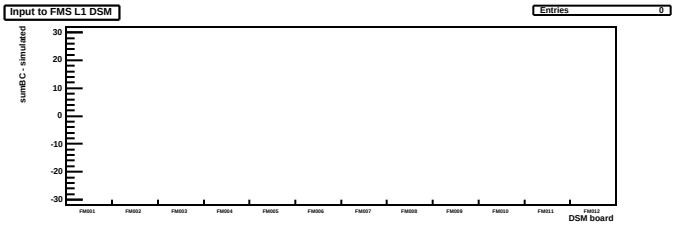

Input to FMS L1 DSM
Entries 3.599999e+07
sumD
DSM board

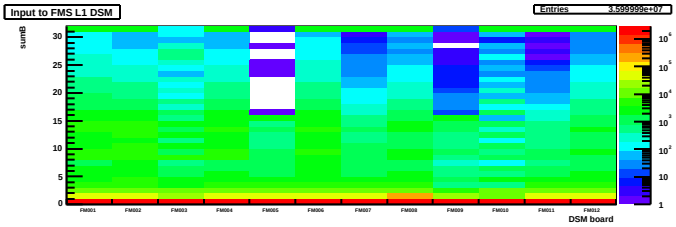

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

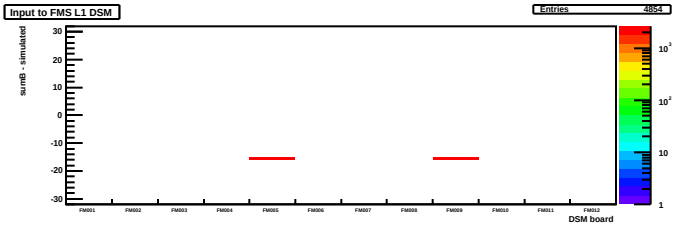

Input to FMS L1 DSM
Entries 3.599999e+07
sumC
DSM board

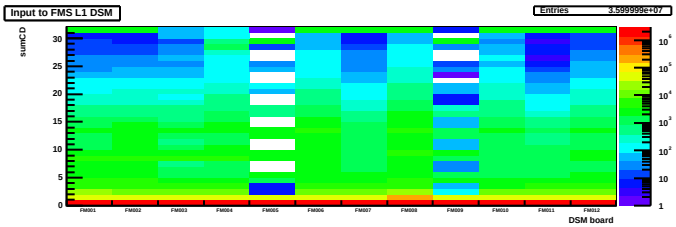

Input to FMS L1 DSM
Entries 26046
sumC - simulated
DSM board

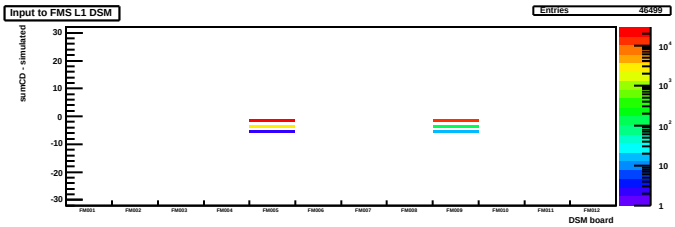

Input to FMS L1 DSM
Entries 3.599999e+07
sumBC
DSM board

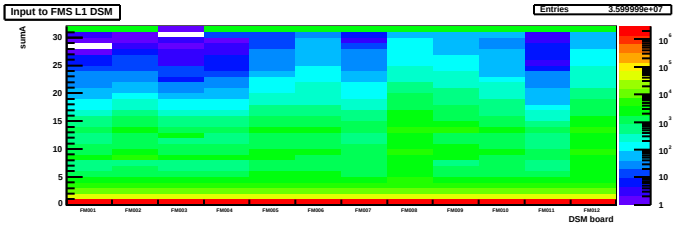

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

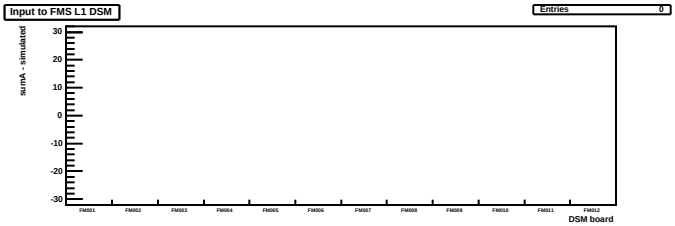

Input to FMS L1 DSM
Entries 3.599999e+07
sumB
DSM board

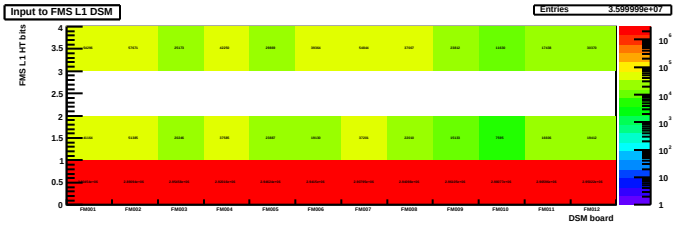

Input to FMS L1 DSM
Entries 4854
sumB - simulated
DSM board

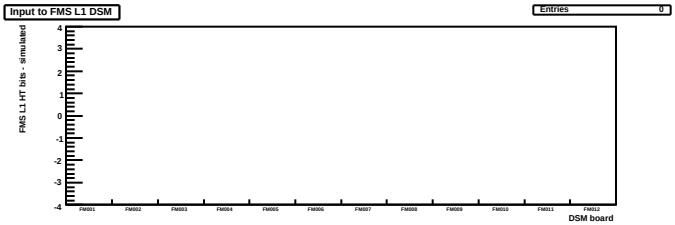

Input to FMS L1 DSM
Entries 3.599999e+07
sumCD
DSM board

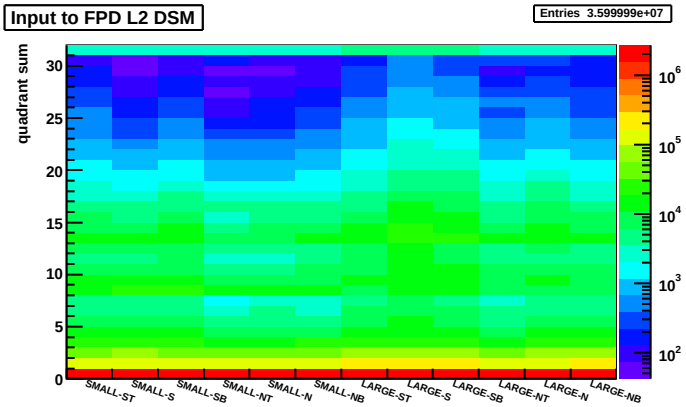

Input to FMS L1 DSM
Entries 46499
sumCD - simulated
DSM board

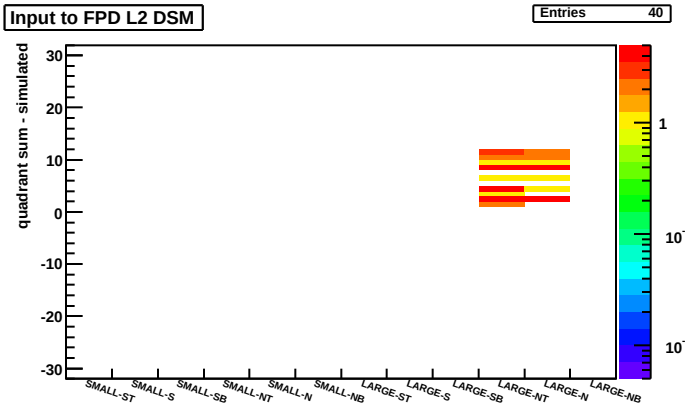

Input to FMS L1 DSM
Entries 3.599999e+07
sumA
DSM board

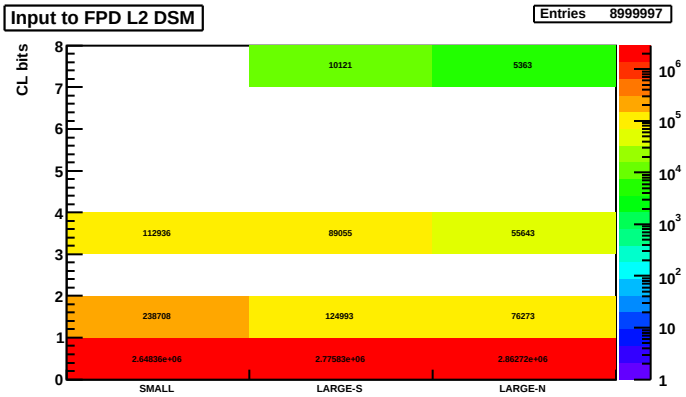

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

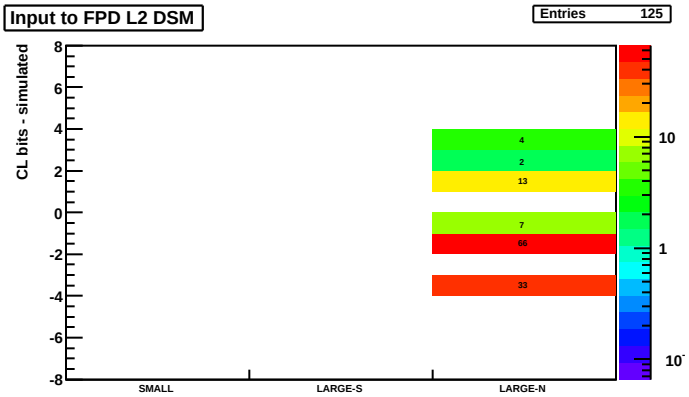

Input to FMS L1 DSM
Entries 3.599999e+07
FMS L1 HT bits
DSM board

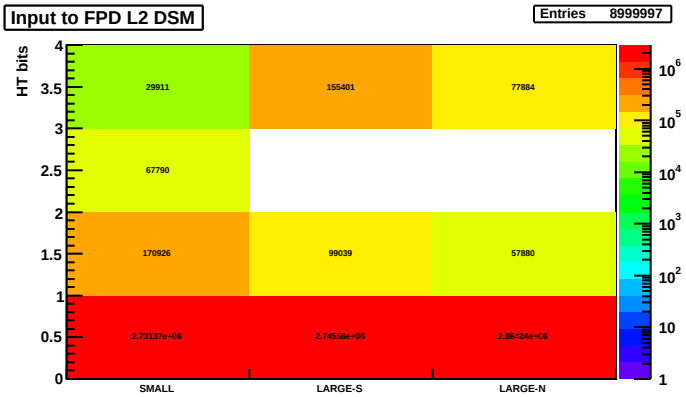

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

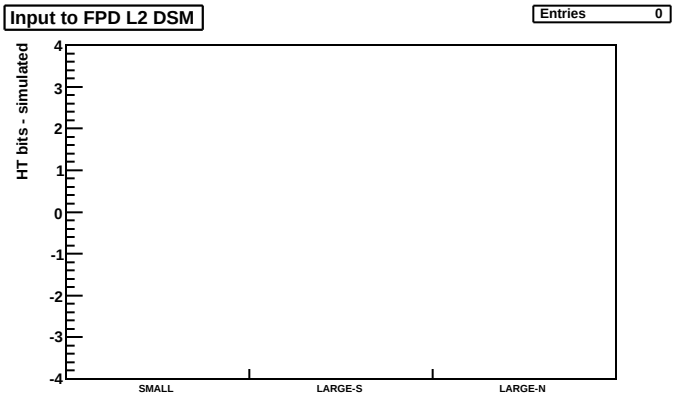

Input to FPD L2 DSM
Entries 3.599999e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

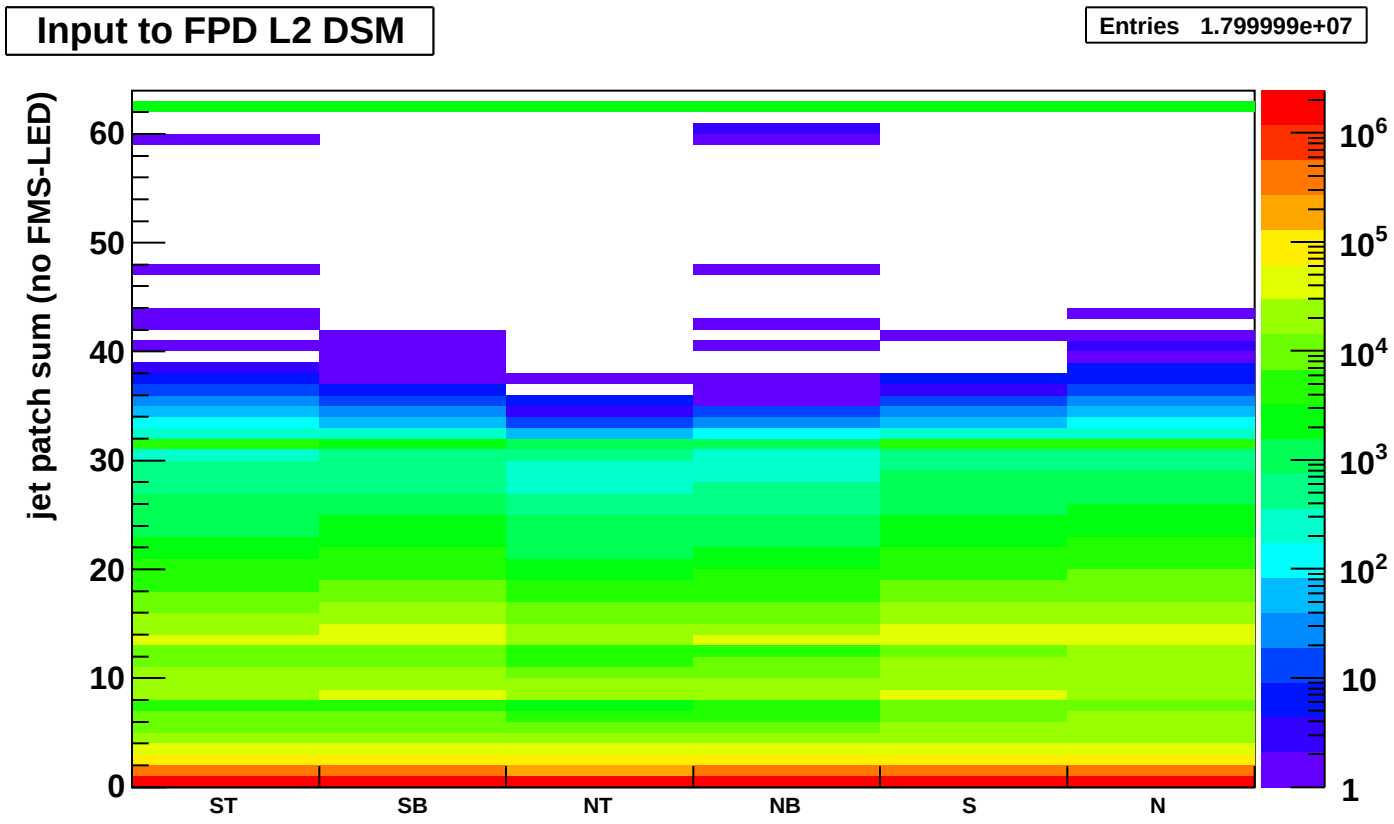

Input to FPD L2 DSM
Entries 40
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

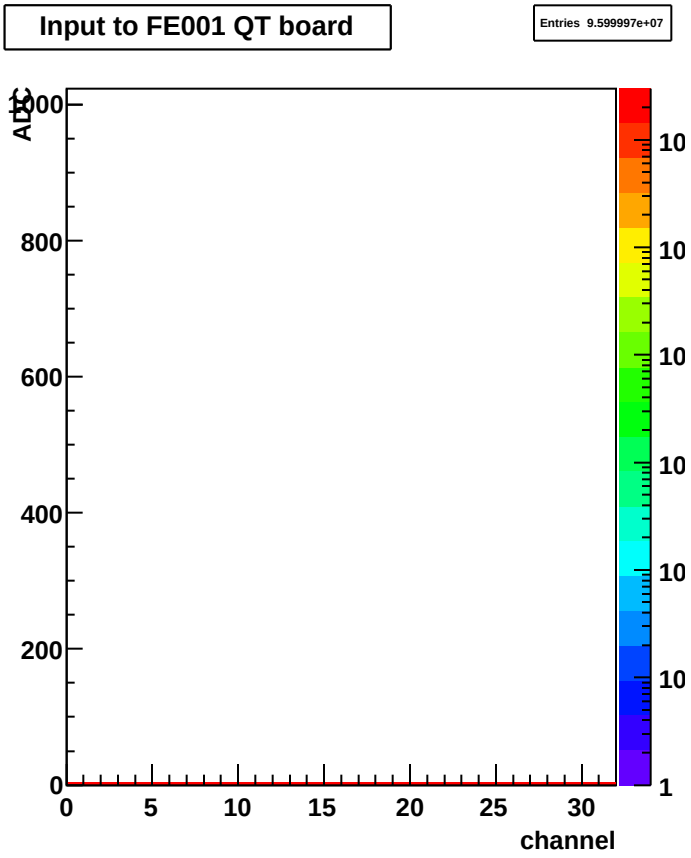

Input to FPD L2 DSM
Entries 8999997
CL bits
10121
5363
112936
89055
55643
238708
124993
76273
2.64836e+06
2.77583e+06
2.86272e+06
SMALL
LARGE-S
LARGE-N

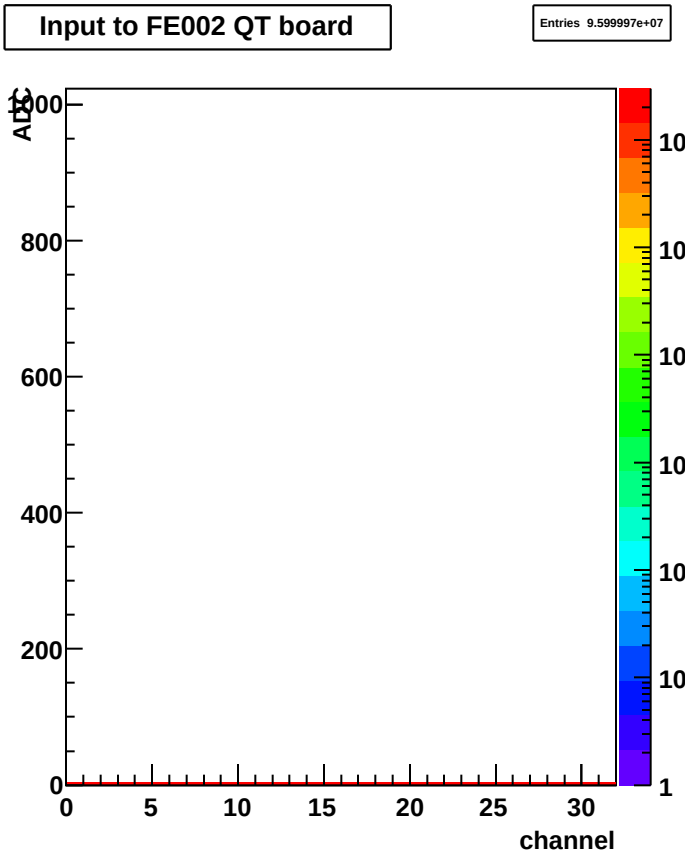

Input to FPD L2 DSM
Entries 125
CL bits - simulated
4
2
13
7
66
33
SMALL
LARGE-S
LARGE-N

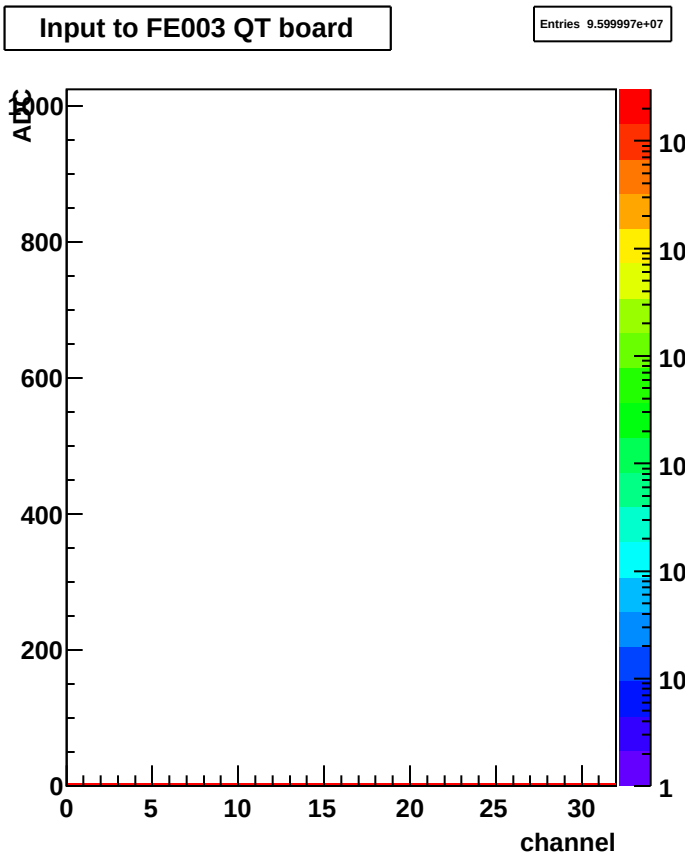

Input to FPD L2 DSM
Entries 8999997
HT bits
29911
155401
77884
67790
170926
99039
57880
2.73137e+06
2.74556e+06
2.86424e+06
SMALL
LARGE-S
LARGE-N

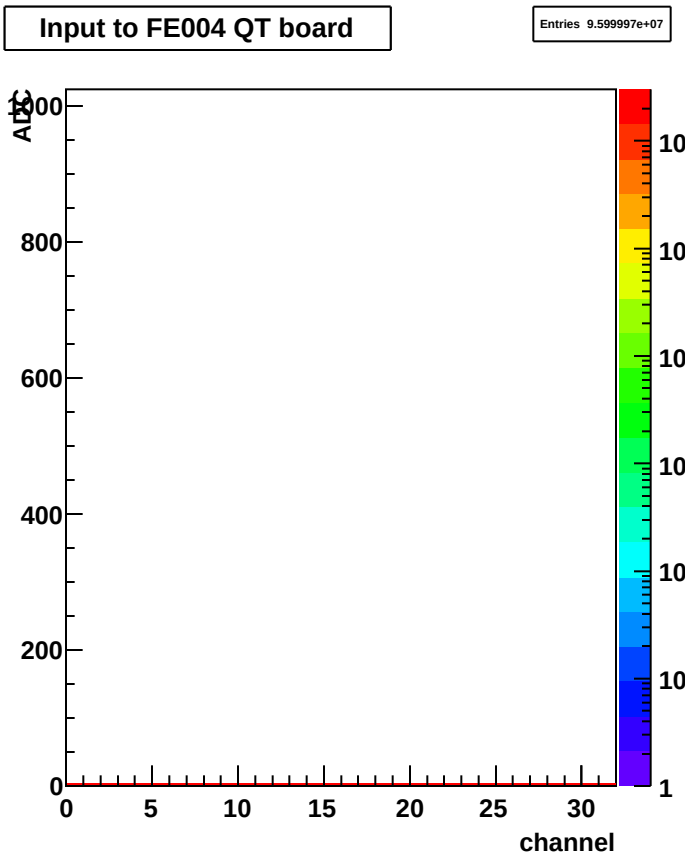

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

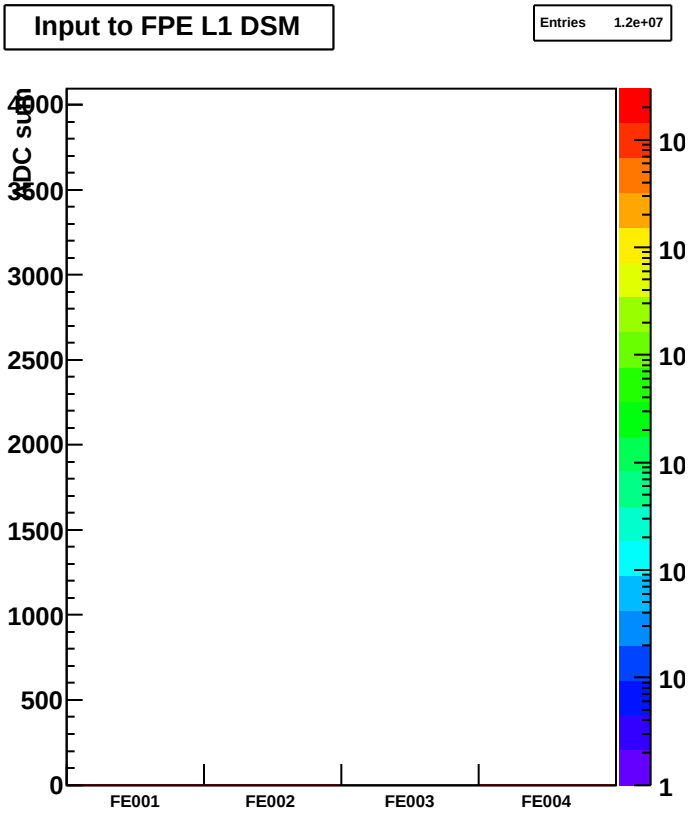

Input to FPD L2 DSM
Entries 1.799999e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

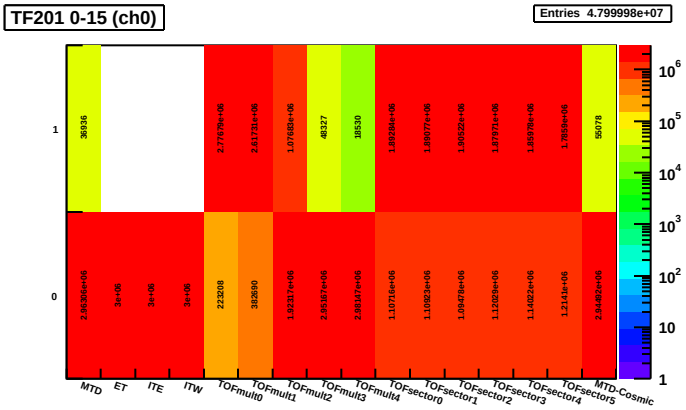

Input to FE001 QT board
Entries 9.599997e+07
ADC
channel

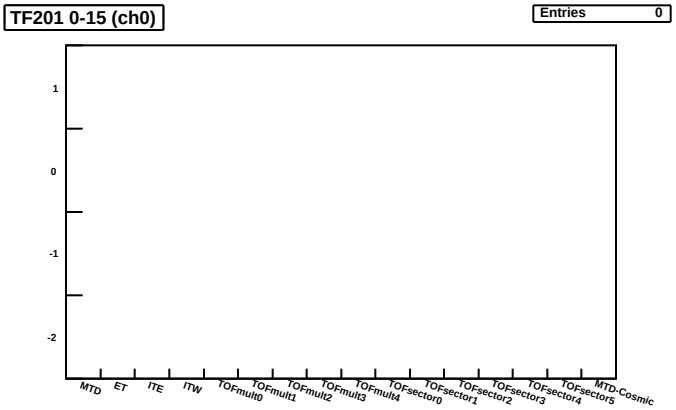

Input to FE002 QT board
Entries 9.599997e+07
ADC
channel


Input to FE003 QT board
Entries 9.599997e+07
ADC
channel


Input to FE004 QT board
Entries 9.599997e+07
ADC
channel


Input to FPE L1 DSM
Entries 1.2e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 4.799998e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
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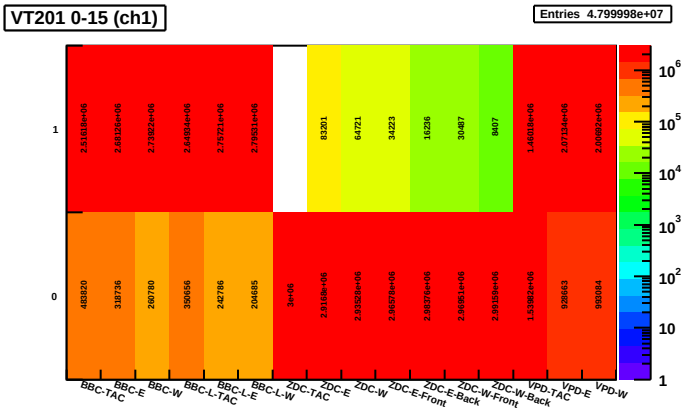
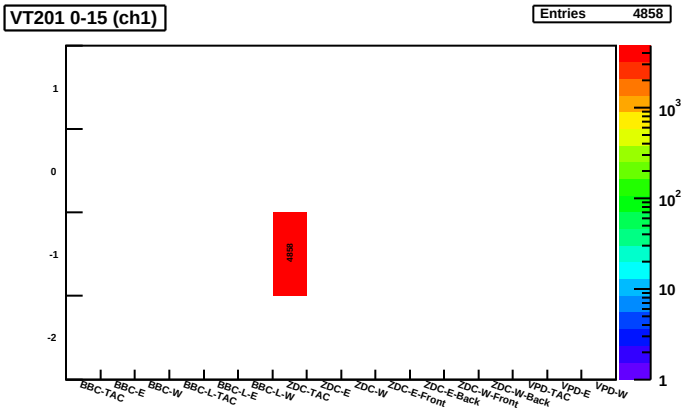

VT201 0-15 (ch1)
Entries 4.799998e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W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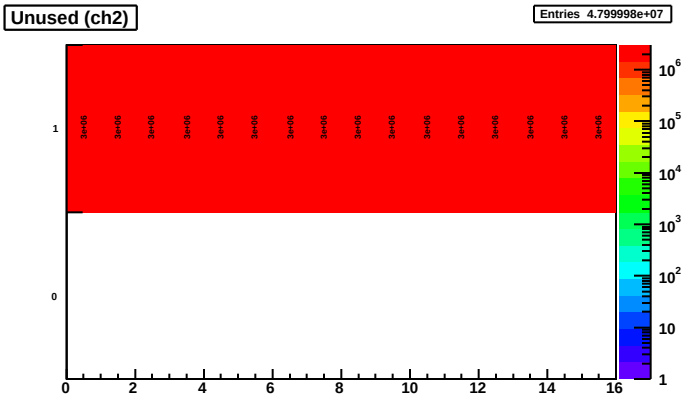
Unused (ch2)
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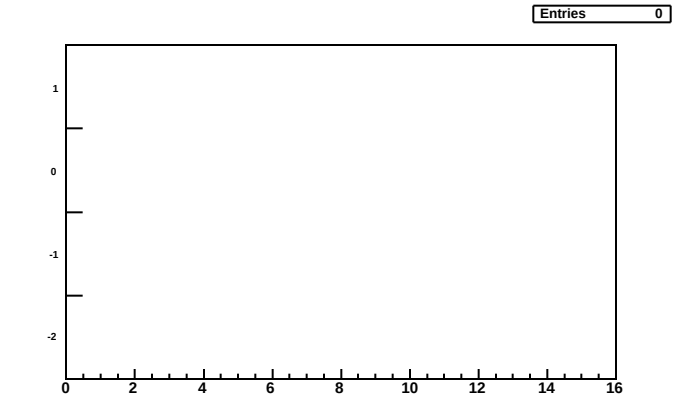
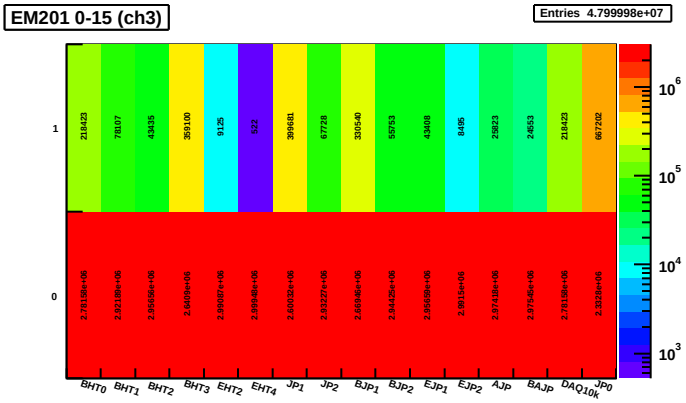
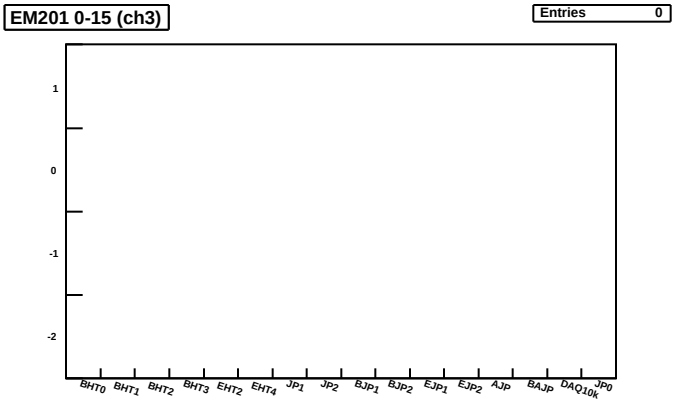

Entries 0


EM201 0-15 (ch3)
Entries 4.799998e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0


EM201 0-15 (ch3)
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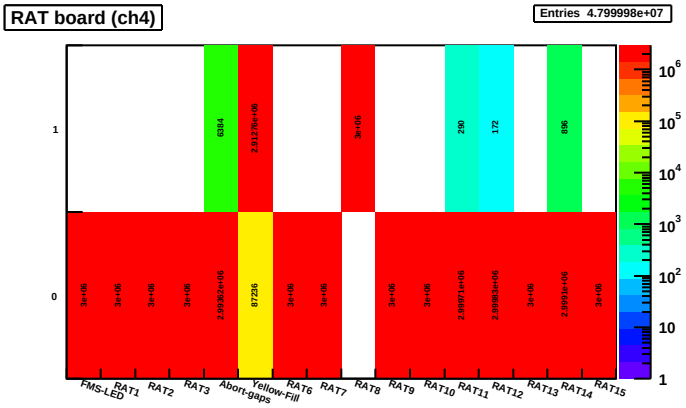
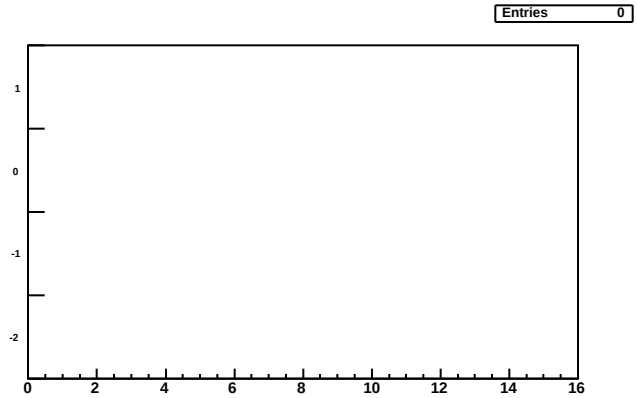
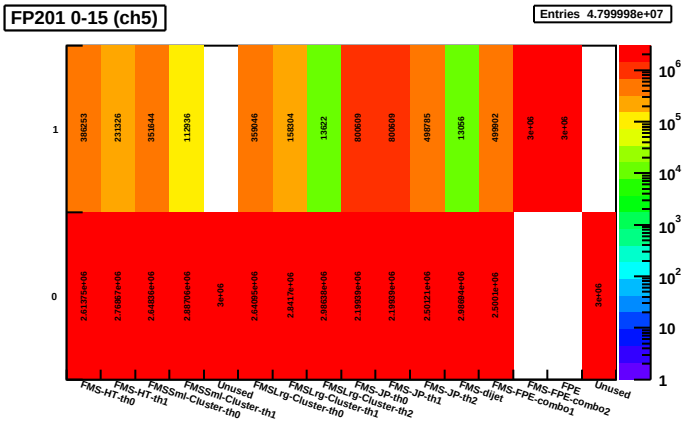
RAT board (ch4)





FP201 0-15 (ch5)



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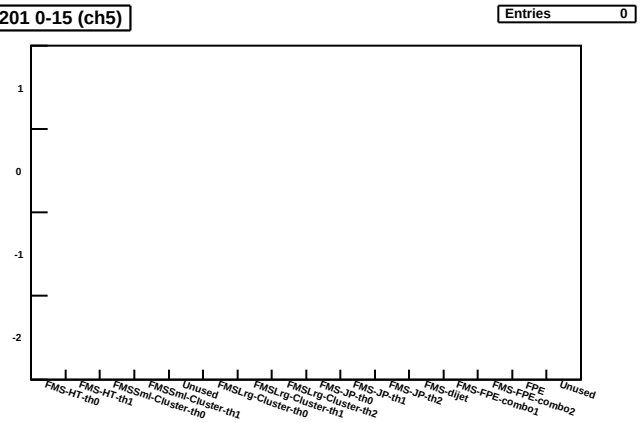
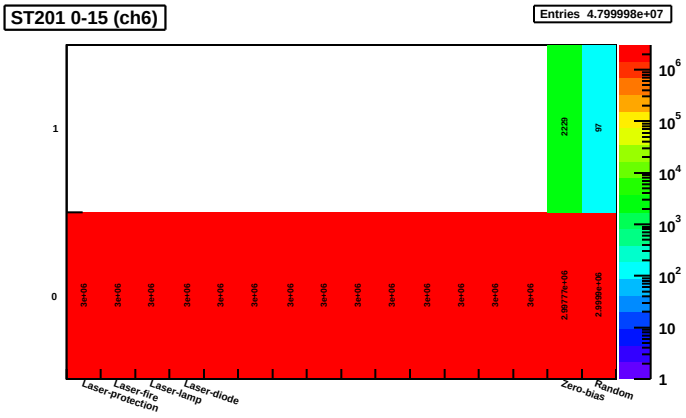
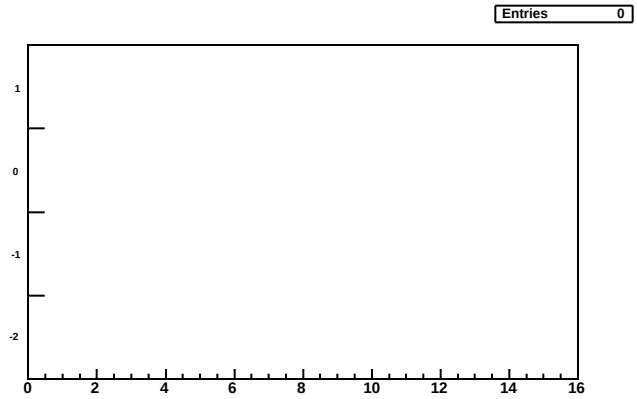
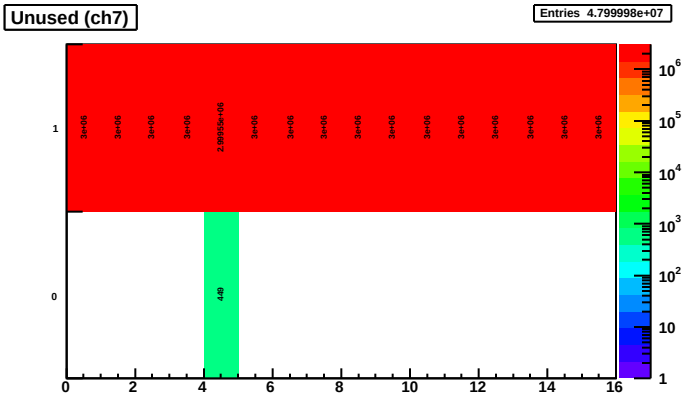
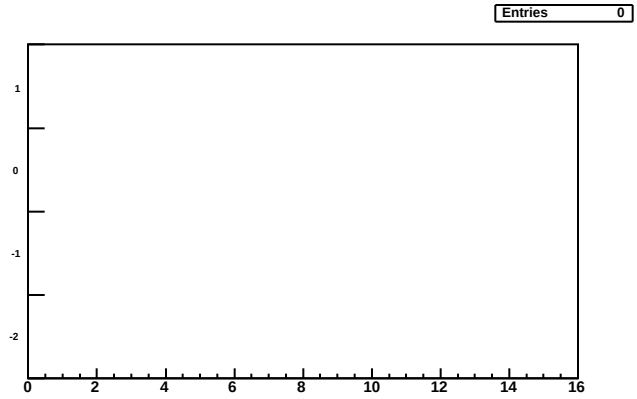


ST201 0-15 (ch6)





Unused (ch7)