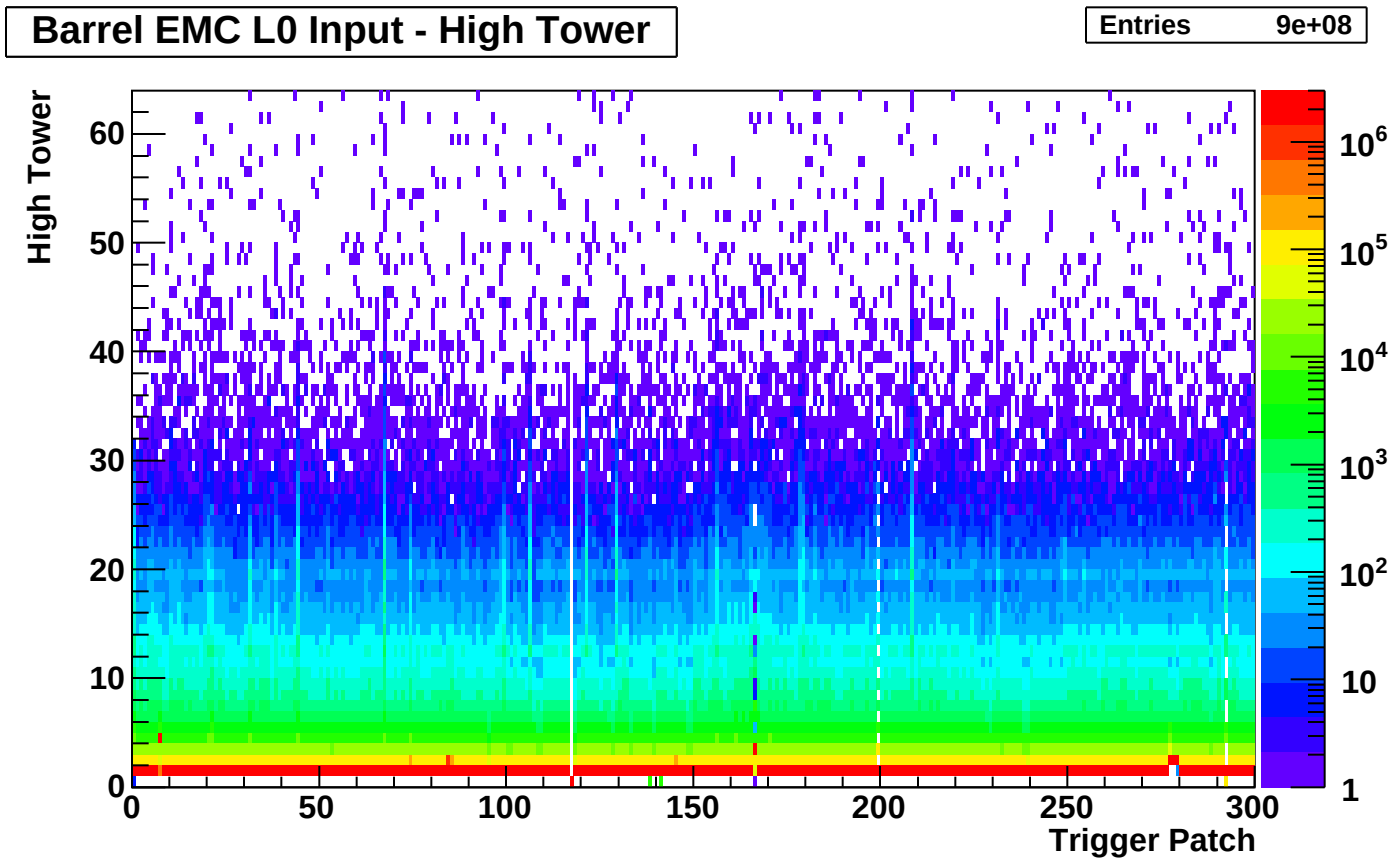
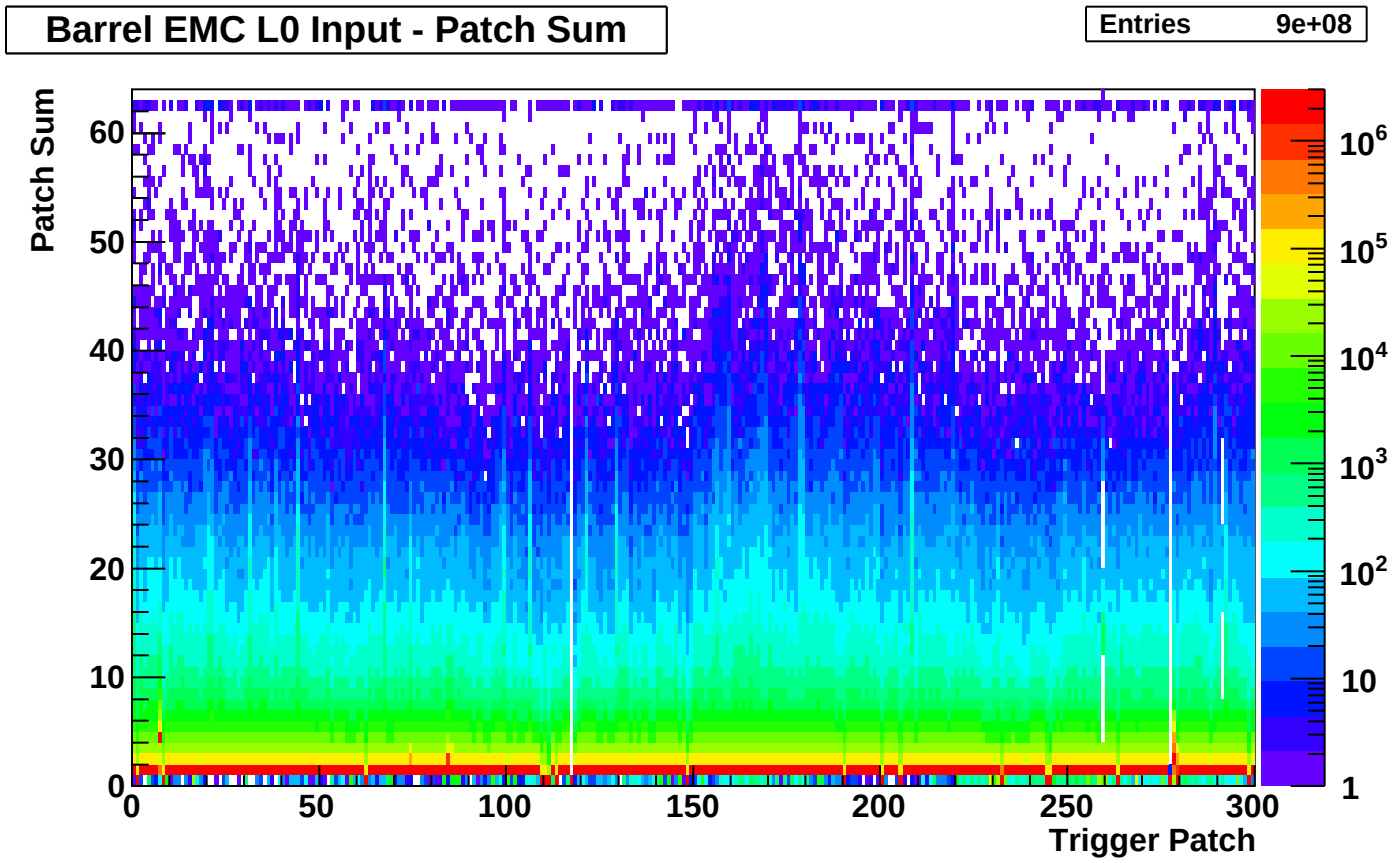
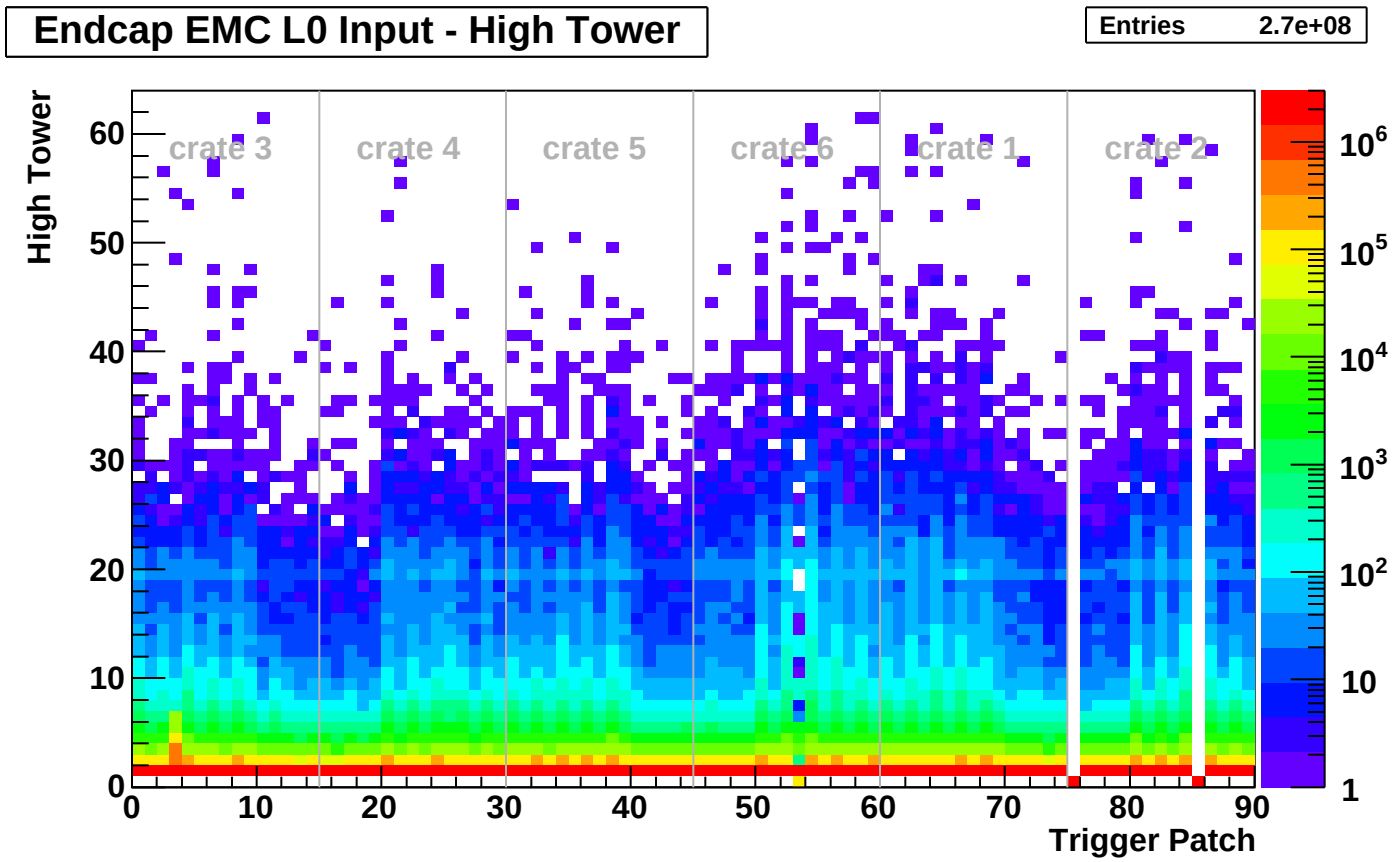
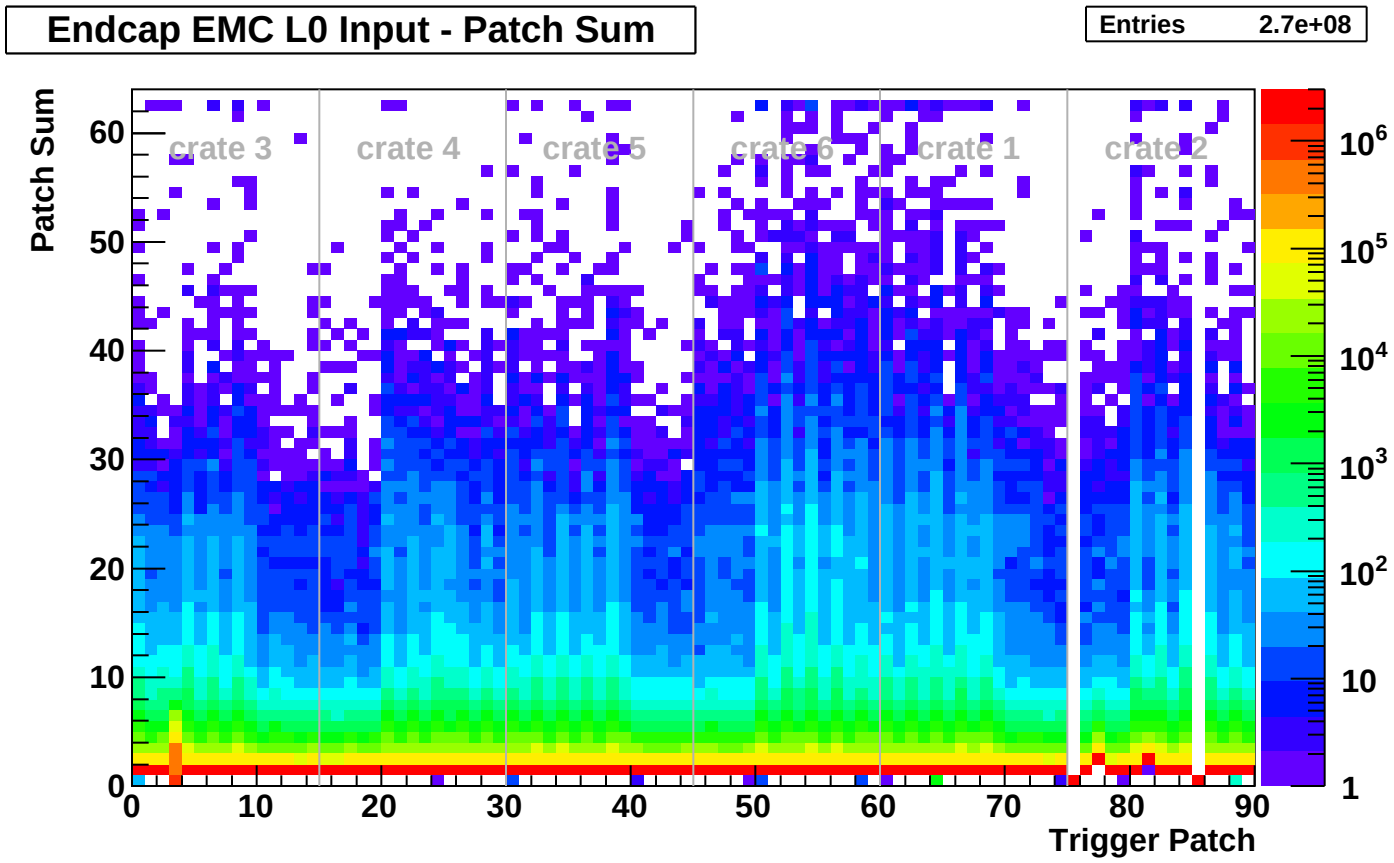
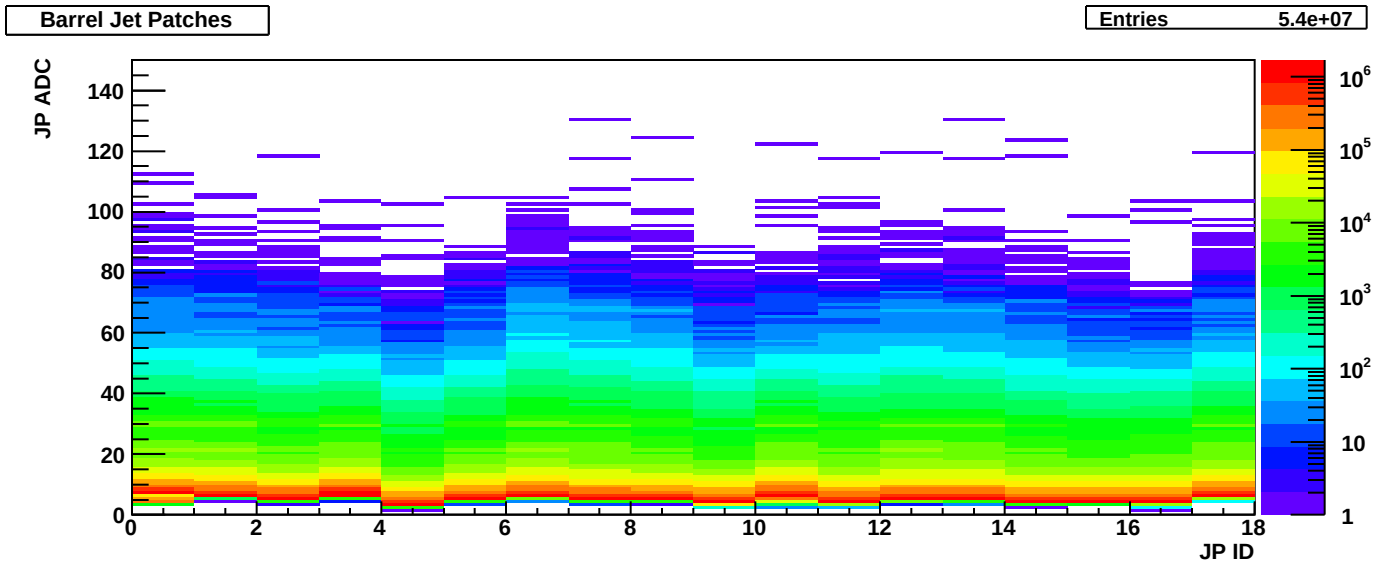
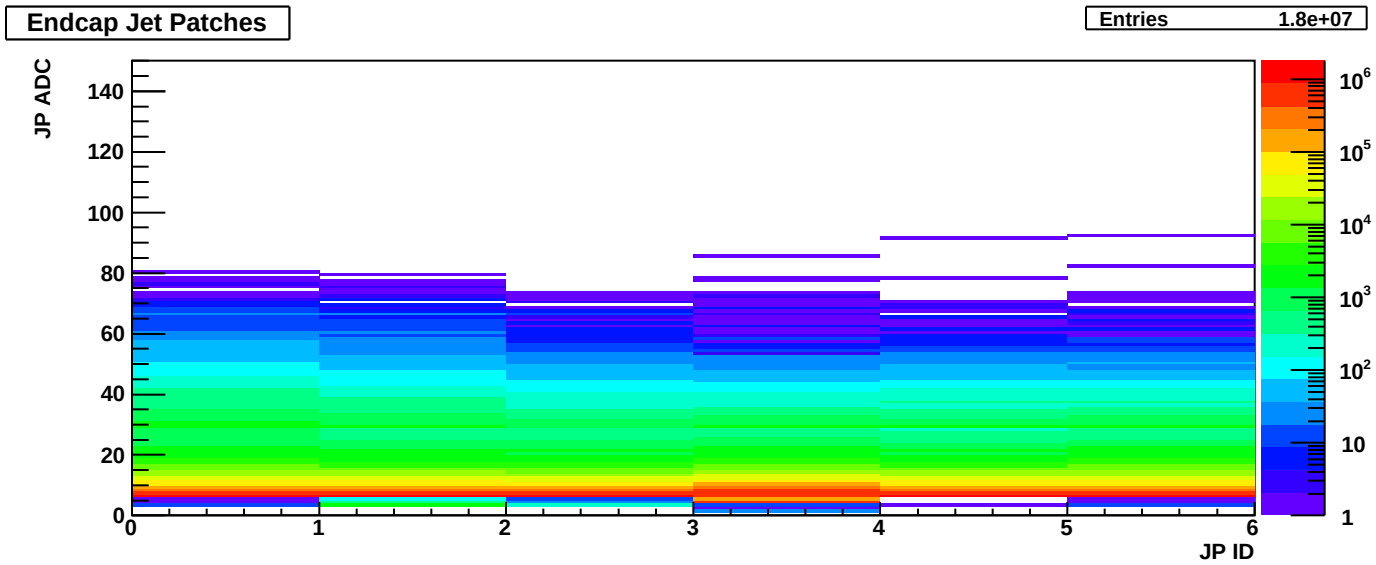

Barrel EMC L0 Input - High Tower
Entries 9e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

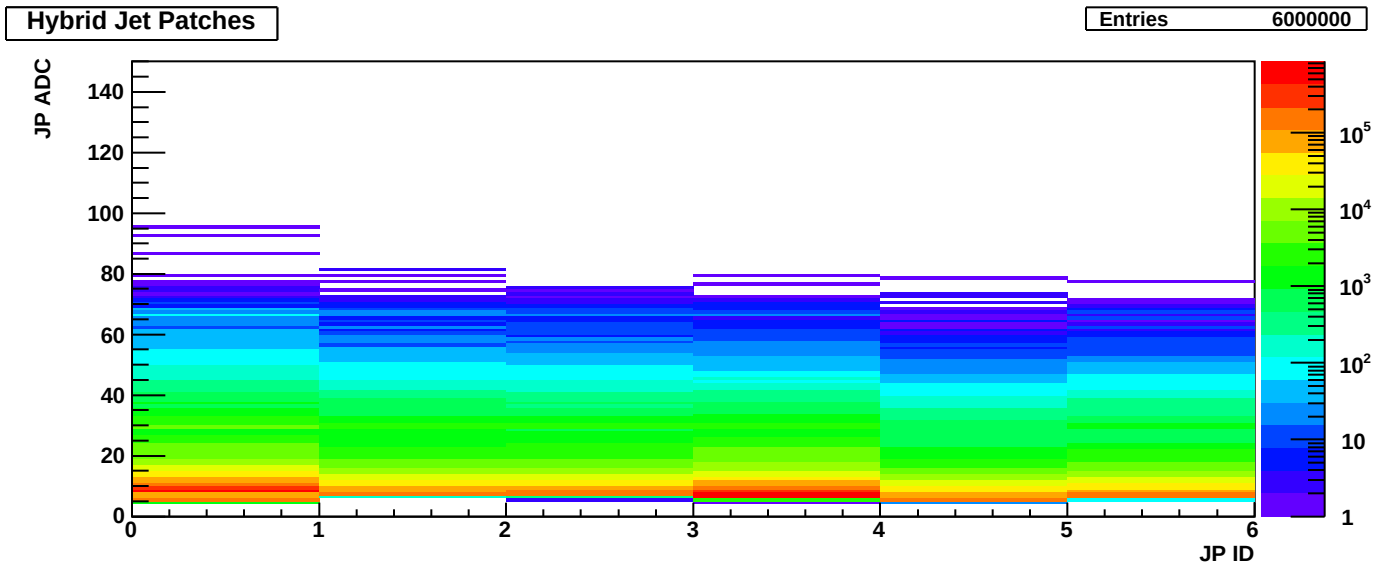

Barrel EMC L0 Input - Patch Sum
Entries 9e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

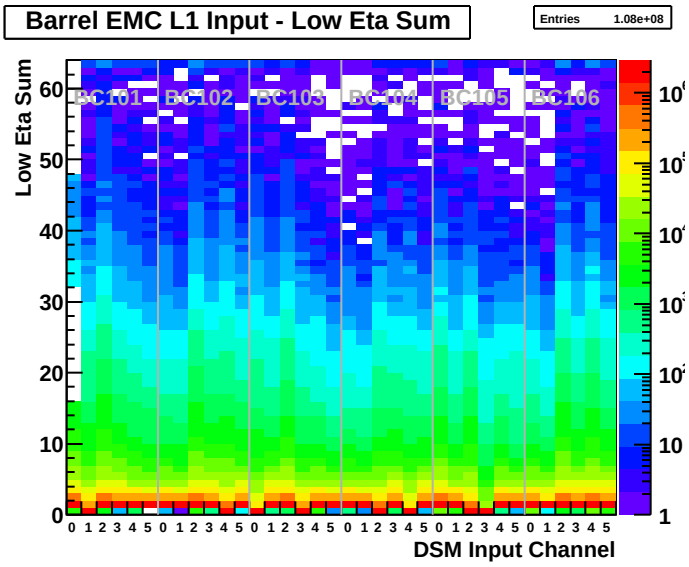

Endcap EMC L0 Input - High Tower
Entries 2.7e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

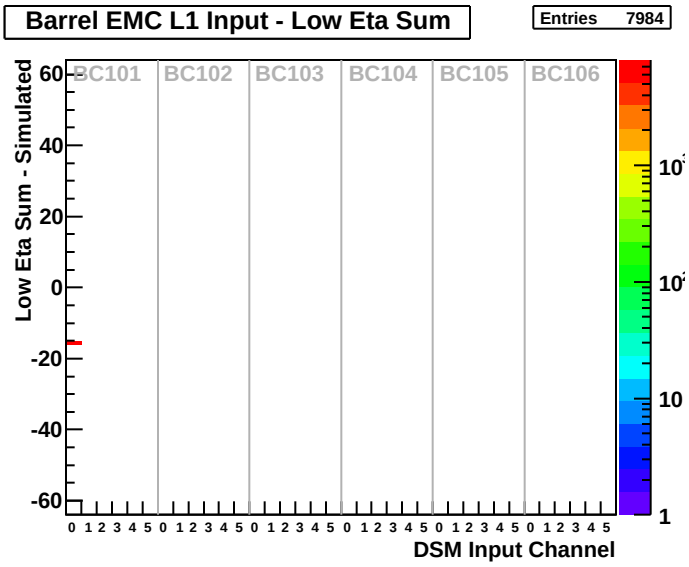

Endcap EMC L0 Input - Patch Sum
Entries 2.7e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

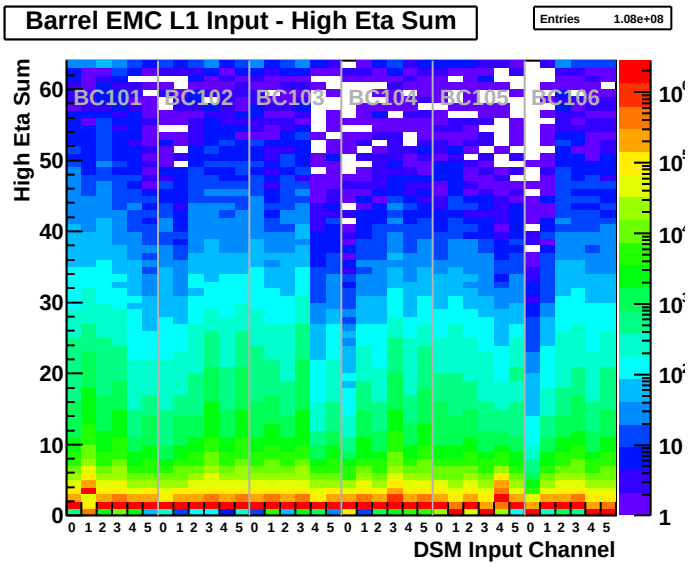

Barrel Jet Patches
Entries 5.4e+07
JP ADC
JP ID

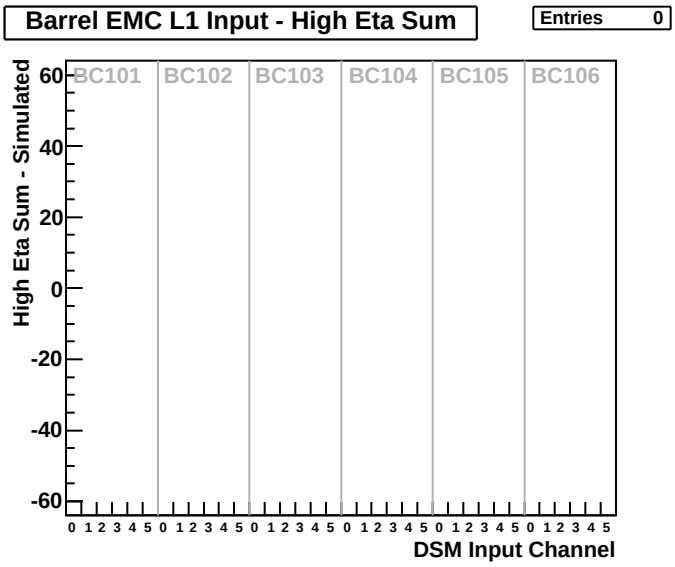

Endcap Jet Patches
Entries 1.8e+07
JP ADC
JP ID

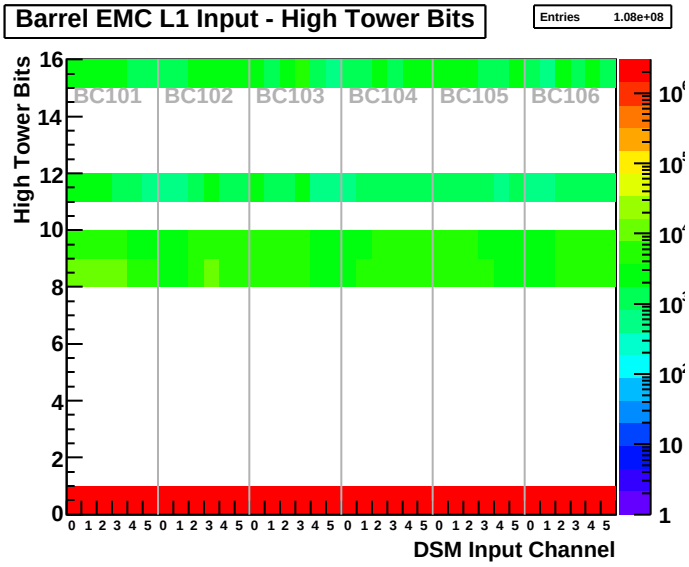

Hybrid Jet Patches
Entries 6000000
JP ADC
JP ID

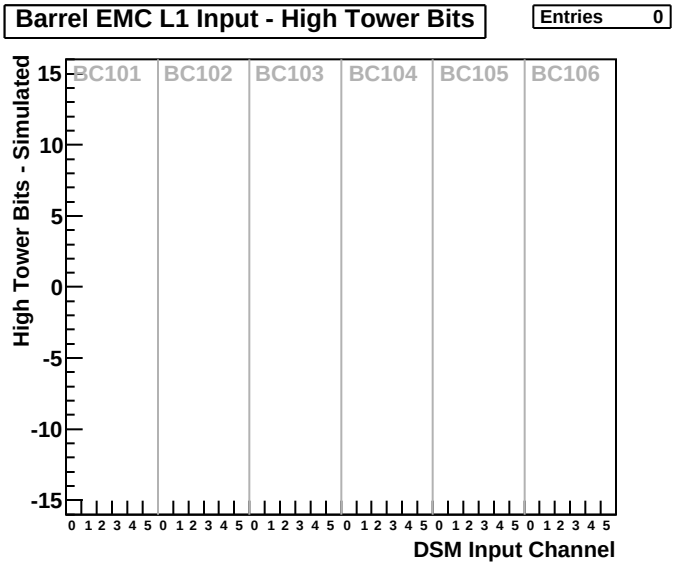

Barrel EMC L1 Input - Low Eta Sum
Entries 1.08e+08
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

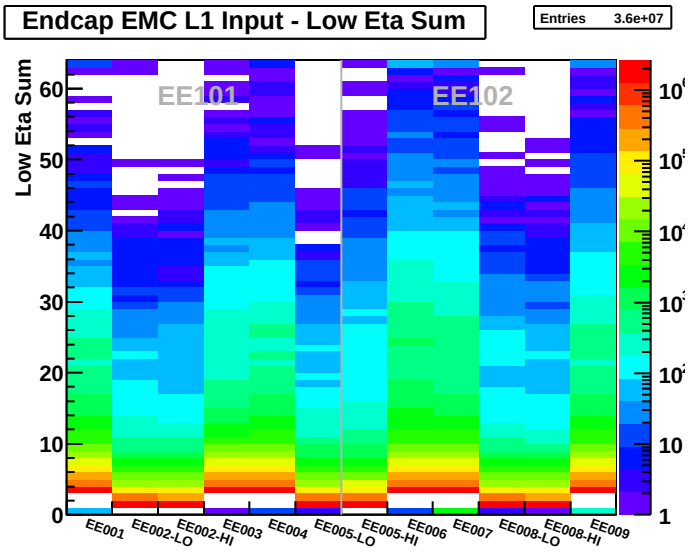

Barrel EMC L1 Input - Low Eta Sum
Entries 7984
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

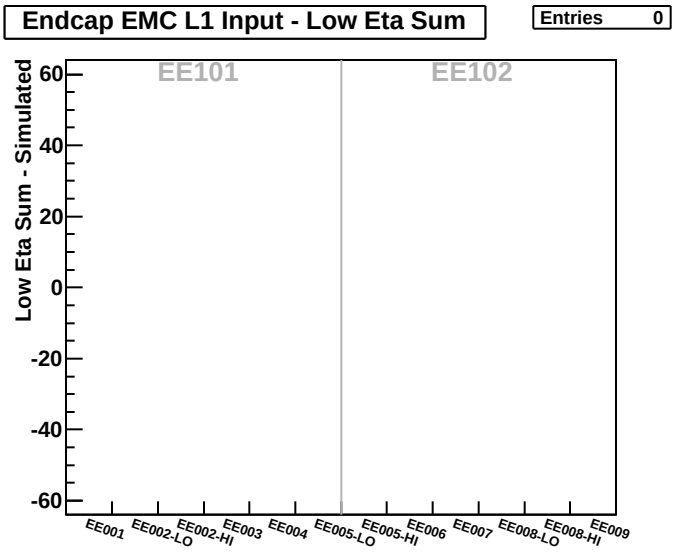

Barrel EMC L1 Input - High Eta Sum
Entries 1.08e+08
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

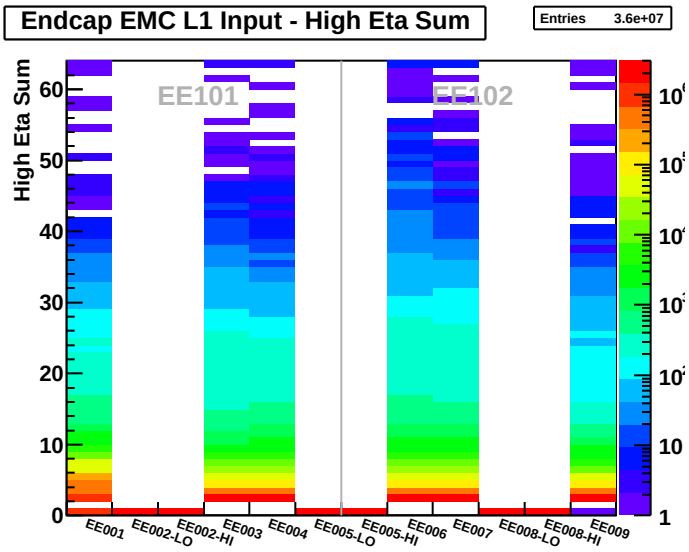

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

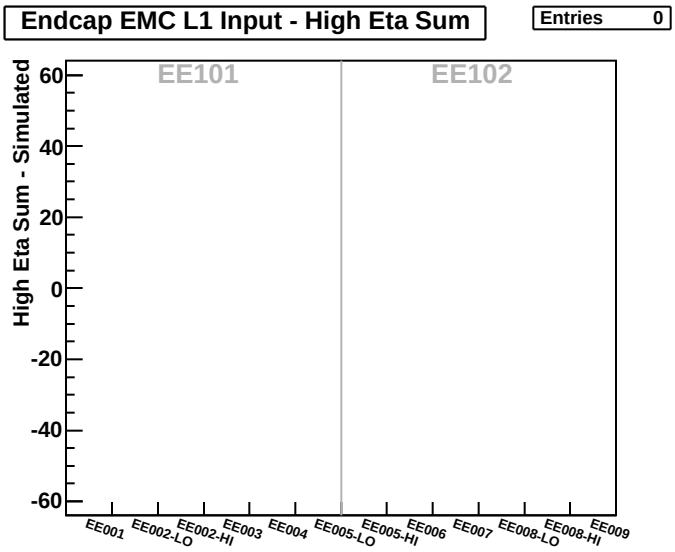

Barrel EMC L1 Input - High Tower Bits
Entries 1.08e+08
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

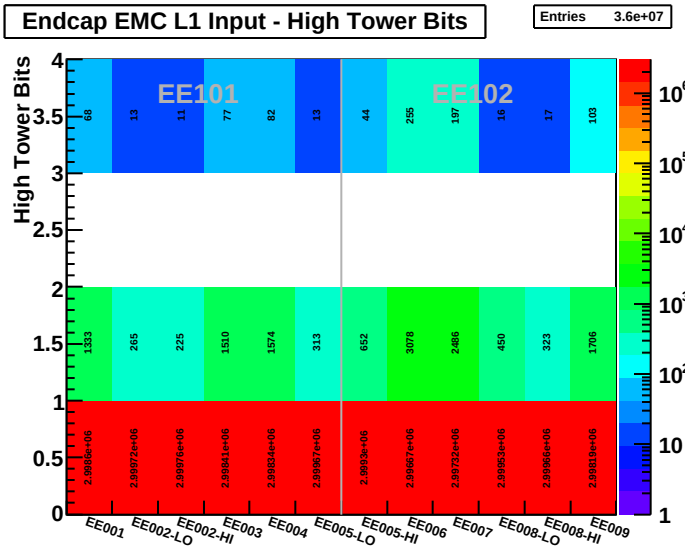

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

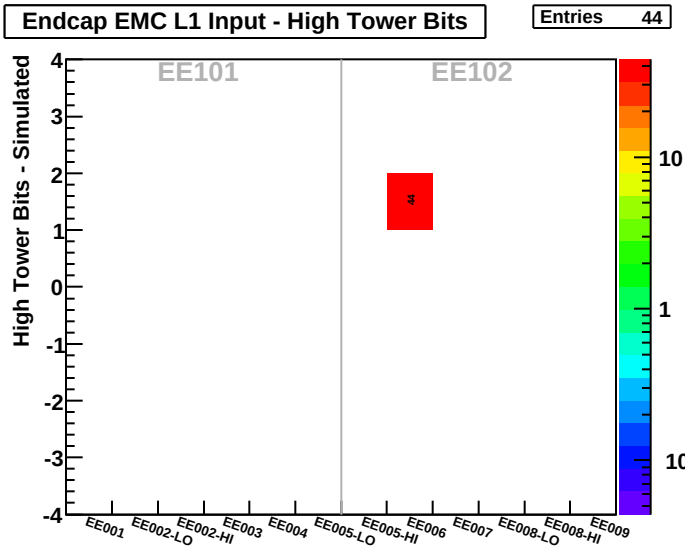

Endcap EMC L1 Input - Low Eta Sum
Entries 3.6e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

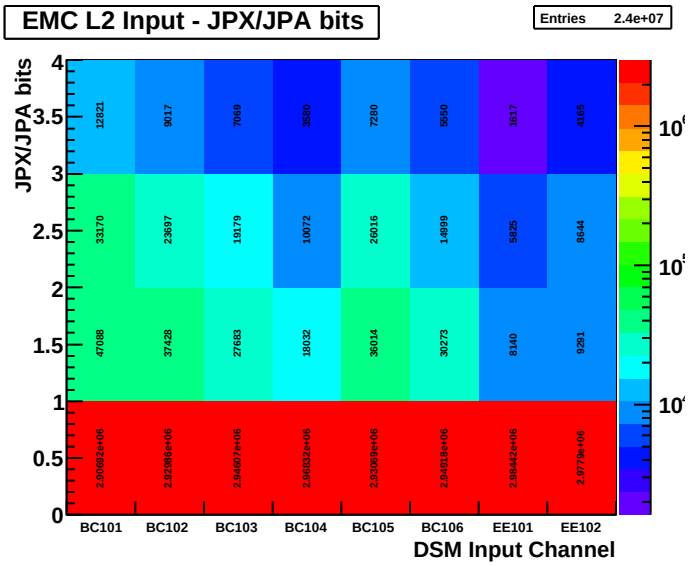

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

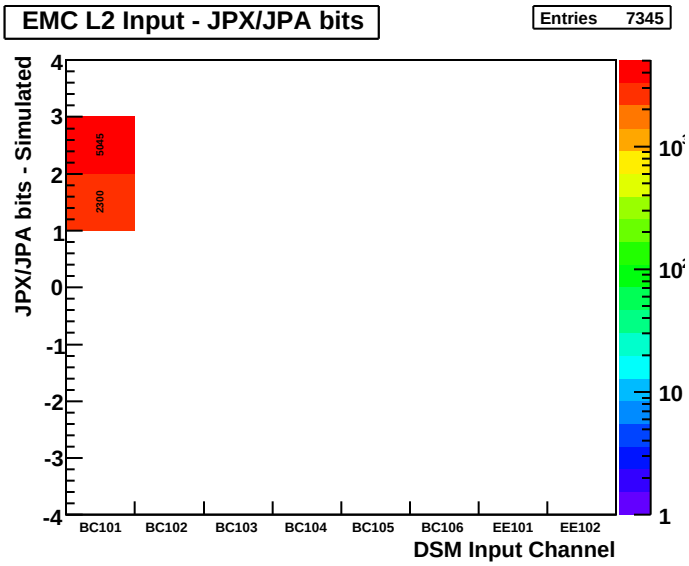

Endcap EMC L1 Input - High Eta Sum
Entries 3.6e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

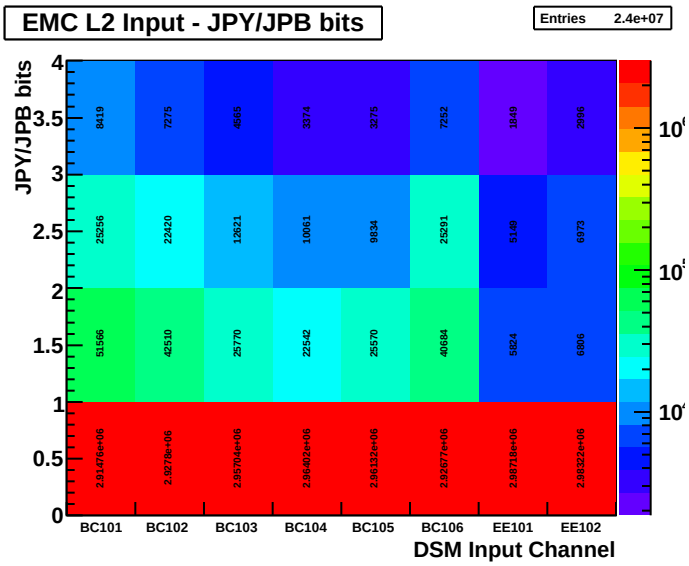

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

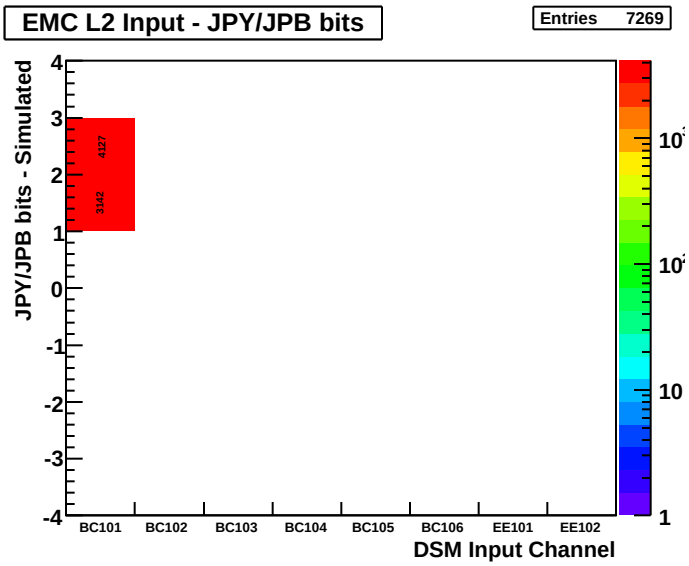

Endcap EMC L1 Input - High Tower Bits
Entries 3.6e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

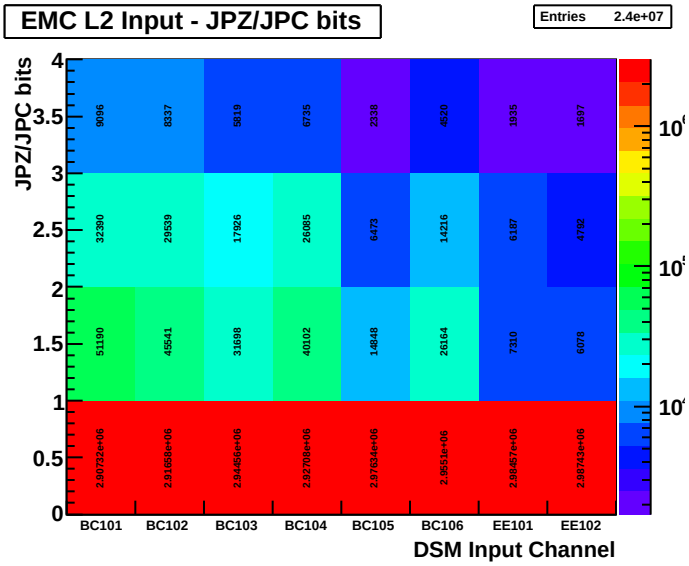

Endcap EMC L1 Input - High Tower Bits
Entries 44
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

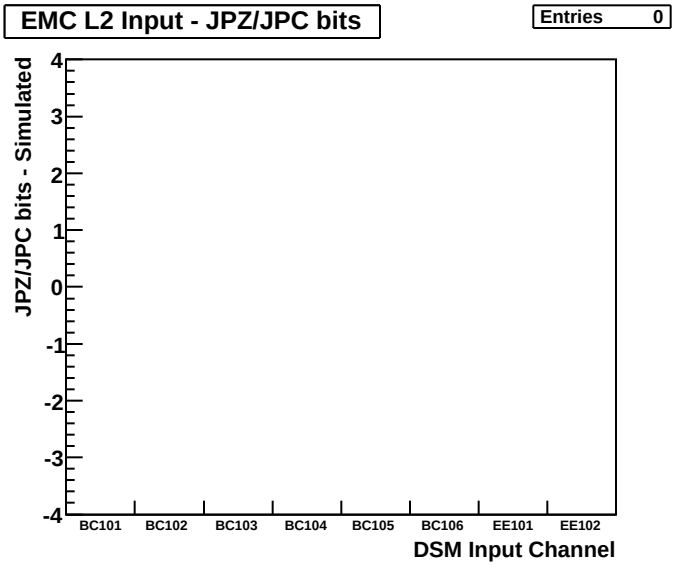

EMC L2 Input - JPX/JPA bits
Entries 2.4e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
12821 9017 7069 3580 7280 5550 1617 4165
33170 23697 19179 10072 26016 14999 5825 8644
47088 37428 27683 18032 36014 30273 8140 9291
2.90692e+06 2.92986e+06 2.94607e+06 2.96832e+06 2.93069e+06 2.94918e+06 2.98442e+06 2.9779e+06

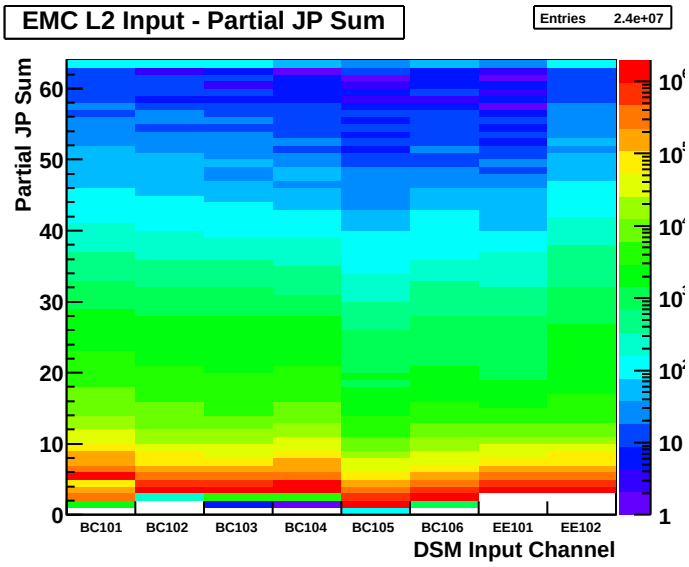

EMC L2 Input - JPX/JPA bits
Entries 7345
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5045
2300

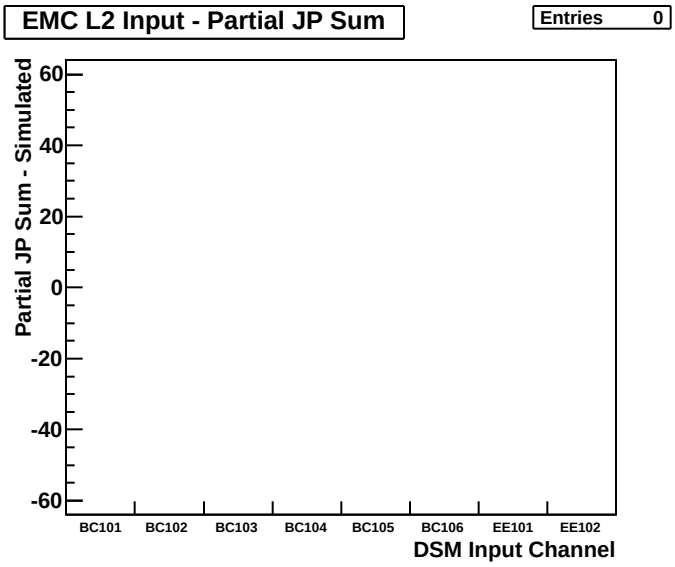

EMC L2 Input - JPY/JPB bits
Entries 2.4e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
8419 7275 4565 3374 3275 7252 1849 2996
29256 23420 12621 10061 9834 25291 5149 6973
51566 42510 25770 22542 25570 40684 5824 6806
2.91476e+06 2.9278e+06 2.95704e+06 2.96402e+06 2.96132e+06 2.92677e+06 2.98718e+06 2.98322e+06

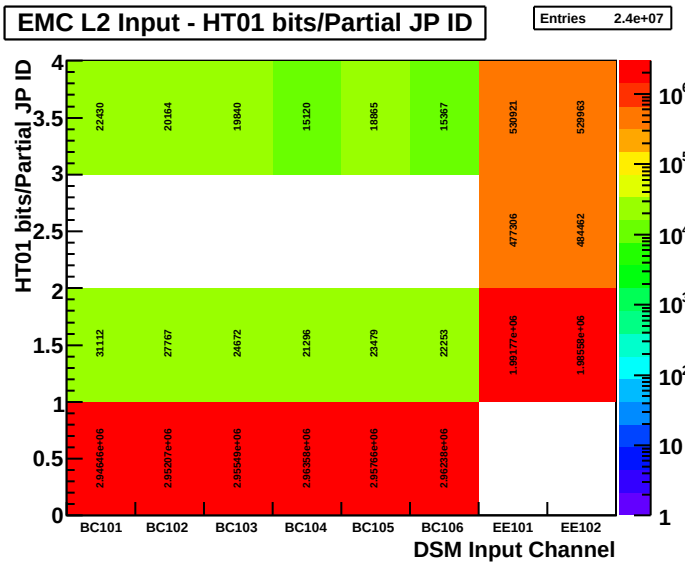

EMC L2 Input - JPY/JPB bits
Entries 7269
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4127
3142

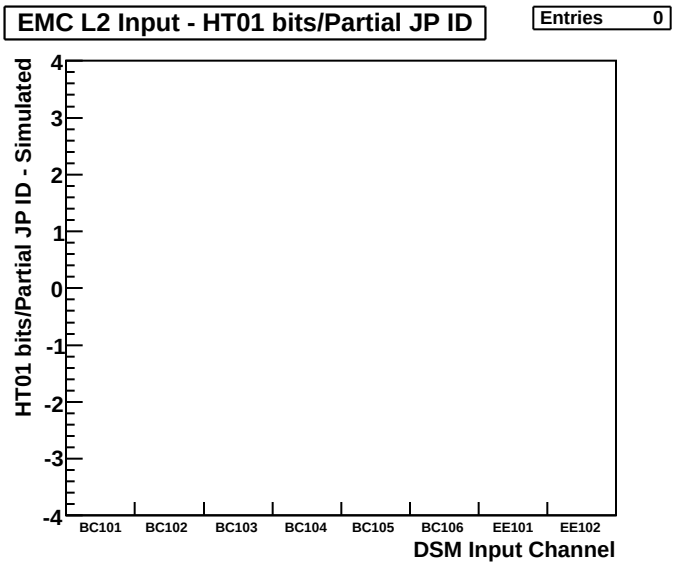

EMC L2 Input - JPZ/JPC bits
Entries 2.4e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
9096 8337 5819 6735 2338 4528 1935 1697
32390 29539 17926 26085 6473 14216 6187 4732
51190 45541 31698 40102 14848 26164 7310 6078
2.90732e+06 2.91658e+06 2.94456e+06 2.92708e+06 2.97634e+06 2.9551e+06 2.98457e+06 2.98743e+06

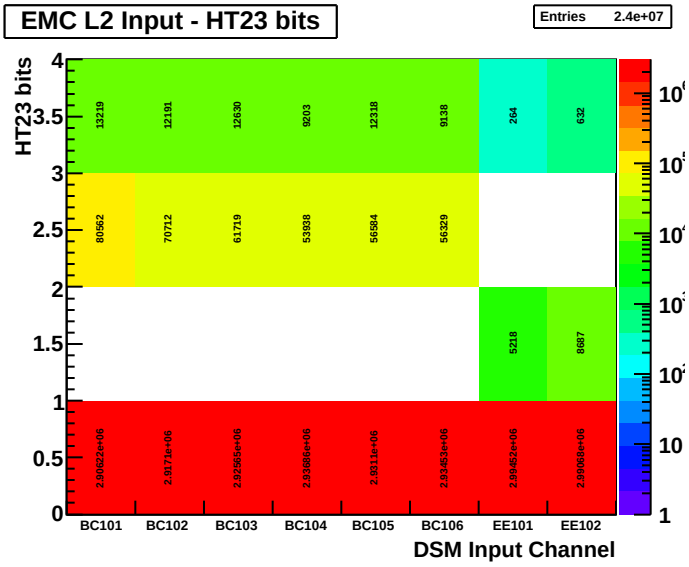

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

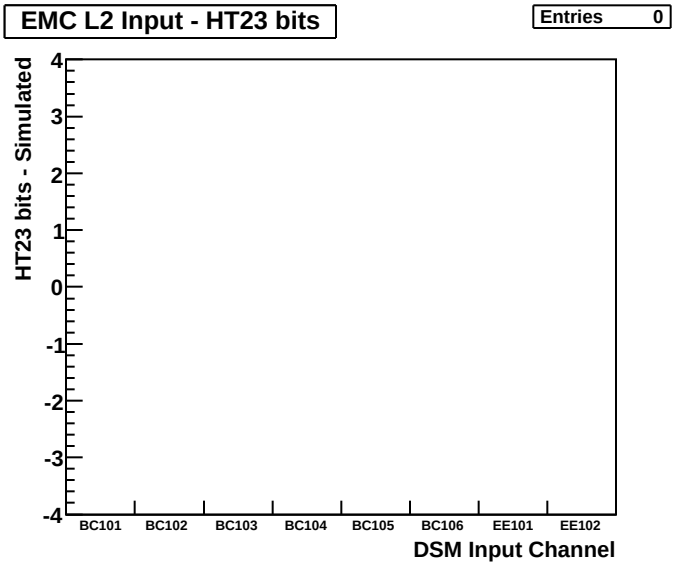

EMC L2 Input - Partial JP Sum
Entries 2.4e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

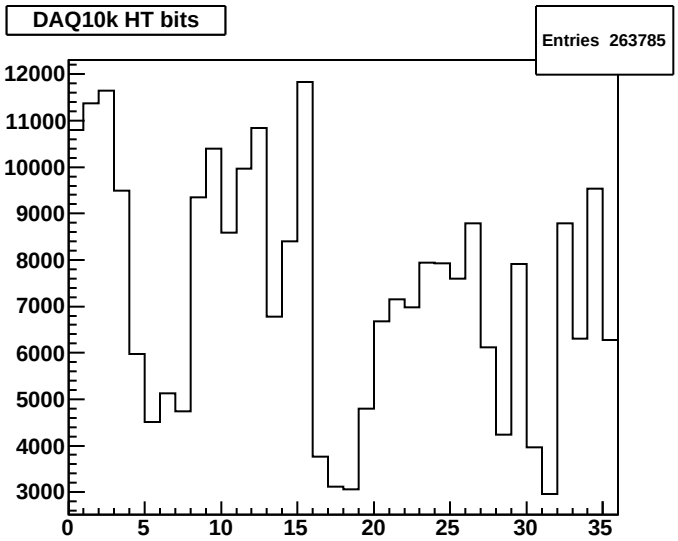

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

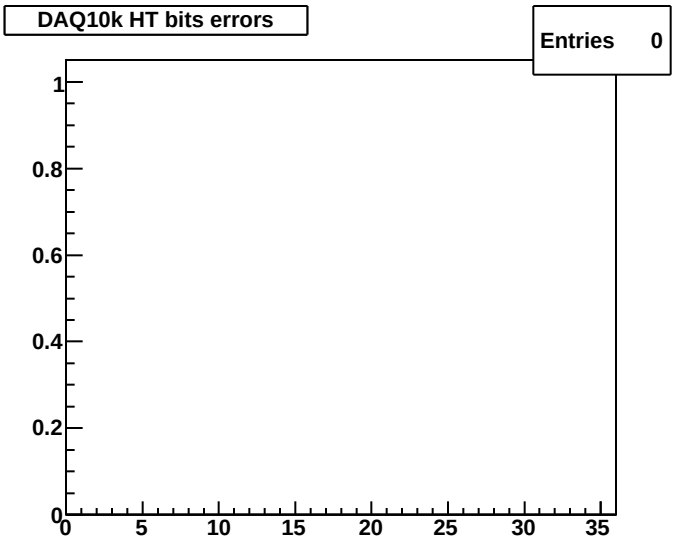

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2.4e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

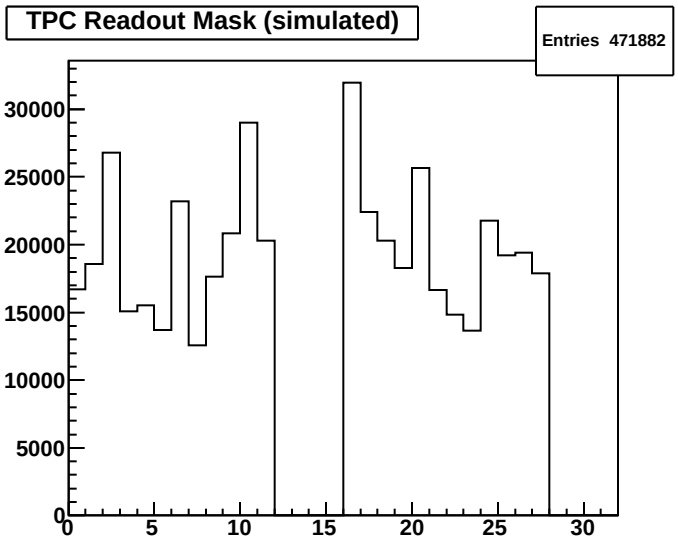

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

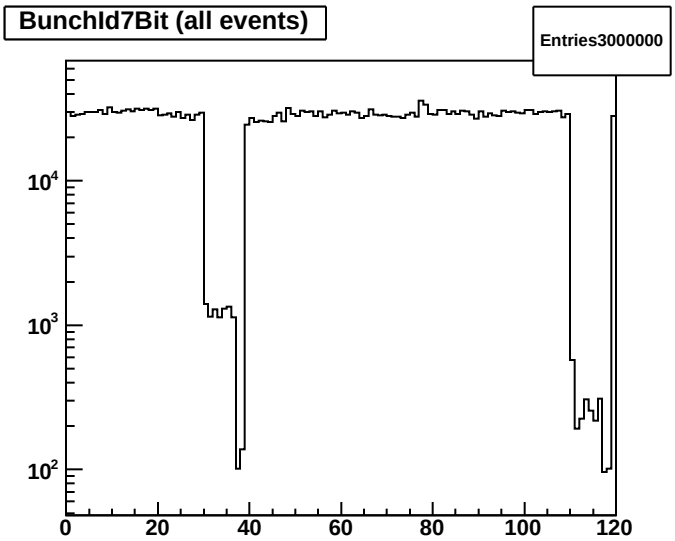

EMC L2 Input - HT23 bits
Entries 2.4e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

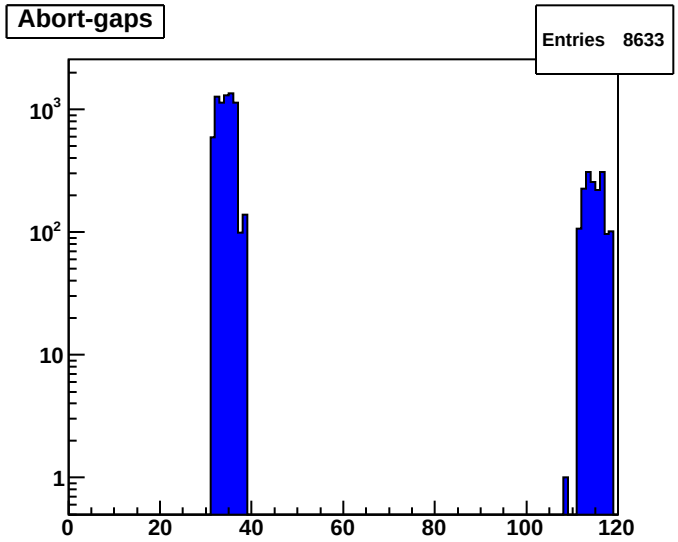

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

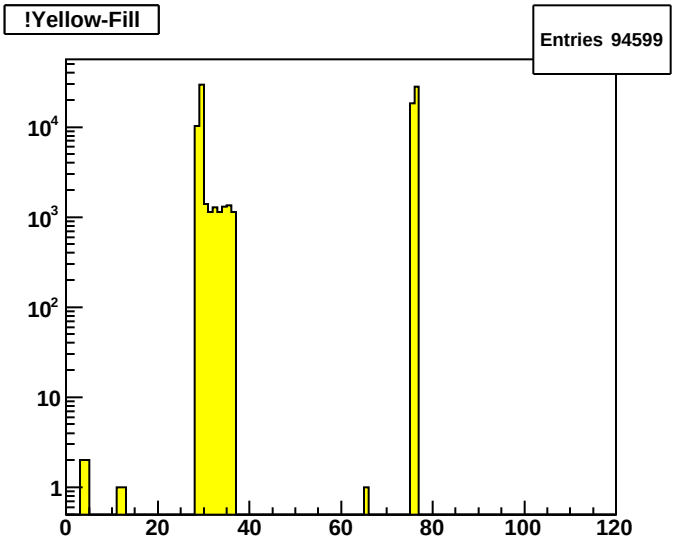

DAQ10k HT bits
Entries 263785

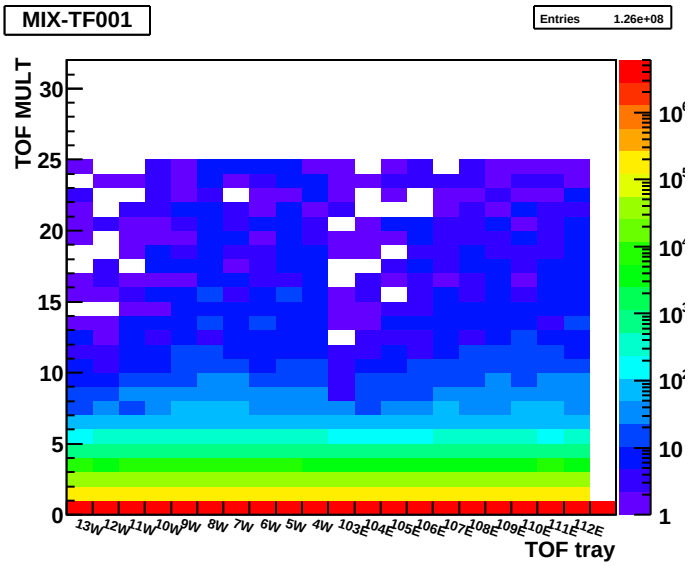

DAQ10k HT bits errors
Entries 0

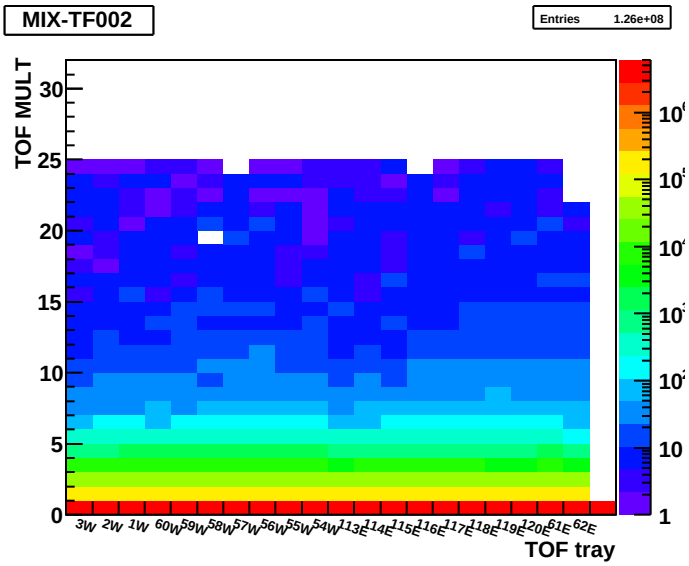

TPC Readout Mask (simulated)
Entries 471882

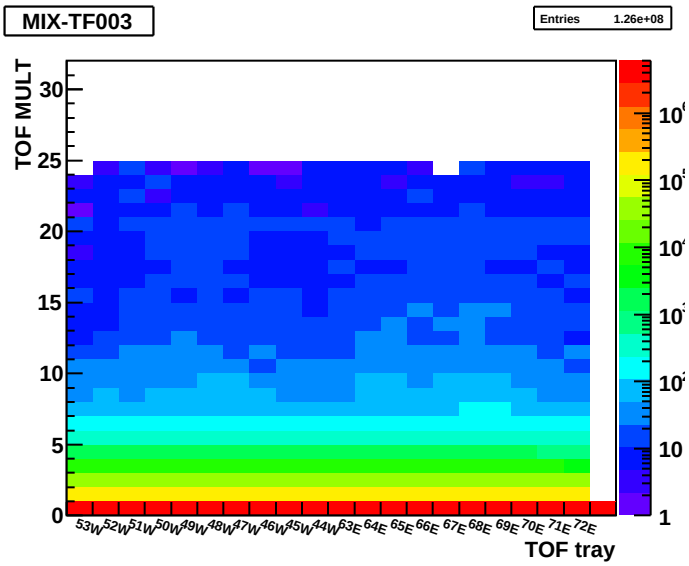

BunchId7Bit (all events)
Entries3000000

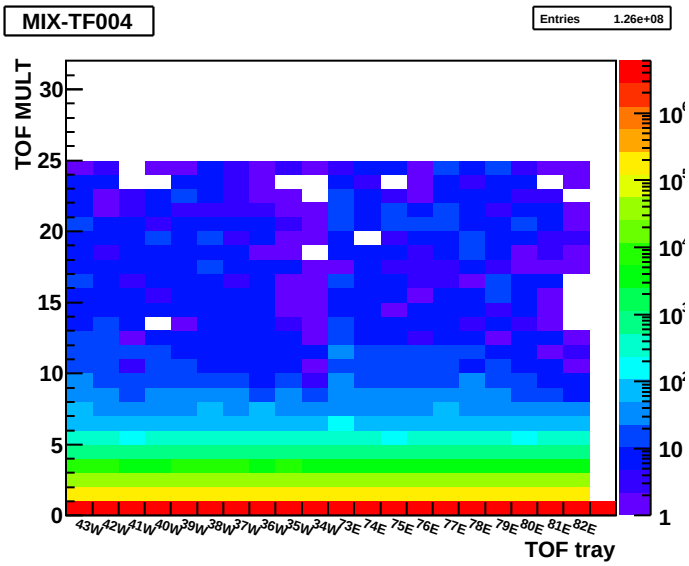

Abort-gaps
Entries 8633

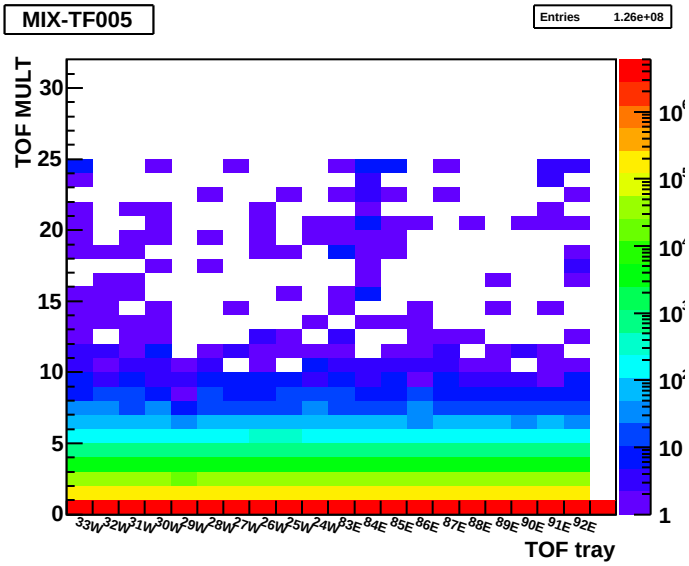

!Yellow-Fill
Entries 94599

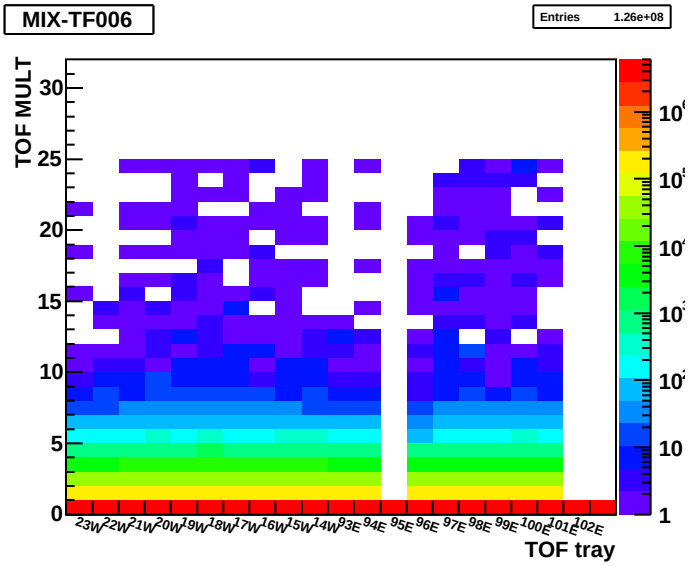

MIX-TF001
Entries 1.26e+08
TOF MULT
TOF tray

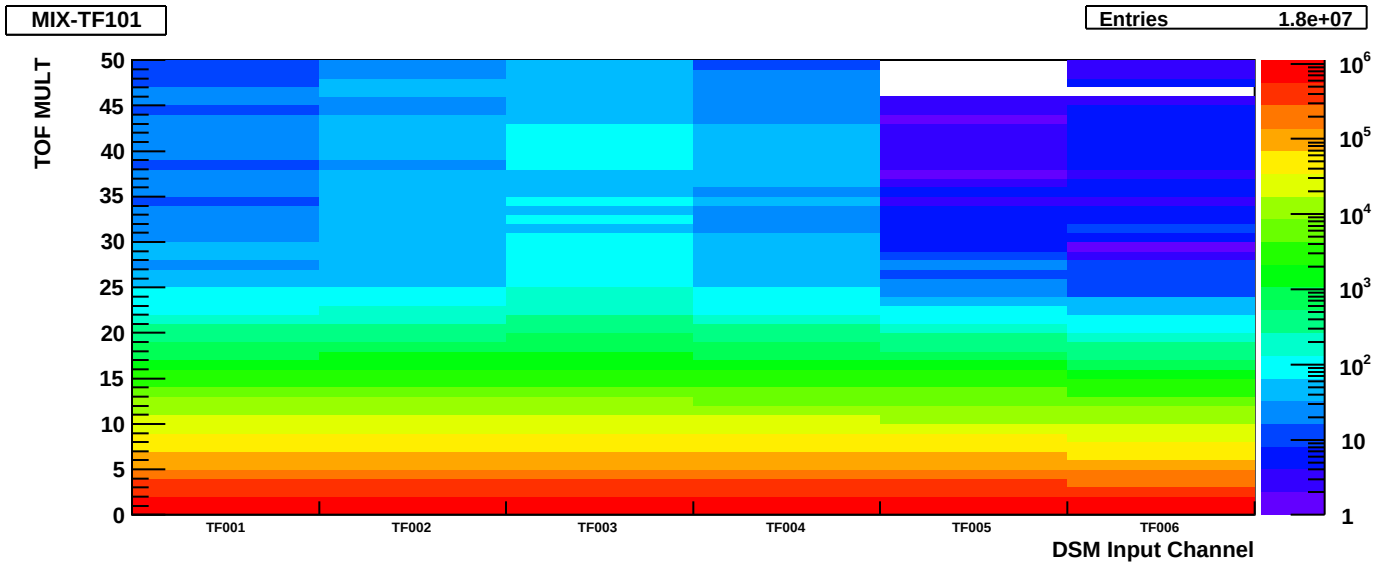

MIX-TF002
Entries 1.26e+08
TOF MULT
TOF tray

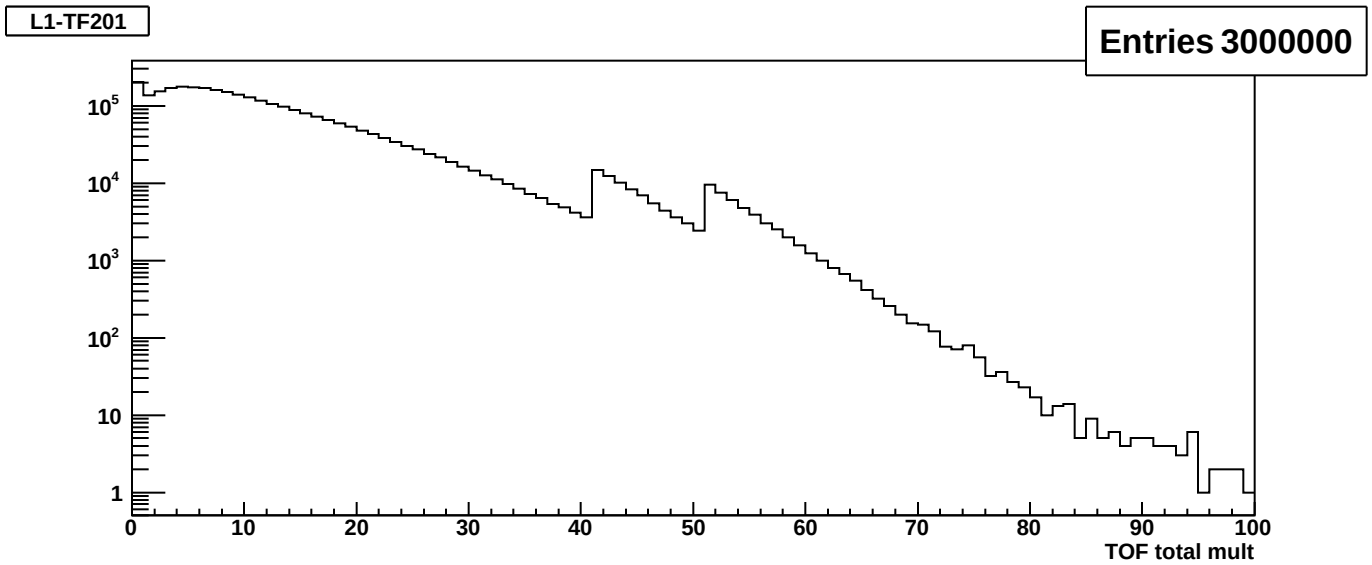

MIX-TF003
Entries 1.26e+08
TOF MULT
TOF tray

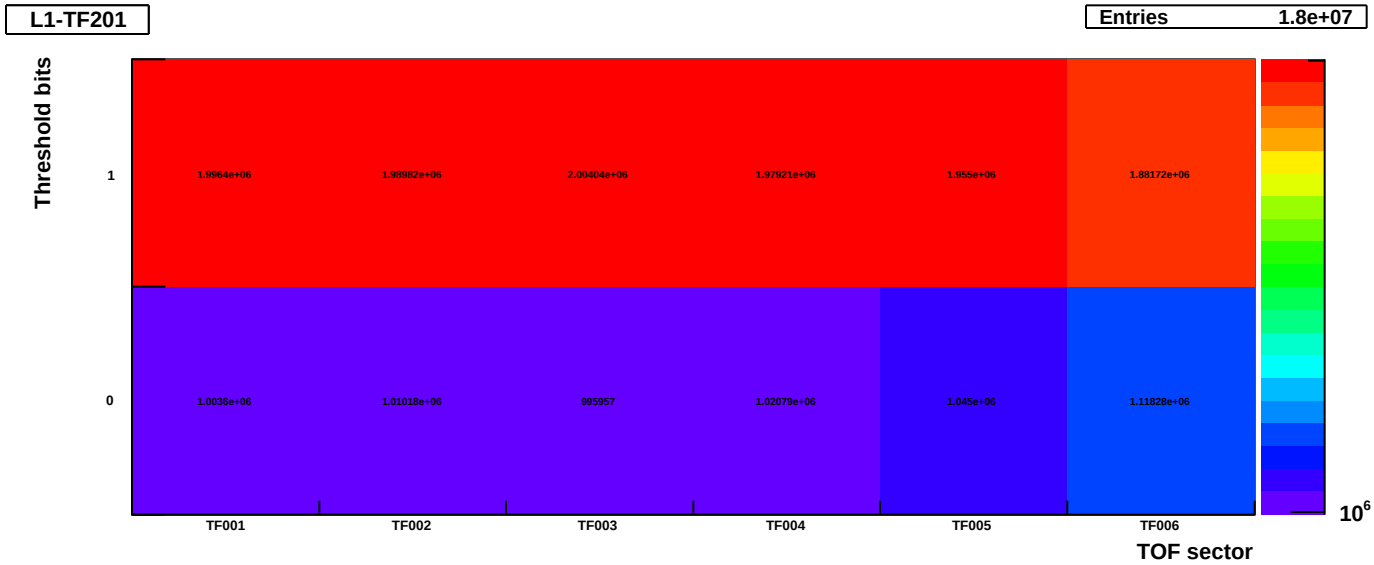

MIX-TF004
Entries 1.26e+08
TOF MULT
TOF tray

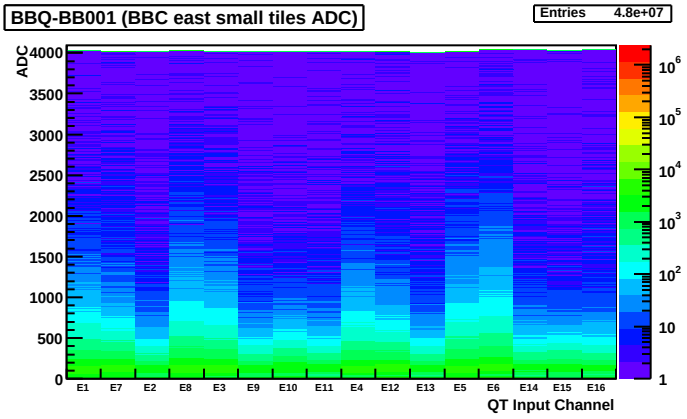

MIX-TF005
Entries 1.26e+08
TOF MULT
TOF tray

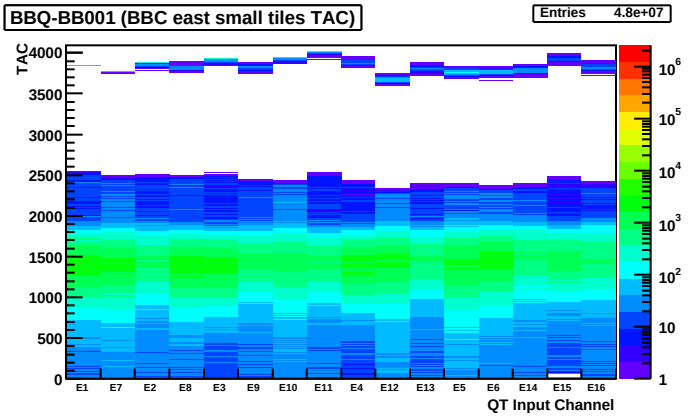

MIX-TF006
Entries 1.26e+08
TOF MULT
TOF tray

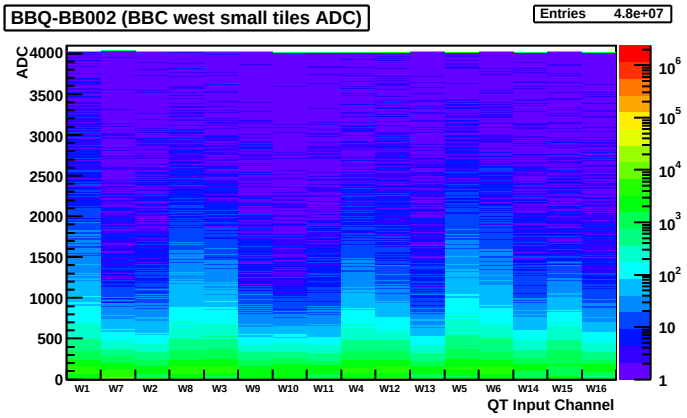

MIX-TF101
Entries 1.8e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

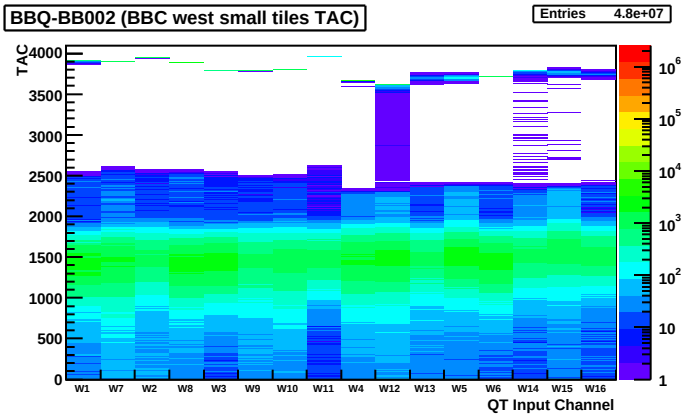

L1-TF201
Entries 3000000
TOF total mult

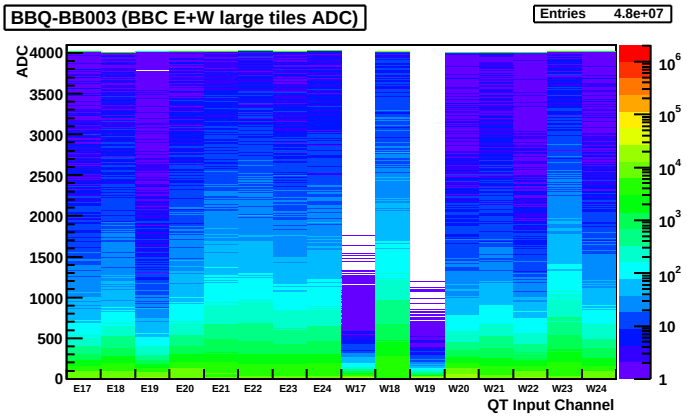

L1-TF201
Entries 1.8e+07
Threshold bits
1.9964e+06
1.98982e+06
2.00404e+06
1.97921e+06
1.955e+06
1.88172e+06
1.0036e+06
1.01018e+06
995957
1.02079e+06
1.045e+06
1.11828e+06
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

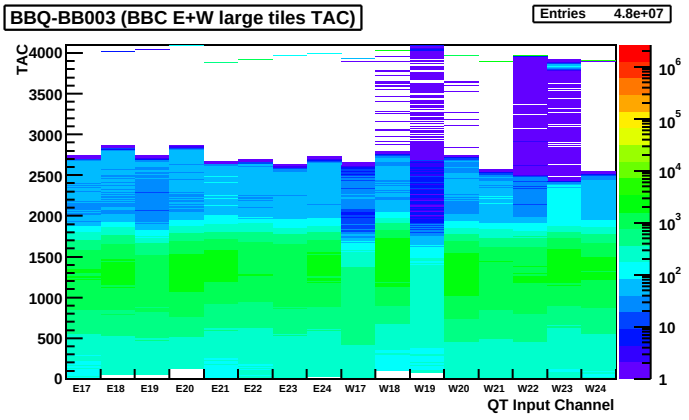

BBQ-BB001 (BBC east small tiles ADC)
Entries 4.8e+07
ADC
QT Input Channel

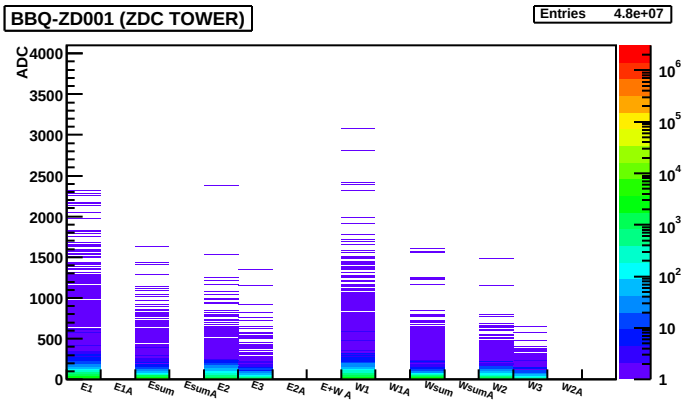

BBQ-BB001 (BBC east small tiles TAC)
Entries 4.8e+07
TAC
QT Input Channel

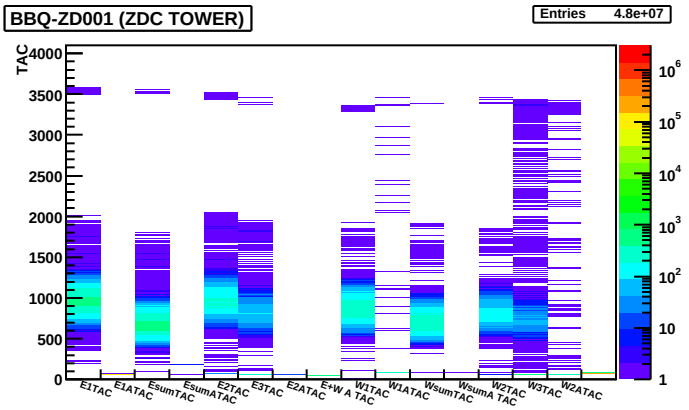

BBQ-BB002 (BBC west small tiles ADC)
Entries 4.8e+07
ADC
QT Input Channel

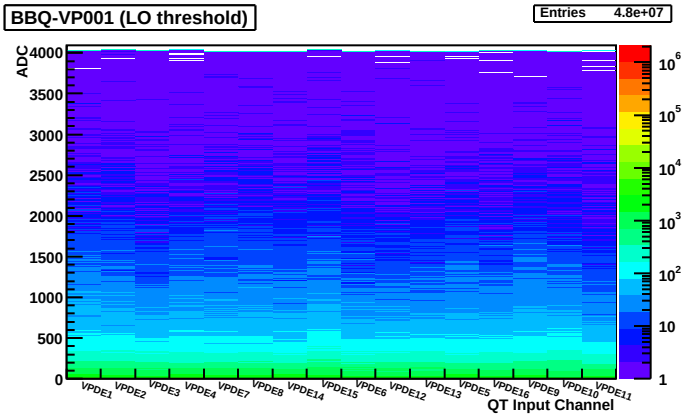

BBQ-BB002 (BBC west small tiles TAC)
Entries 4.8e+07
TAC
QT Input Channel

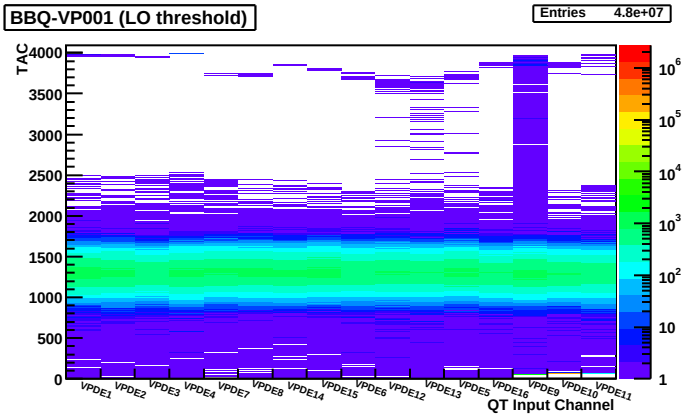

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4.8e+07
ADC
QT Input Channel

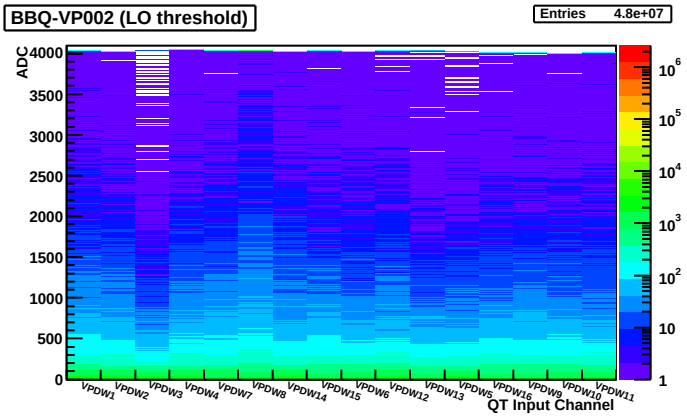

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4.8e+07
TAC
QT Input Channel

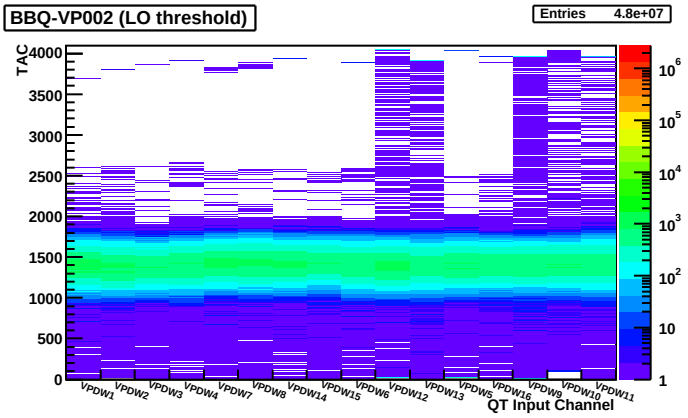

BBQ-ZD001 (ZDC TOWER)
Entries 4.8e+07
ADC

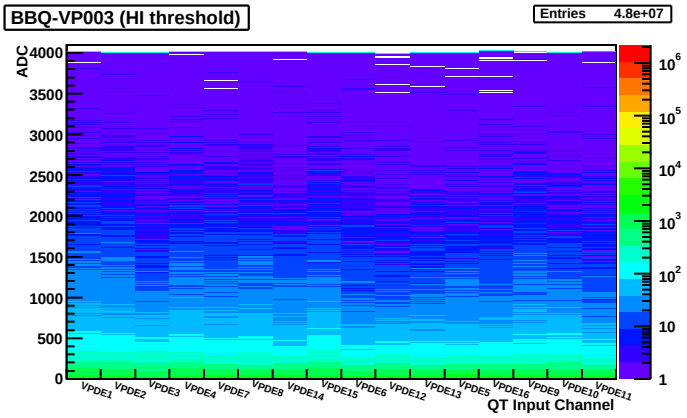

BBQ-ZD001 (ZDC TOWER)
Entries 4.8e+07
TAC

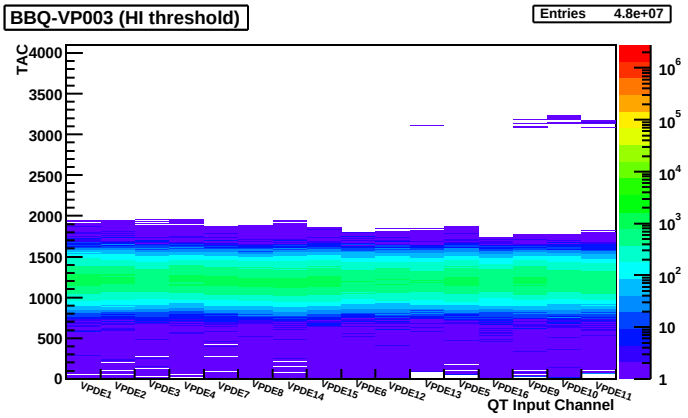

BBQ-VP001 (LO threshold)
Entries 4.8e+07
ADC
QT Input Channel

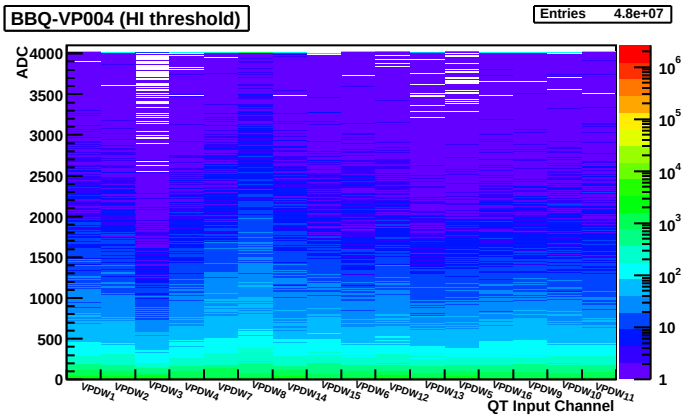

BBQ-VP001 (LO threshold)
Entries 4.8e+07
TAC
QT Input Channel

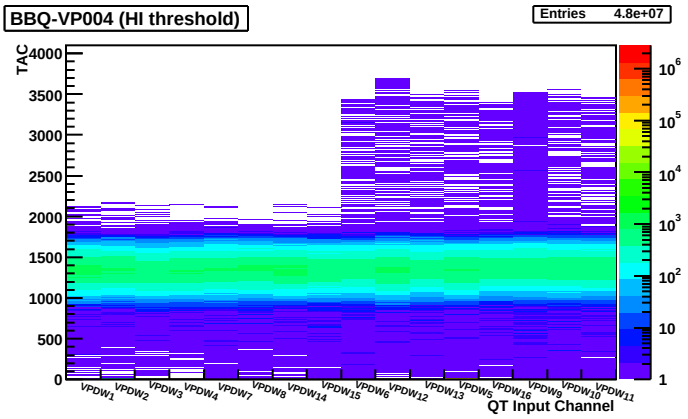

BBQ-VP002 (LO threshold)
Entries 4.8e+07
ADC
QT Input Channel

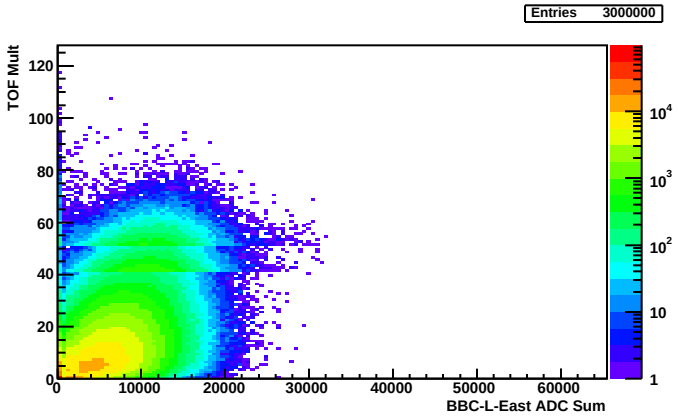

BBQ-VP002 (LO threshold)
Entries 4.8e+07
TAC
QT Input Channel

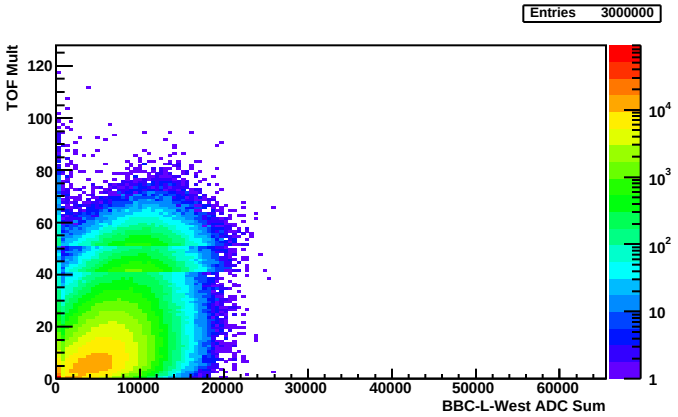

BBQ-VP003 (HI threshold)
Entries 4.8e+07
ADC
QT Input Channel

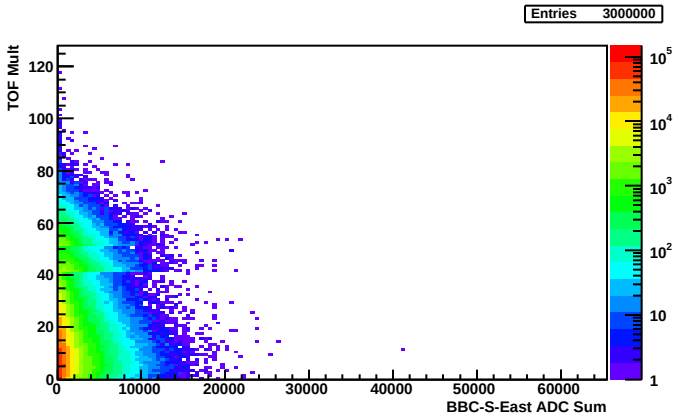

BBQ-VP003 (HI threshold)
Entries 4.8e+07
TAC
QT Input Channel

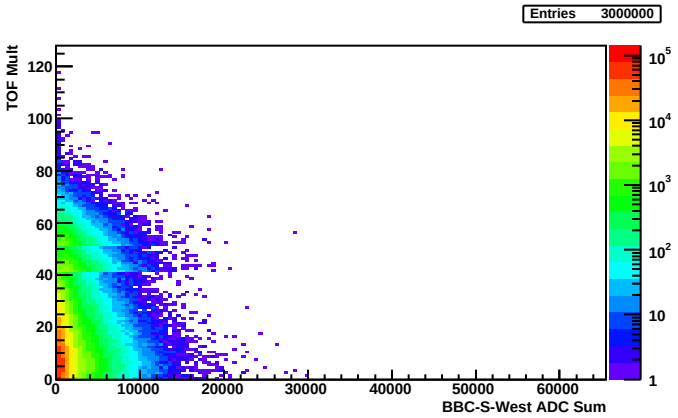

BBQ-VP004 (HI threshold)
Entries 4.8e+07
ADC
QT Input Channel

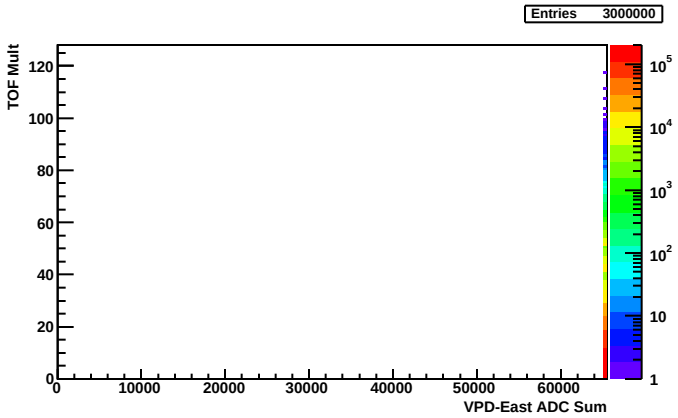

BBQ-VP004 (HI threshold)
Entries 4.8e+07
TAC
QT Input Channel

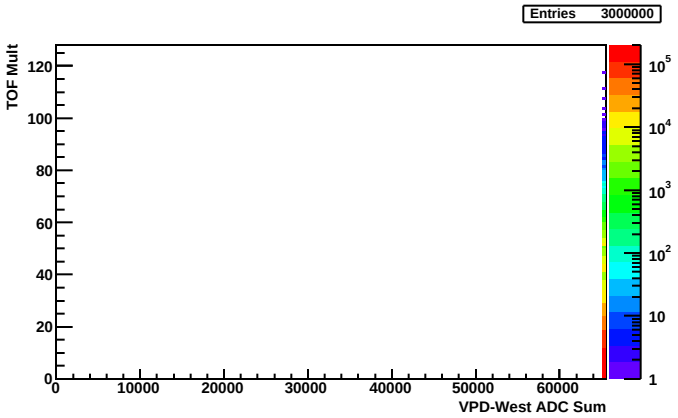

Entries 3000000
TOF Mult
BBC-L-East ADC Sum

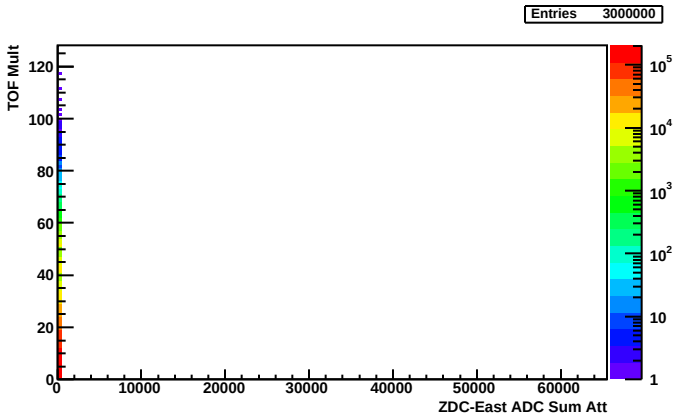

Entries 3000000
TOF Mult
BBC-L-West ADC Sum

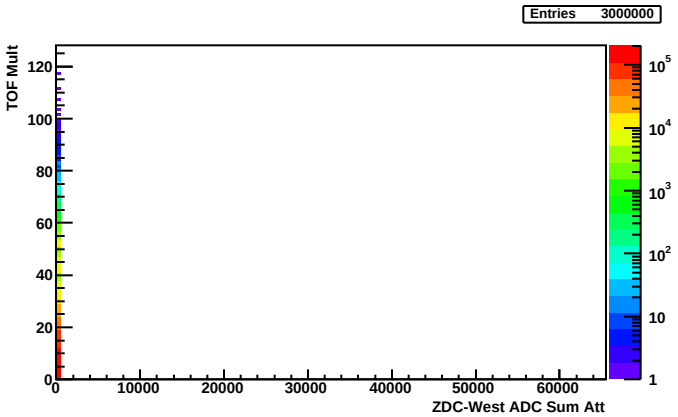

Entries 3000000
TOF Mult
BBC-S-East ADC Sum

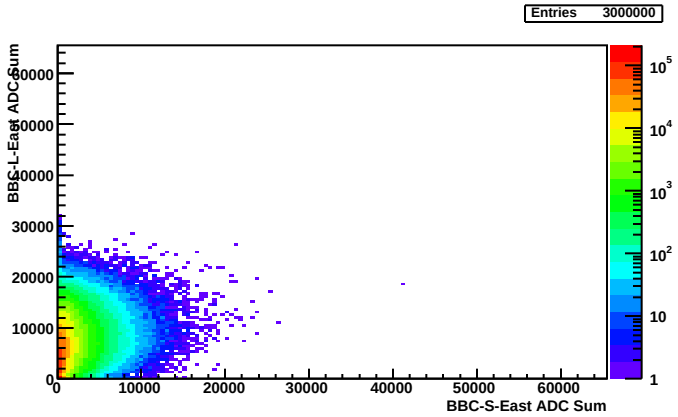

Entries 3000000
TOF Mult
BBC-S-West ADC Sum

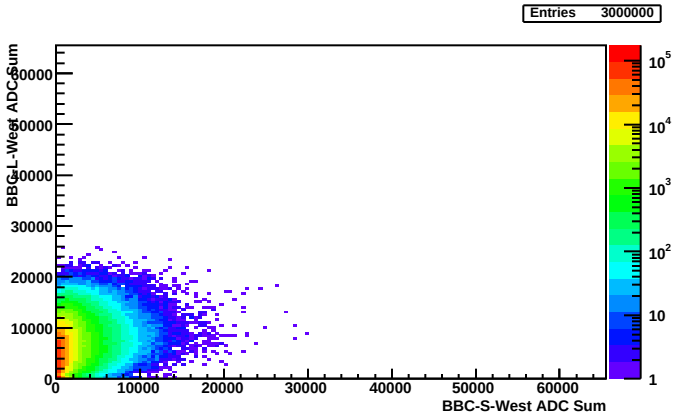

Entries 3000000
TOF Mult
VPD-East ADC Sum

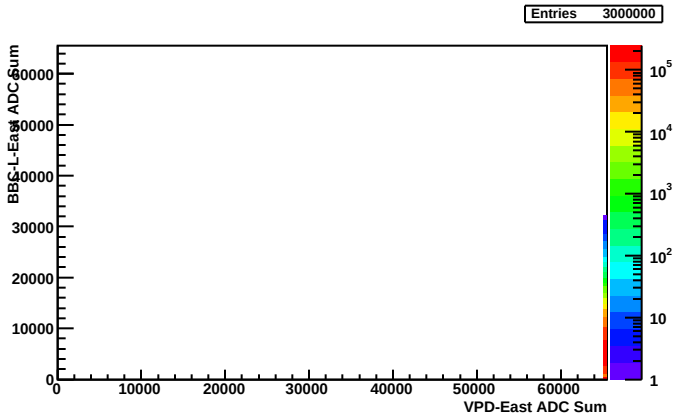

Entries 3000000
TOF Mult
VPD-West ADC Sum

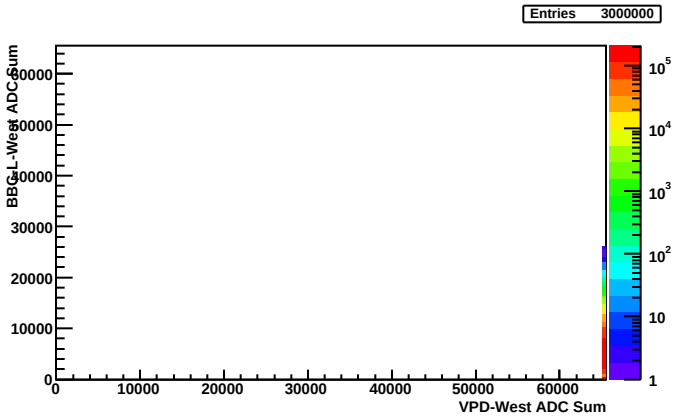

Entries 3000000
TOF Mult
ZDC-East ADC Sum Att

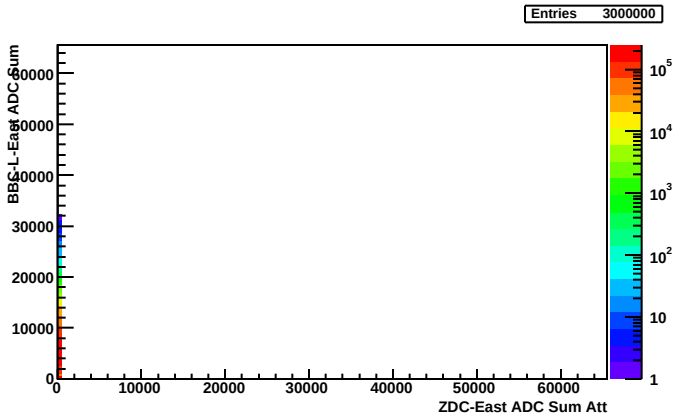

Entries 3000000
TOF Mult
ZDC-West ADC Sum Att

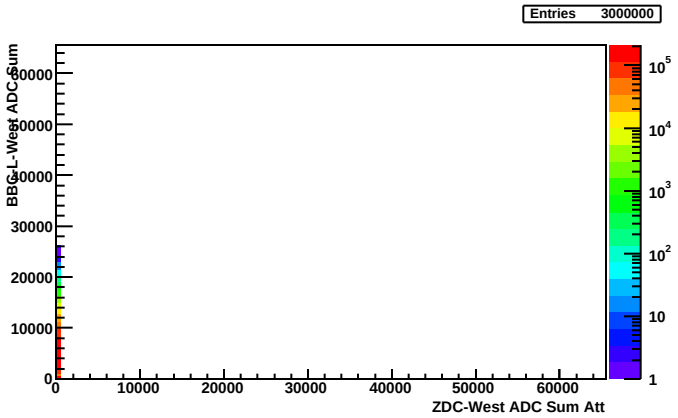

Entries 3000000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

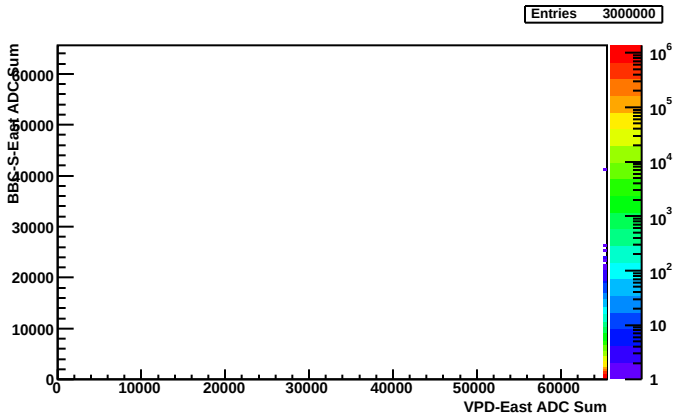

Entries 3000000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

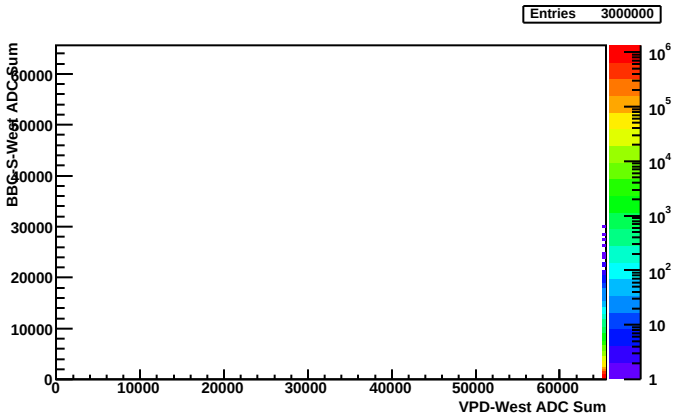

Entries 3000000
BBC-L-East ADC Sum
VPD-East ADC Sum

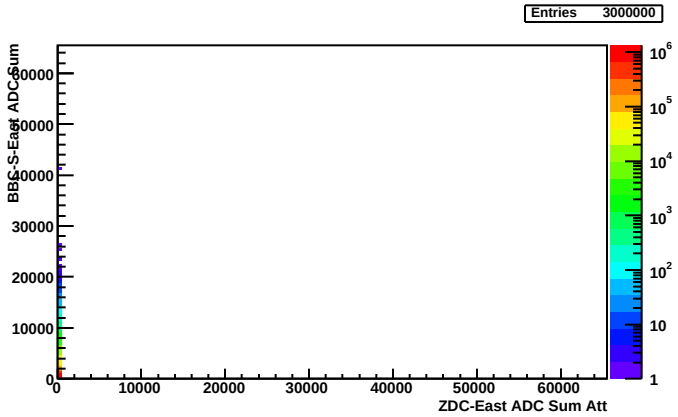

Entries 3000000
BBC-L-West ADC Sum
VPD-West ADC Sum

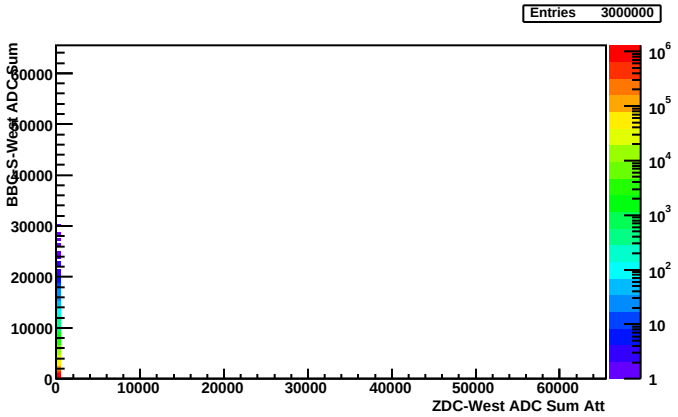

Entries 3000000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

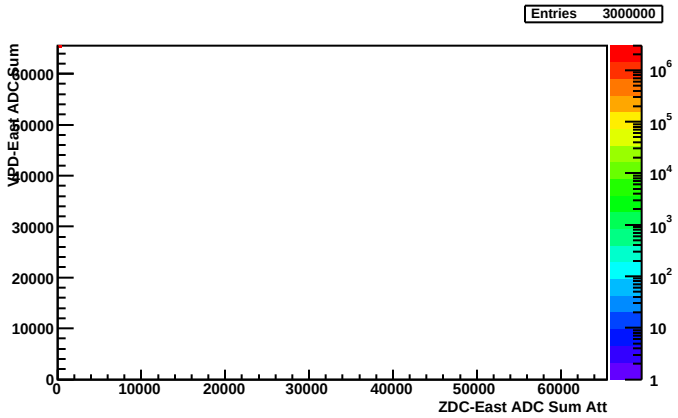

Entries 3000000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

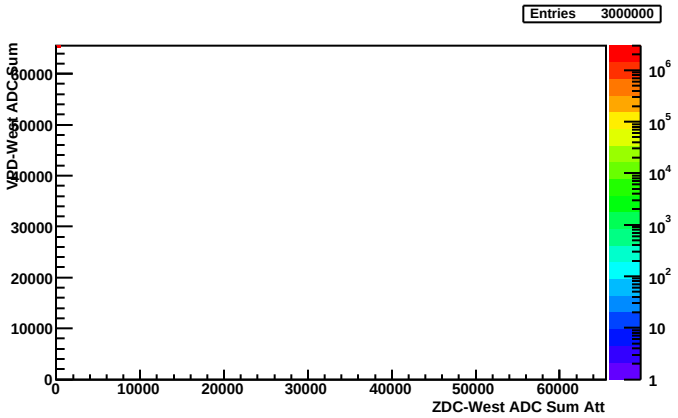

Entries 3000000
BBC-S-East ADC Sum
VPD-East ADC Sum

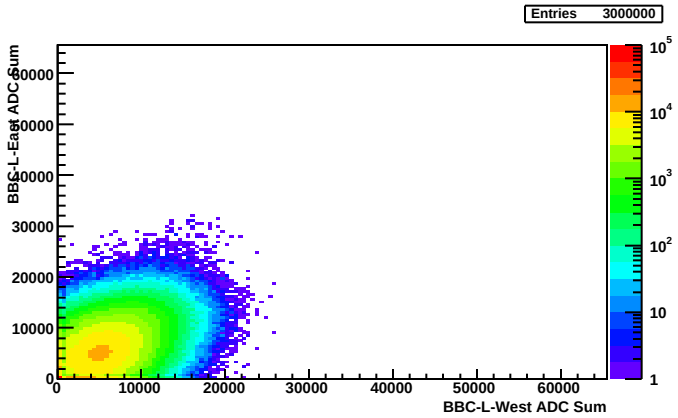

Entries 3000000
BBC-S-West ADC Sum
VPD-West ADC Sum

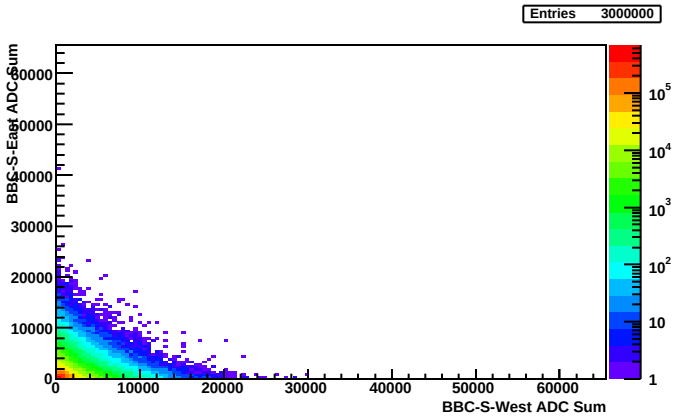

Entries 3000000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

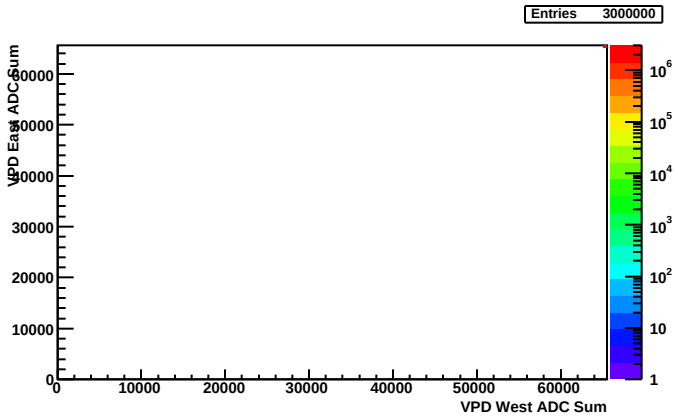

Entries 3000000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

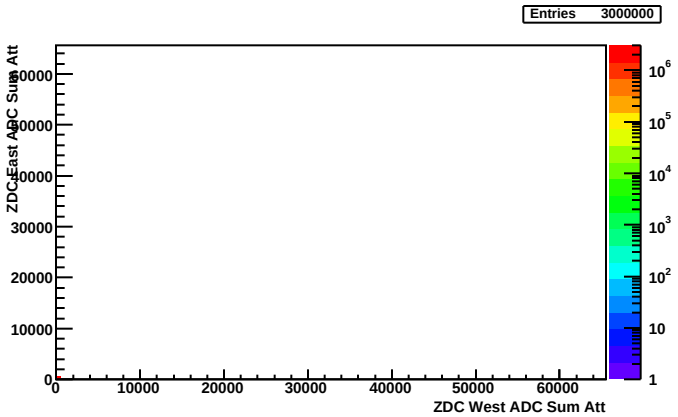

Entries 3000000
VPD-East ADC Sum
ZDC-East ADC Sum Att

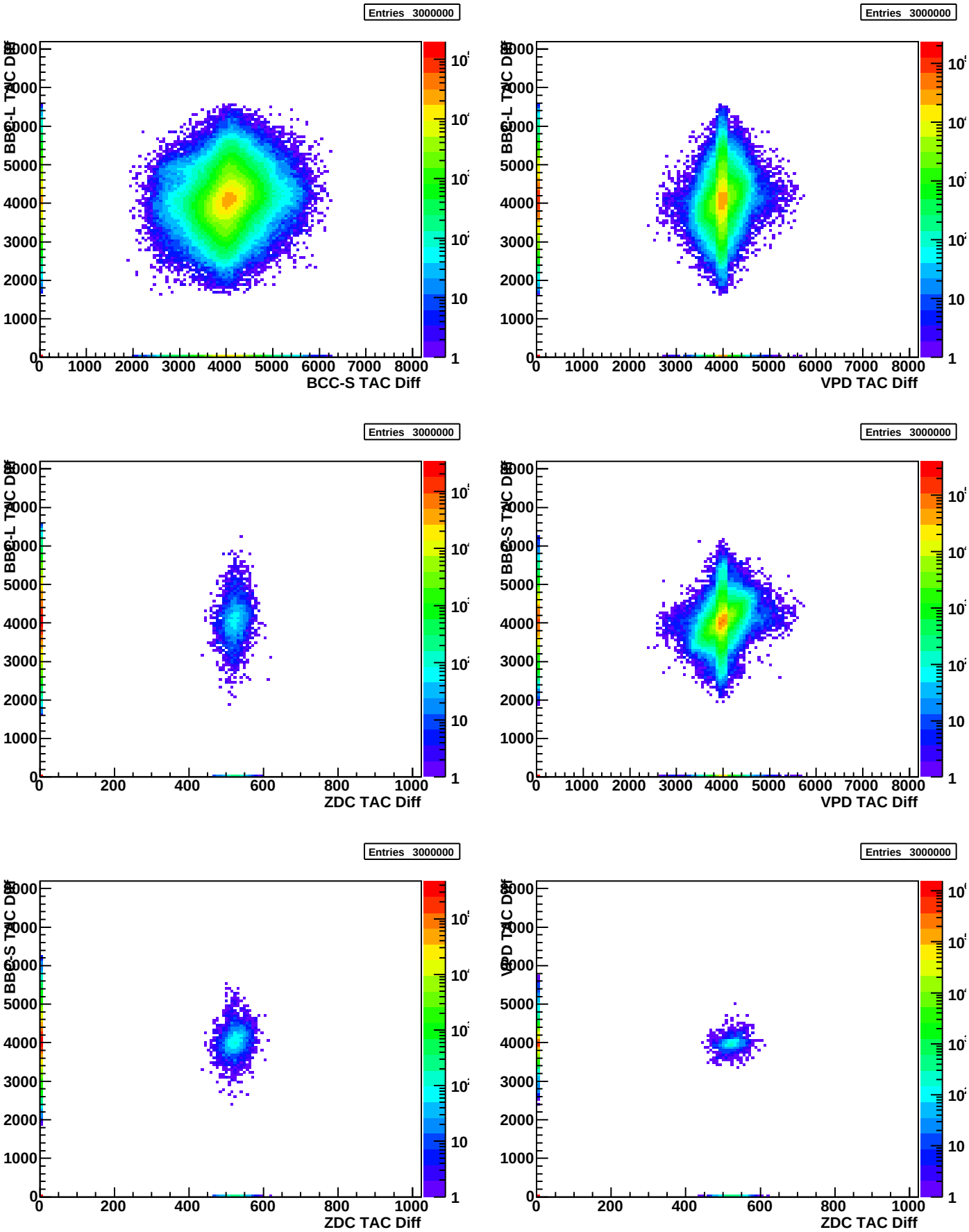

Entries 3000000
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 3000000
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 3000000
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 3000000
VPD East ADC Sum
VPD West ADC Sum


Entries 3000000
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 3000000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 3000000
BBC-L TAC Diff
VPD TAC Diff
Entries 3000000
BBC-L TAC Diff
ZDC TAC Diff
Entries 3000000
BBC-S TAC Diff
VPD TAC Diff
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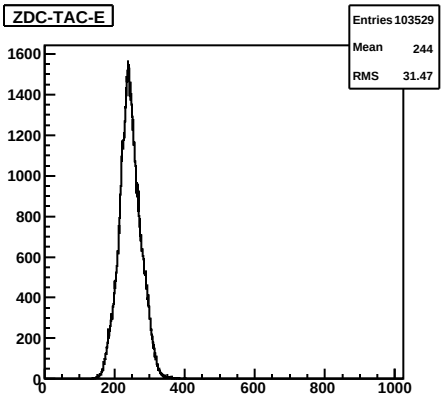
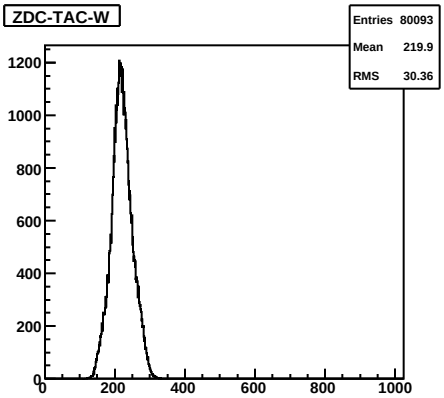
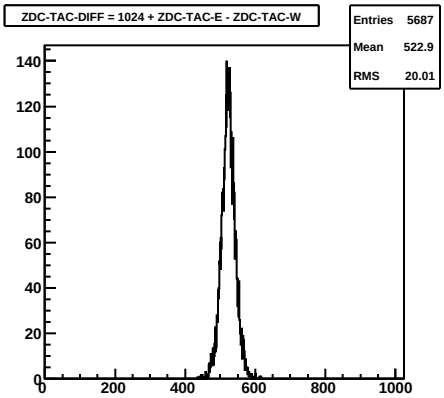
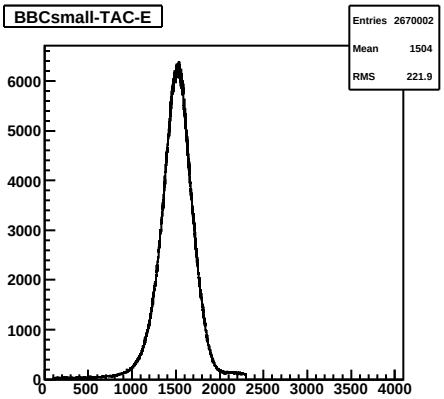
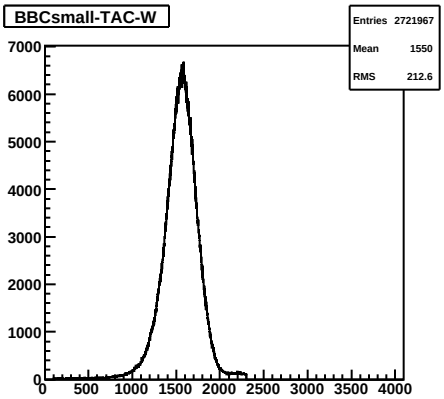
BBC-S TAC Diff
ZDC TAC Diff
Entries 3000000
VPD TAC Diff
ZDC TAC Diff

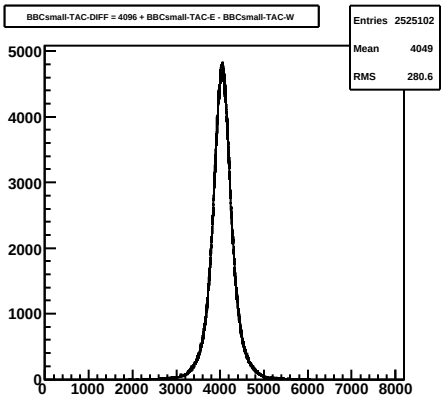

ZDC-TAC-E
Entries 103529
Mean 244
RMS 31.47

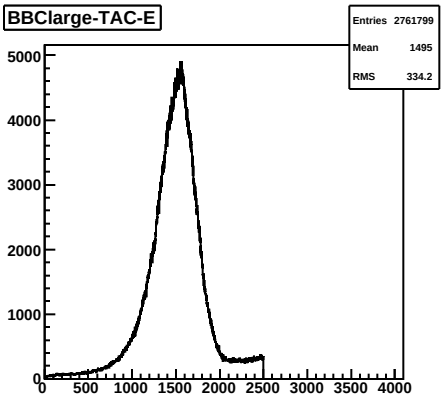

ZDC-TAC-W
Entries 80093
Mean 219.9
RMS 30.36

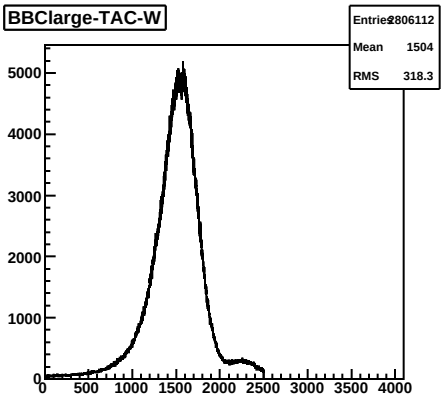

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5687
Mean 522.9
RMS 20.01

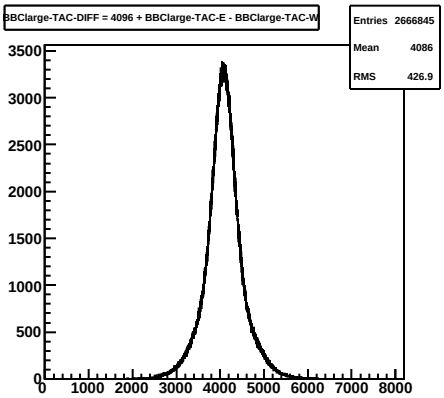

BBCsmall-TAC-E
Entries 2670002
Mean 1504
RMS 221.9

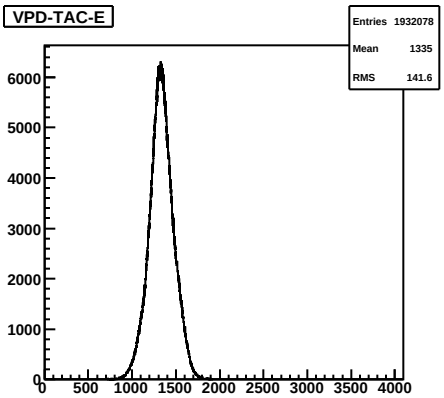

BBCsmall-TAC-W
Entries 2721967
Mean 1550
RMS 212.6

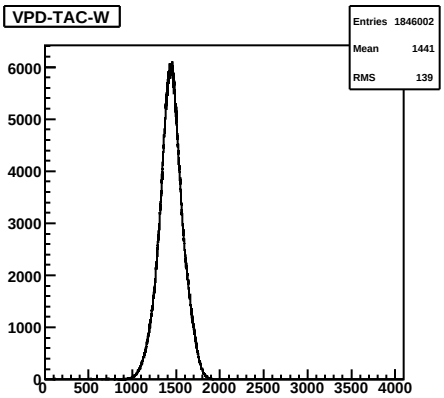

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2525102
Mean 4049
RMS 280.6

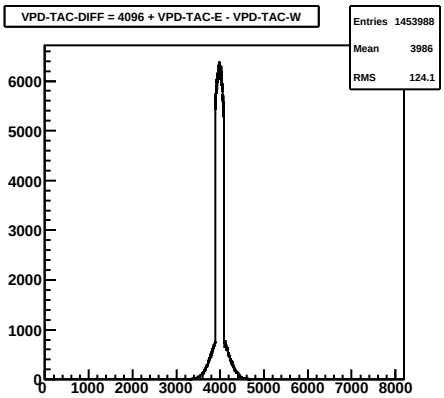

BBClarge-TAC-E
Entries 2761799
Mean 1495
RMS 334.2

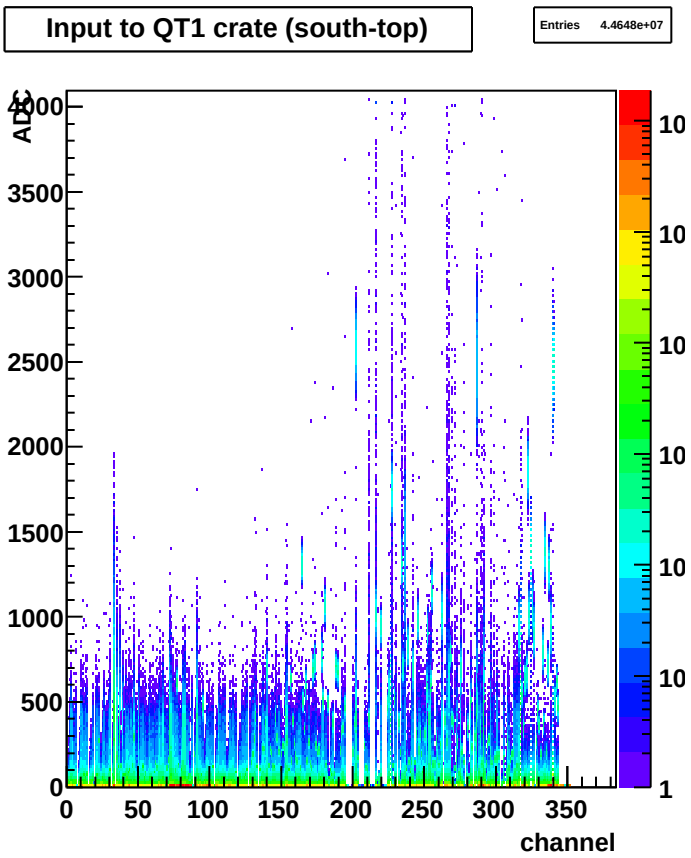

BBClarge-TAC-W
Entries 2806112
Mean 1504
RMS 318.3

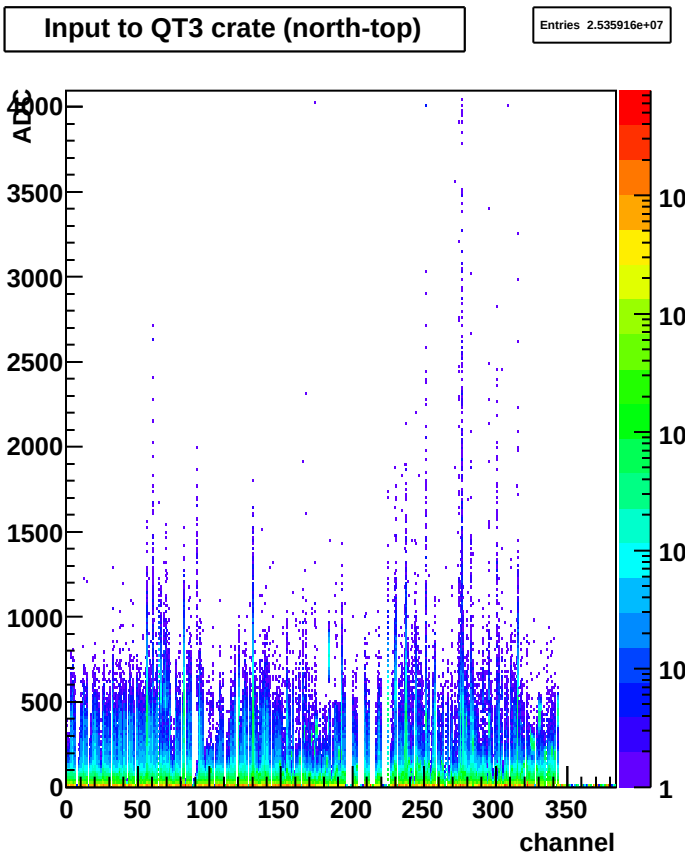

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2666845
Mean 4086
RMS 426.9

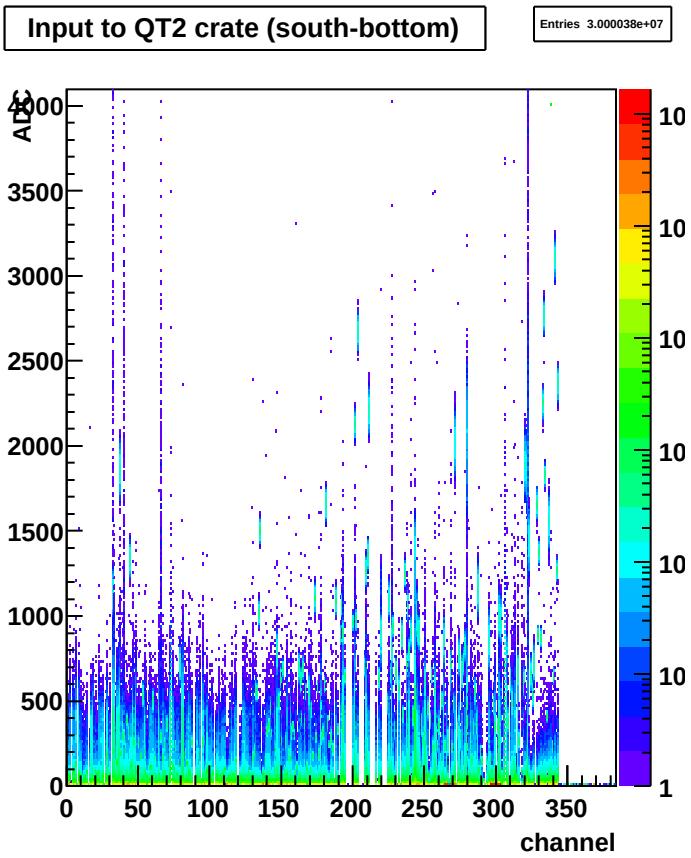

VPD-TAC-E
Entries 1932078
Mean 1335
RMS 141.6

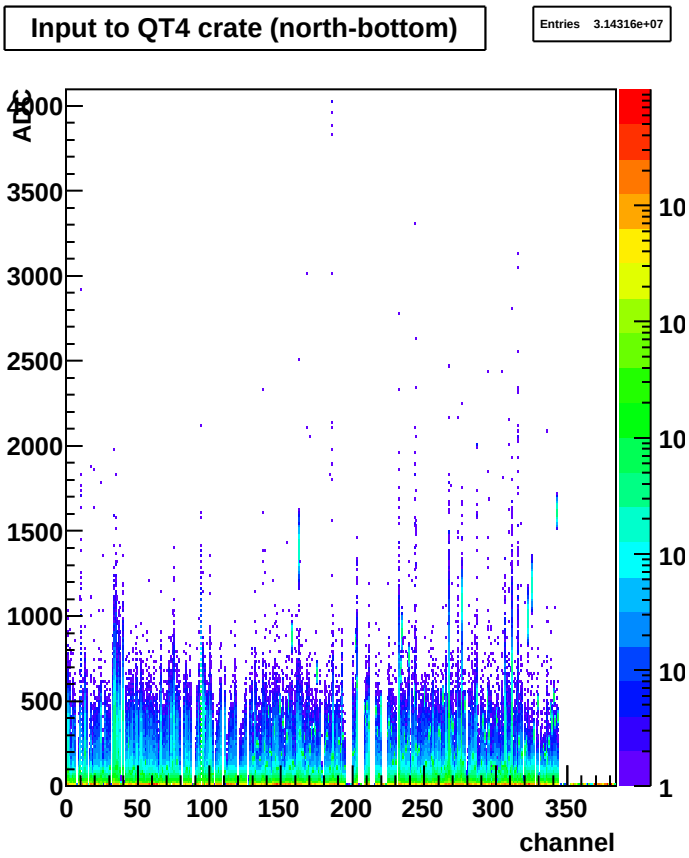

VPD-TAC-W
Entries 1846002
Mean 1441
RMS 139

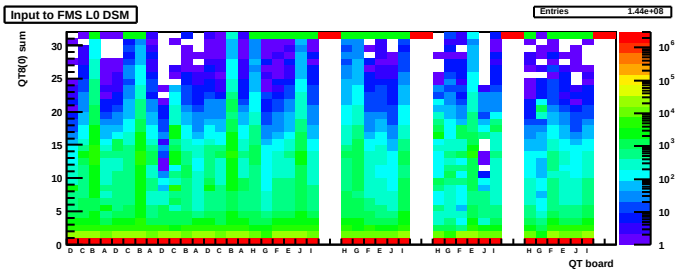

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1453988
Mean 3986
RMS 124.1

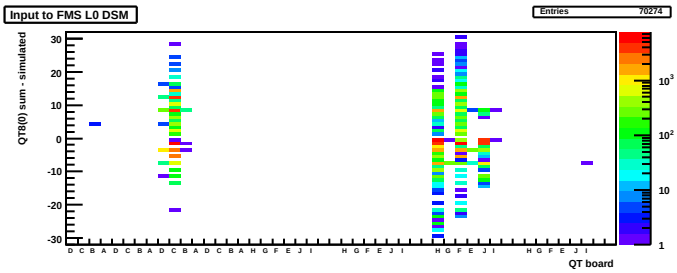

Input to QT1 crate (south-top)
Entries 4.4648e+07
ADC
channel

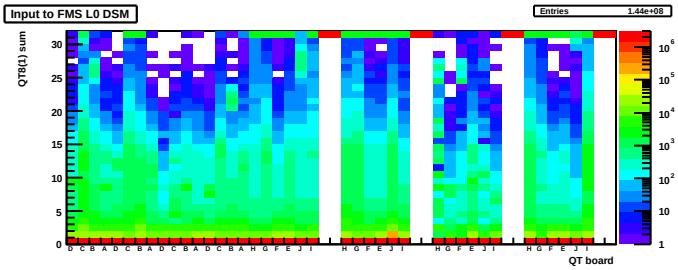

Input to QT3 crate (north-top)
Entries 2.535916e+07
ADC
channel

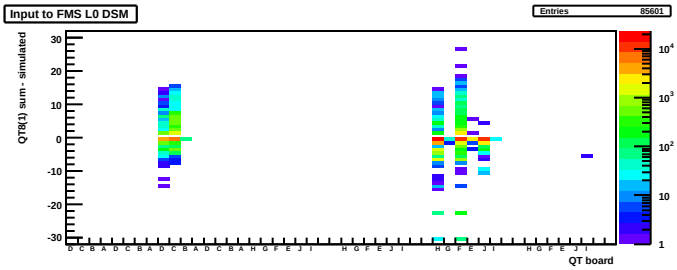

Input to QT2 crate (south-bottom)
Entries 3.000038e+07
ADC
channel

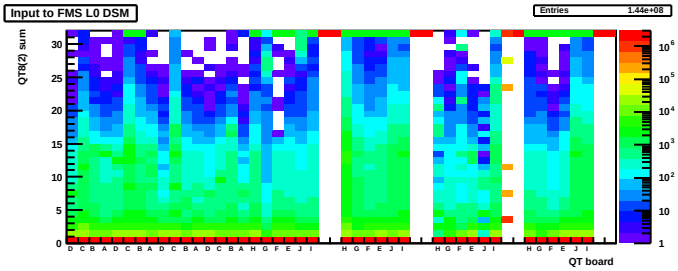

Input to QT4 crate (north-bottom)
Entries 3.14316e+07
ADC
channel

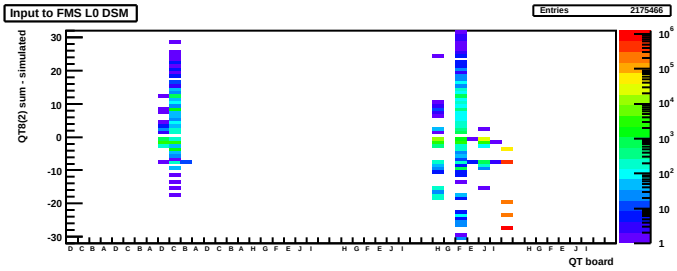

Input to FMS L0 DSM
Entries 1.44e+08
QT8(0) sum
QT board

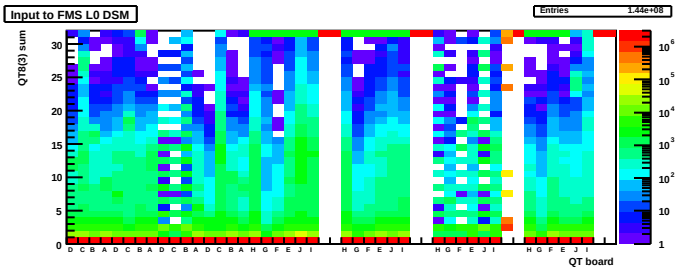

Input to FMS L0 DSM
Entries 70274
QT8(0) sum - simulated
QT board

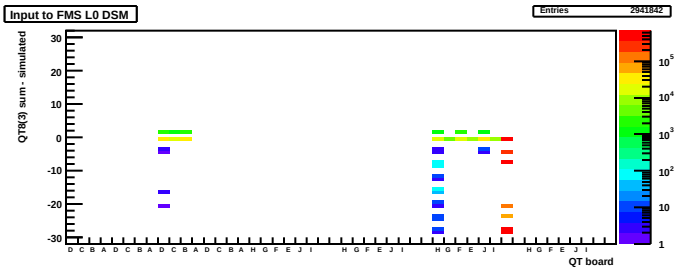

Input to FMS L0 DSM
Entries 1.44e+08
QT8(1) sum
QT board

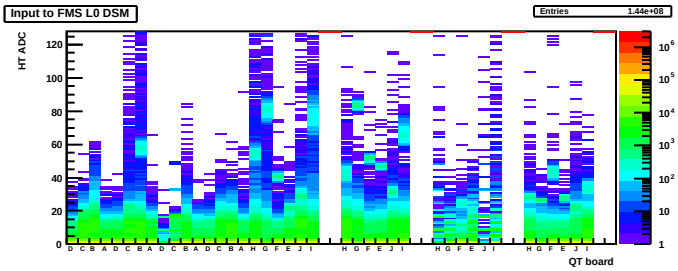

Input to FMS L0 DSM
Entries 85601
QT8(1) sum - simulated
QT board

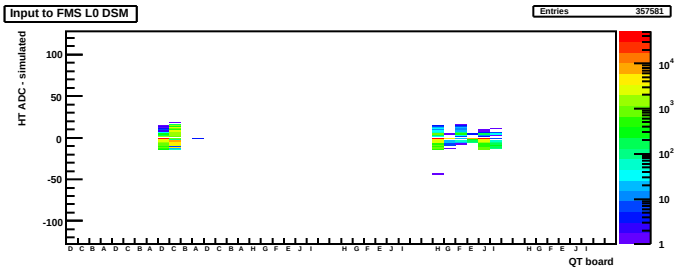

Input to FMS L0 DSM
Entries 1.44e+08
QT8(2) sum
QT board

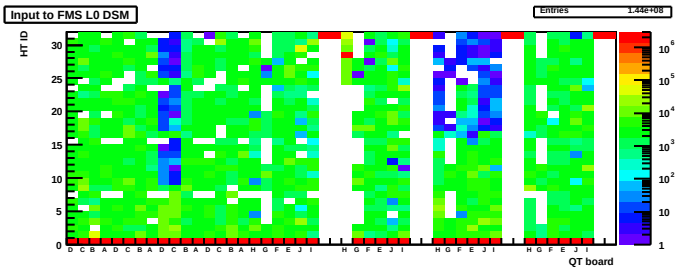

Input to FMS L0 DSM
Entries 2175466
QT8(2) sum - simulated
QT board

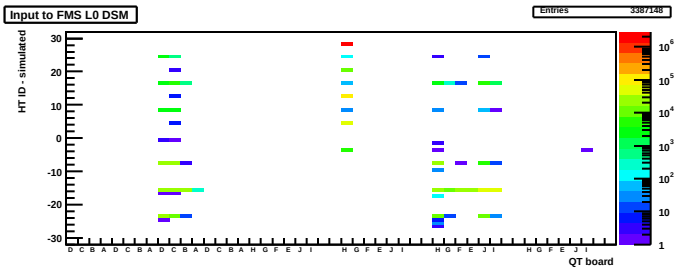

Input to FMS L0 DSM
Entries 1.44e+08
QT8(3) sum
QT board

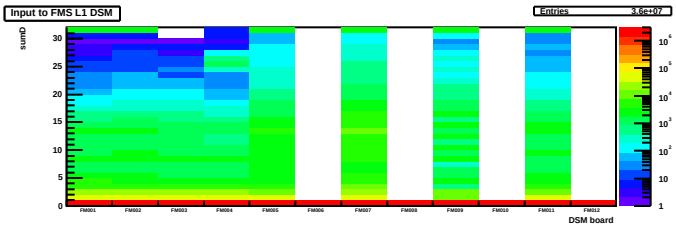

Input to FMS L0 DSM
Entries 2941842
QT8(3) sum - simulated
QT board

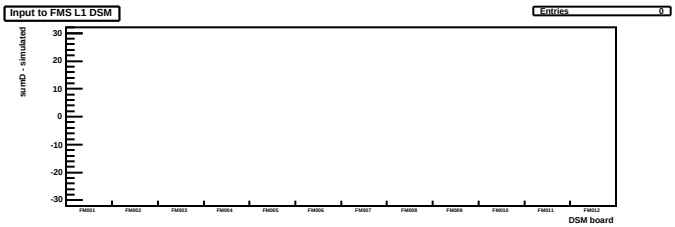

Input to FMS L0 DSM
Entries 1.44e+08
HT ADC
QT board

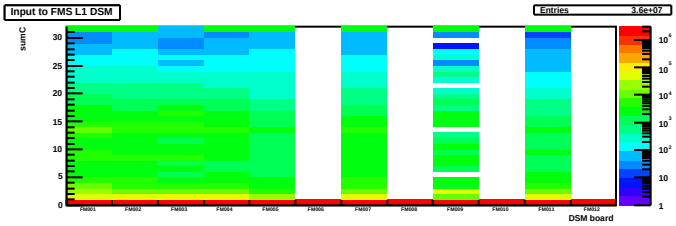

Input to FMS L0 DSM
Entries 357581
HT ADC - simulated
QT board

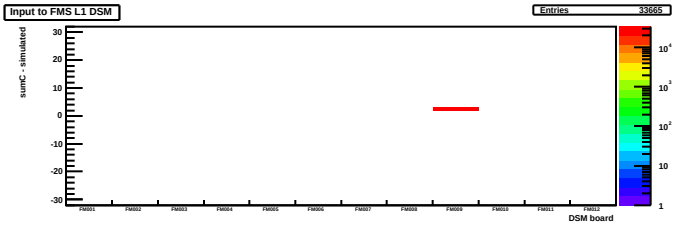

Input to FMS L0 DSM
Entries 1.44e+08
HT ID
QT board

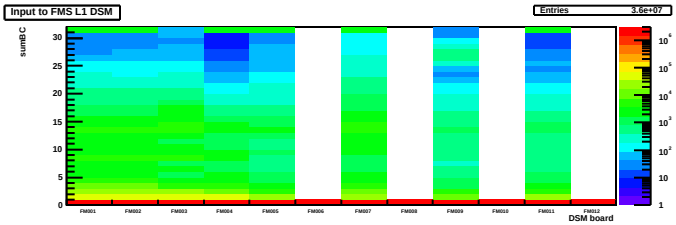

Input to FMS L0 DSM
Entries 3387148
HT ID - simulated
QT board

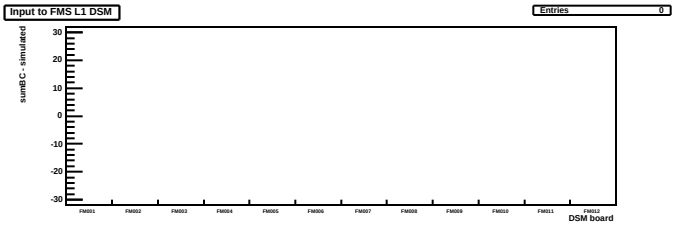

Input to FMS L1 DSM
Entries 3.6e+07
sumD
DSM board

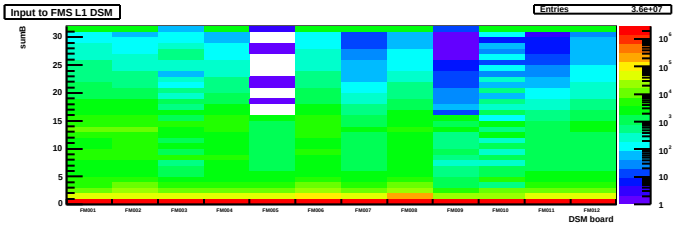

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

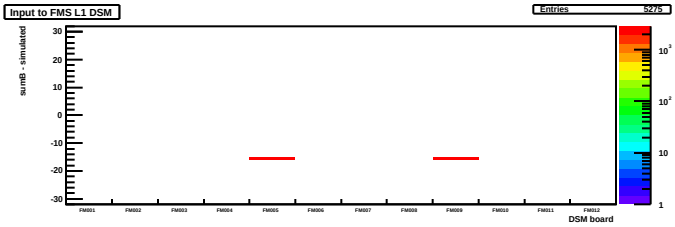

Input to FMS L1 DSM
Entries 3.6e+07
sumC
DSM board

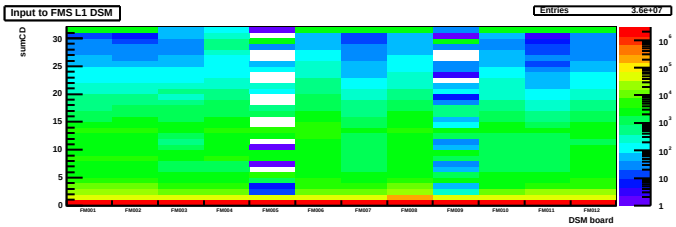

Input to FMS L1 DSM
Entries 33665
sumC - simulated
DSM board

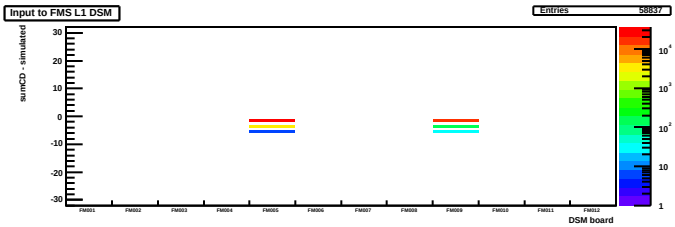

Input to FMS L1 DSM
Entries 3.6e+07
sumBC
DSM board

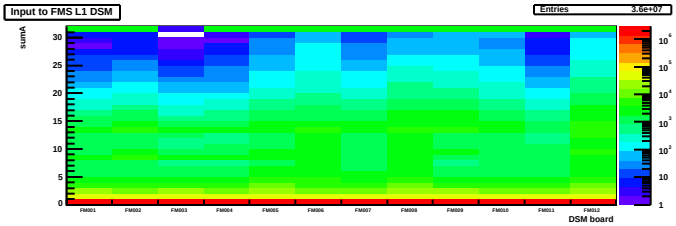

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

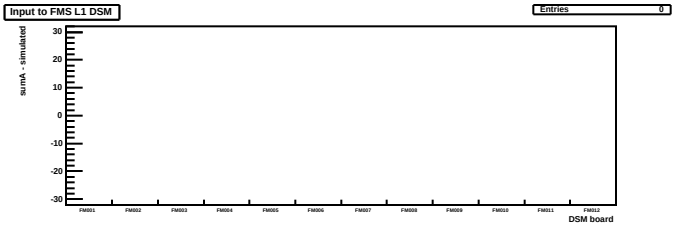

Input to FMS L1 DSM
Entries 3.6e+07
sumB
DSM board

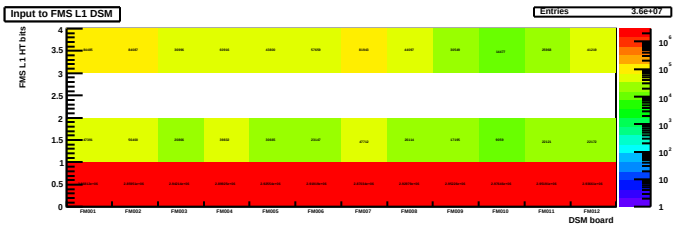

Input to FMS L1 DSM
Entries 5275
sumB - simulated
DSM board

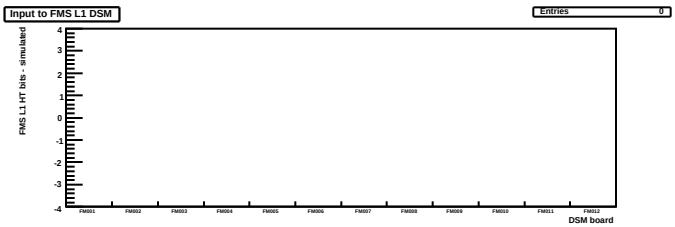

Input to FMS L1 DSM
Entries 3.6e+07
sumCD
DSM board

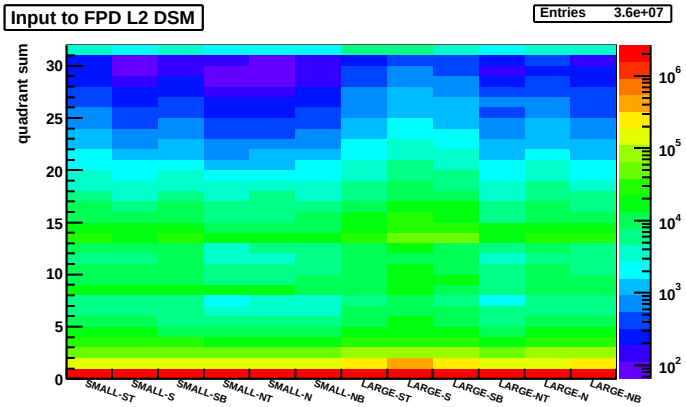

Input to FMS L1 DSM
Entries 58837
sumCD - simulated
DSM board

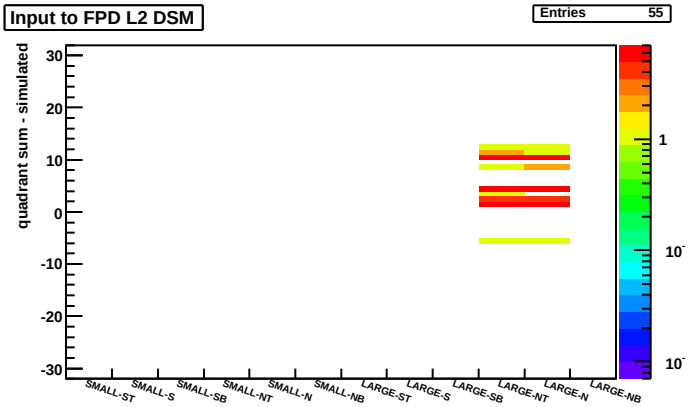

Input to FMS L1 DSM
Entries 3.6e+07
sumA
DSM board

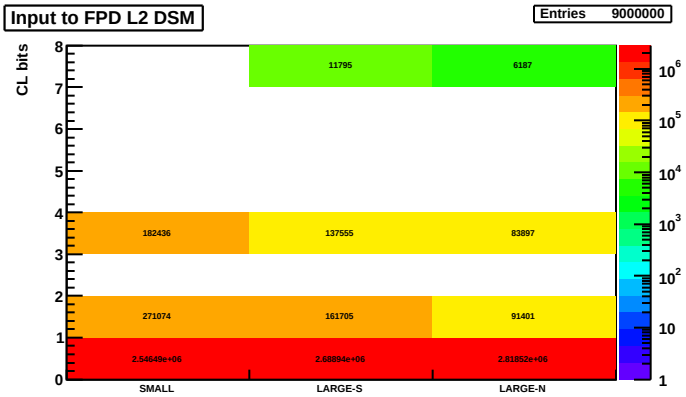

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

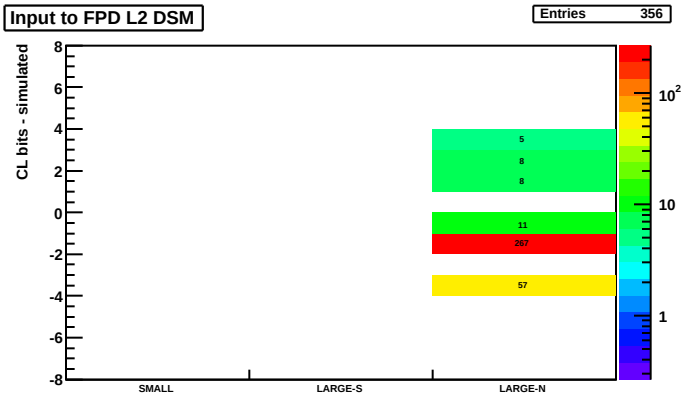

Input to FMS L1 DSM
Entries 3.6e+07
FMS L1 HT bits
DSM board

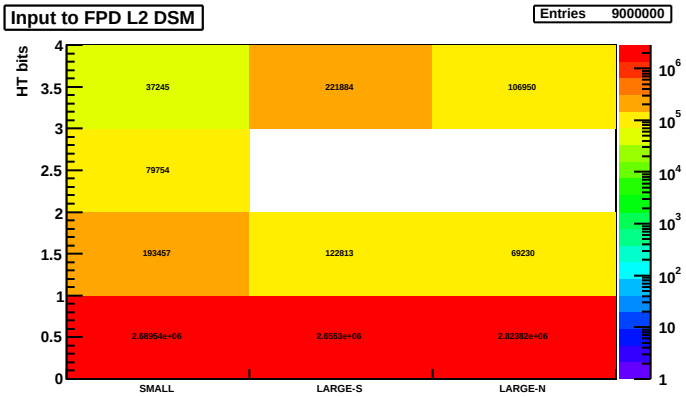

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

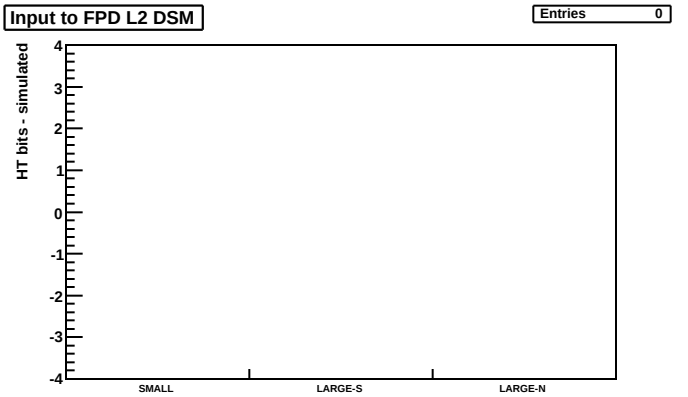

Input to FPD L2 DSM
Entries 3.6e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

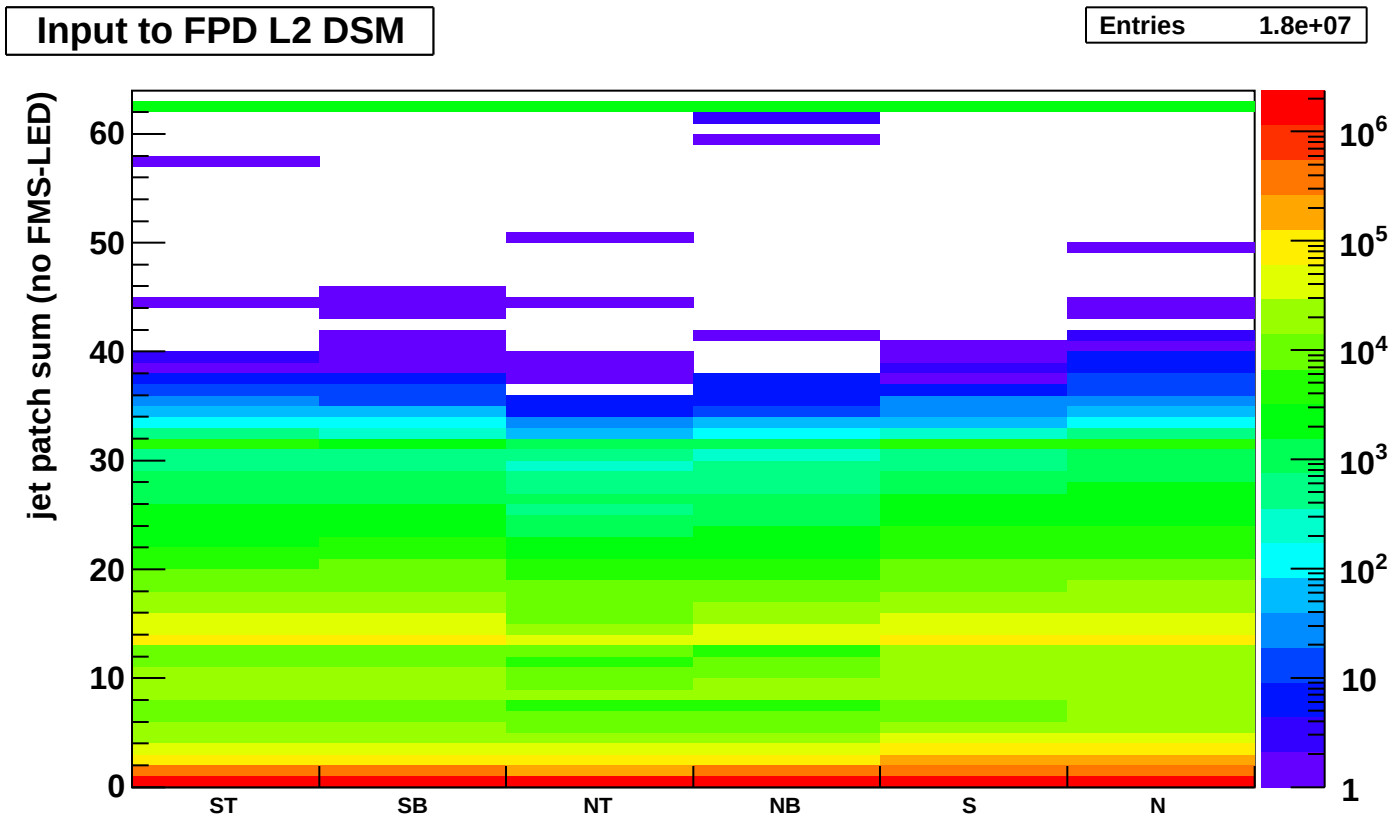

Input to FPD L2 DSM
Entries 55
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

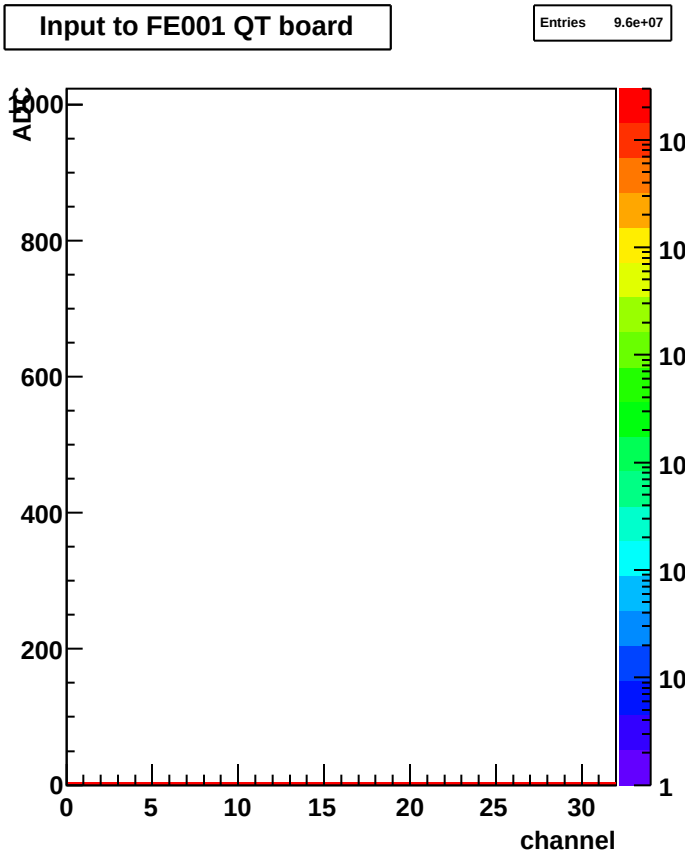

Input to FPD L2 DSM
Entries 9000000
CL bits
11795
6187
182436
137555
83897
271074
161705
91401
2.54649e+06
2.68894e+06
2.81852e+06
SMALL
LARGE-S
LARGE-N

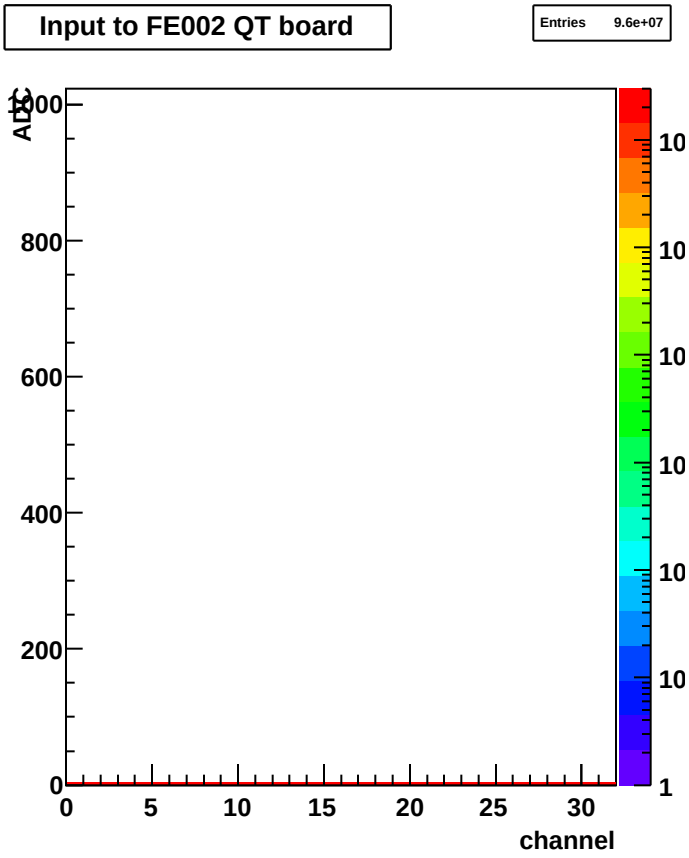

Input to FPD L2 DSM
Entries 356
CL bits - simulated
5
8
8
11
267
57
SMALL
LARGE-S
LARGE-N

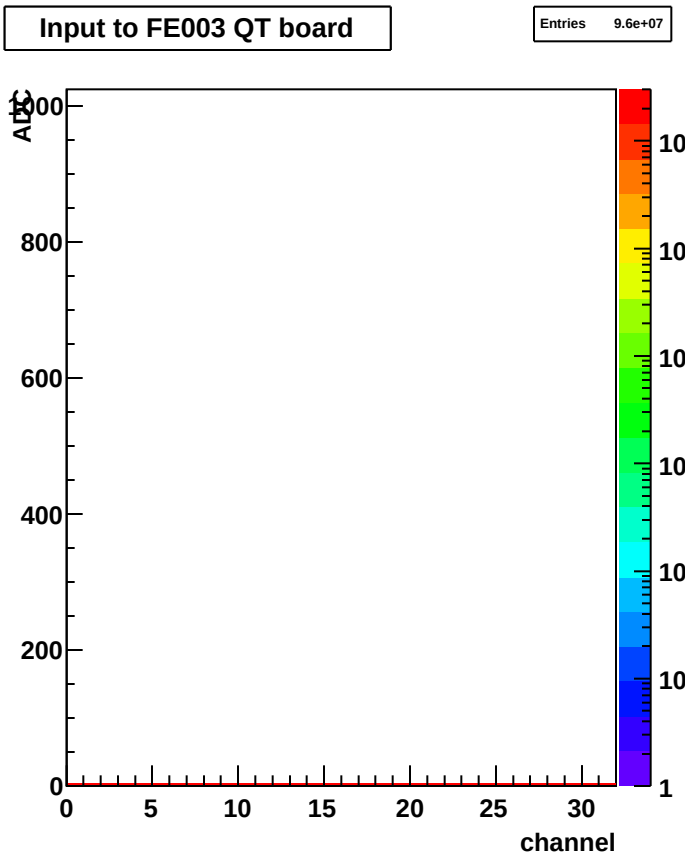

Input to FPD L2 DSM
Entries 9000000
HT bits
37245
221884
106950
79754
193457
122813
69230
2.68954e+06
2.6553e+06
2.82382e+06
SMALL
LARGE-S
LARGE-N

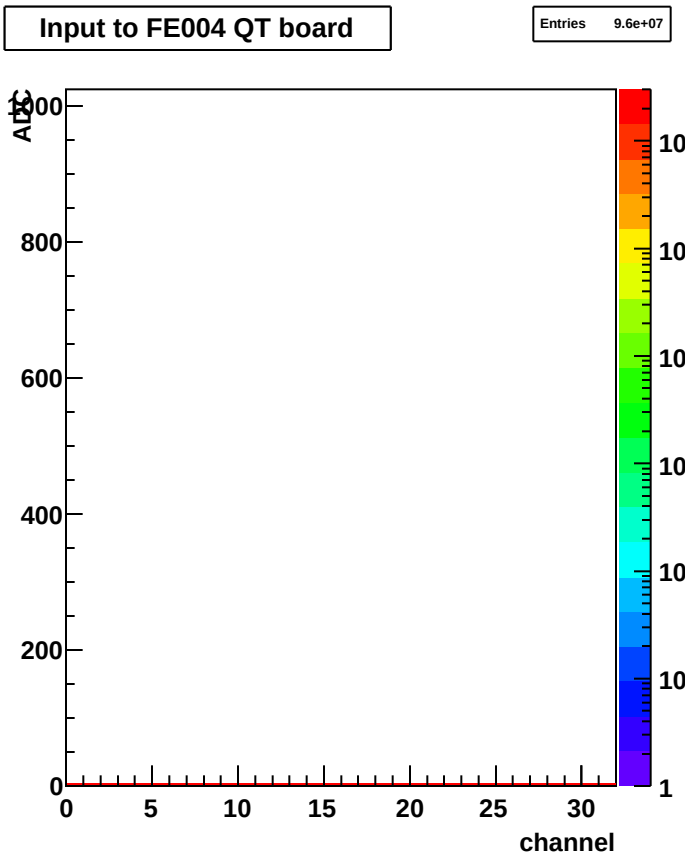

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

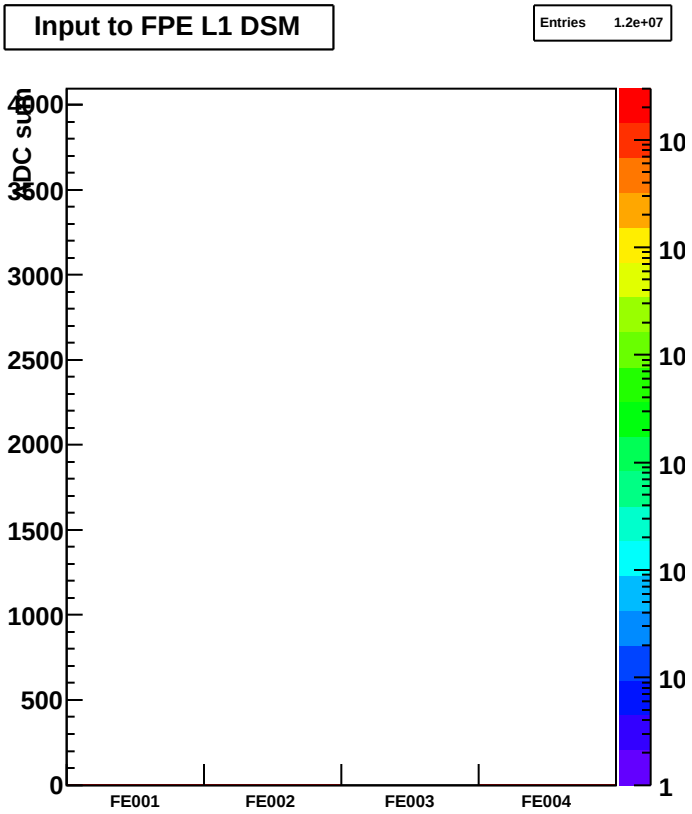

Input to FPD L2 DSM
Entries 1.8e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

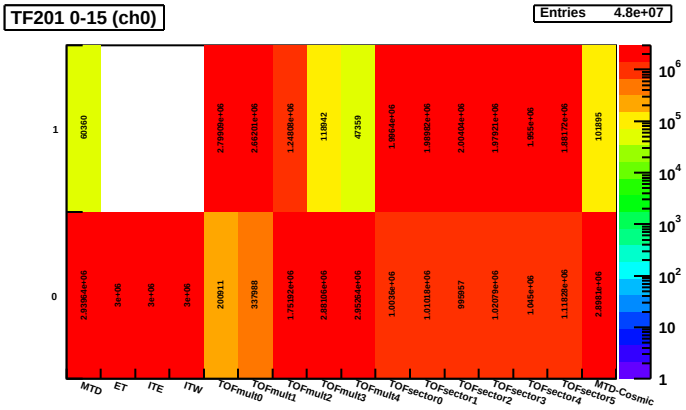

Input to FE001 QT board
Entries 9.6e+07
ADC
channel

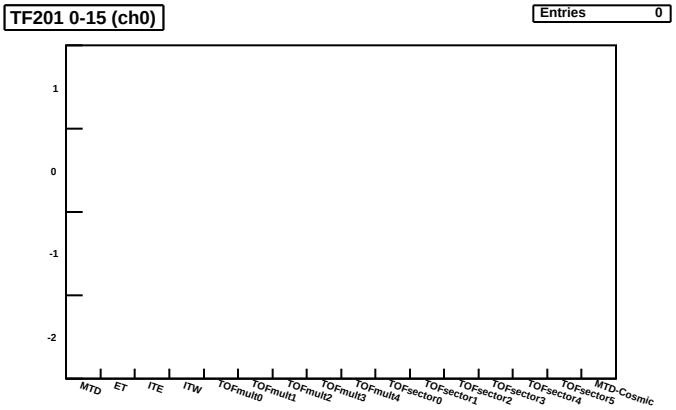

Input to FE002 QT board
Entries 9.6e+07
ADC
channel

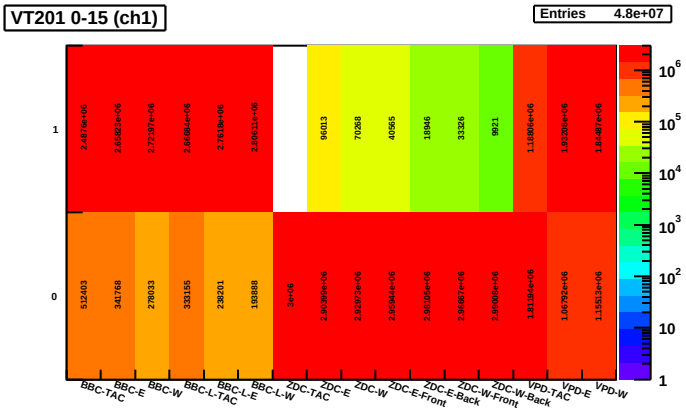

Input to FE003 QT board
Entries 9.6e+07
ADC
channel


Input to FE004 QT board
Entries 9.6e+07
ADC
channel


Input to FPE L1 DSM
Entries 1.2e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 4.8e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 4.8e+07
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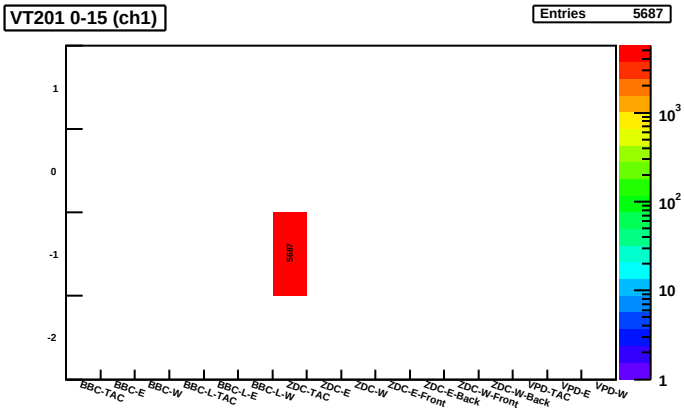
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Entries 5687
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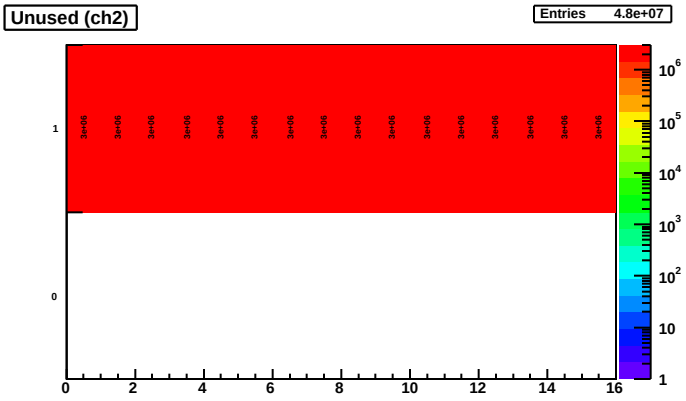
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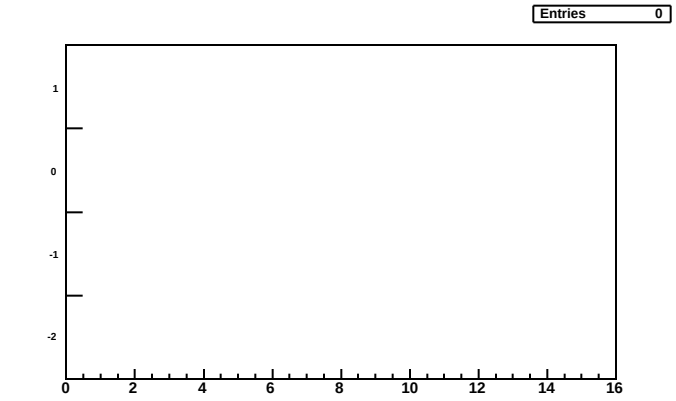
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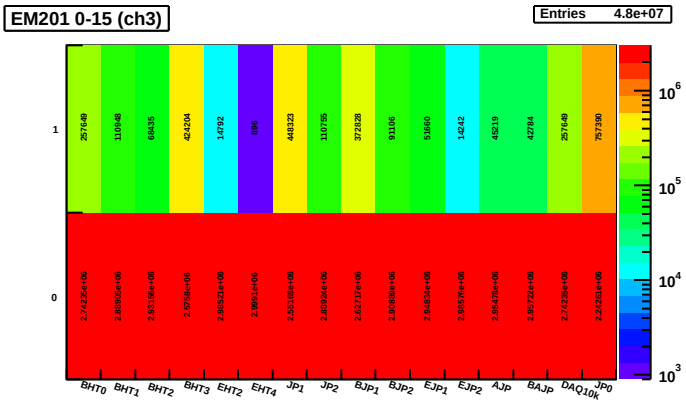
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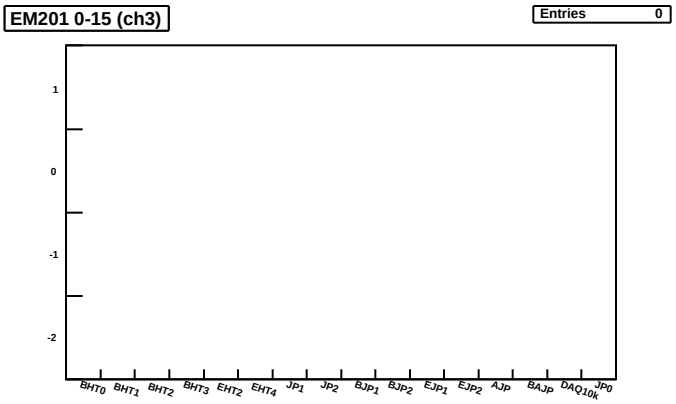
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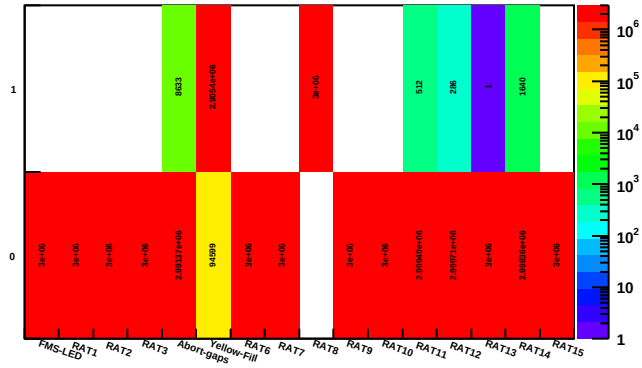
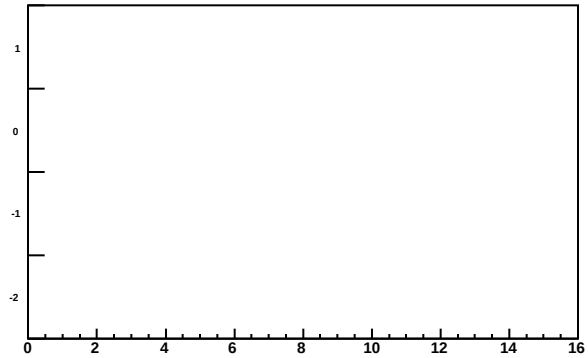
RAT board (ch4)





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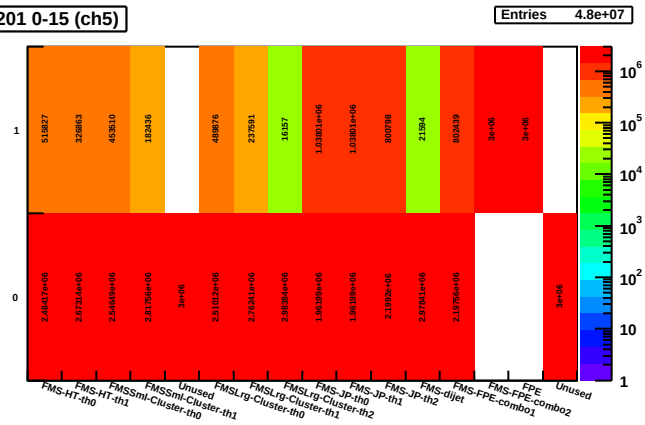

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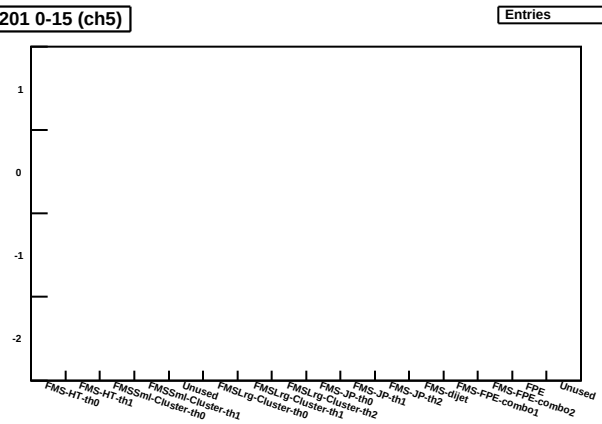
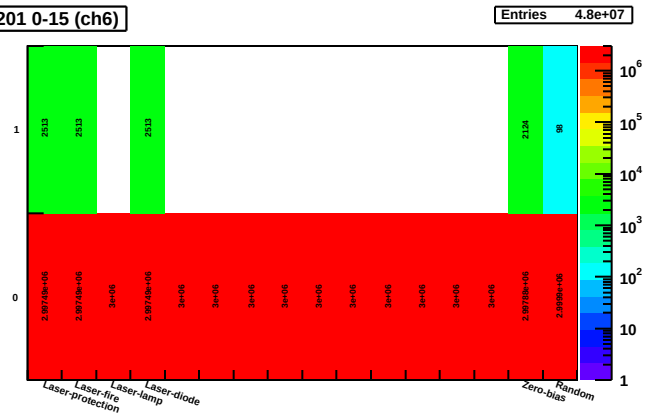
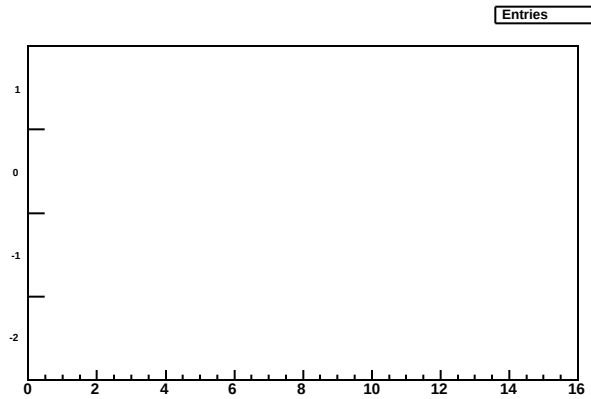
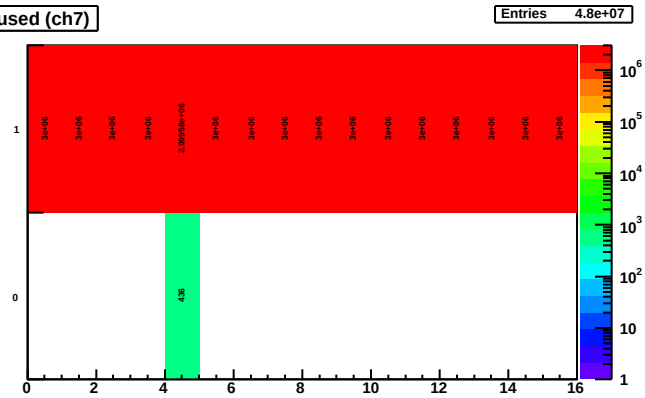
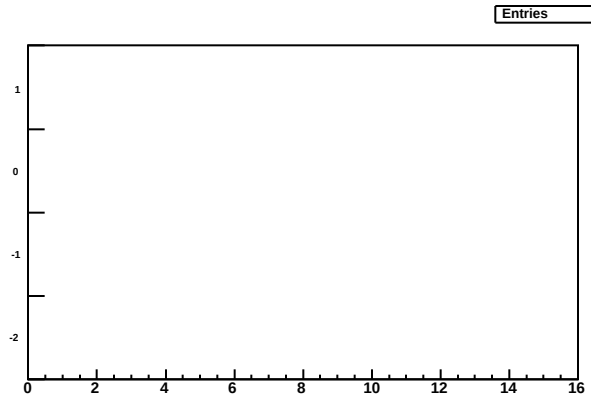


ST201 0-15 (ch6)





Unused (ch7)