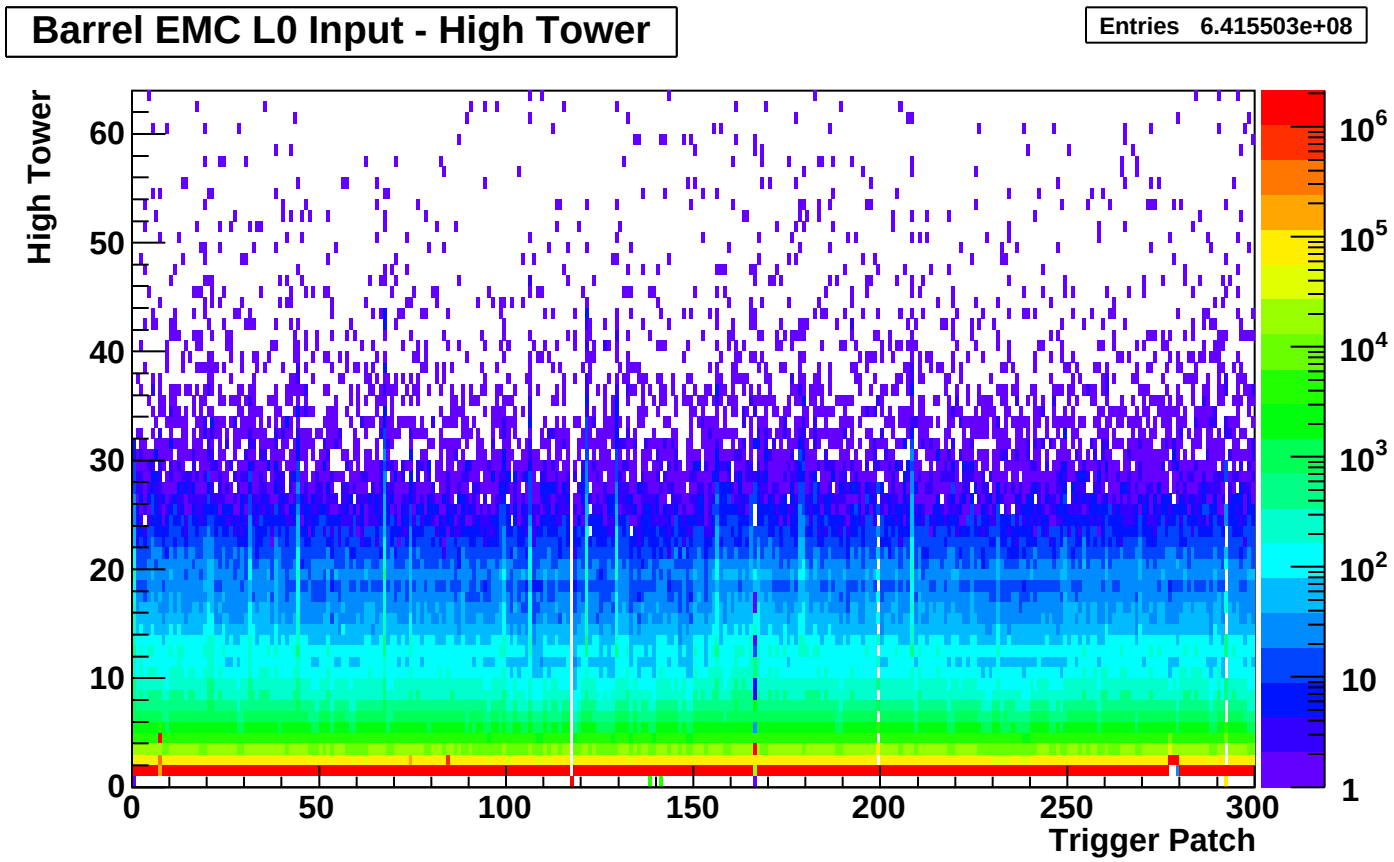
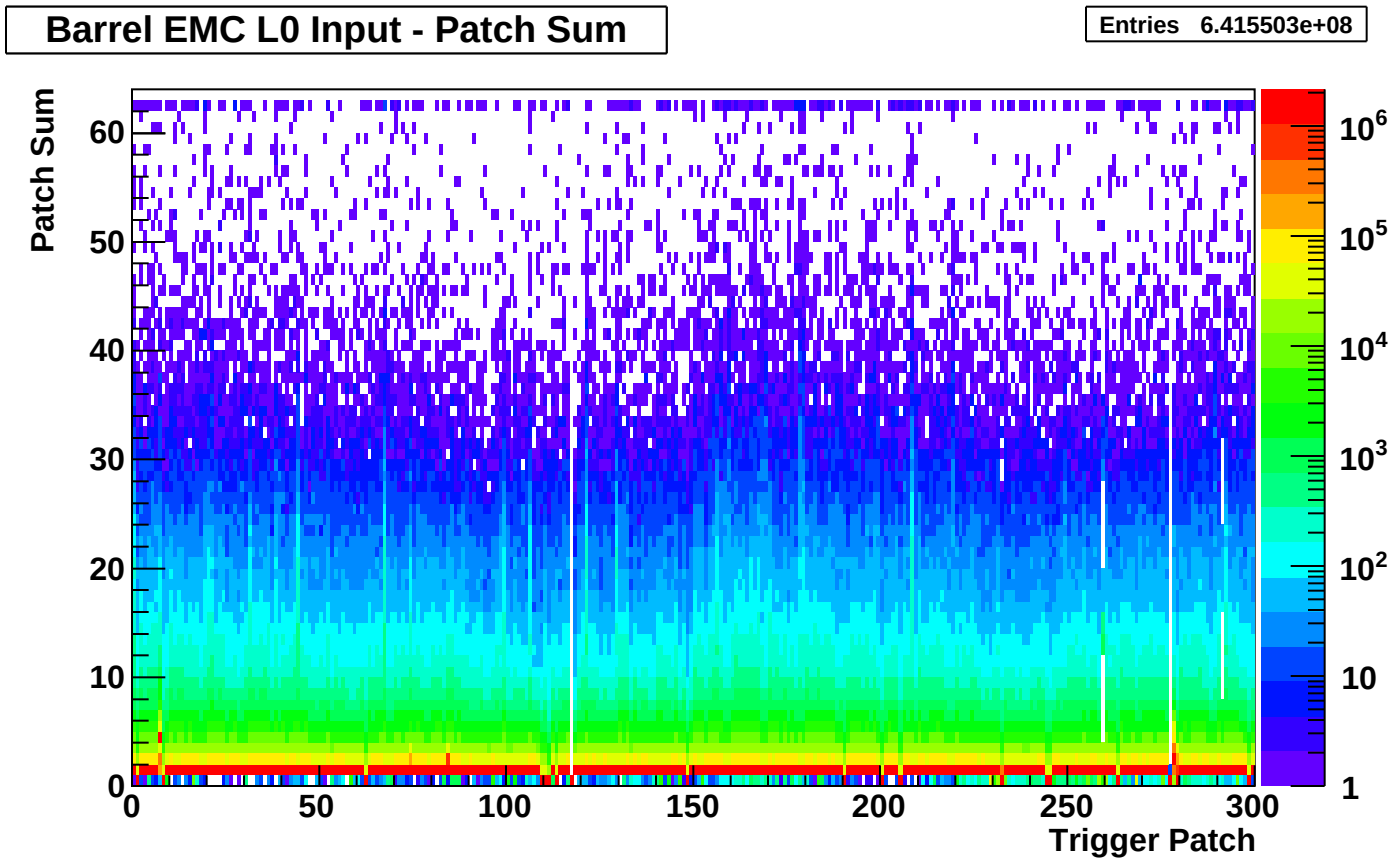
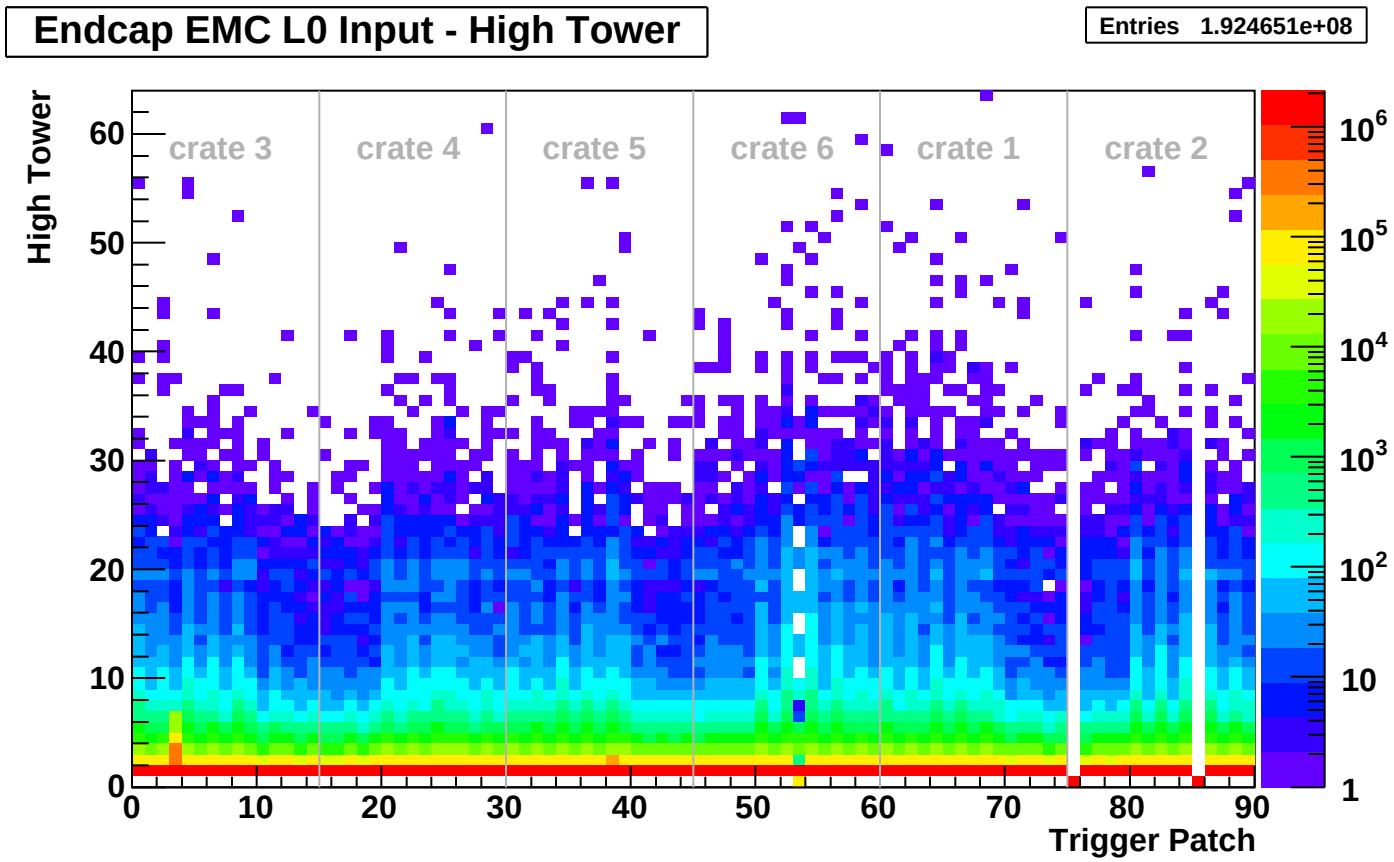
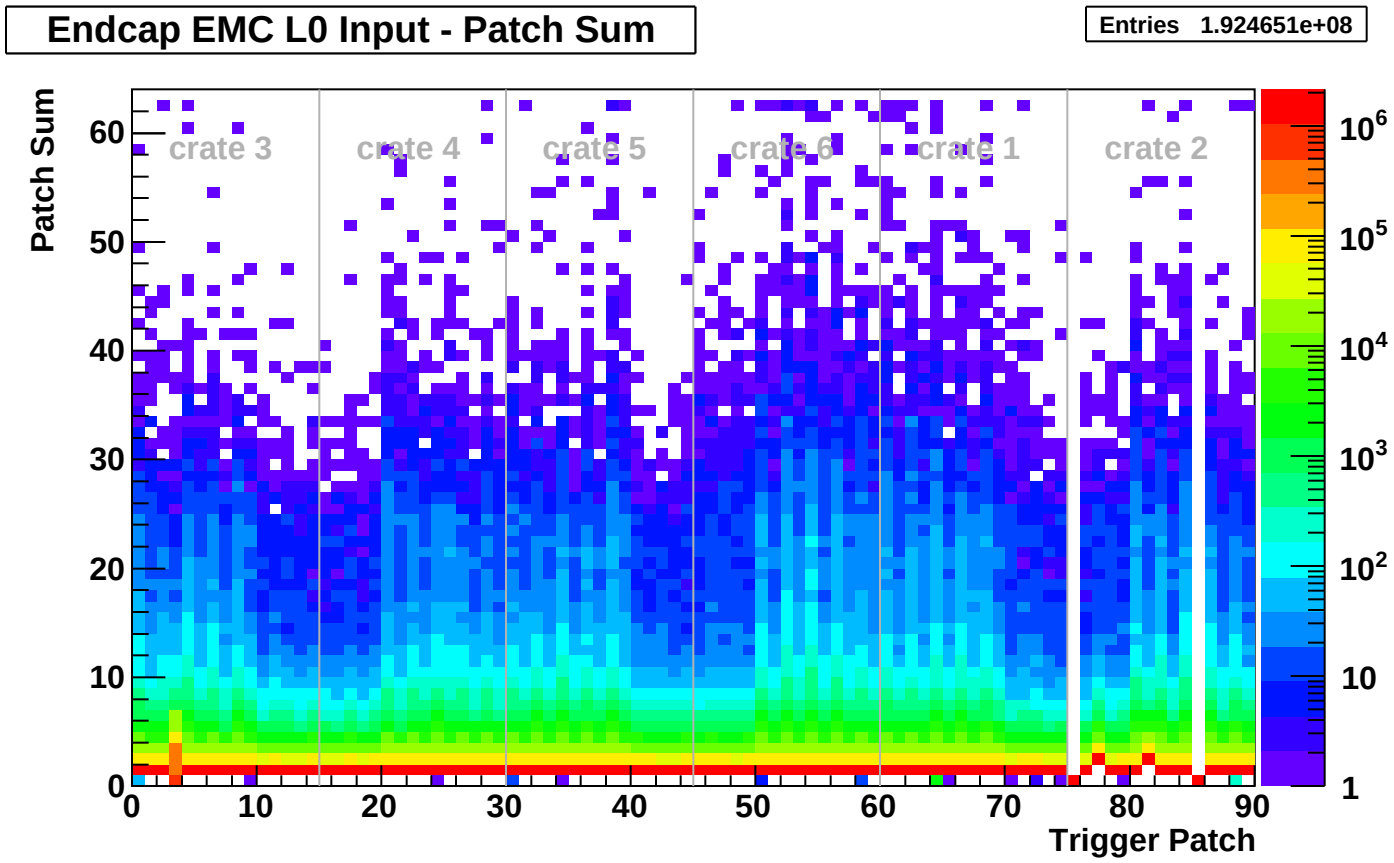
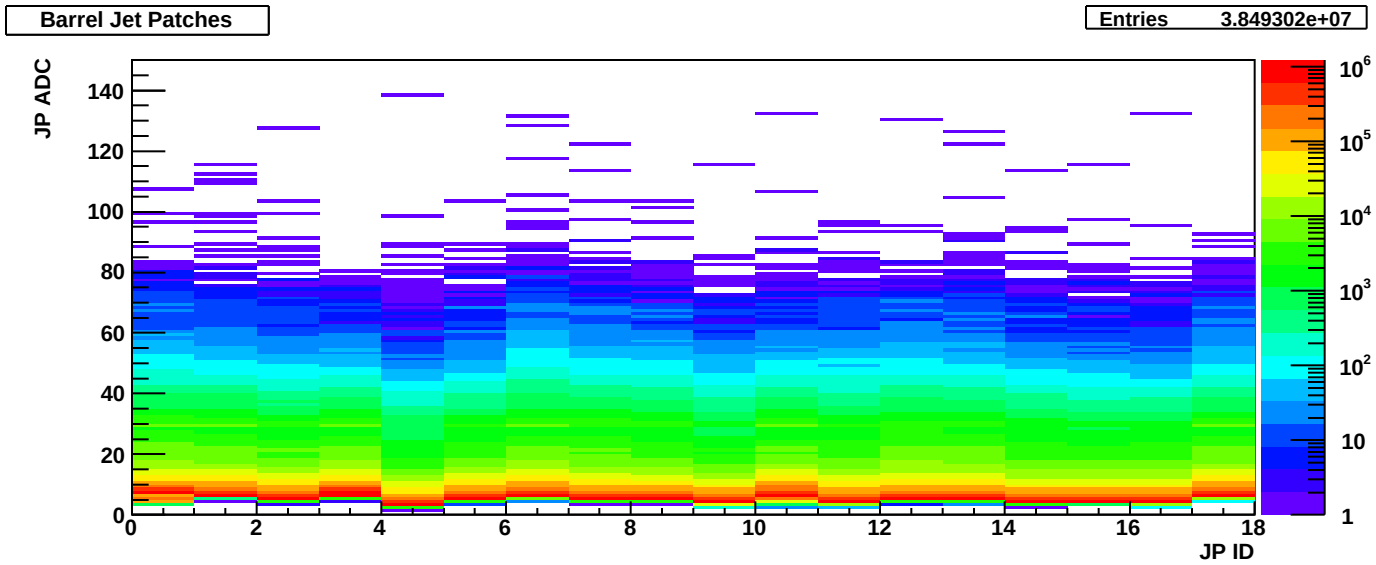
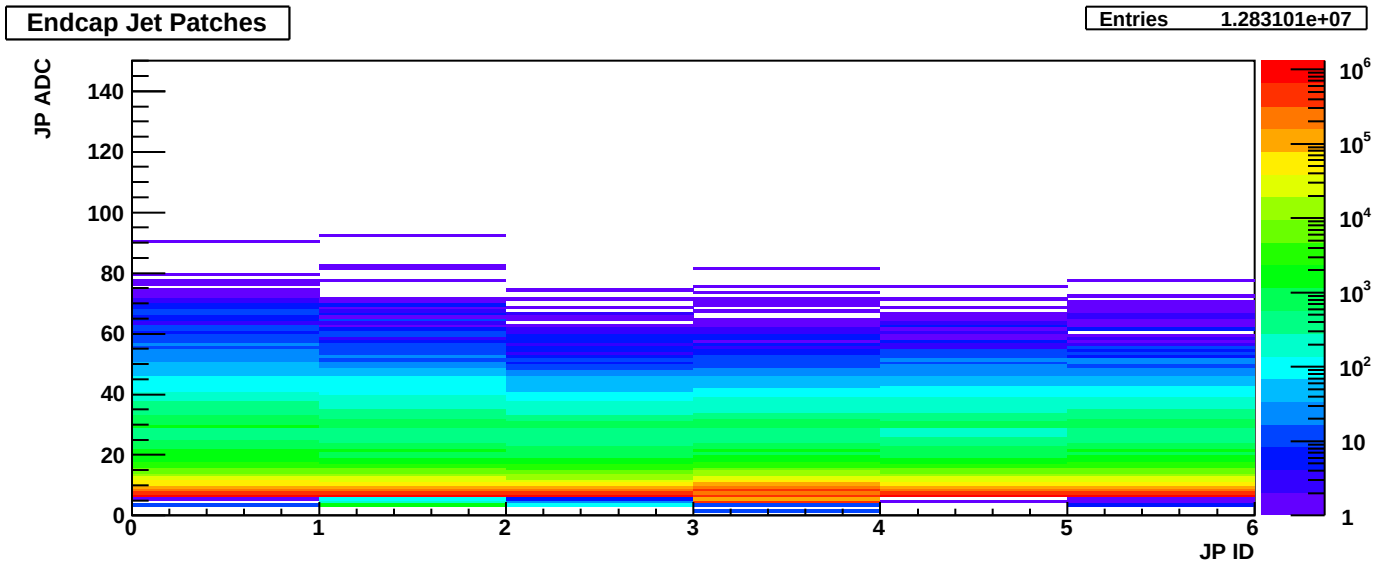

Barrel EMC L0 Input - High Tower
Entries 6.415503e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

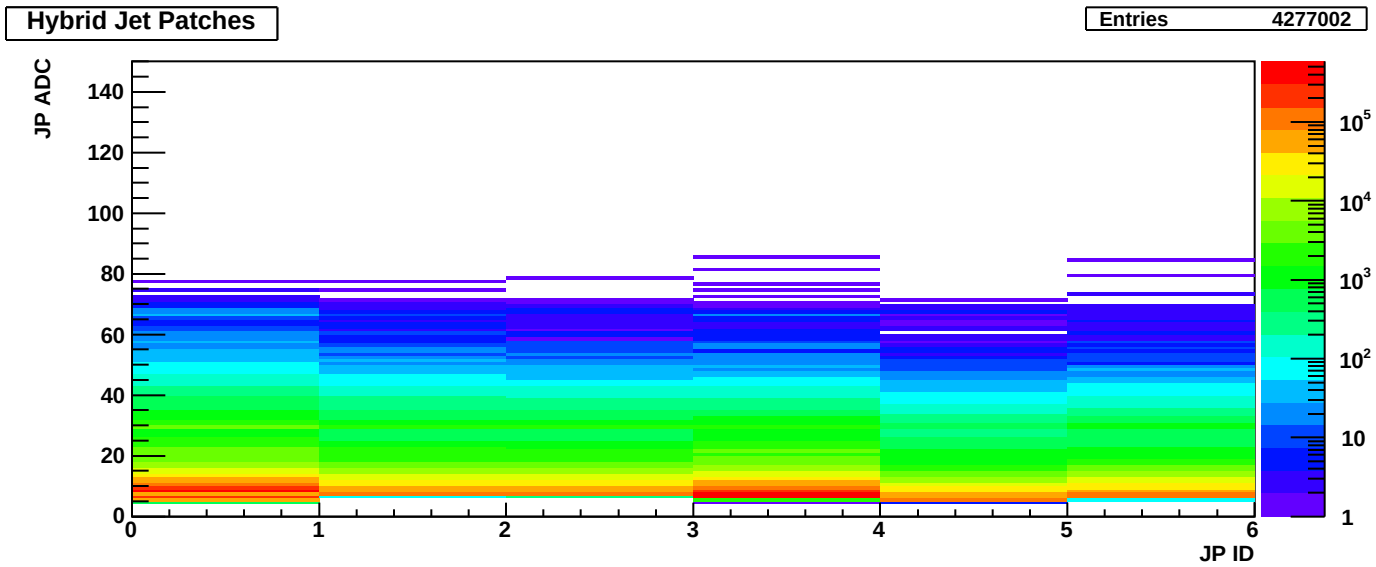

Barrel EMC L0 Input - Patch Sum
Entries 6.415503e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

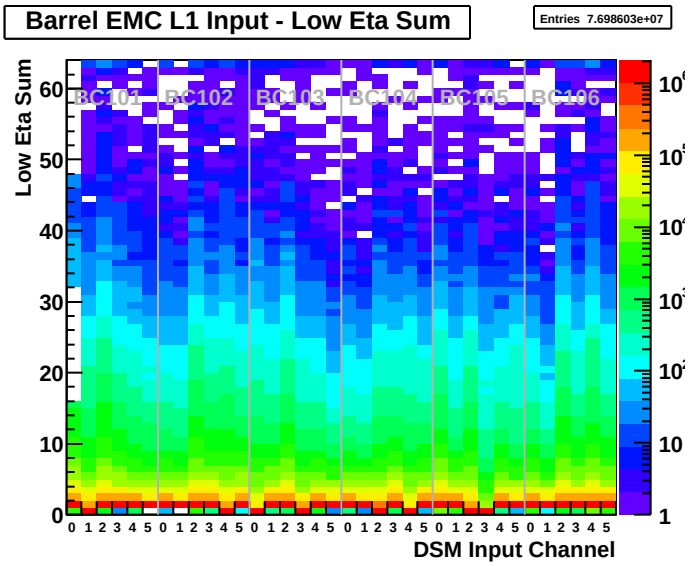

Endcap EMC L0 Input - High Tower
Entries 1.924651e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

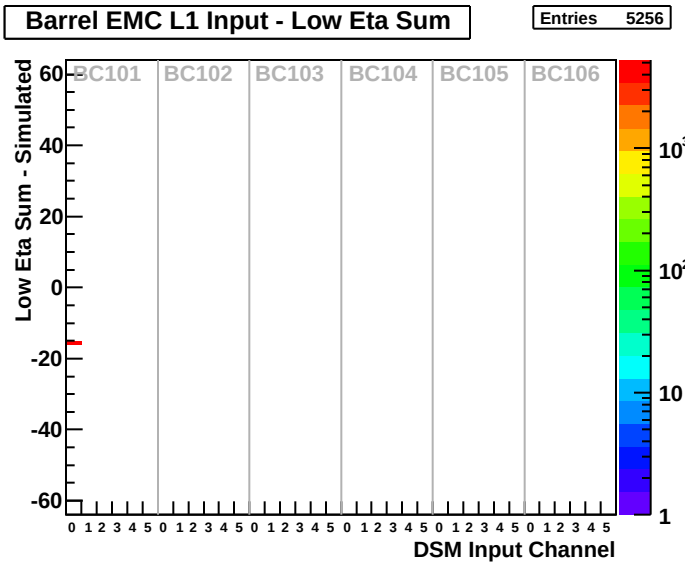

Endcap EMC L0 Input - Patch Sum
Entries 1.924651e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

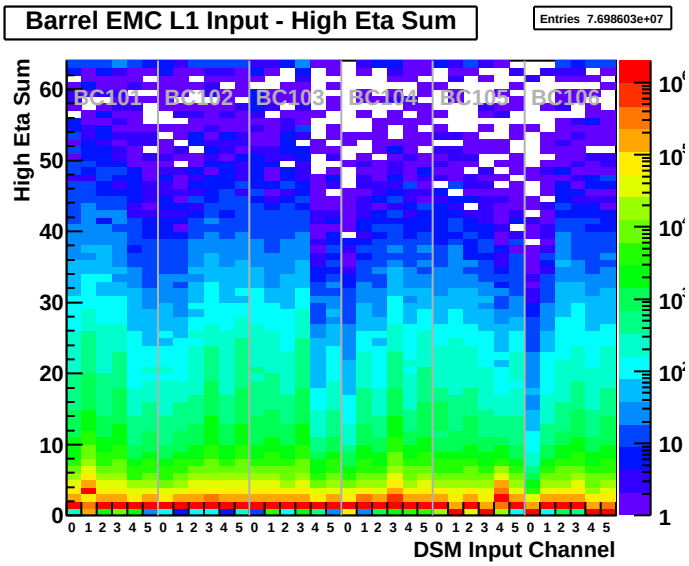

Barrel Jet Patches
Entries 3.849302e+07
JP ADC
JP ID

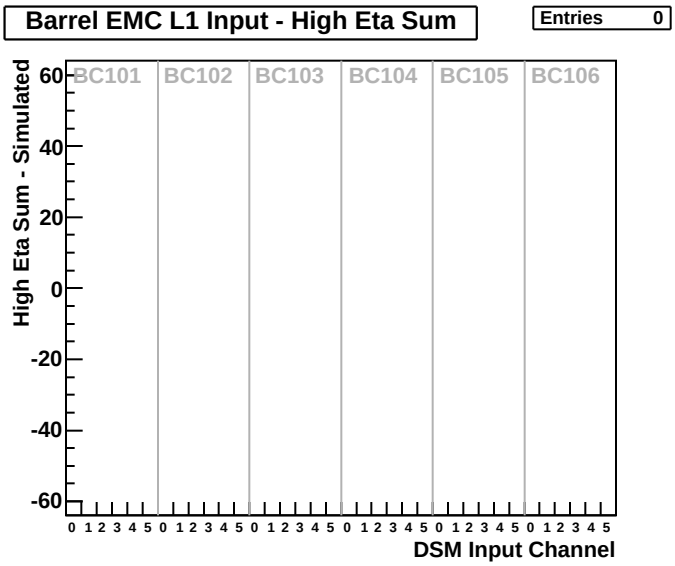

Endcap Jet Patches
Entries 1.283101e+07
JP ADC
JP ID

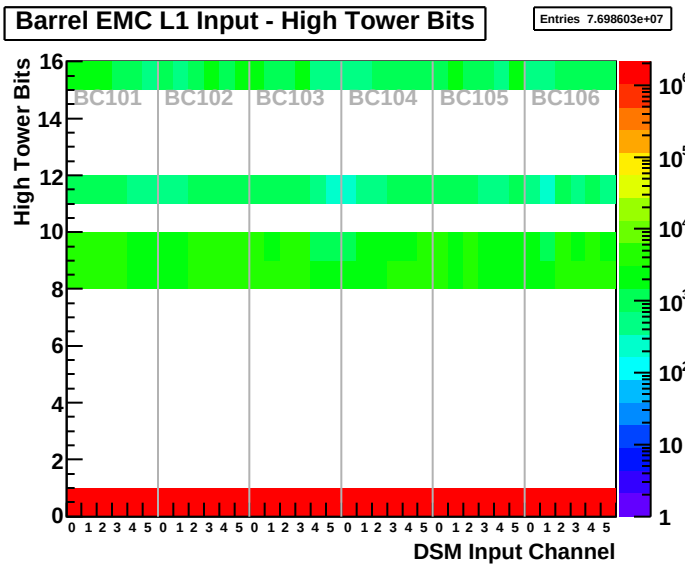

Hybrid Jet Patches
Entries 4277002
JP ADC
JP ID

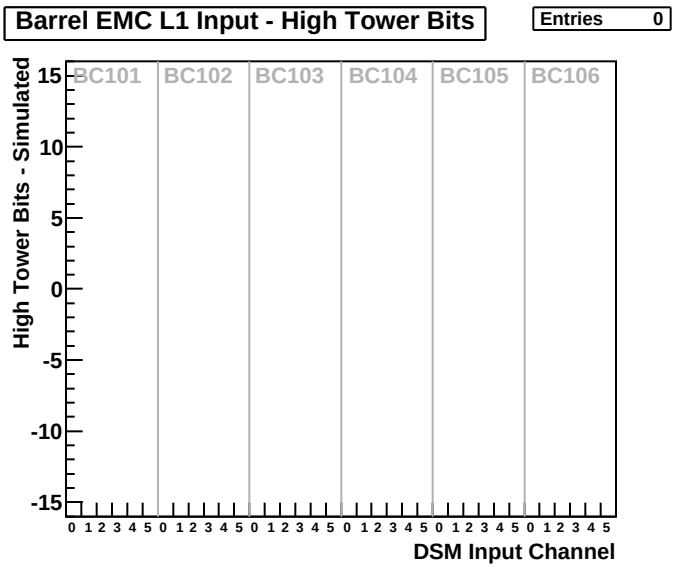

Barrel EMC L1 Input - Low Eta Sum
Entries 7.698603e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

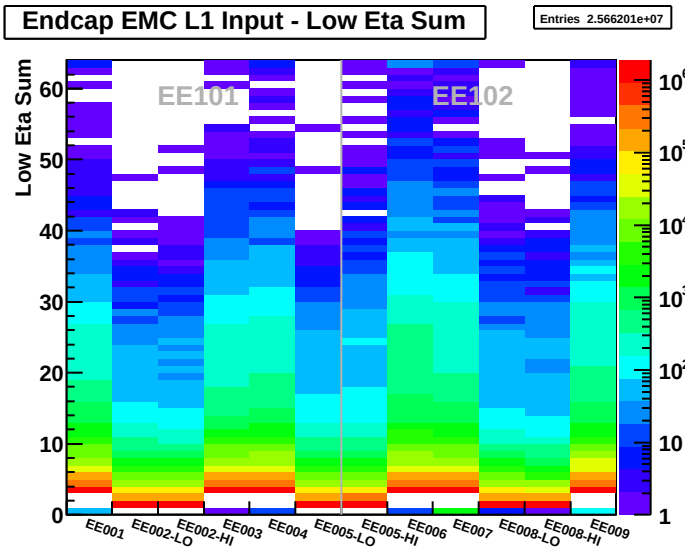

Barrel EMC L1 Input - Low Eta Sum
Entries 5256
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

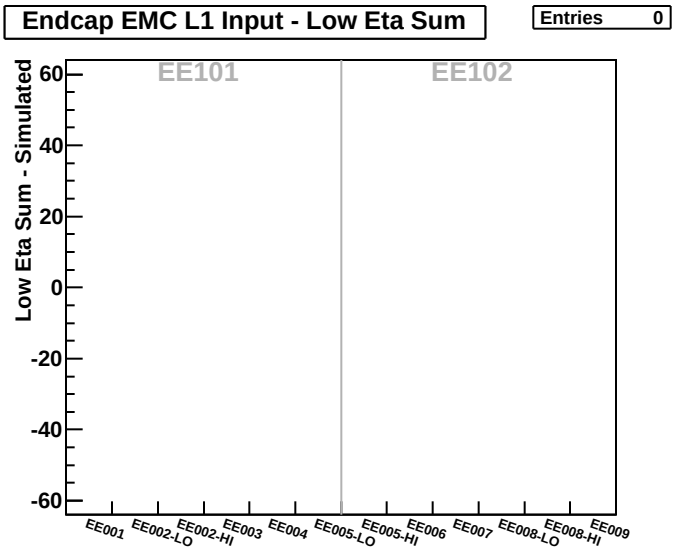

Barrel EMC L1 Input - High Eta Sum
Entries 7.698603e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

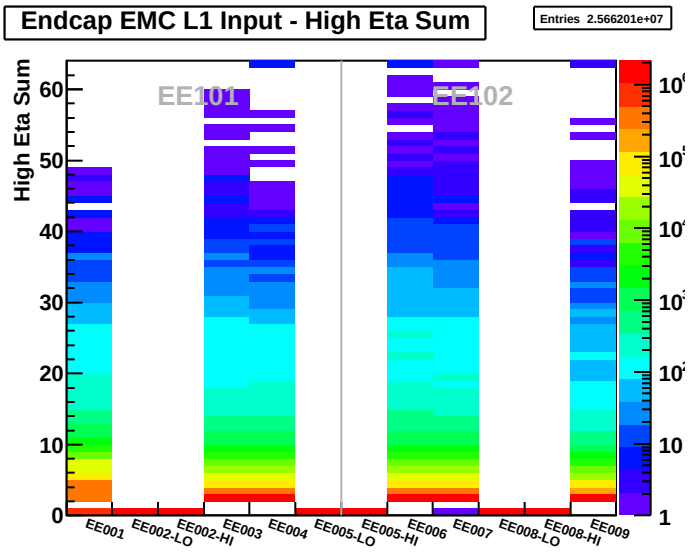

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

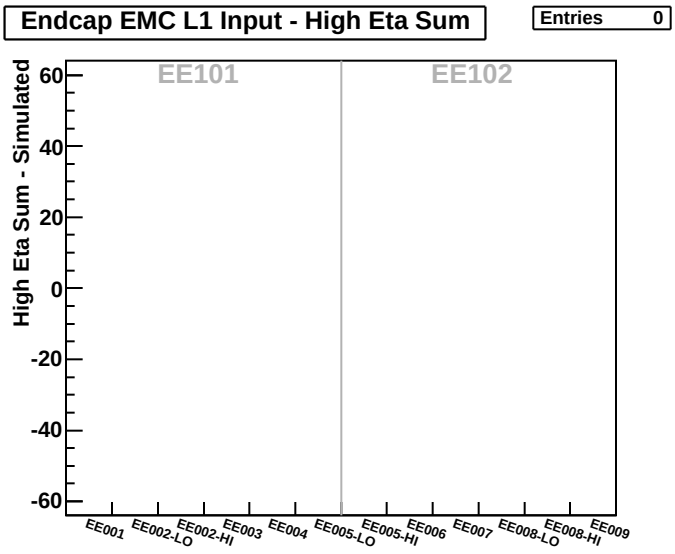

Barrel EMC L1 Input - High Tower Bits
Entries 7.698603e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

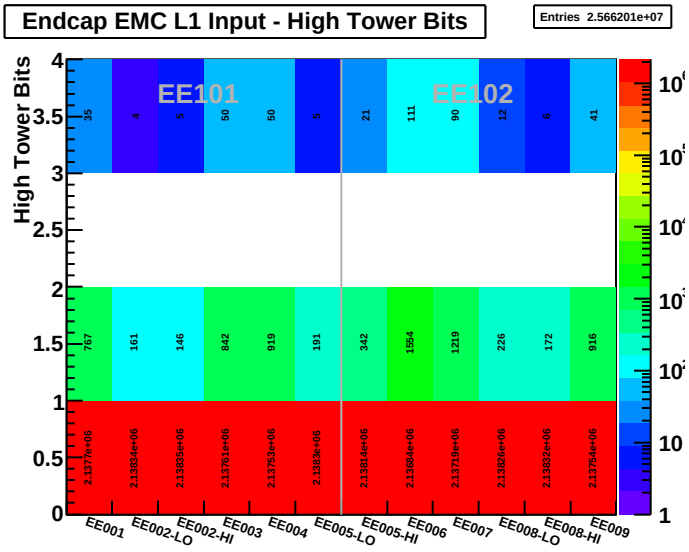

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

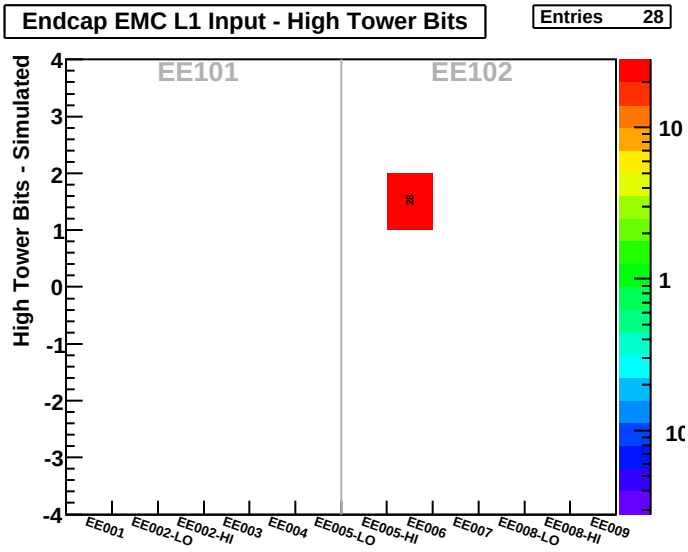

Endcap EMC L1 Input - Low Eta Sum
Entries 2.566201e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

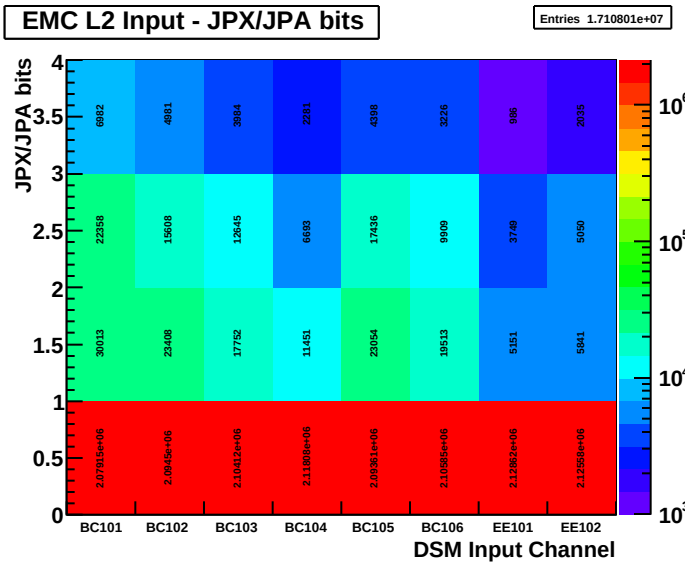

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

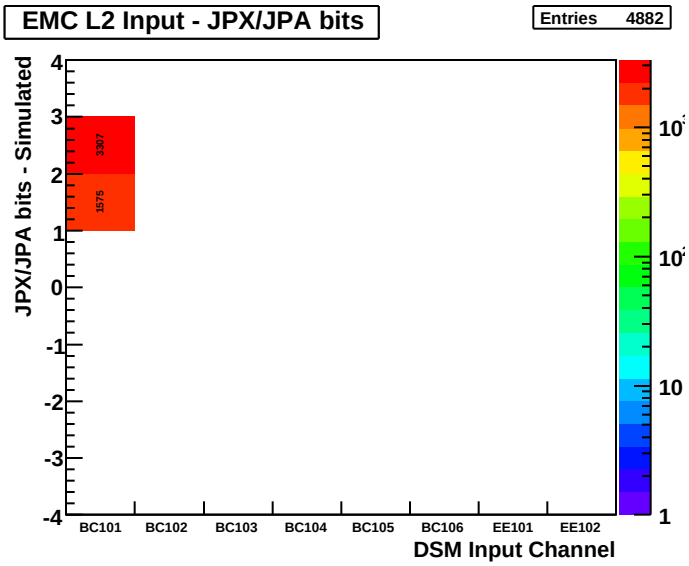

Endcap EMC L1 Input - High Eta Sum
Entries 2.566201e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

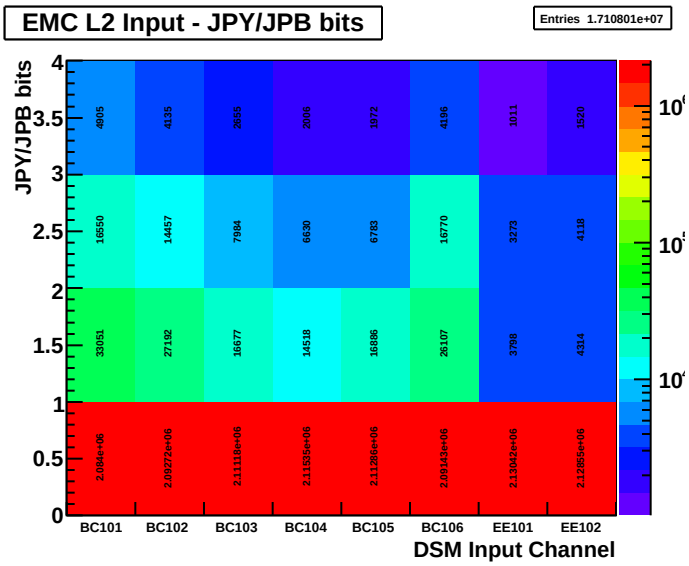

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

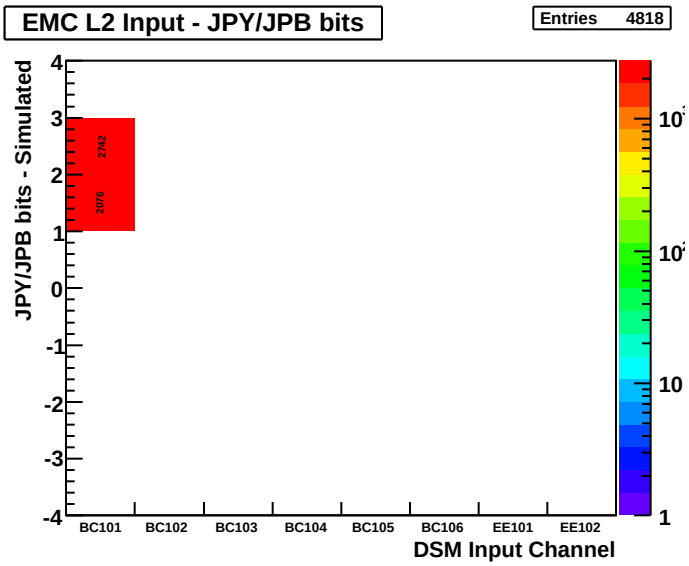

Endcap EMC L1 Input - High Tower Bits
Entries 2.566201e+07
High Tower Bits
EE101
EE102
35 4 5 50 50 5 21 111 90 12 6 41
757 161 146 842 919 191 342 1554 1219 226 172 916
2.1377e+06 2.13834e+06 2.13835e+06 2.13761e+06 2.13753e+06 2.1383e+06 2.13814e+06 2.13884e+06 2.13719e+06 2.13826e+06 2.13832e+06 2.13754e+06
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

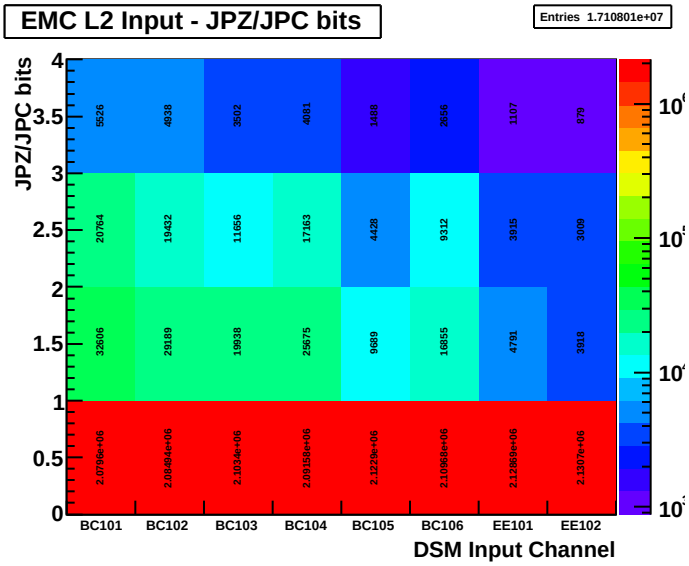

Endcap EMC L1 Input - High Tower Bits
Entries 28
High Tower Bits - Simulated
EE101
EE102
28
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

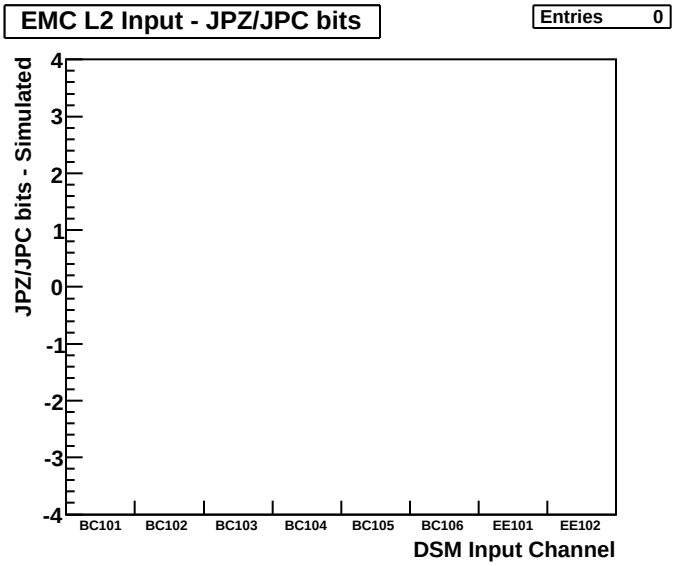

EMC L2 Input - JPX/JPA bits
Entries 1.710801e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6982 4981 3984 2281 4398 3226 986 2035
22358 15608 12645 6693 17436 9909 3749 5050
30013 23408 17752 11451 23054 19513 5151 5841
2.07915e+06 2.0945e+06 2.10412e+06 2.11808e+06 2.09361e+06 2.10585e+06 2.12852e+06 2.12553e+06

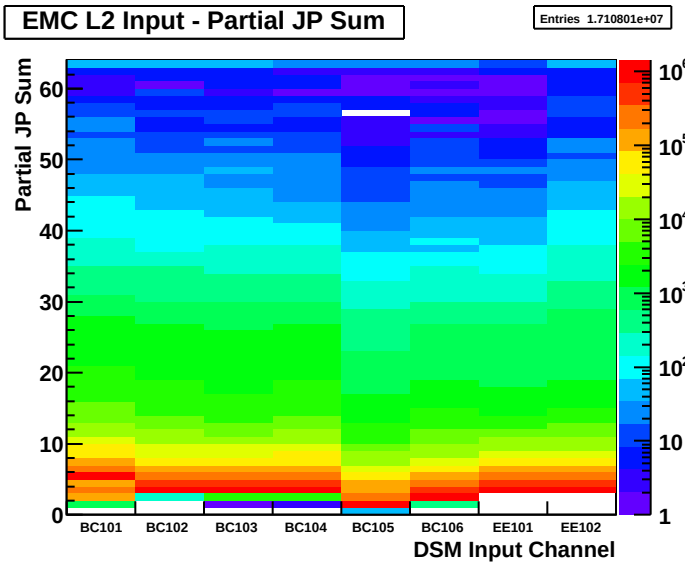

EMC L2 Input - JPX/JPA bits
Entries 4882
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3307
1575

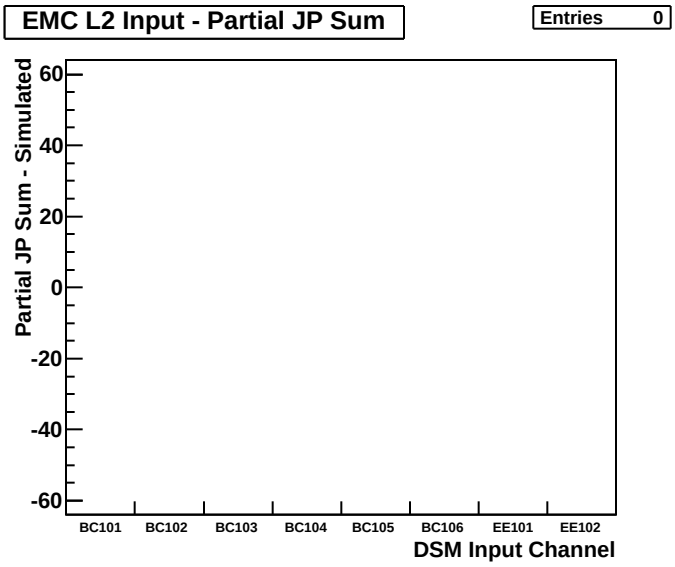

EMC L2 Input - JPY/JPB bits
Entries 1.710801e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4905 4135 2655 2006 1972 4196 1011 1520
16550 14457 7984 6630 6783 16770 3273 4118
33051 27192 16677 14518 16886 26107 3798 4314
2.084e+06 2.09272e+06 2.11118e+06 2.11535e+06 2.11286e+06 2.09143e+06 2.13042e+06 2.12855e+06

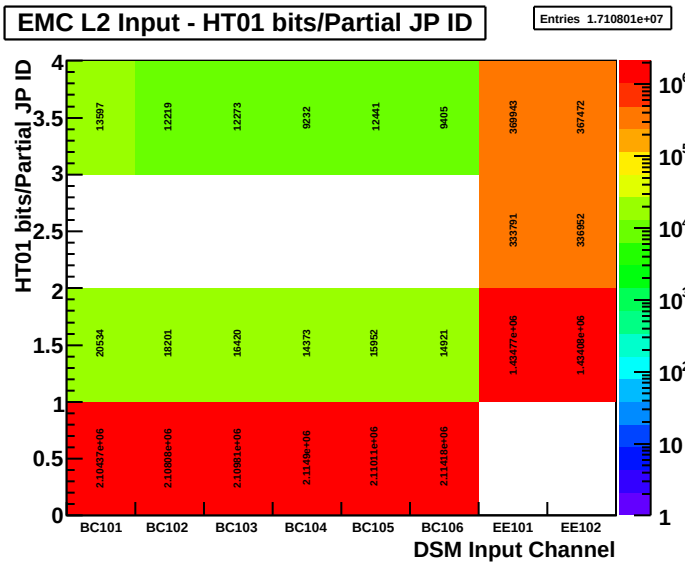

EMC L2 Input - JPY/JPB bits
Entries 4818
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2742
2076

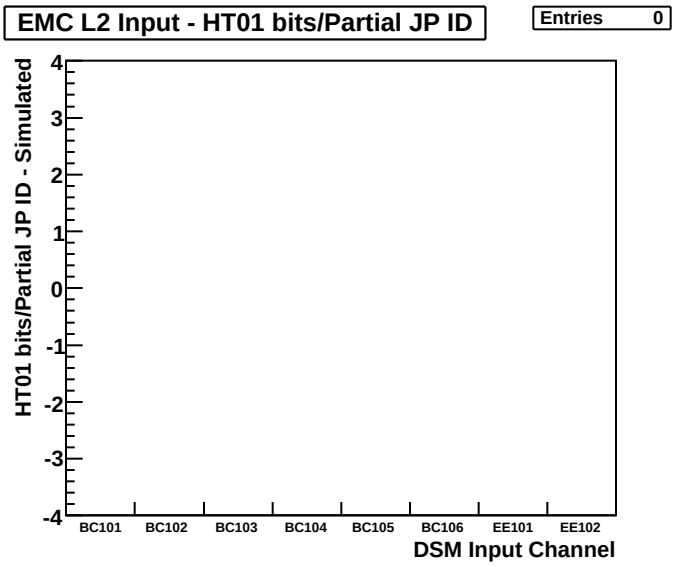

EMC L2 Input - JPZ/JPC bits
Entries 1.710801e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5526 4938 3502 4081 1488 2656 1107 879
20764 19432 11656 17163 4428 9312 3915 3009
32606 29189 19938 25675 9689 16855 4791 3918
2.0796e+06 2.08494e+06 2.1034e+06 2.09158e+06 2.1229e+06 2.10968e+06 2.12869e+06 2.1307e+06

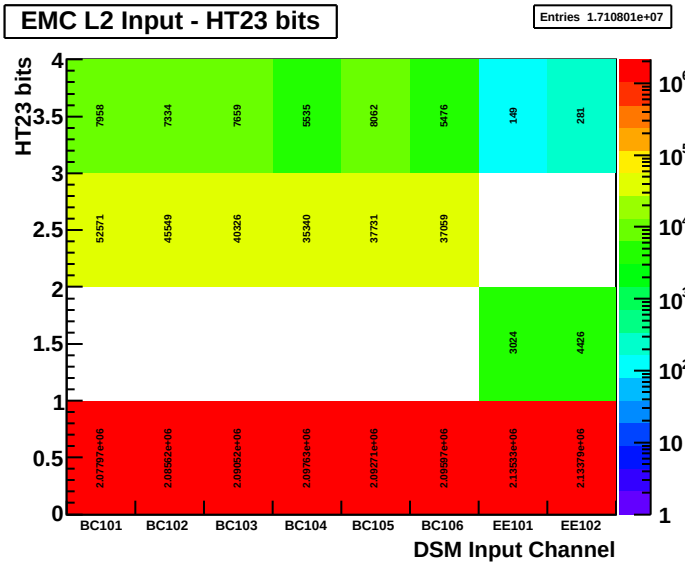

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

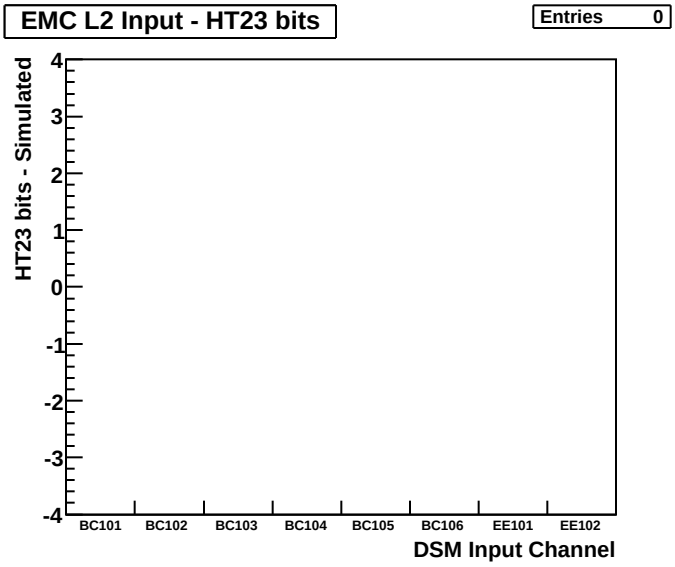

EMC L2 Input - Partial JP Sum
Entries 1.710801e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

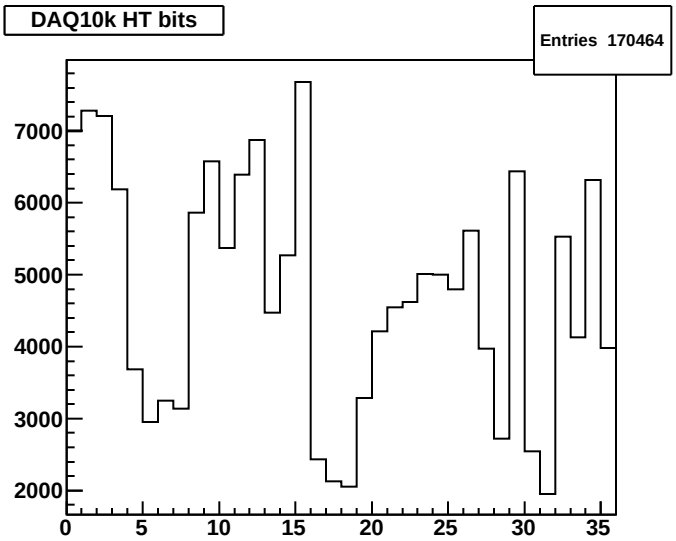

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

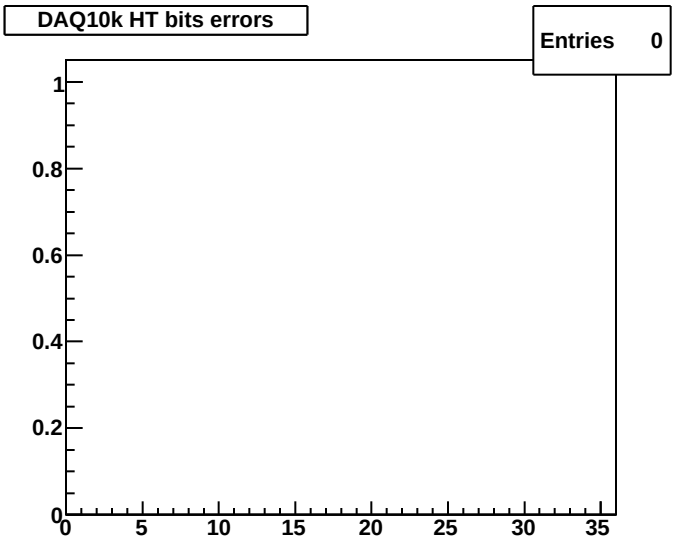

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.710801e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

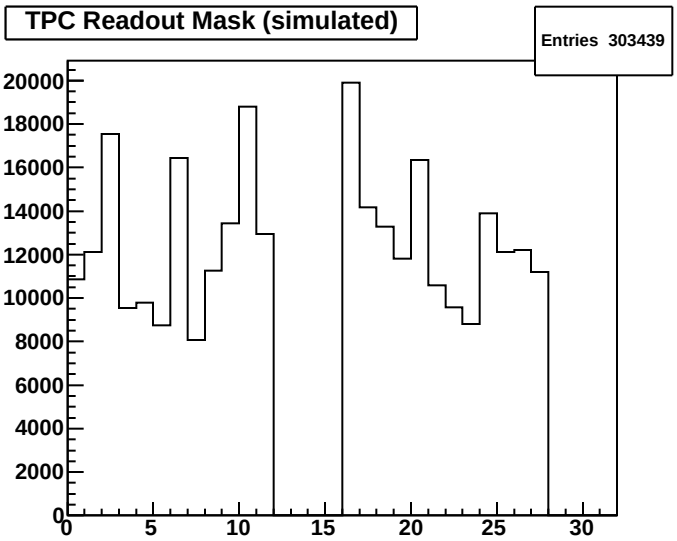

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

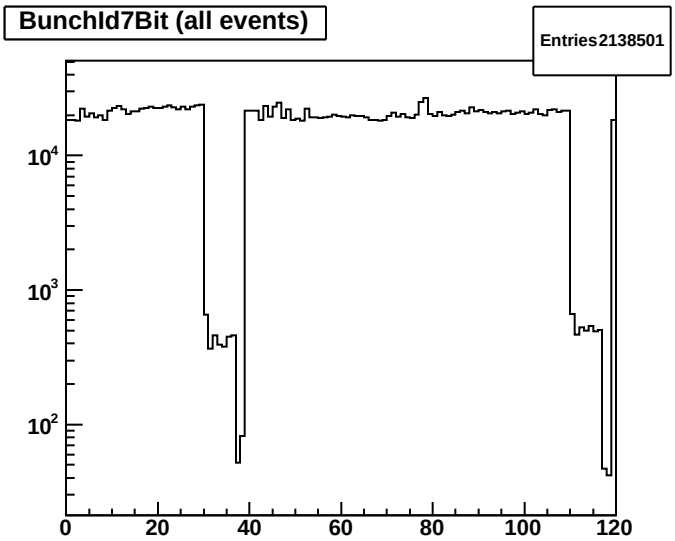

EMC L2 Input - HT23 bits
Entries 1.710801e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

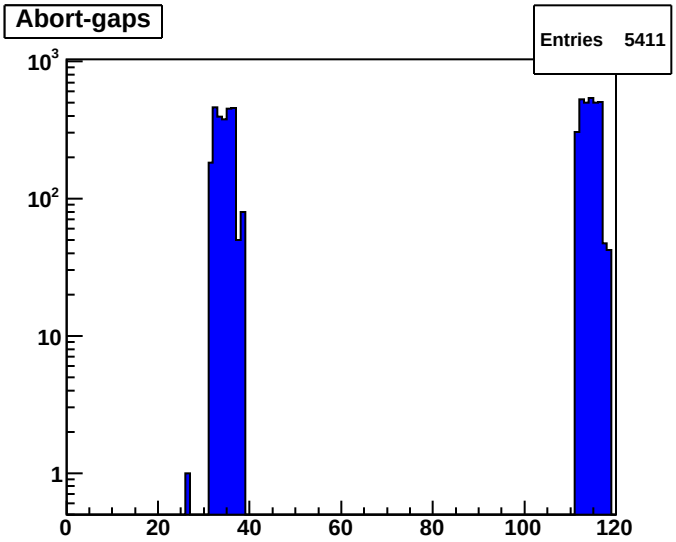

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

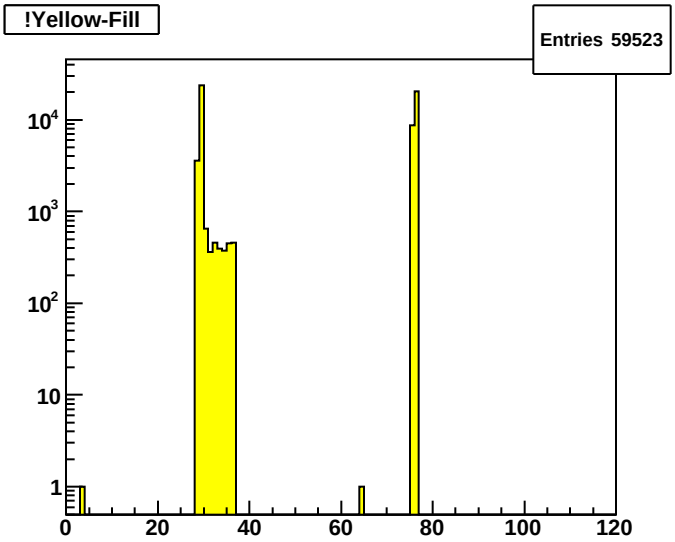

DAQ10k HT bits
Entries 170464

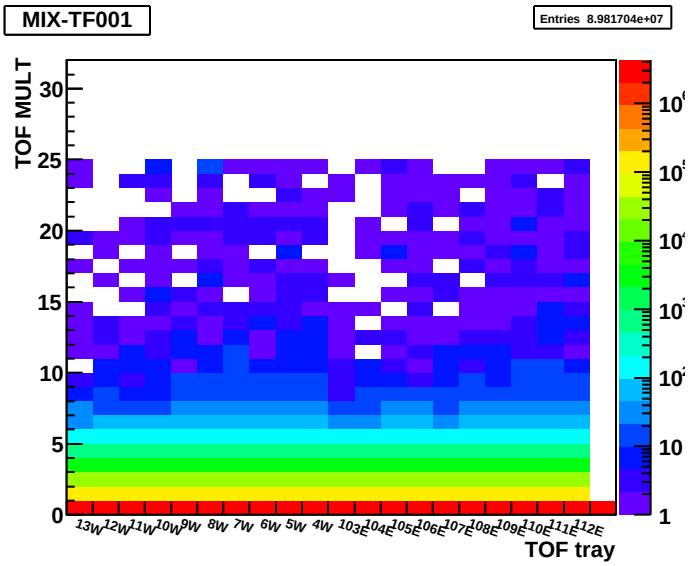

DAQ10k HT bits errors
Entries 0

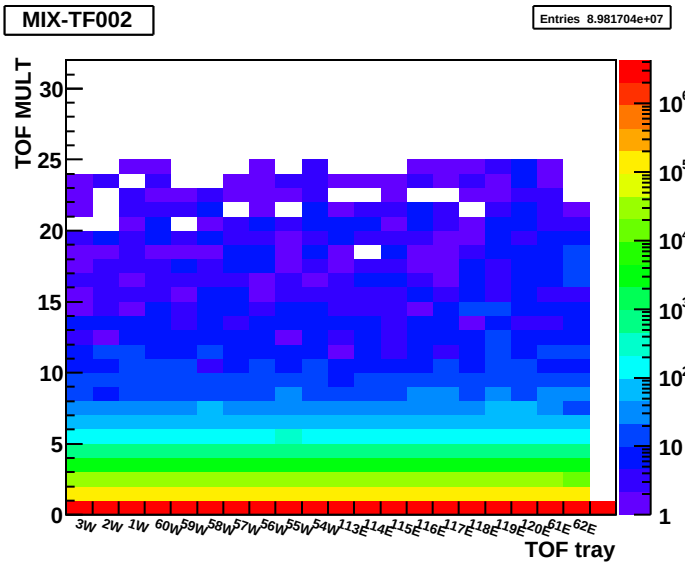

TPC Readout Mask (simulated)
Entries 303439

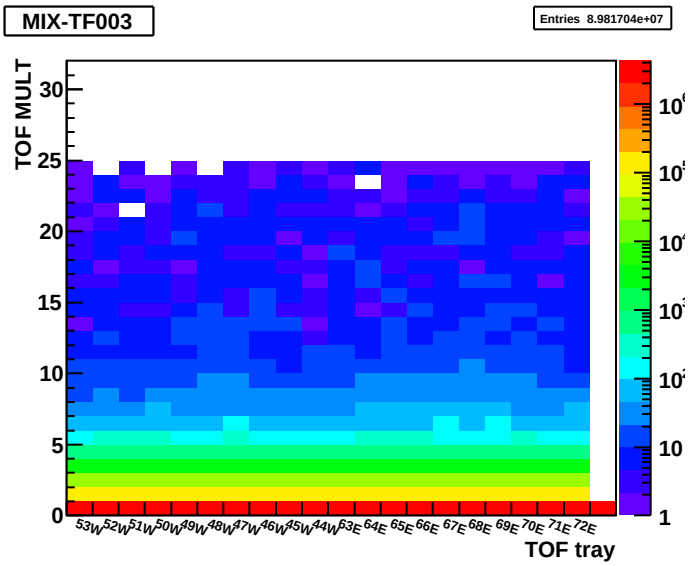

BunchId7Bit (all events)
Entries 2138501

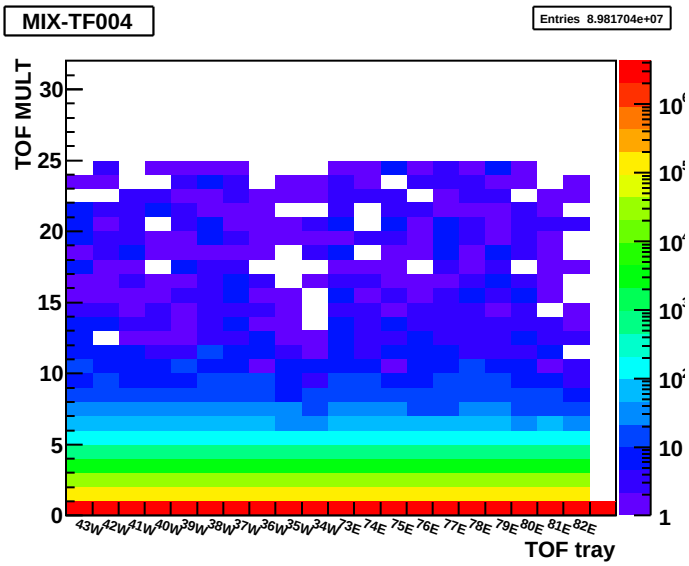

Abort-gaps
Entries 5411

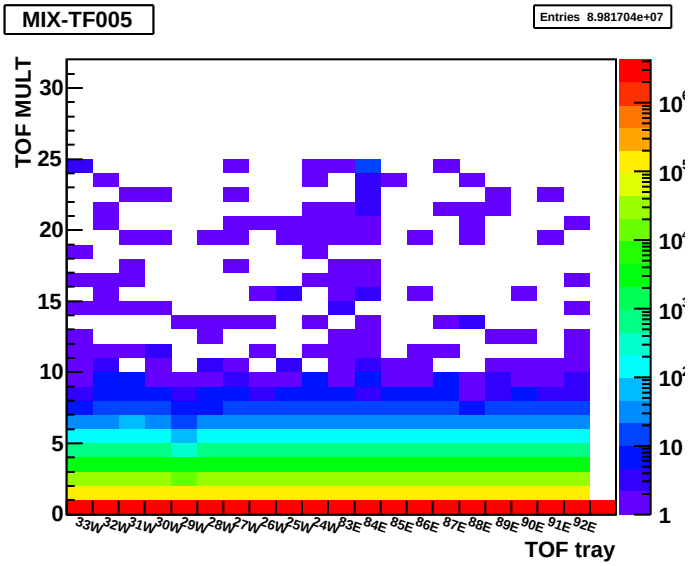

!Yellow-Fill
Entries 59523

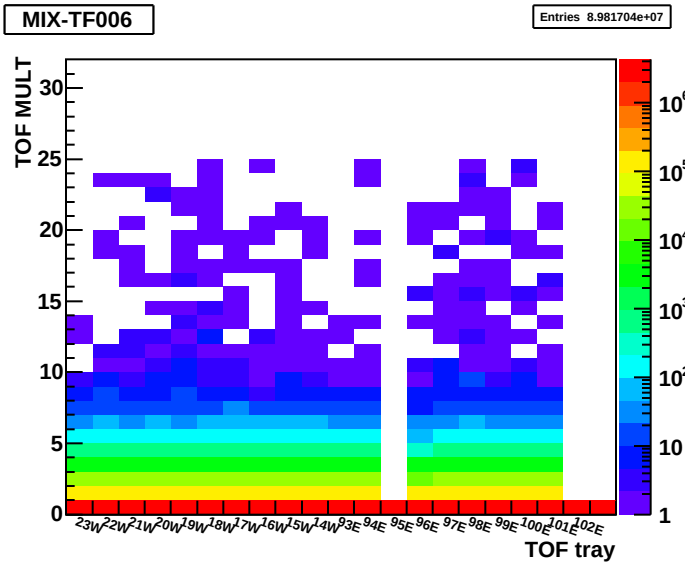

MIX-TF001
Entries 8.981704e+07
TOF MULT
TOF tray

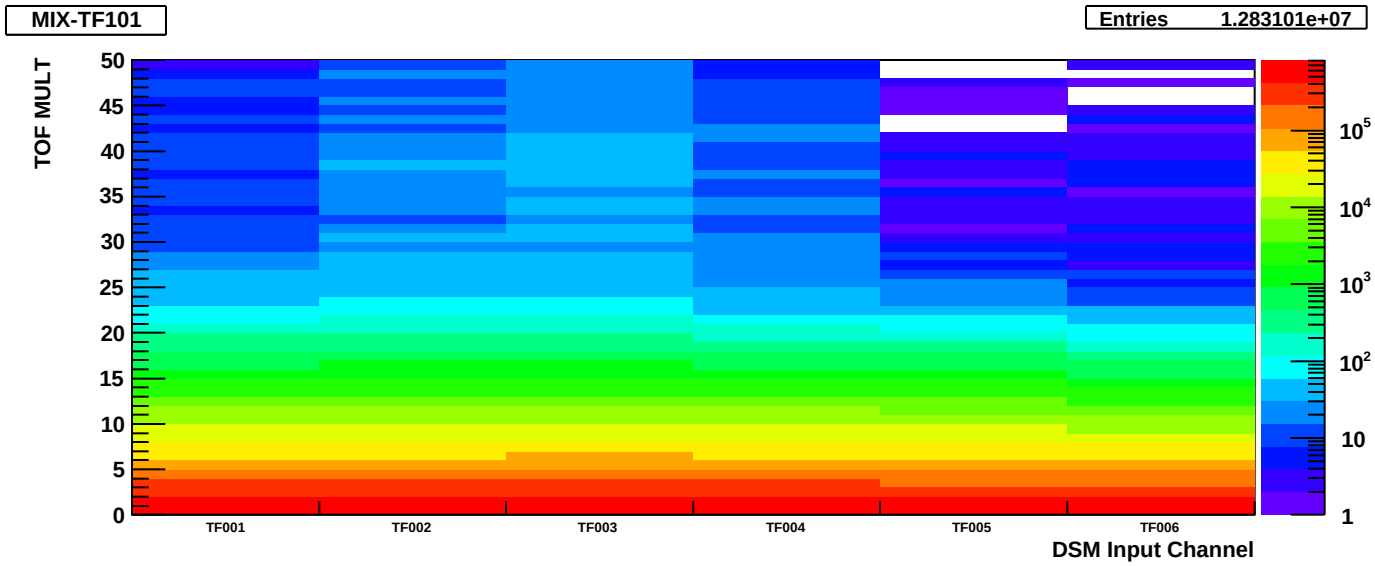

MIX-TF002
Entries 8.981704e+07
TOF MULT
TOF tray

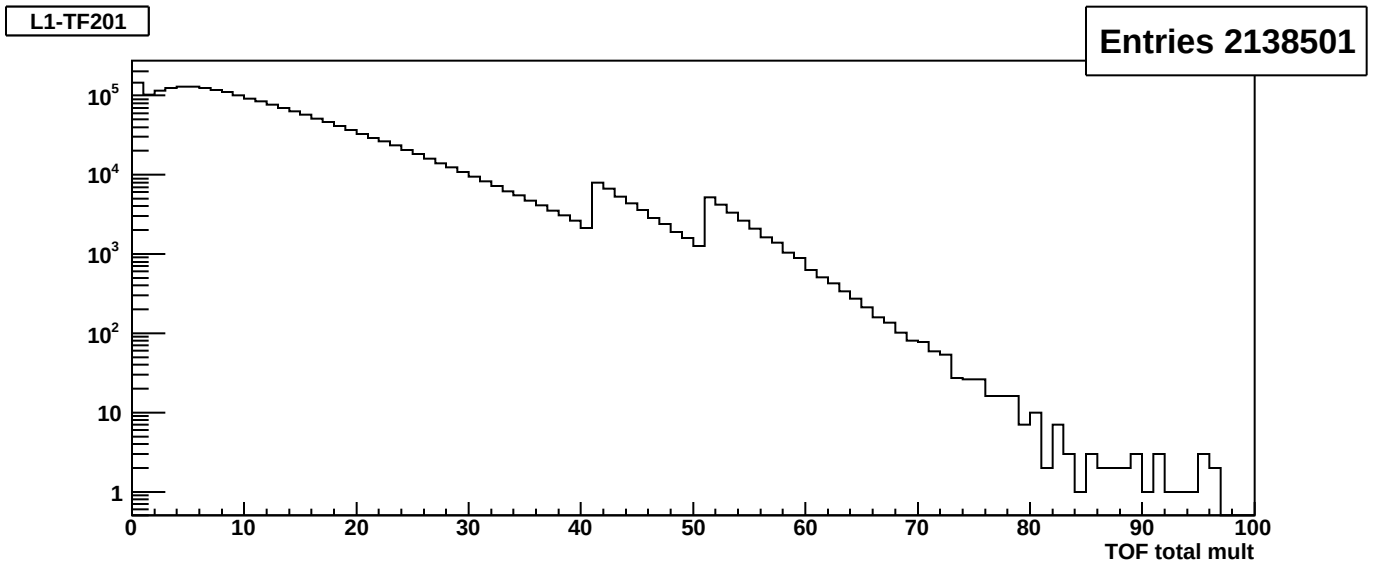

MIX-TF003
Entries 8.981704e+07
TOF MULT
TOF tray

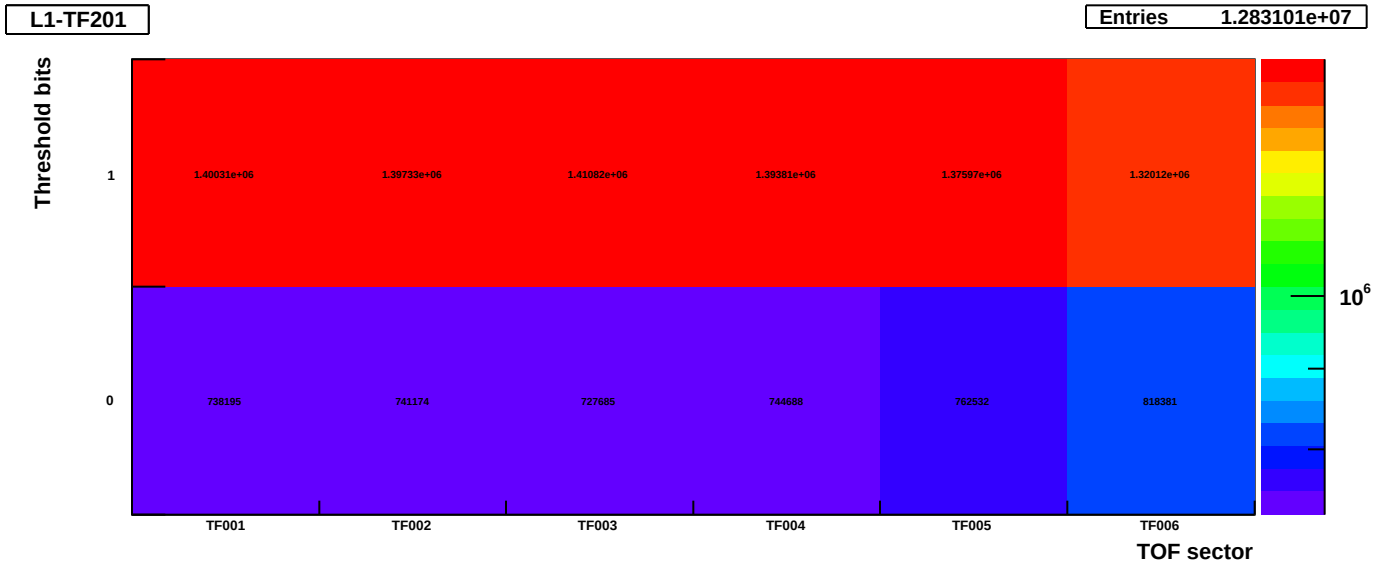

MIX-TF004
Entries 8.981704e+07
TOF MULT
TOF tray

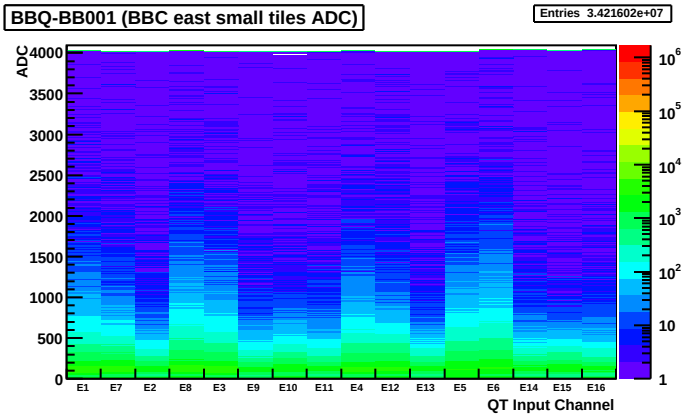

MIX-TF005
Entries 8.981704e+07
TOF MULT
TOF tray

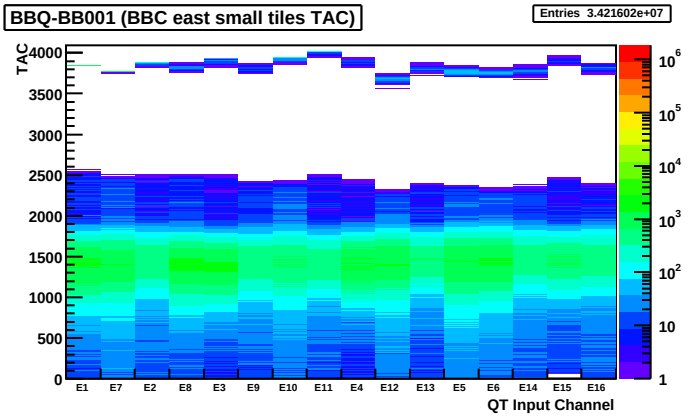

MIX-TF006
Entries 8.981704e+07
TOF MULT
TOF tray

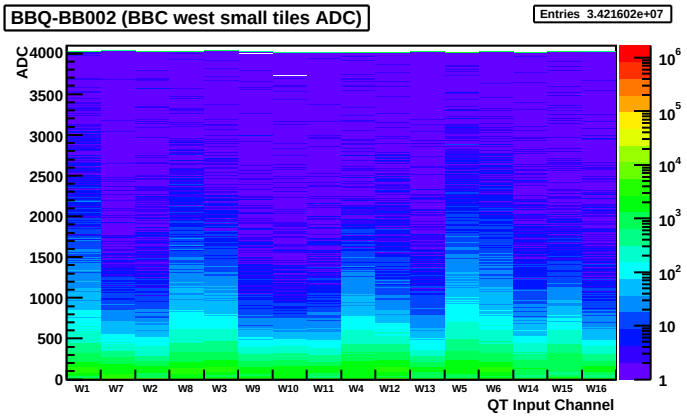

MIX-TF101
Entries 1.283101e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

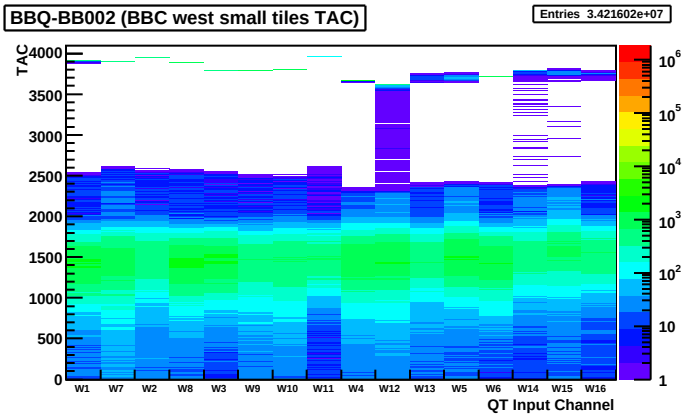

L1-TF201
Entries 2138501
TOF total mult

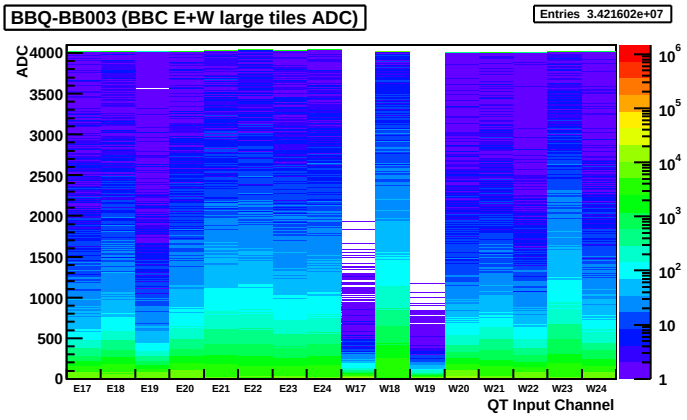

L1-TF201
Entries 1.283101e+07
Threshold bits
1.40031e+06
1.39733e+06
1.41082e+06
1.39381e+06
1.37597e+06
1.32012e+06
738195
741174
727685
744688
762532
818381
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

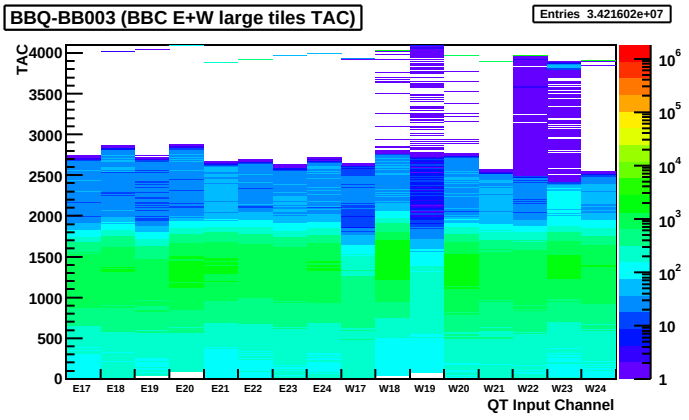

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.421602e+07
ADC
QT Input Channel

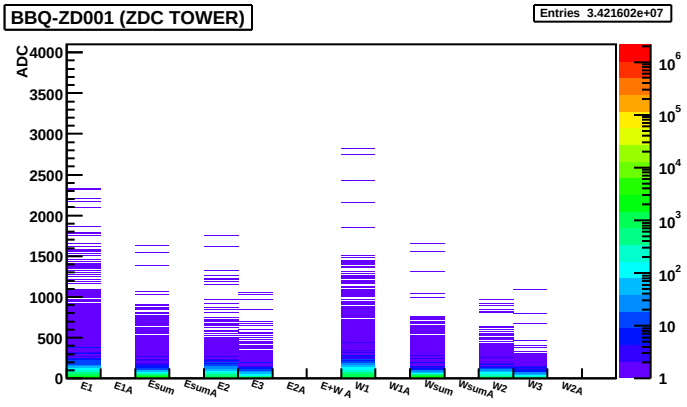

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.421602e+07
TAC
QT Input Channel

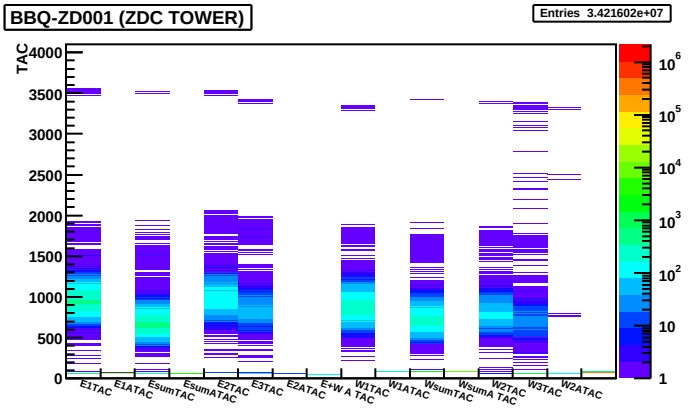

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.421602e+07
ADC
QT Input Channel

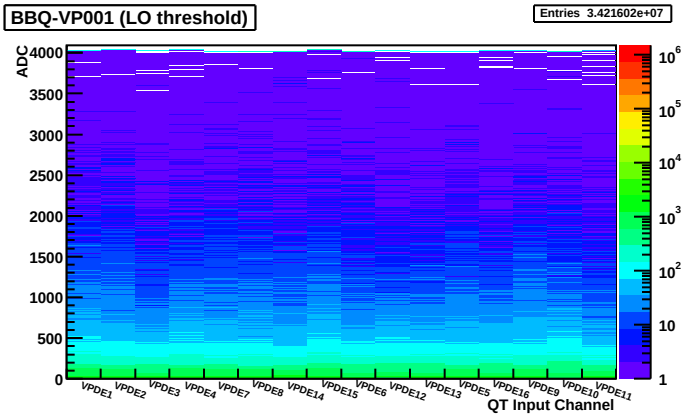

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.421602e+07
TAC
QT Input Channel

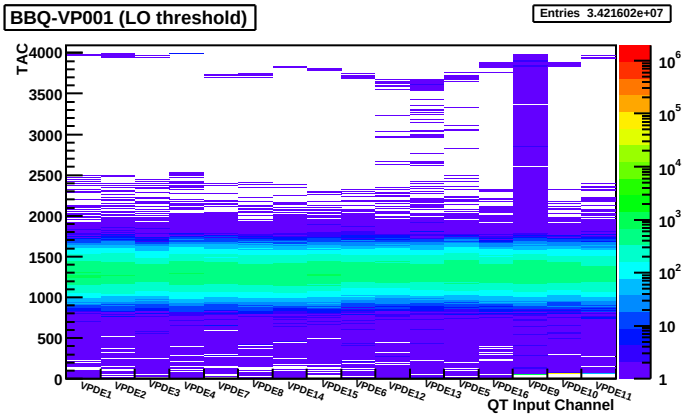

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.421602e+07
ADC
QT Input Channel

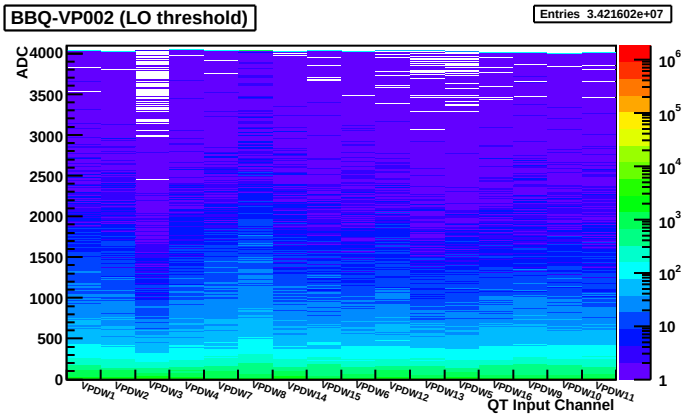

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.421602e+07
TAC
QT Input Channel

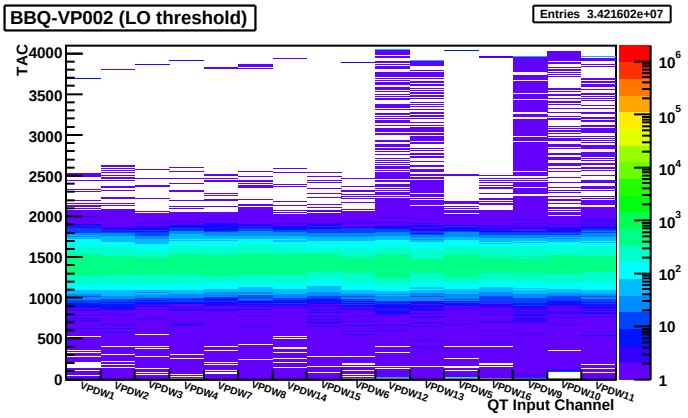

BBQ-ZD001 (ZDC TOWER)
Entries 3.421602e+07
ADC

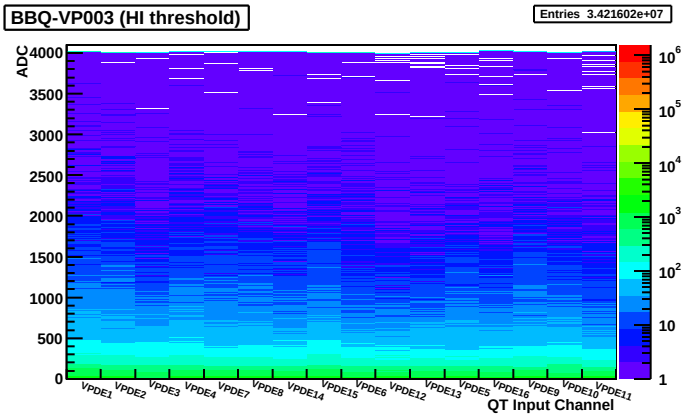

BBQ-ZD001 (ZDC TOWER)
Entries 3.421602e+07
TAC

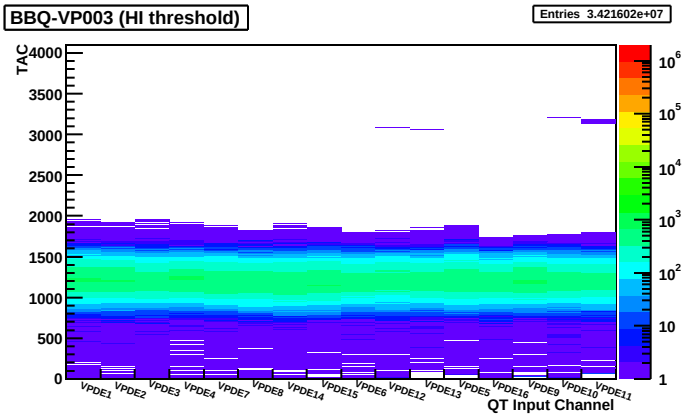

BBQ-VP001 (LO threshold)
Entries 3.421602e+07
ADC
QT Input Channel

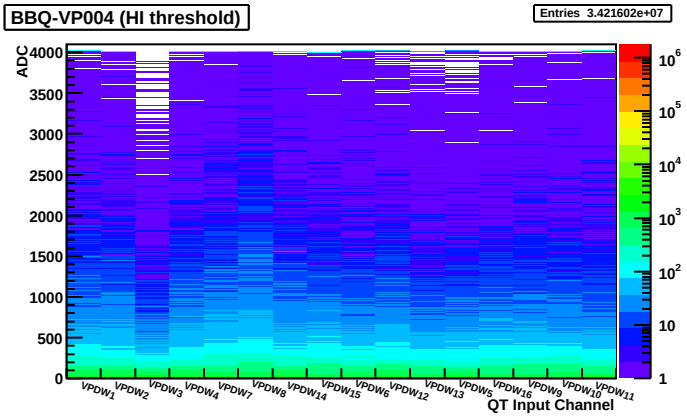

BBQ-VP001 (LO threshold)
Entries 3.421602e+07
TAC
QT Input Channel

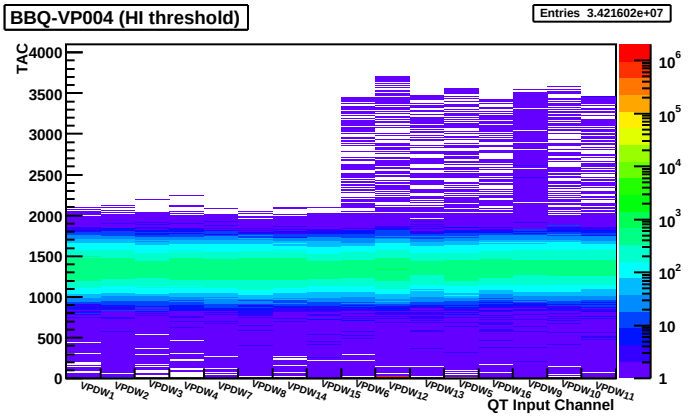

BBQ-VP002 (LO threshold)
Entries 3.421602e+07
ADC
QT Input Channel

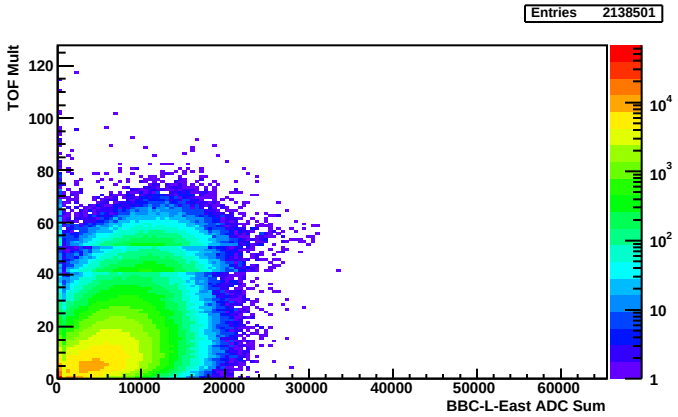

BBQ-VP002 (LO threshold)
Entries 3.421602e+07
TAC
QT Input Channel

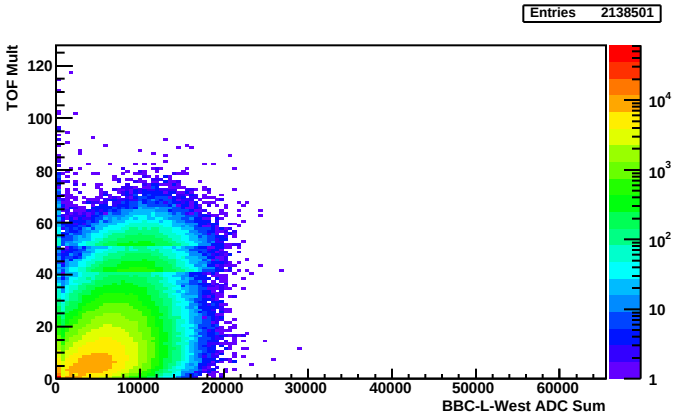

BBQ-VP003 (HI threshold)
Entries 3.421602e+07
ADC
QT Input Channel

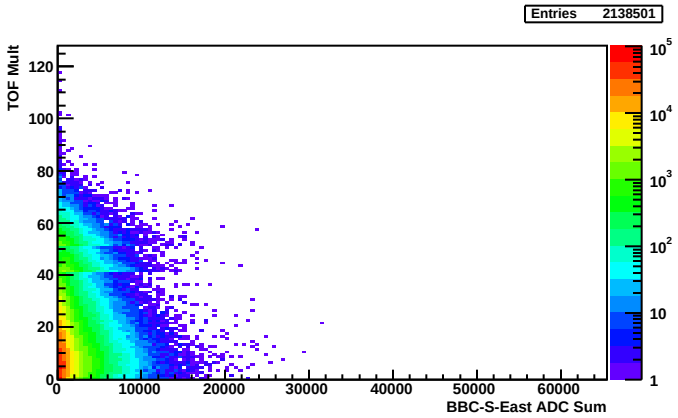

BBQ-VP003 (HI threshold)
Entries 3.421602e+07
TAC
QT Input Channel

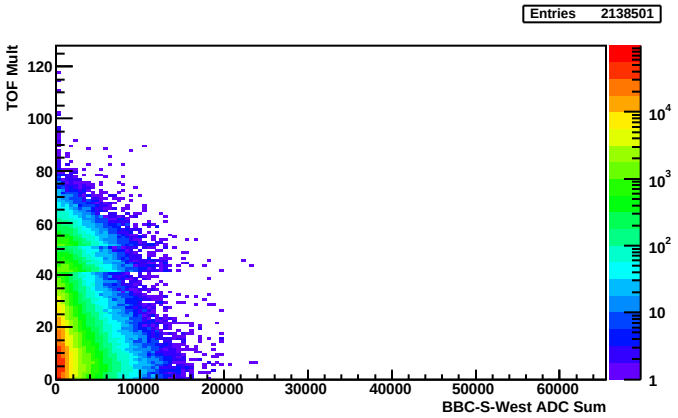

BBQ-VP004 (HI threshold)
Entries 3.421602e+07
ADC
QT Input Channel

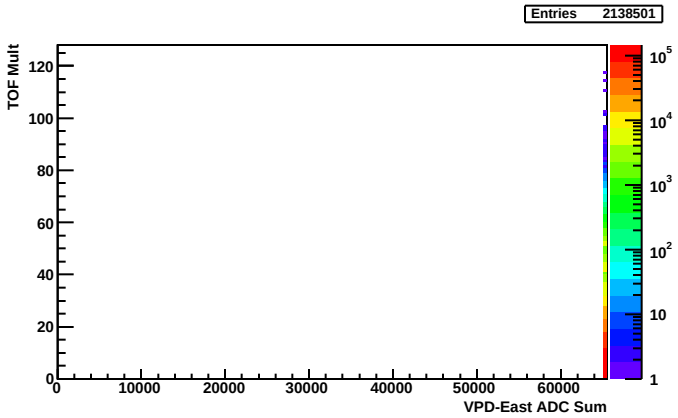

BBQ-VP004 (HI threshold)
Entries 3.421602e+07
TAC
QT Input Channel

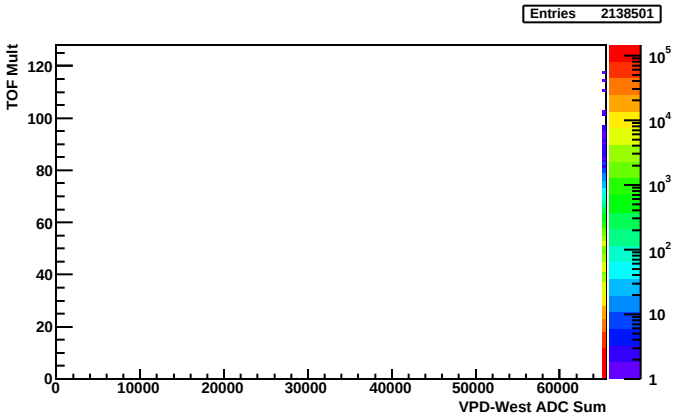

Entries 2138501
TOF Mult
BBC-L-East ADC Sum

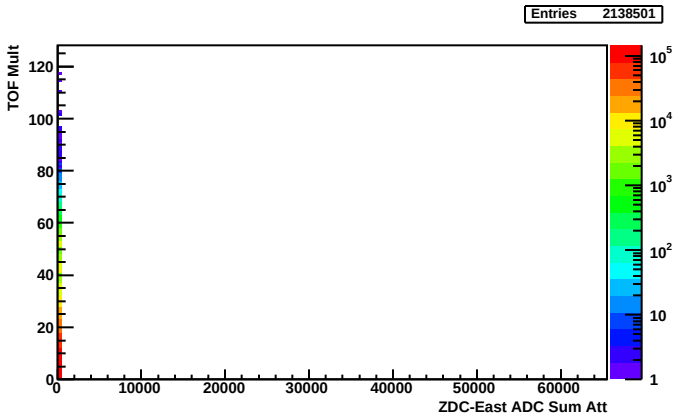

Entries 2138501
TOF Mult
BBC-L-West ADC Sum

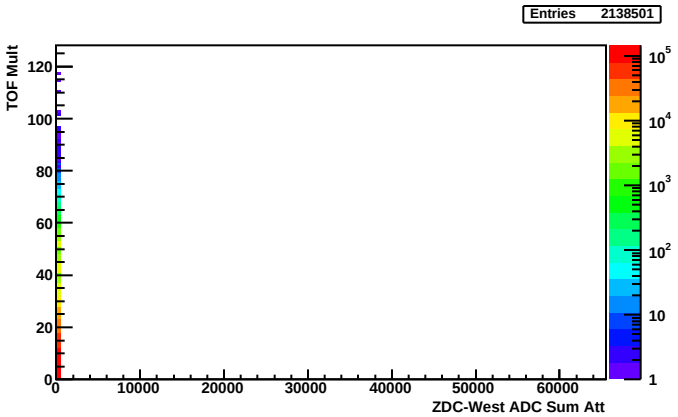

Entries 2138501
TOF Mult
BBC-S-East ADC Sum

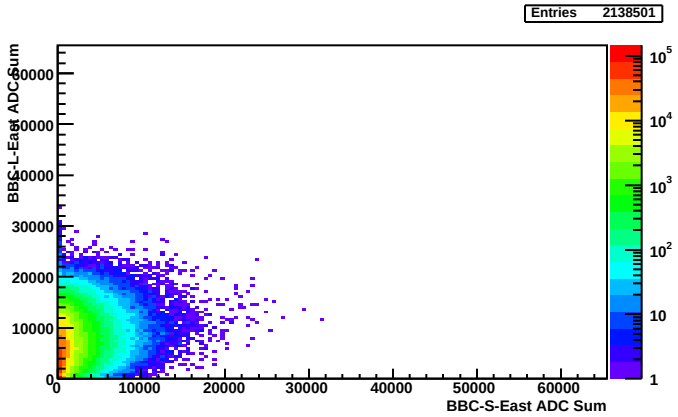

Entries 2138501
TOF Mult
BBC-S-West ADC Sum

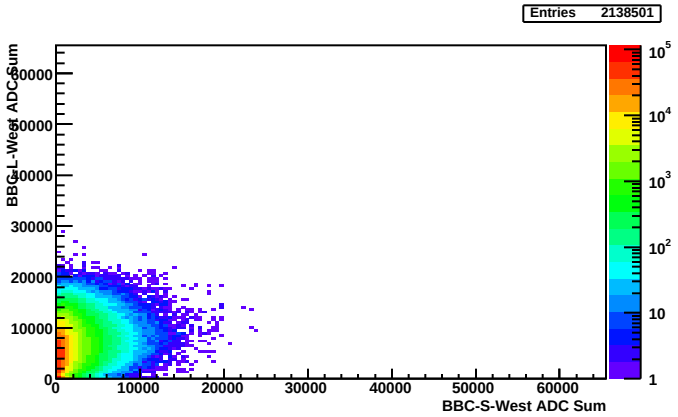

Entries 2138501
TOF Mult
VPD-East ADC Sum

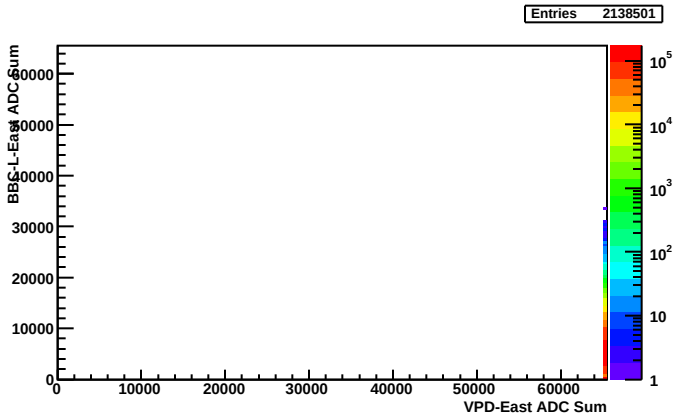

Entries 2138501
TOF Mult
VPD-West ADC Sum

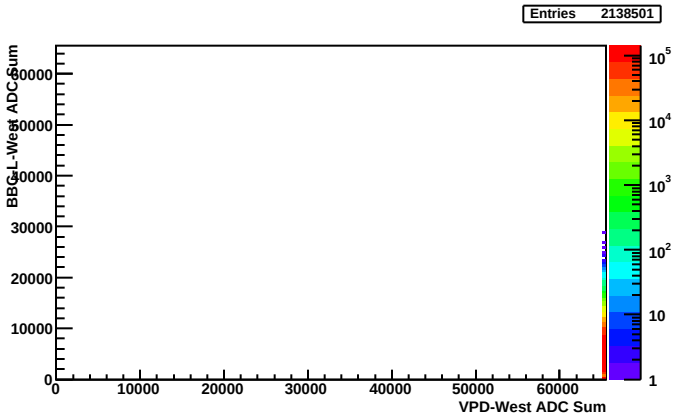

Entries 2138501
TOF Mult
ZDC-East ADC Sum Att

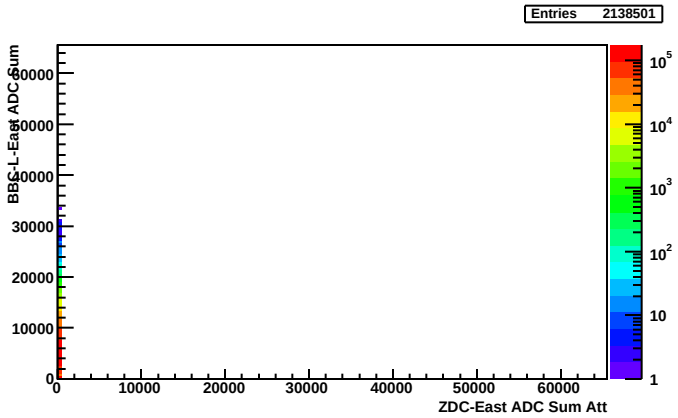

Entries 2138501
TOF Mult
ZDC-West ADC Sum Att

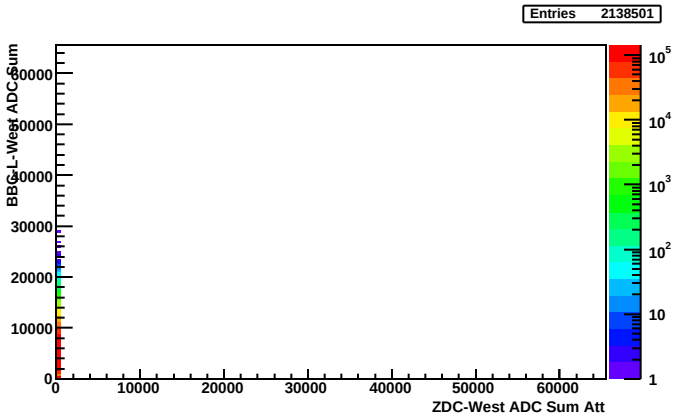

Entries 2138501
BBC-L-East ADC Sum
BBC-S-East ADC Sum

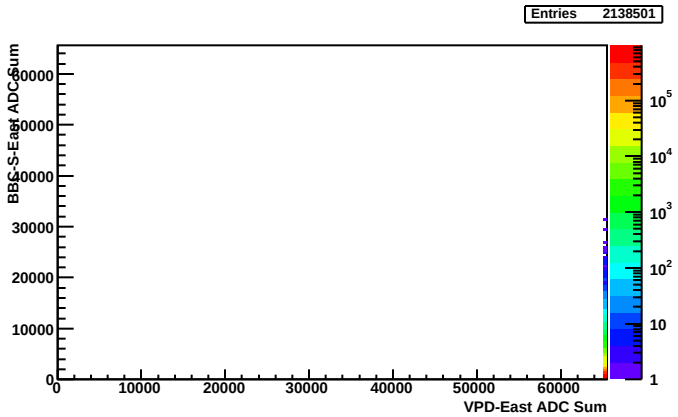

Entries 2138501
BBC-L-West ADC Sum
BBC-S-West ADC Sum

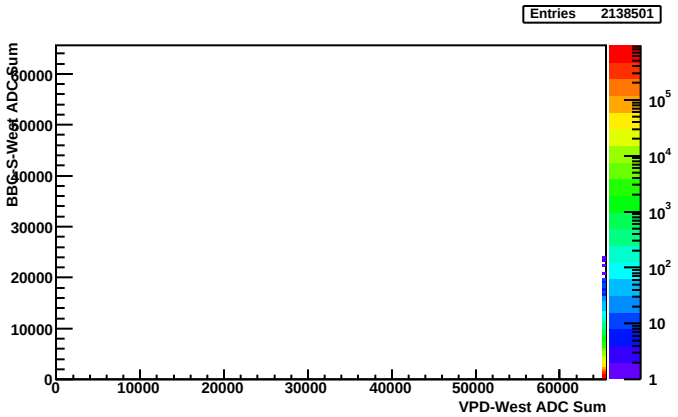

Entries 2138501
BBC-L-East ADC Sum
VPD-East ADC Sum

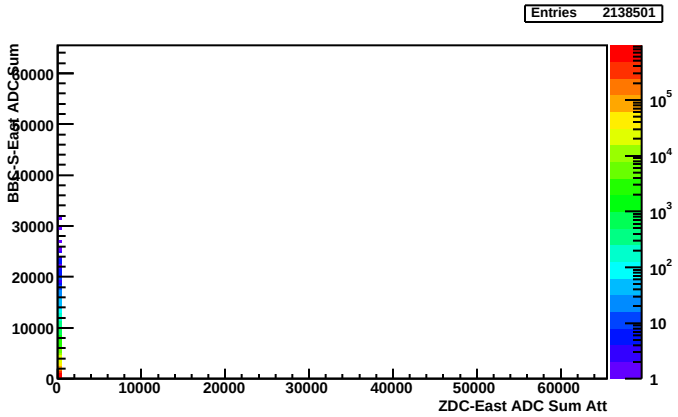

Entries 2138501
BBC-L-West ADC Sum
VPD-West ADC Sum

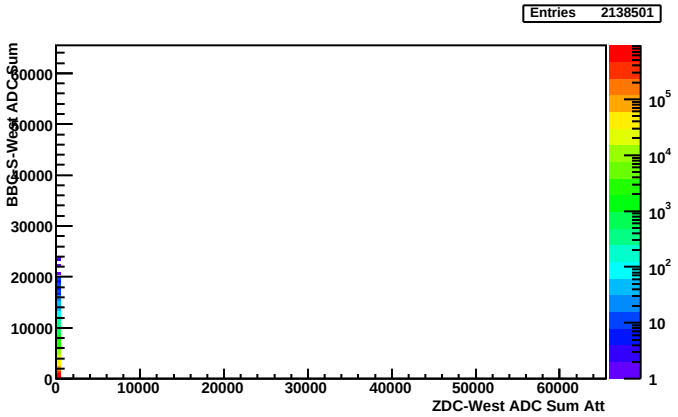

Entries 2138501
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

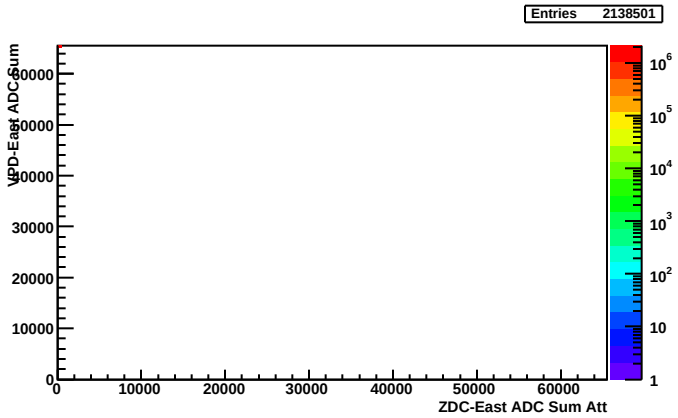

Entries 2138501
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

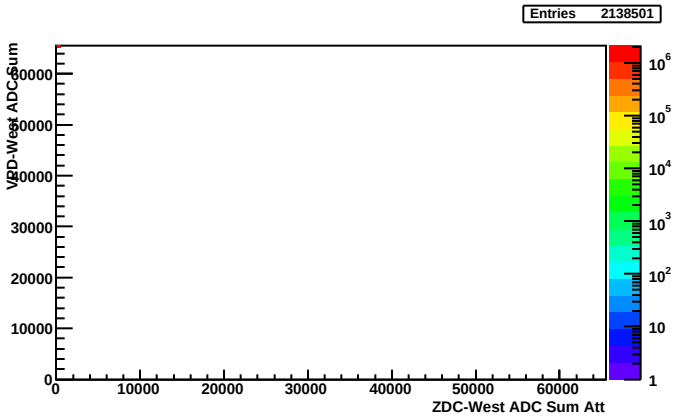

Entries 2138501
BBC-S-East ADC Sum
VPD-East ADC Sum

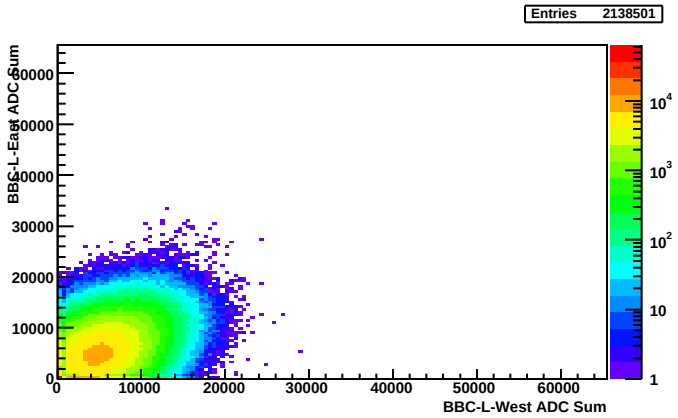

Entries 2138501
BBC-S-West ADC Sum
VPD-West ADC Sum

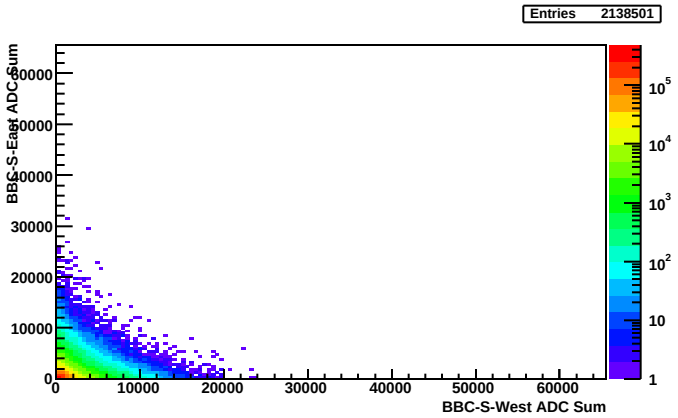

Entries 2138501
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

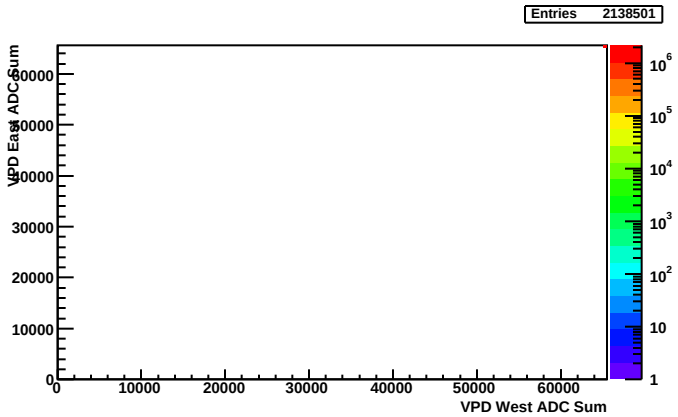

Entries 2138501
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

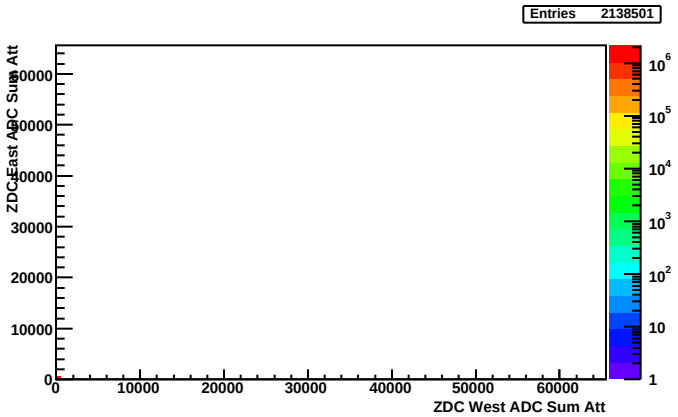

Entries 2138501
VPD-East ADC Sum
ZDC-East ADC Sum Att

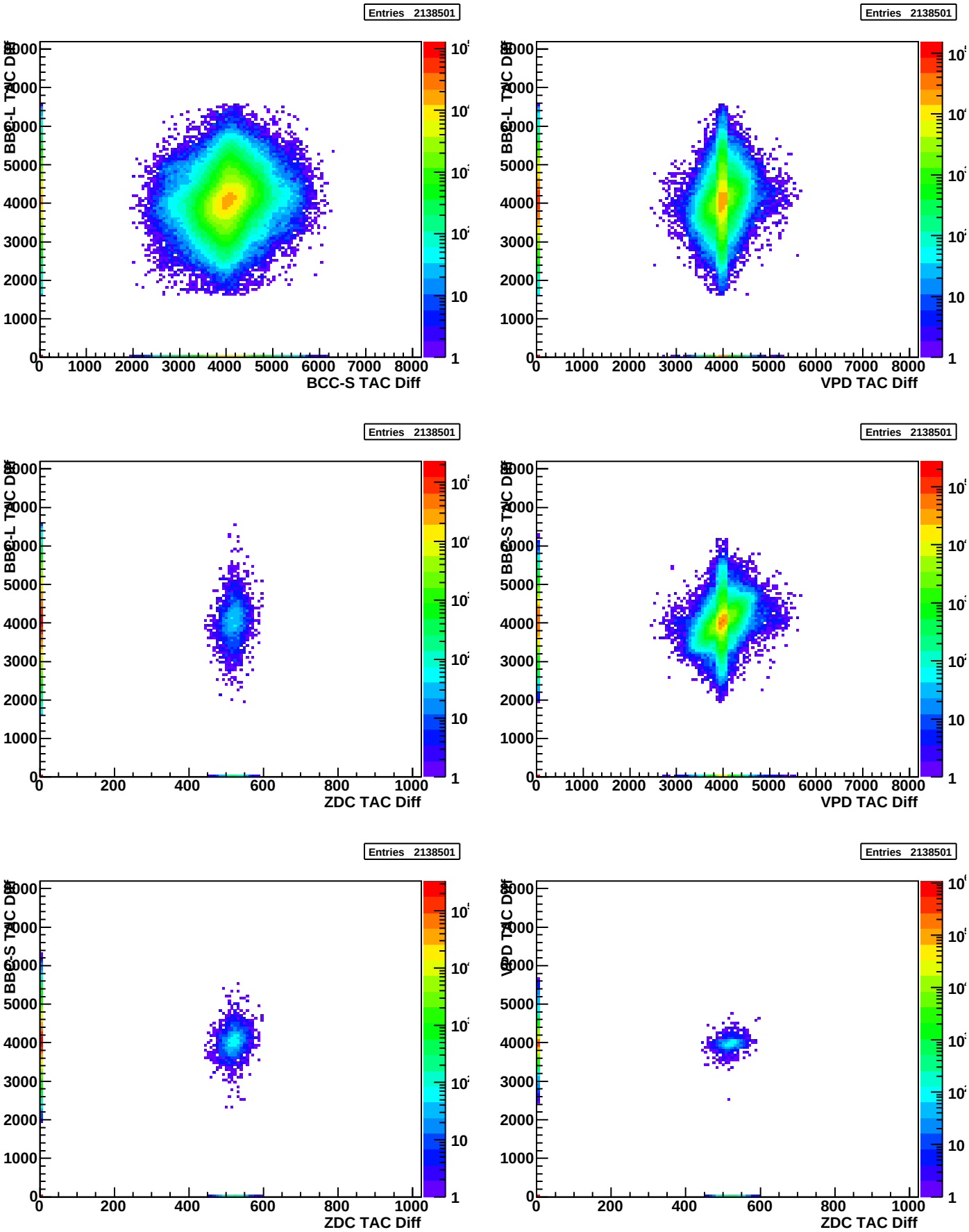

Entries 2138501
VPD-West ADC Sum
ZDC-West ADC Sum Att

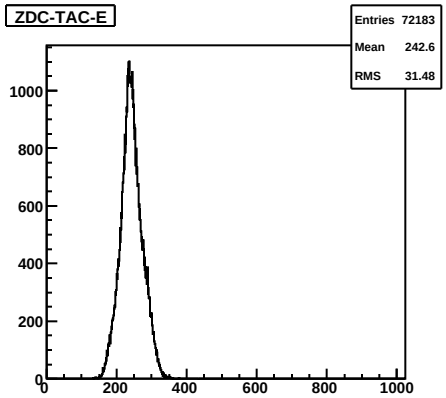

Entries 2138501
BBC-L-East ADC Sum
BBC-L-West ADC Sum

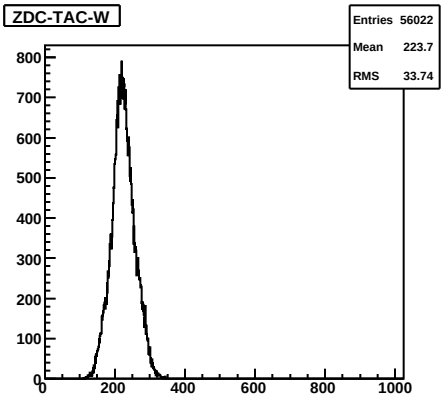

Entries 2138501
BBC-S-East ADC Sum
BBC-S-West ADC Sum

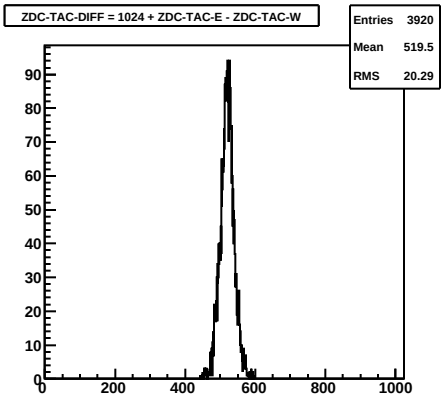

Entries 2138501
VPD East ADC Sum
VPD West ADC Sum

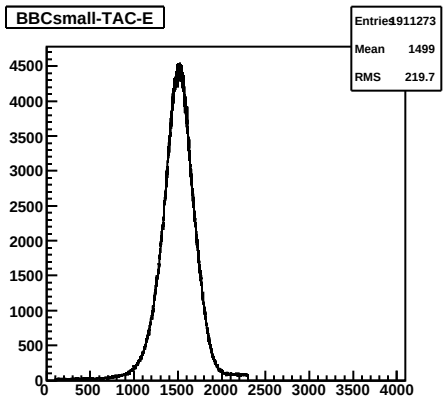

Entries 2138501
ZDC East ADC Sum Att
ZDC West ADC Sum Att

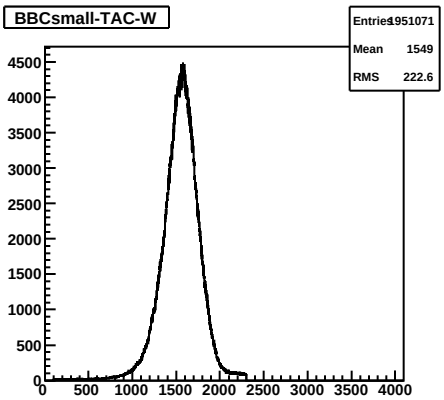

Entries 2138501
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2138501
BBC-L TAC Diff
VPD TAC Diff
Entries 2138501
BBC-L TAC Diff
ZDC TAC Diff
Entries 2138501
BBC-S TAC Diff
VPD TAC Diff
Entries 2138501
BBC-S TAC Diff
ZDC TAC Diff
Entries 2138501
VPD TAC Diff
ZDC TAC Diff

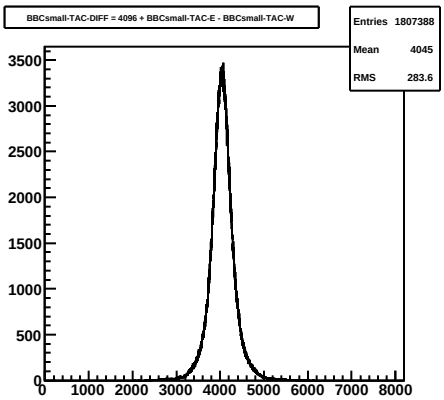

ZDC-TAC-E
Entries 72183
Mean 242.6
RMS 31.48

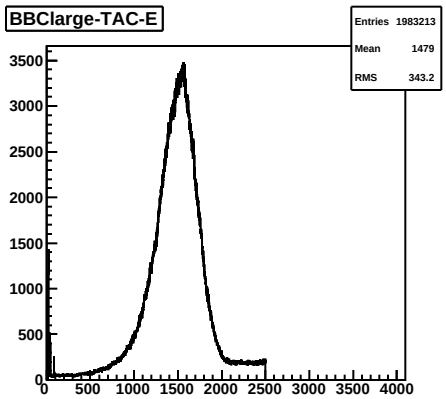

ZDC-TAC-W
Entries 56022
Mean 223.7
RMS 33.74

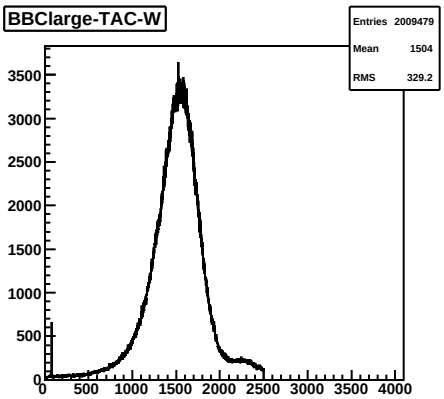

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3920
Mean 519.5
RMS 20.29

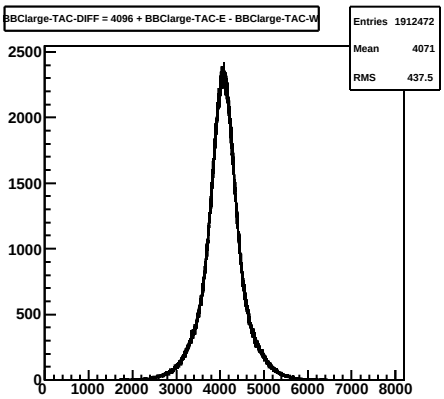

BBCsmall-TAC-E
Entries 1911273
Mean 1499
RMS 219.7

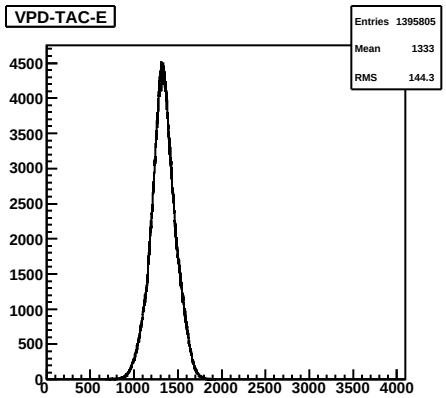

BBCsmall-TAC-W
Entries 1951071
Mean 1549
RMS 222.6

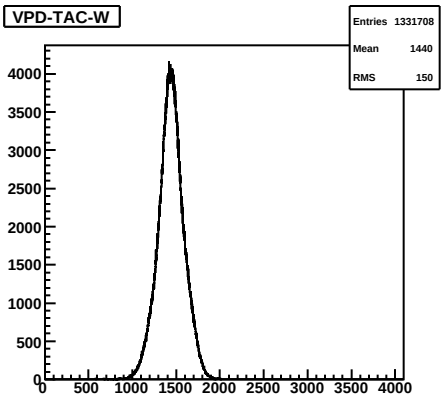

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1807388
Mean 4045
RMS 283.6

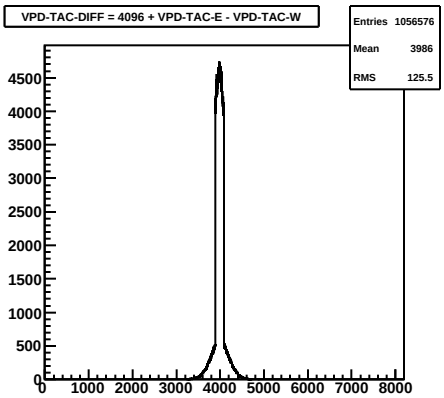

BBClarge-TAC-E
Entries 1983213
Mean 1479
RMS 343.2

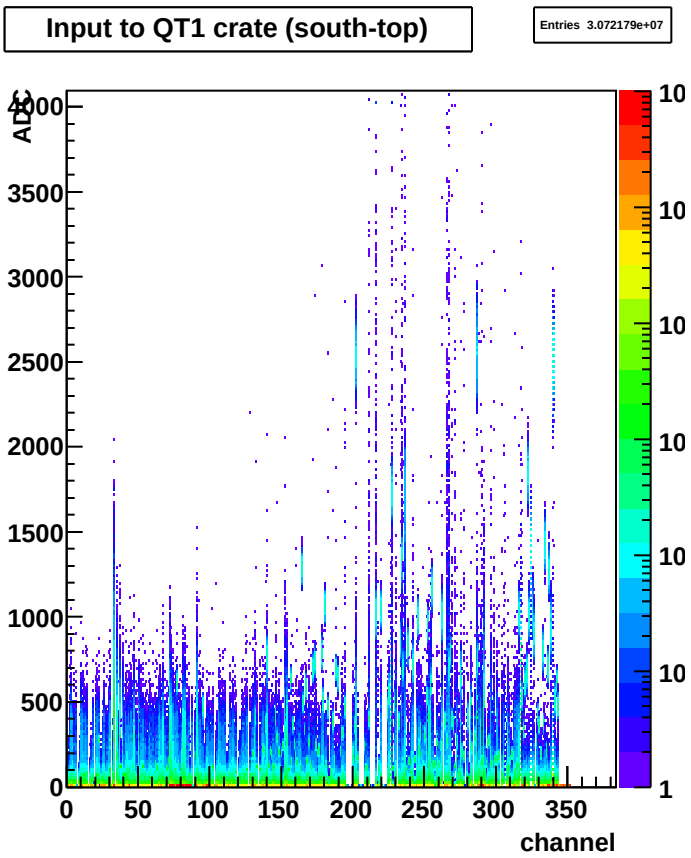

BBClarge-TAC-W
Entries 2009479
Mean 1504
RMS 329.2

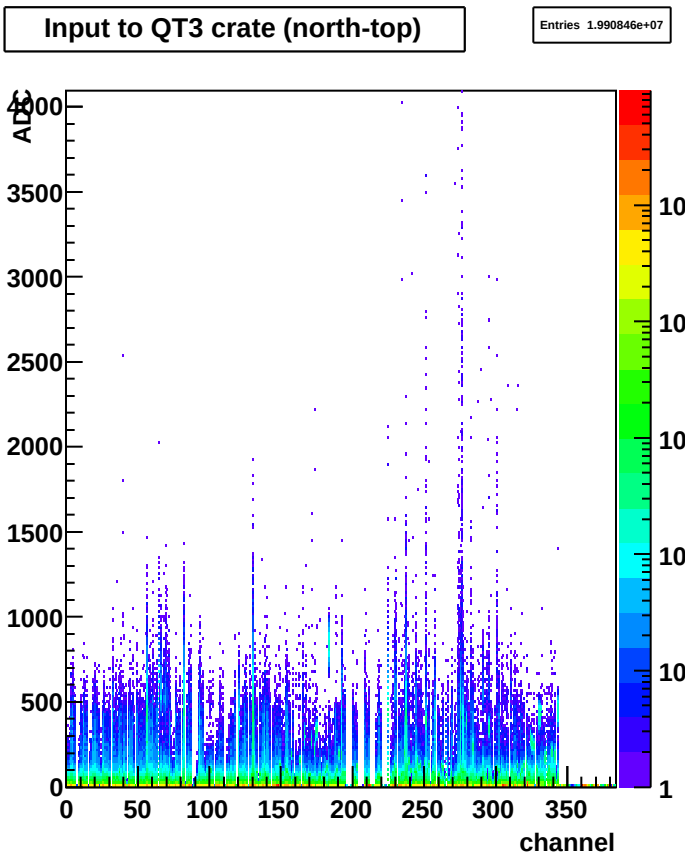

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1912472
Mean 4071
RMS 437.5

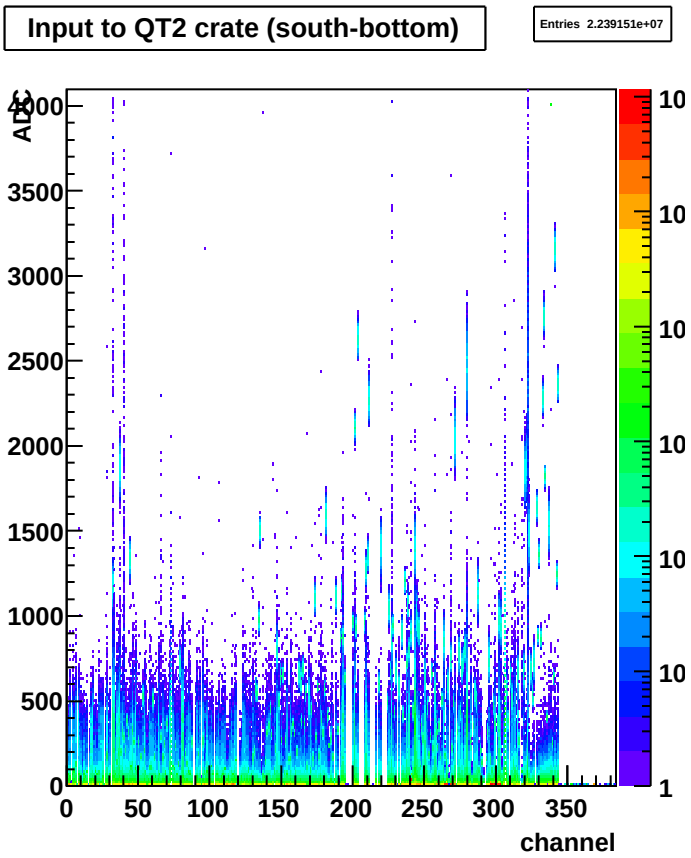

VPD-TAC-E
Entries 1395805
Mean 1333
RMS 144.3

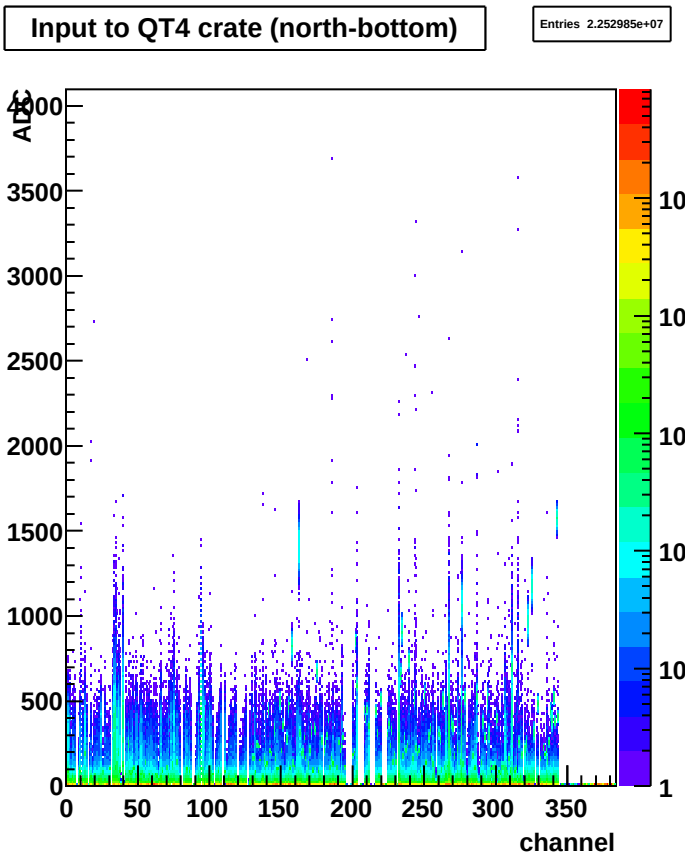

VPD-TAC-W
Entries 1331708
Mean 1440
RMS 150

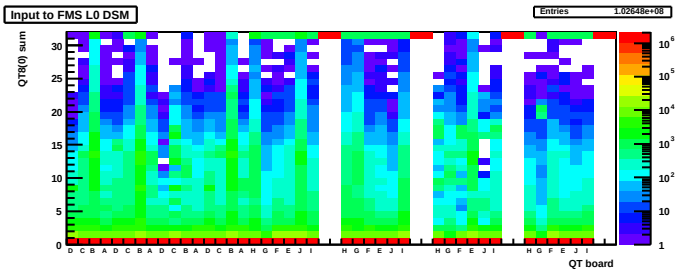

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1056576
Mean 3986
RMS 125.5

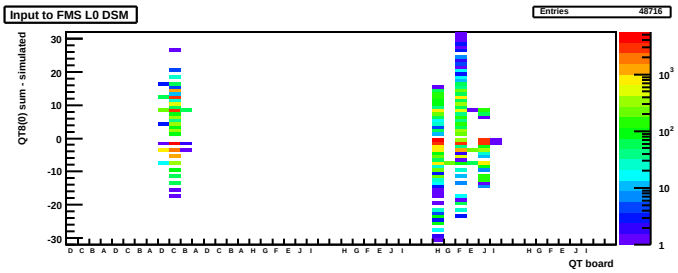

Input to QT1 crate (south-top)
Entries 3.072179e+07
ADC
channel

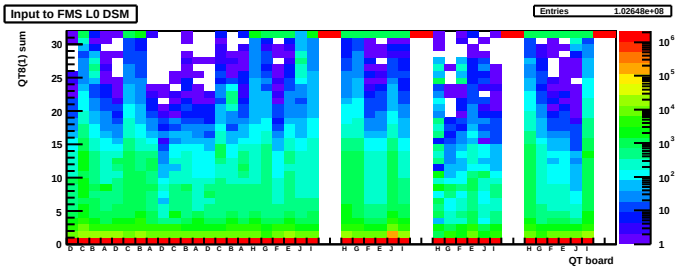

Input to QT3 crate (north-top)
Entries 1.990846e+07
ADC
channel

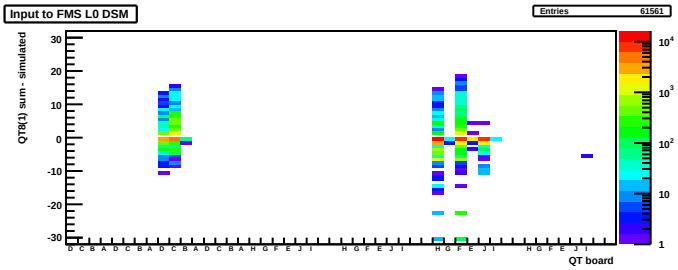

Input to QT2 crate (south-bottom)
Entries 2.239151e+07
ADC
channel

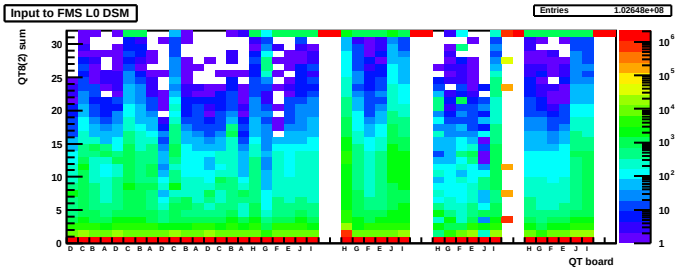

Input to QT4 crate (north-bottom)
Entries 2.252985e+07
ADC
channel

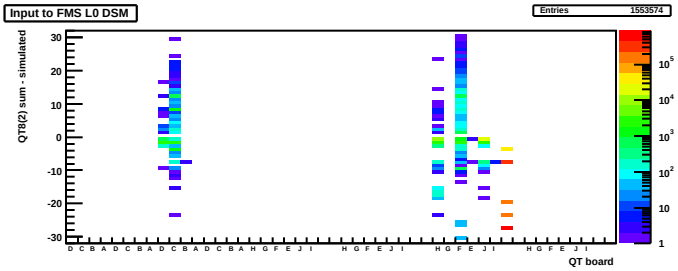

Input to FMS L0 DSM
Entries 1.02648e+08
QT8(0) sum
QT board

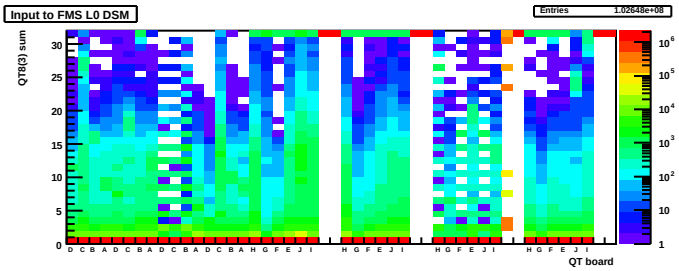

Input to FMS L0 DSM
Entries 48716
QT8(0) sum - simulated
QT board

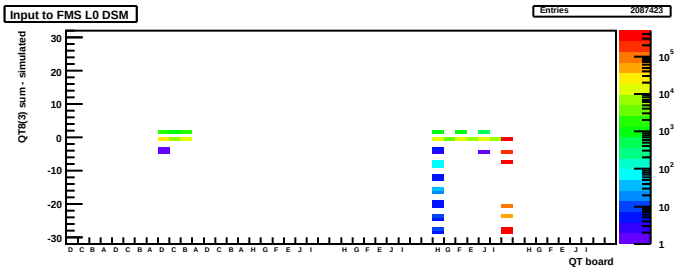

Input to FMS L0 DSM
Entries 1.02648e+08
QT8(1) sum
QT board

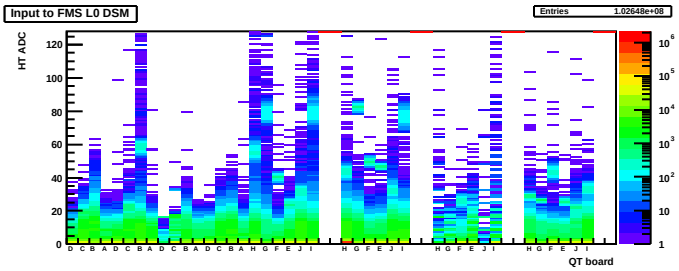

Input to FMS L0 DSM
Entries 61561
QT8(1) sum - simulated
QT board

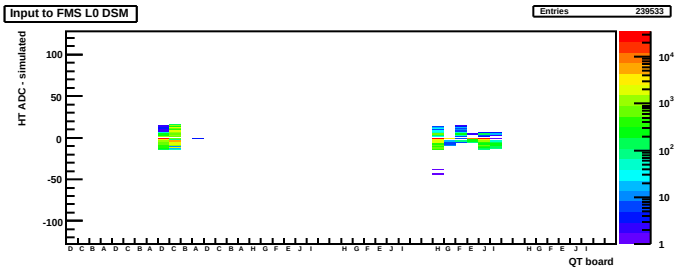

Input to FMS L0 DSM
Entries 1.02648e+08
QT8(2) sum
QT board

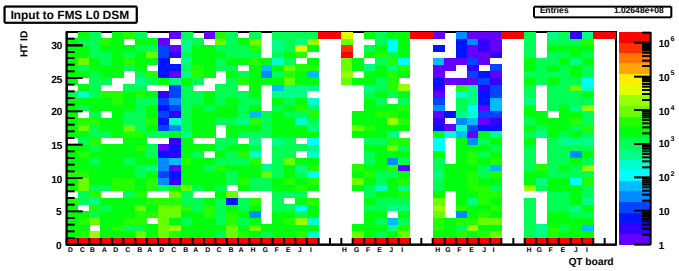

Input to FMS L0 DSM
Entries 1553574
QT8(2) sum - simulated
QT board

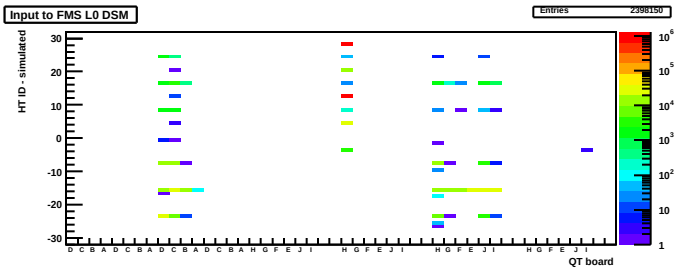

Input to FMS L0 DSM
Entries 1.02648e+08
QT8(3) sum
QT board

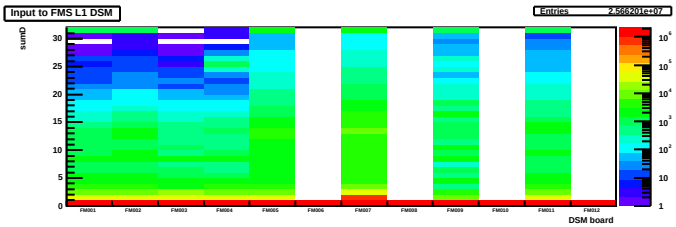

Input to FMS L0 DSM
Entries 2087423
QT8(3) sum - simulated
QT board

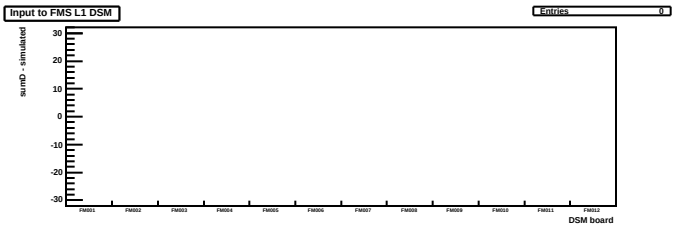

Input to FMS L0 DSM
Entries 1.02648e+08
HT ADC
QT board

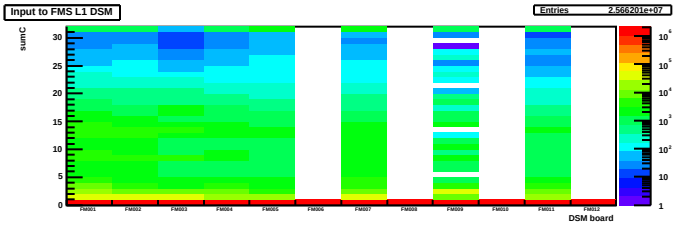

Input to FMS L0 DSM
Entries 239533
HT ADC - simulated
QT board

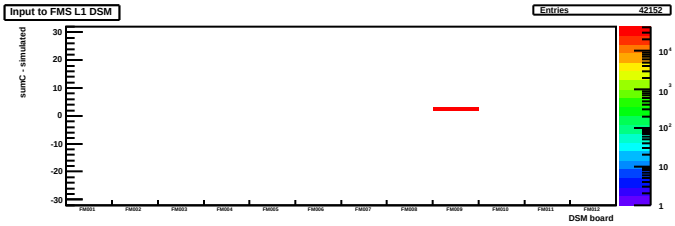

Input to FMS L0 DSM
Entries 1.02648e+08
HT ID
QT board

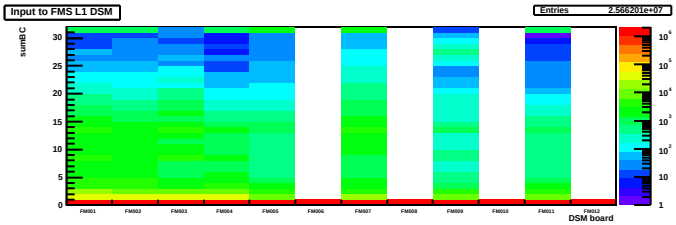

Input to FMS L0 DSM
Entries 2398150
HT ID - simulated
QT board

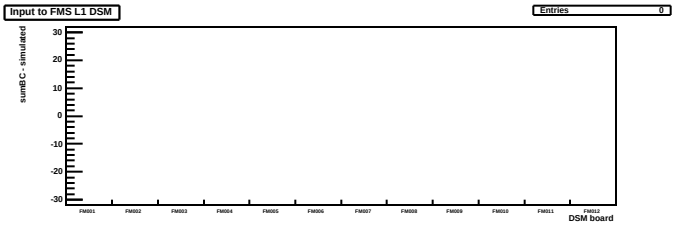

Input to FMS L1 DSM
Entries 2.566201e+07
sumD
DSM board

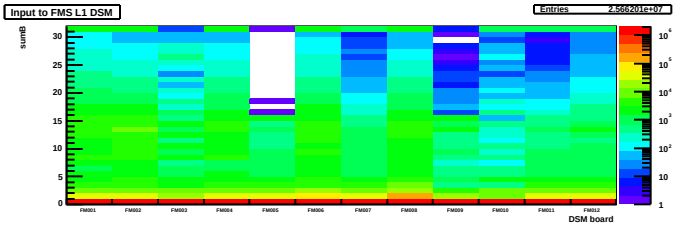

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

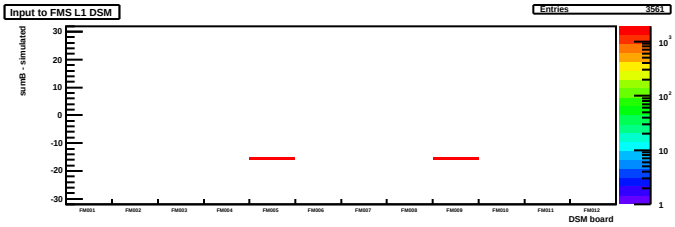

Input to FMS L1 DSM
Entries 2.566201e+07
sumC
DSM board

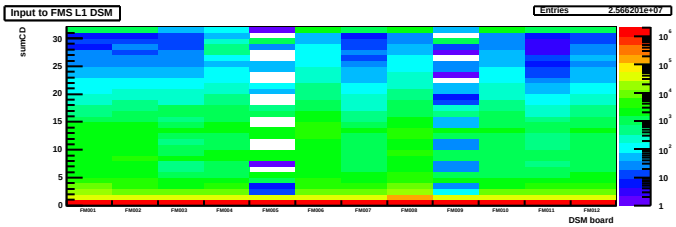

Input to FMS L1 DSM
Entries 42152
sumC - simulated
DSM board

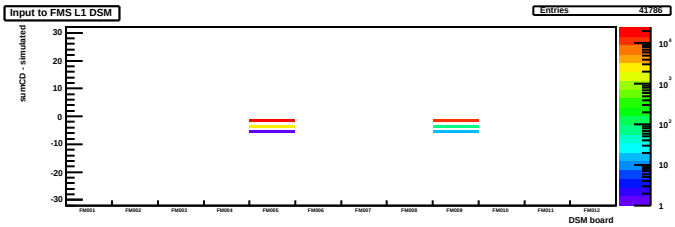

Input to FMS L1 DSM
Entries 2.566201e+07
sumBC
DSM board

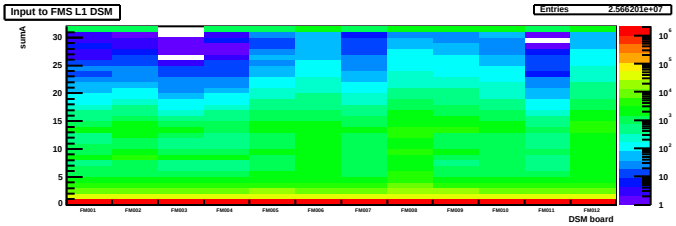

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

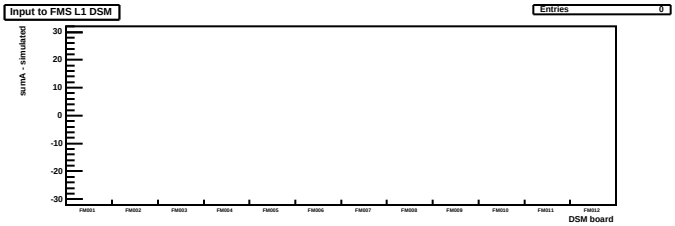

Input to FMS L1 DSM
Entries 2.566201e+07
sumB
DSM board

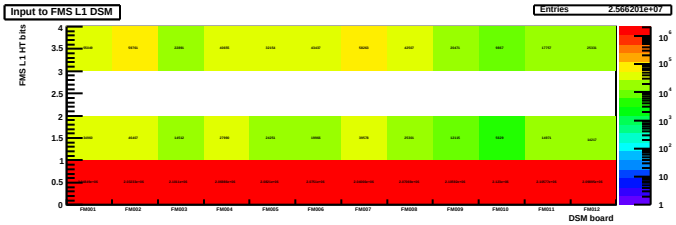

Input to FMS L1 DSM
Entries 3561
sumB - simulated
DSM board

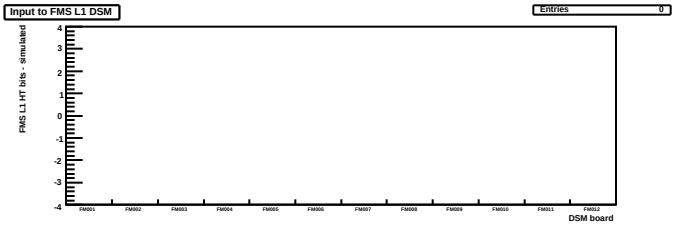

Input to FMS L1 DSM
Entries 2.566201e+07
sumCD
DSM board

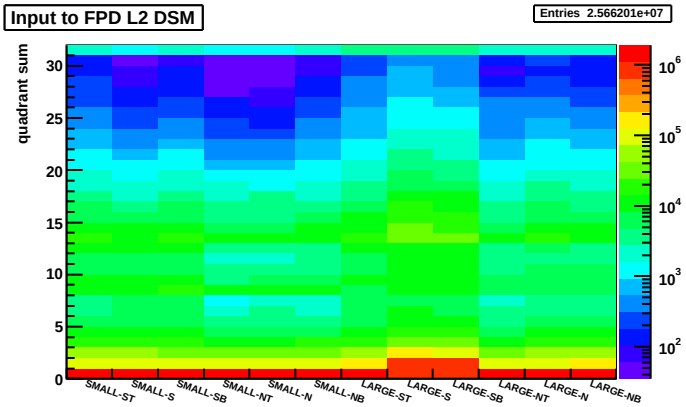

Input to FMS L1 DSM
Entries 41786
sumCD - simulated
DSM board

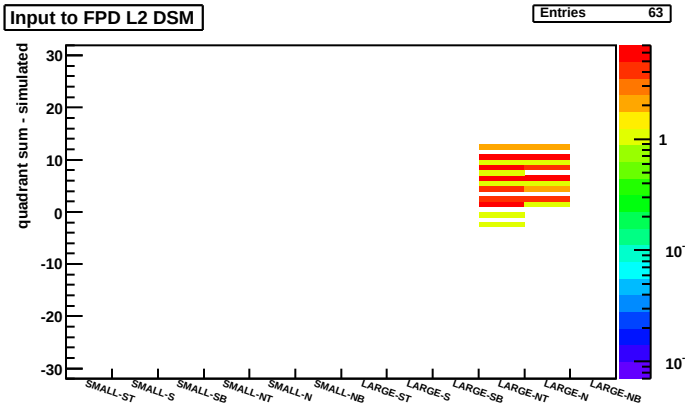

Input to FMS L1 DSM
Entries 2.566201e+07
sumA
DSM board

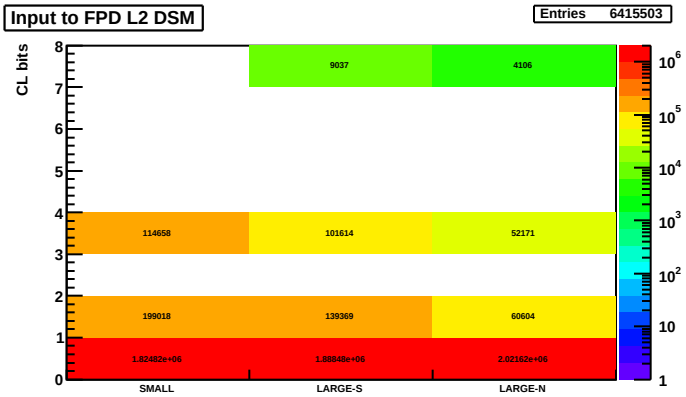

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

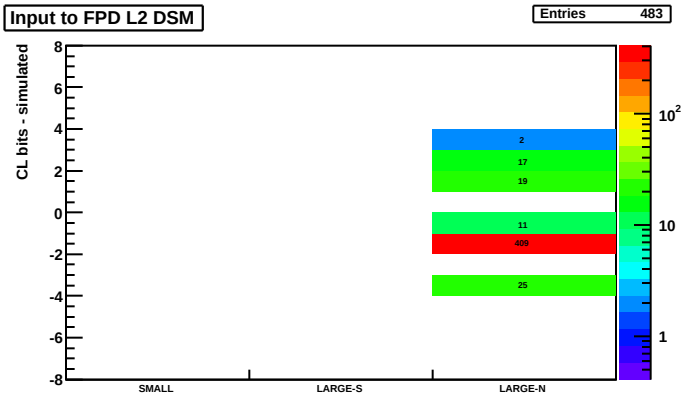

Input to FMS L1 DSM
Entries 2.566201e+07
FMS L1 HT bits
DSM board

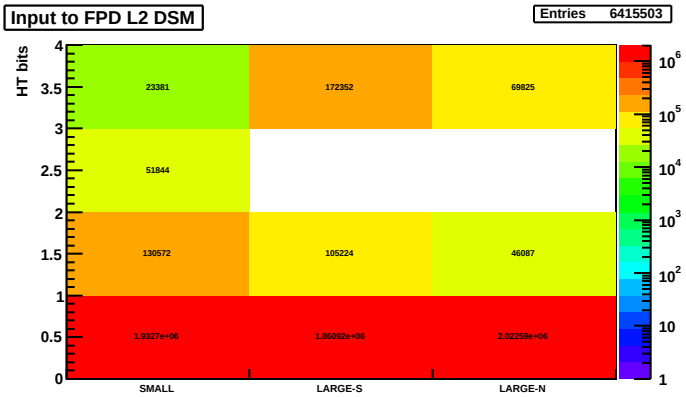

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

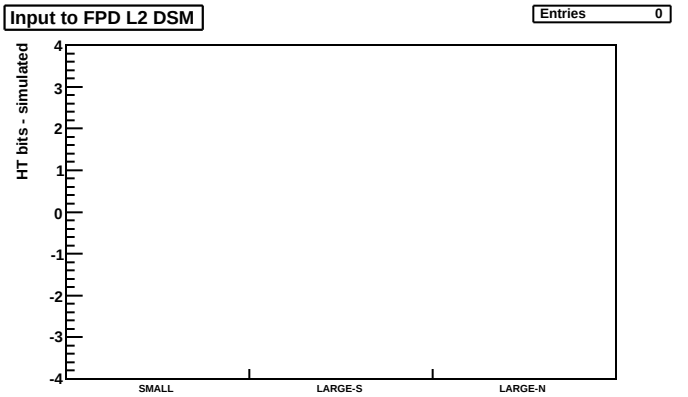

Input to FPD L2 DSM
Entries 2.566201e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

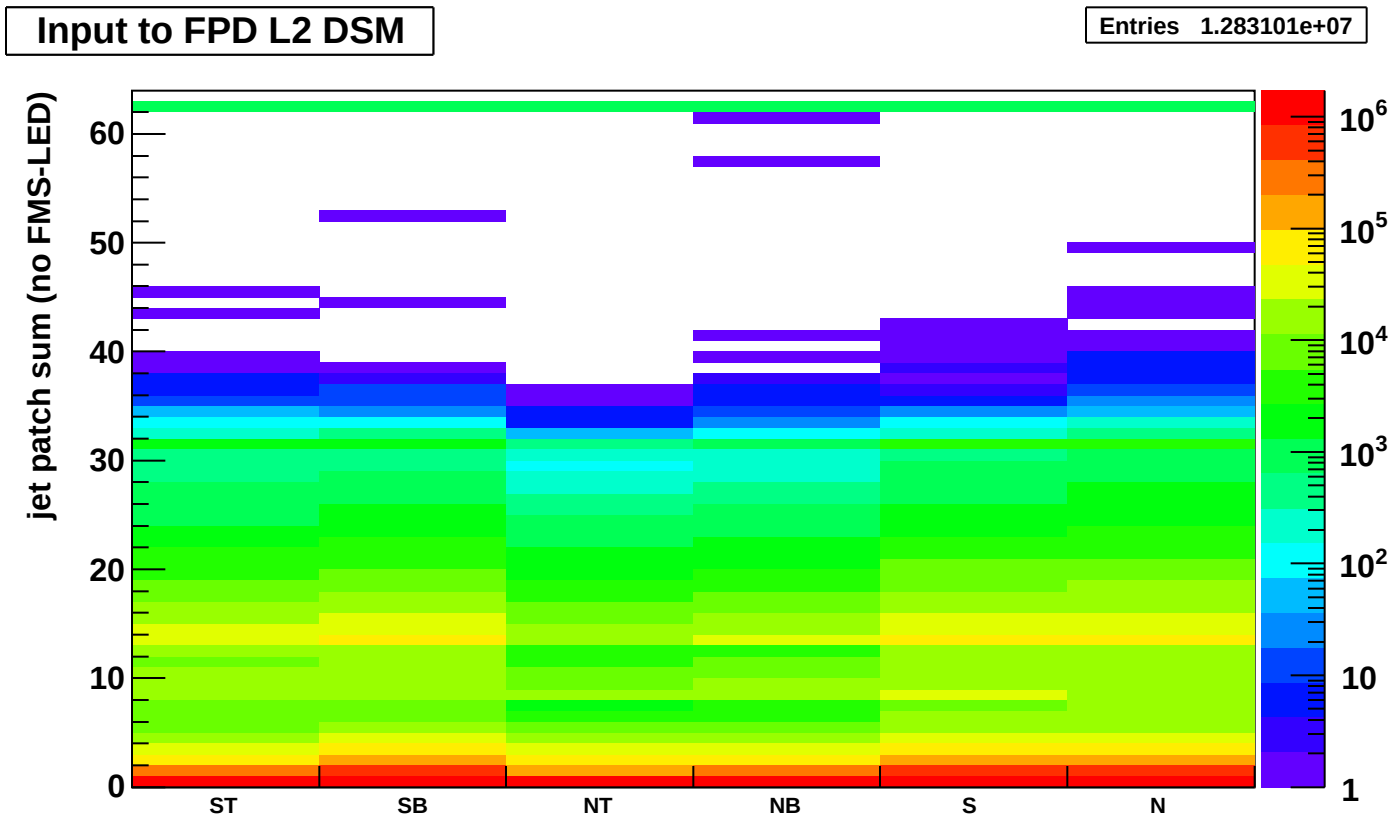

Input to FPD L2 DSM
Entries 63
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

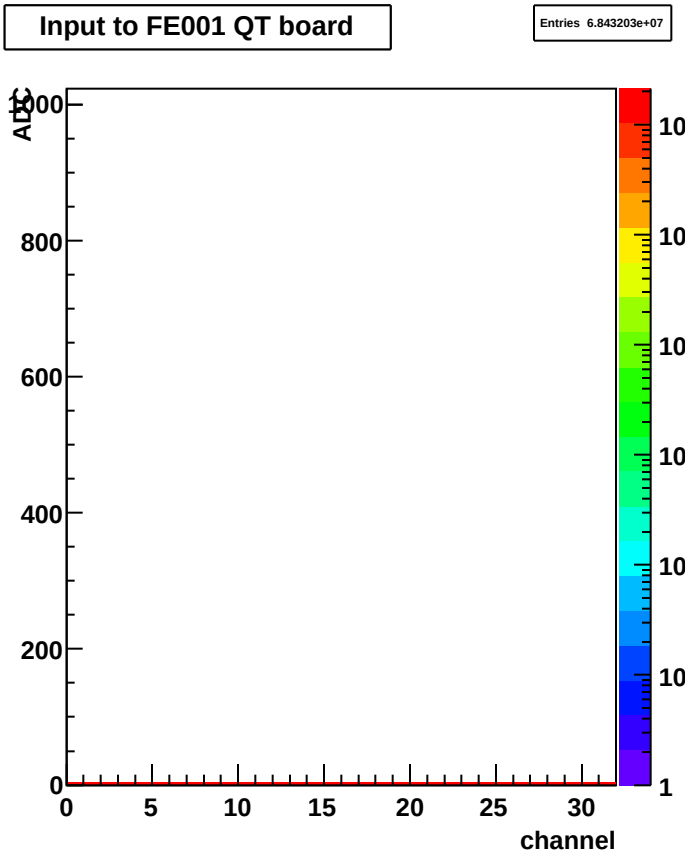

Input to FPD L2 DSM
Entries 6415503
CL bits
9037
4106
114658
101614
52171
199018
139369
60604
1.82482e+06
1.88848e+06
2.02162e+06
SMALL
LARGE-S
LARGE-N

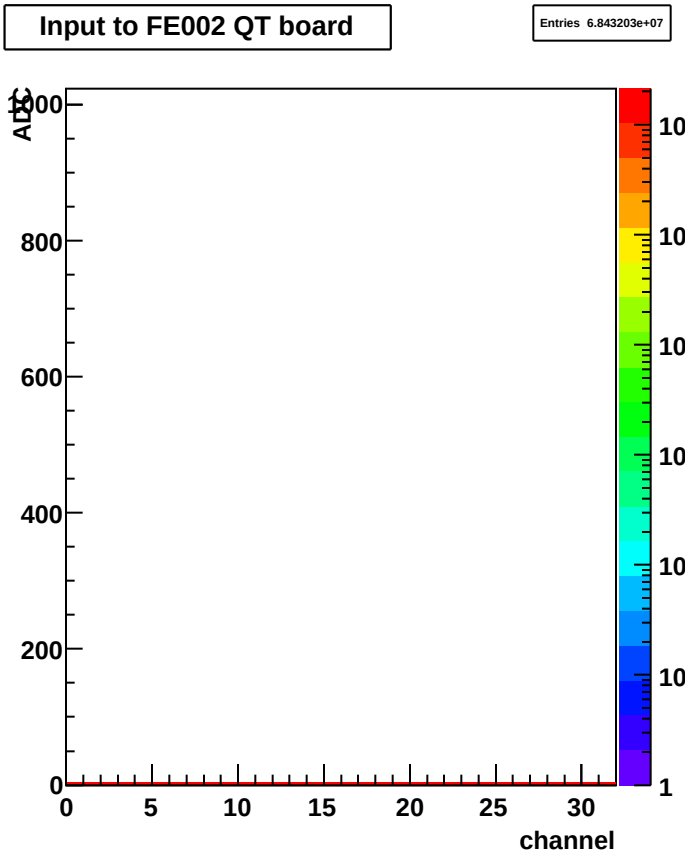

Input to FPD L2 DSM
Entries 483
CL bits - simulated
2
17
19
11
409
25
SMALL
LARGE-S
LARGE-N

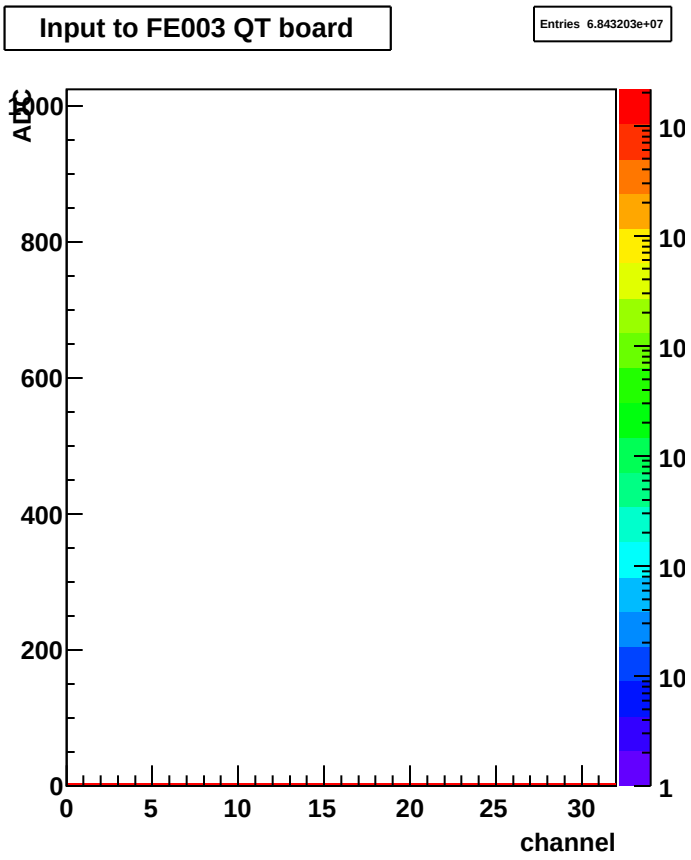

Input to FPD L2 DSM
Entries 6415503
HT bits
23381
172352
69825
51844
130572
105224
46087
1.9327e+06
1.86092e+06
2.02259e+06
SMALL
LARGE-S
LARGE-N

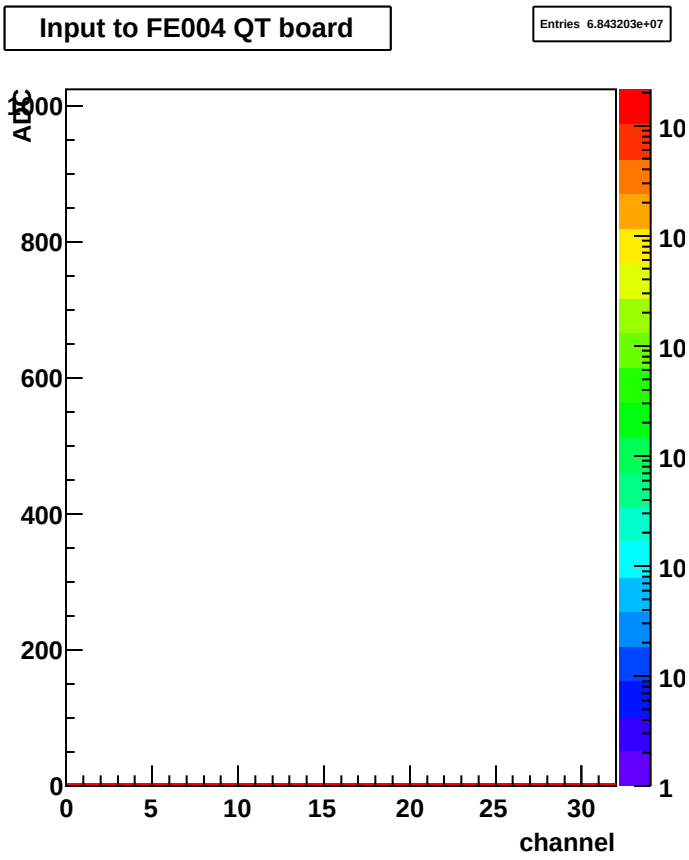

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

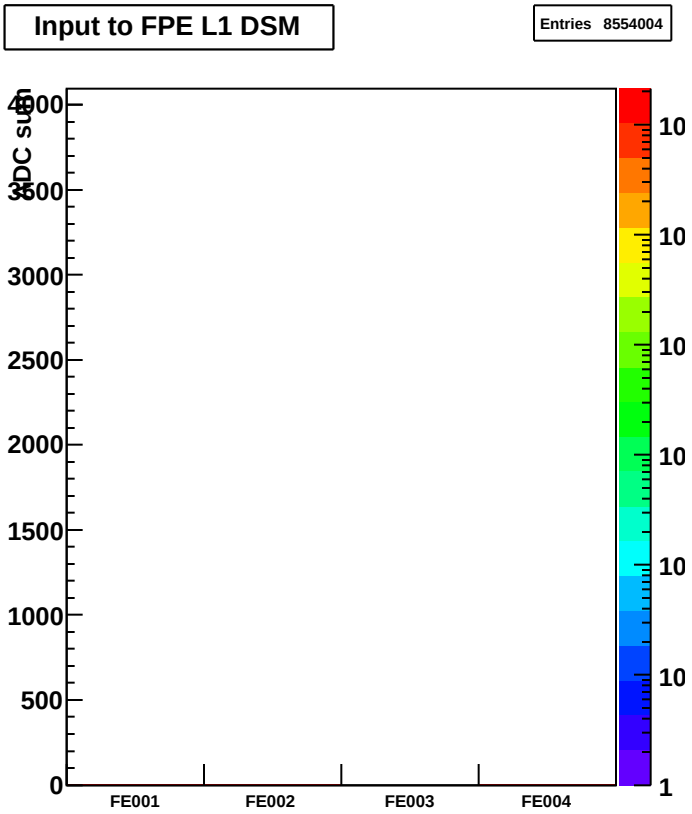

Input to FPD L2 DSM
Entries 1.283101e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

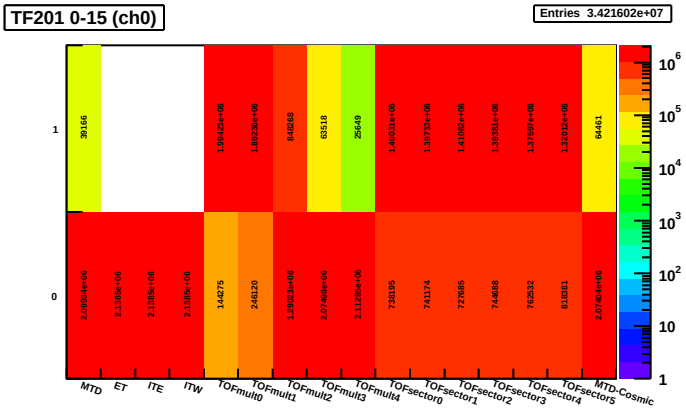

Input to FE001 QT board
Entries 6.843203e+07
ADC
channel

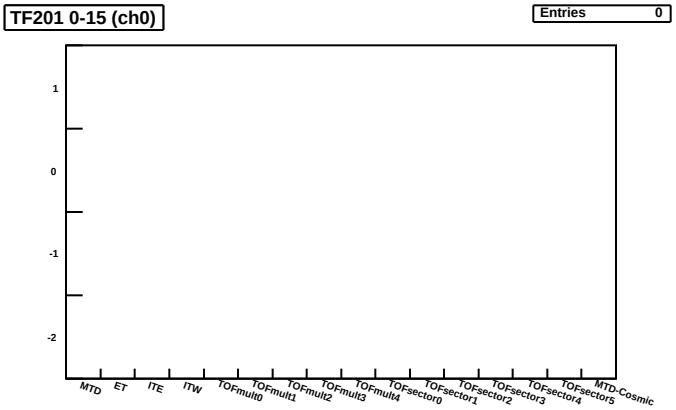

Input to FE002 QT board
Entries 6.843203e+07
ADC
channel

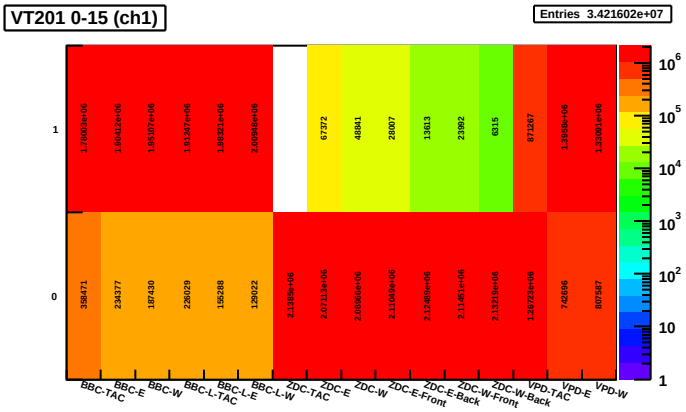

Input to FE003 QT board
Entries 6.843203e+07
ADC
channel

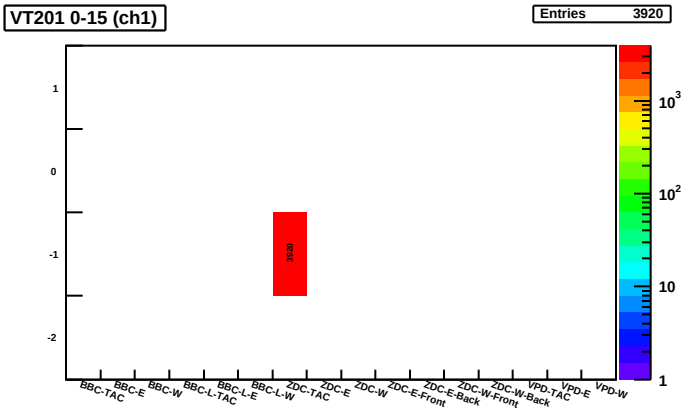

Input to FE004 QT board
Entries 6.843203e+07
ADC
channel

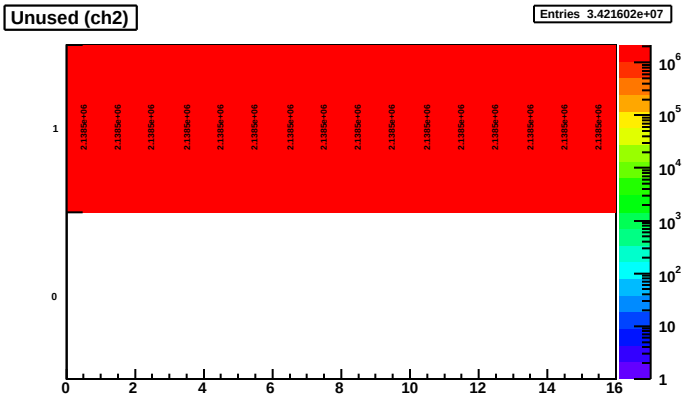

Input to FPE L1 DSM
Entries 8554004
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

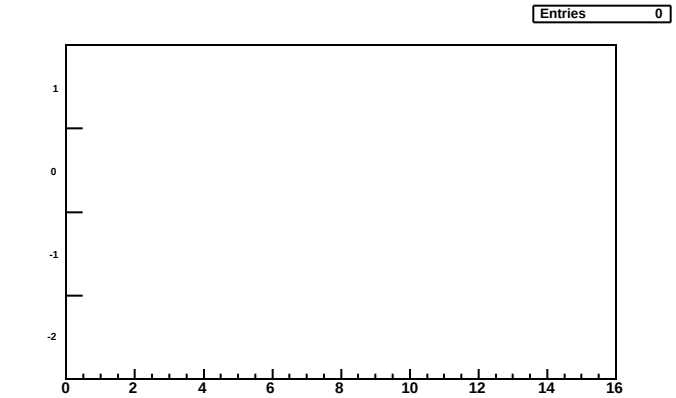

TF201 0-15 (ch0)
Entries 3.421602e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

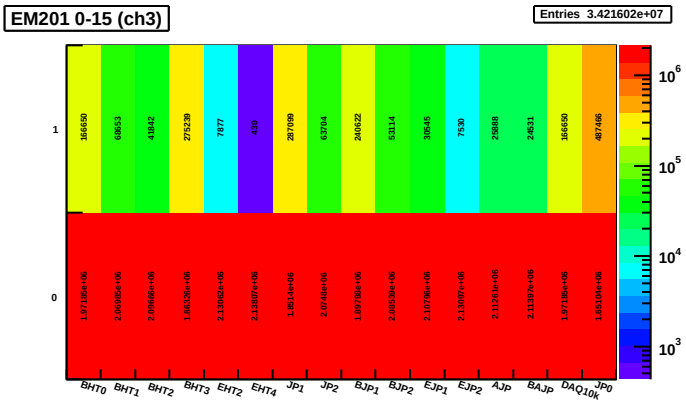

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

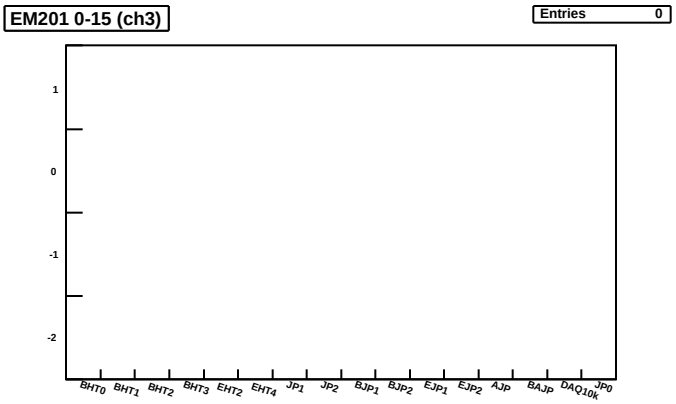

VT201 0-15 (ch1)
Entries 3.421602e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 3920
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

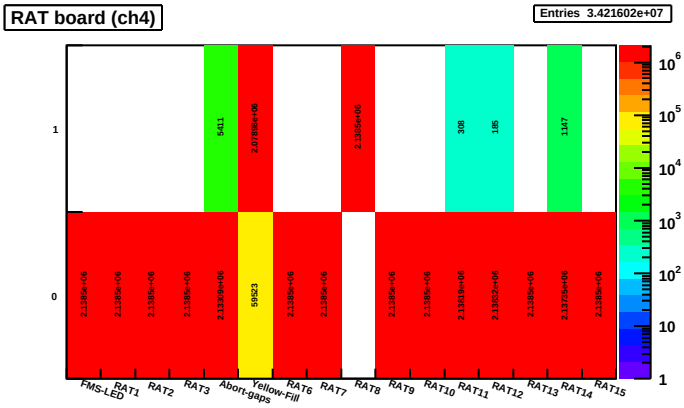

Unused (ch2)
Entries 3.421602e+07

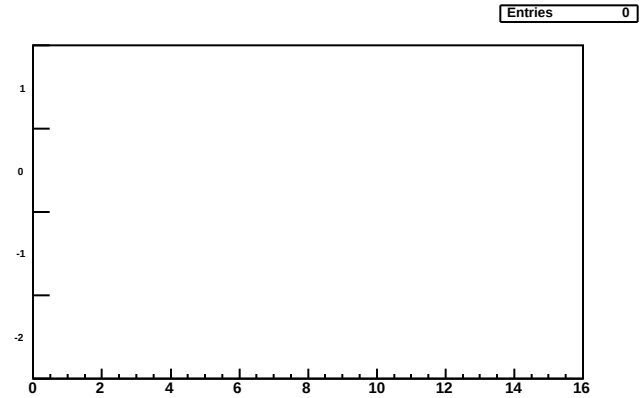

Entries 0


EM201 0-15 (ch3)
Entries 3.421602e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

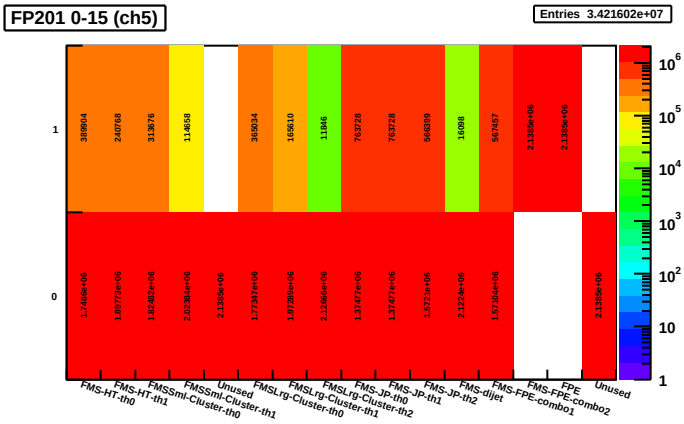

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

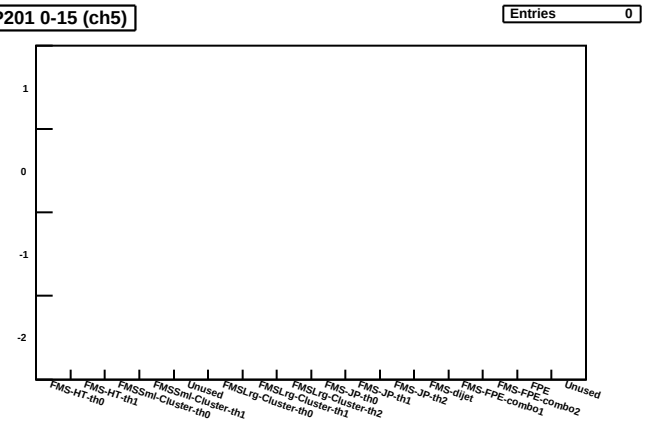




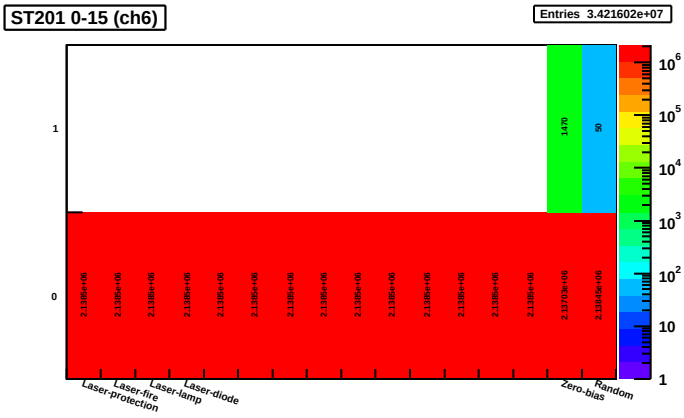
FP201 0-15 (ch5)

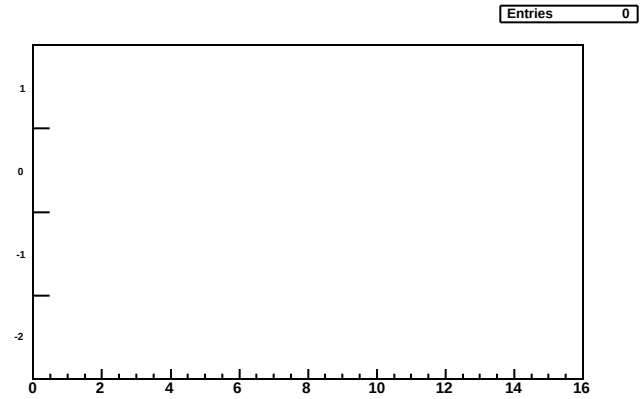
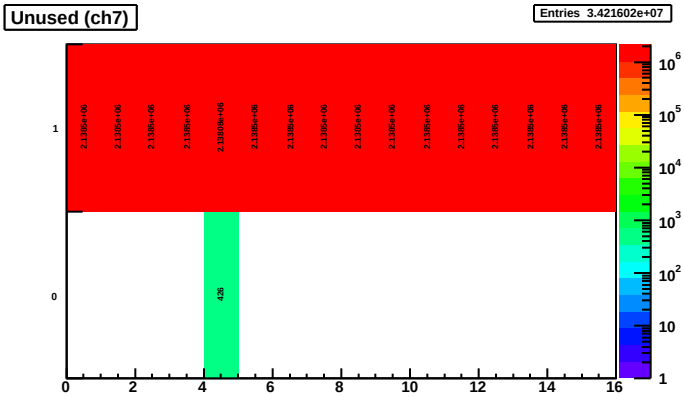
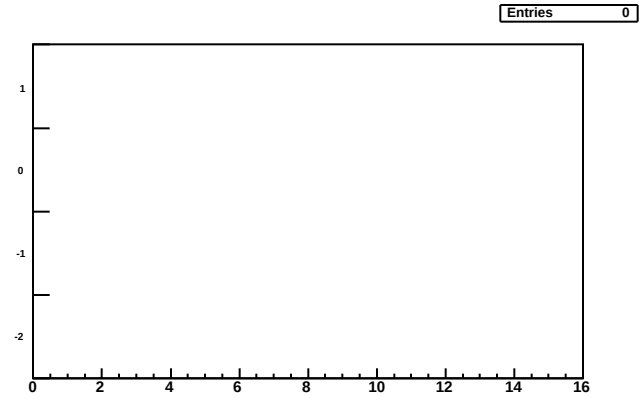


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)