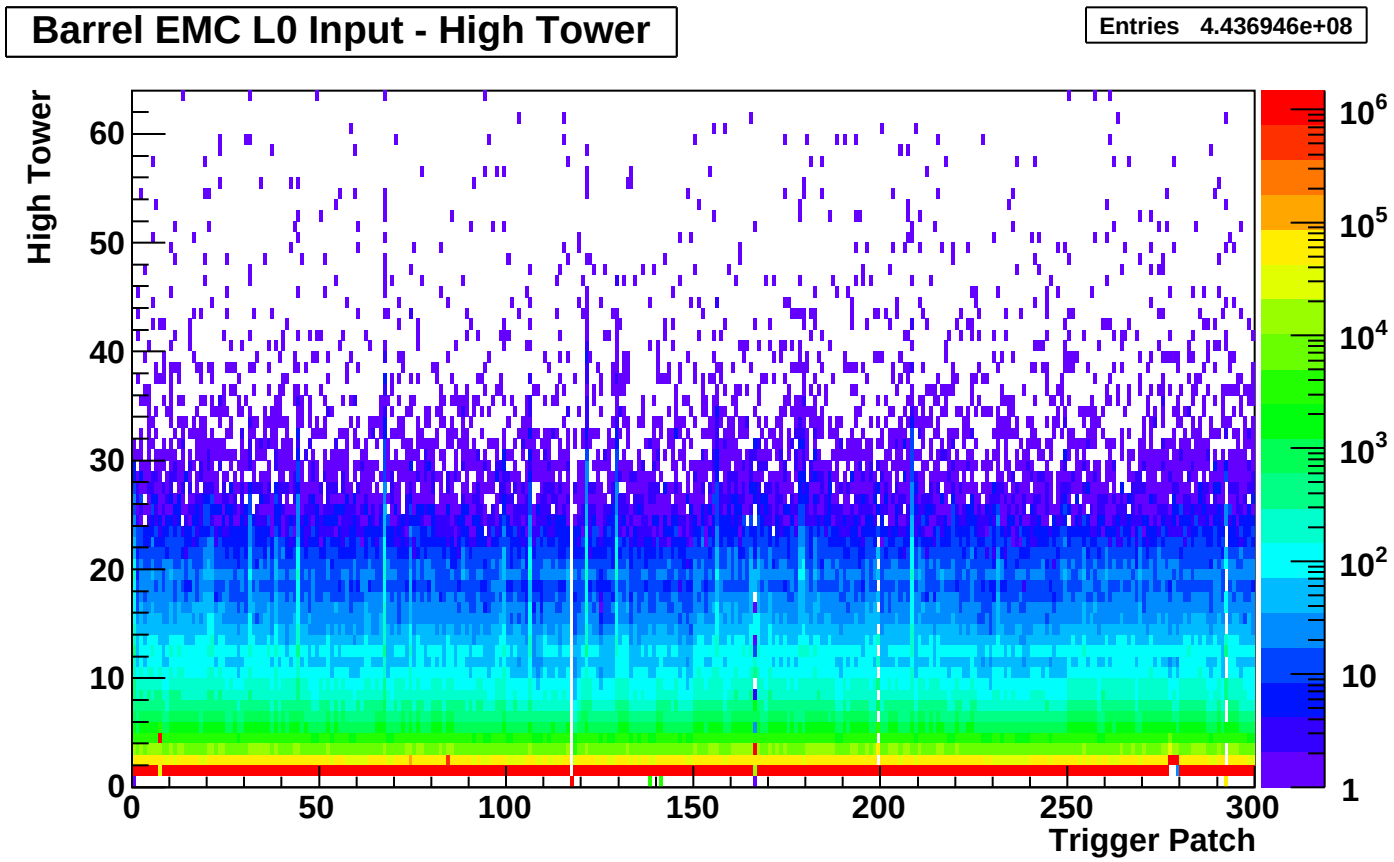
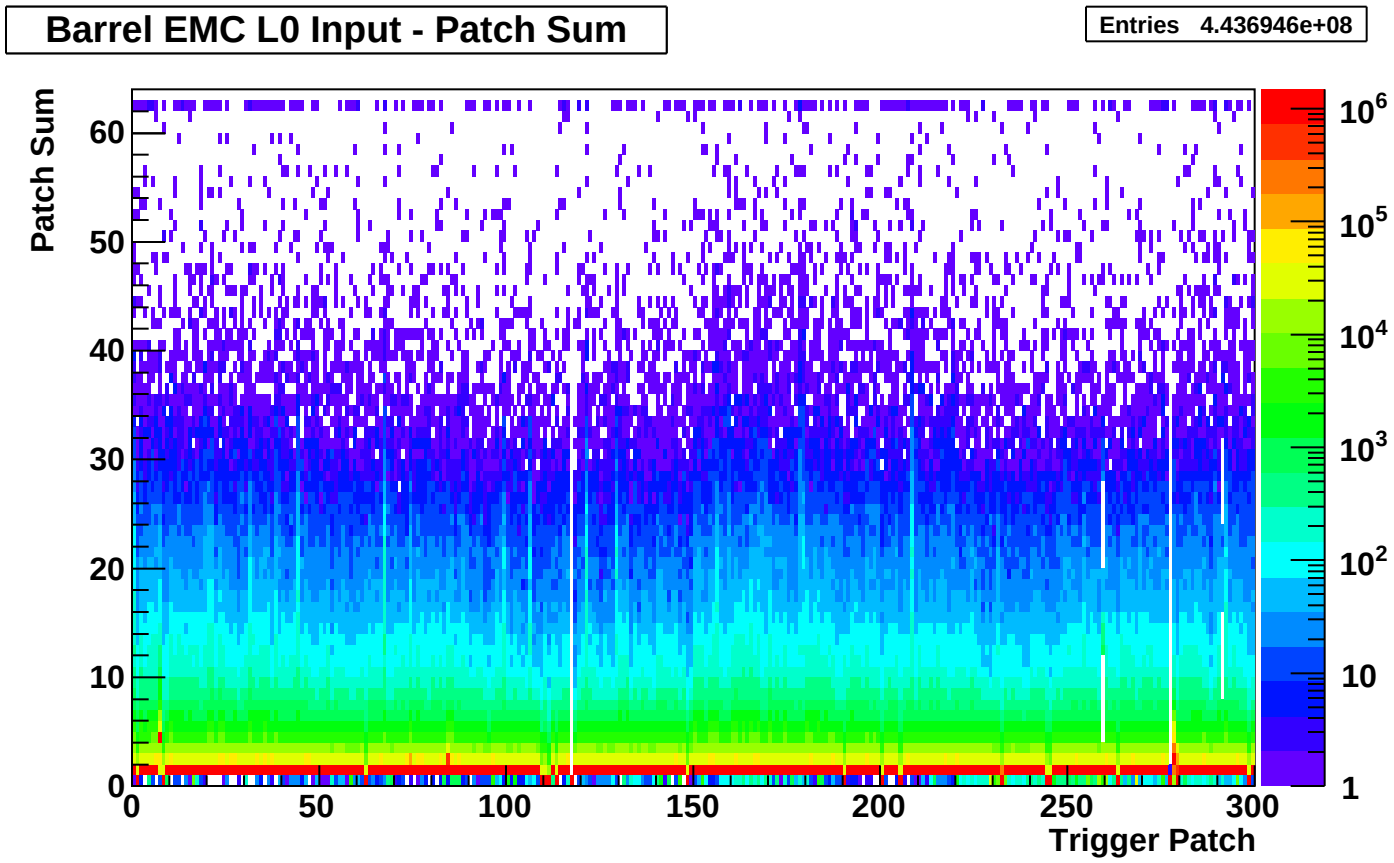
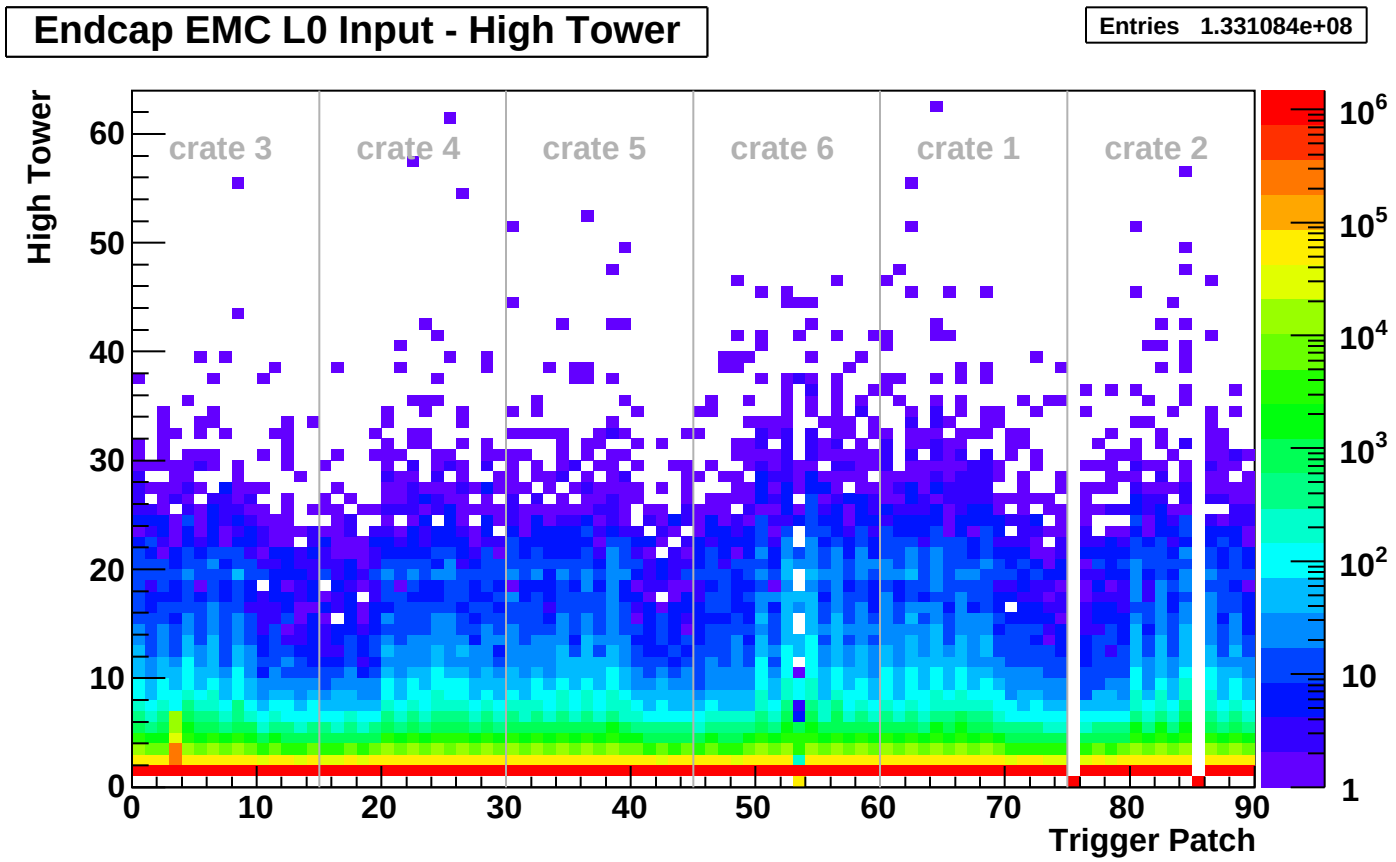
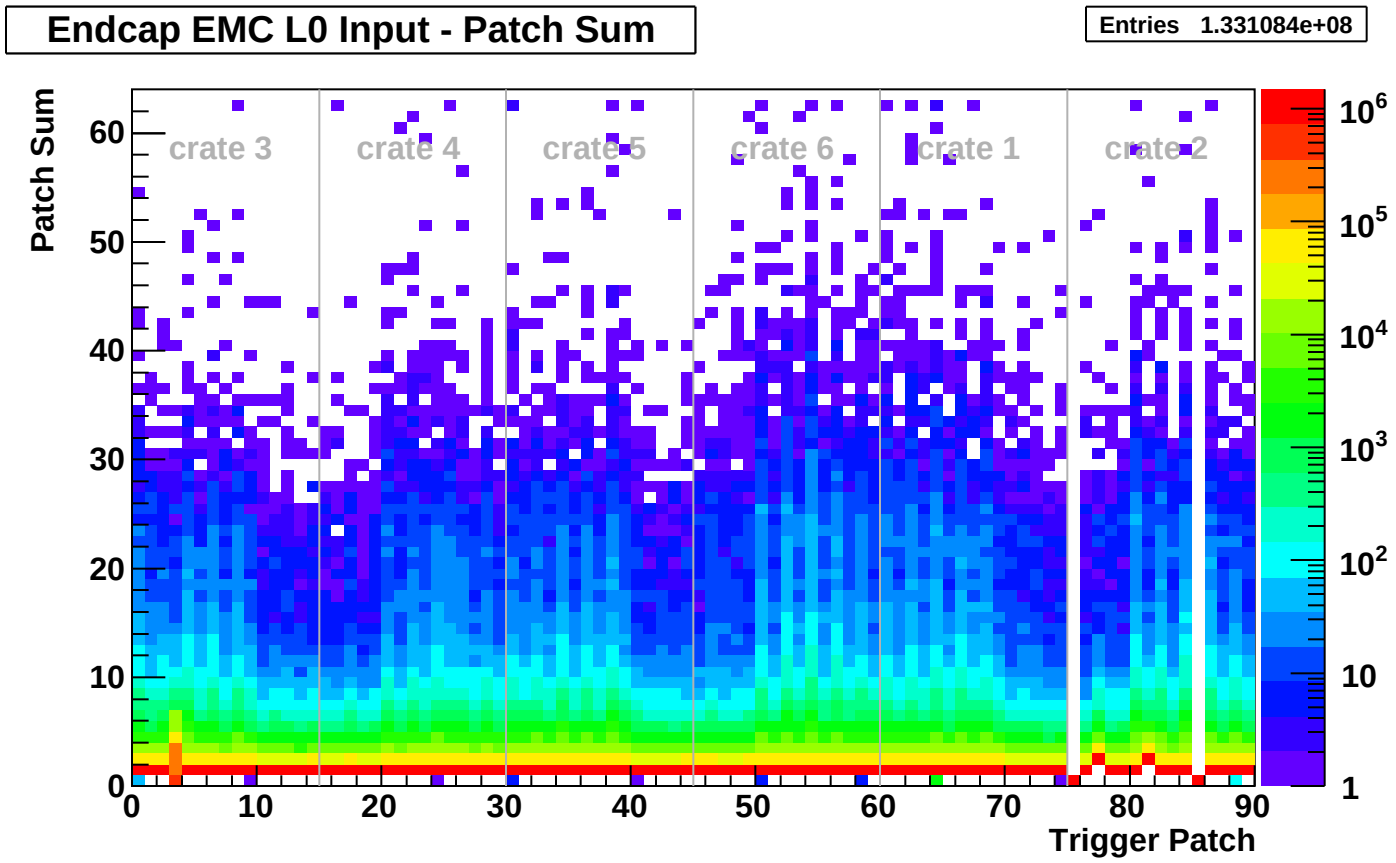
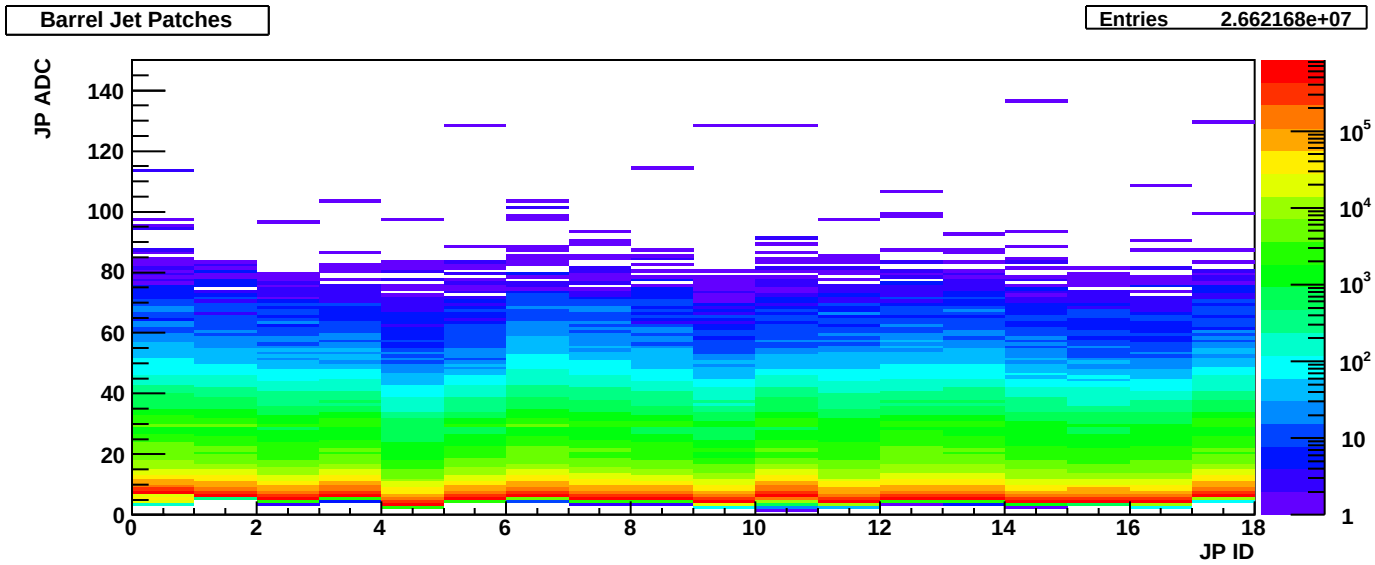
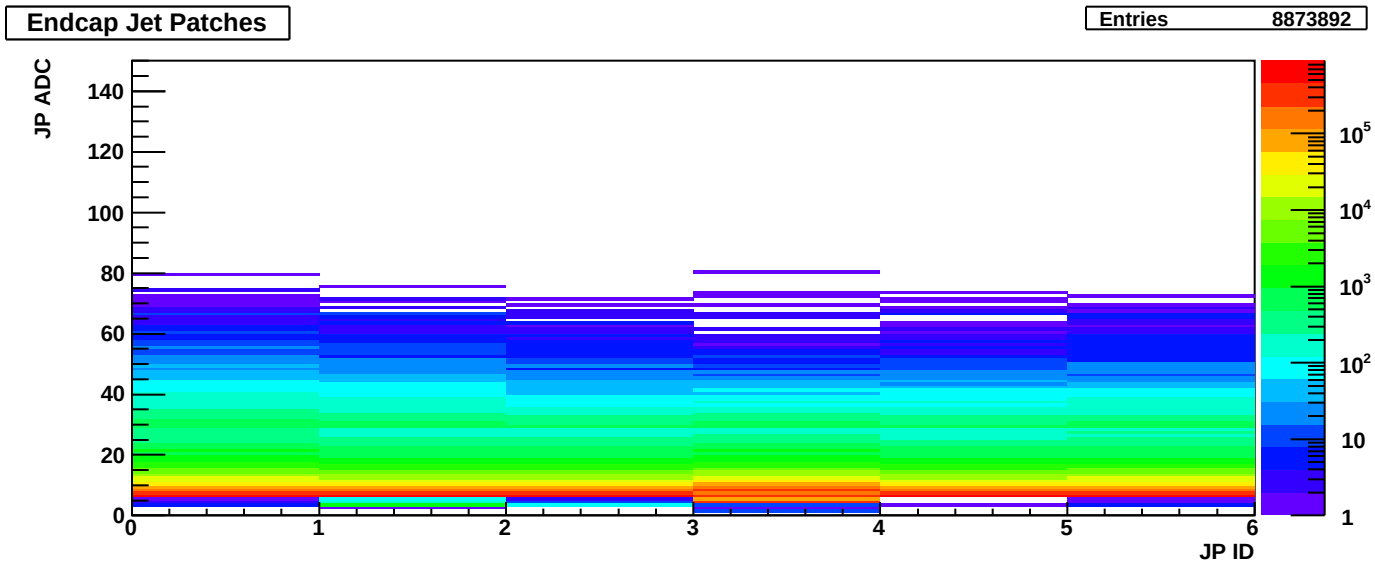

Barrel EMC L0 Input - High Tower
Entries 4.436946e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

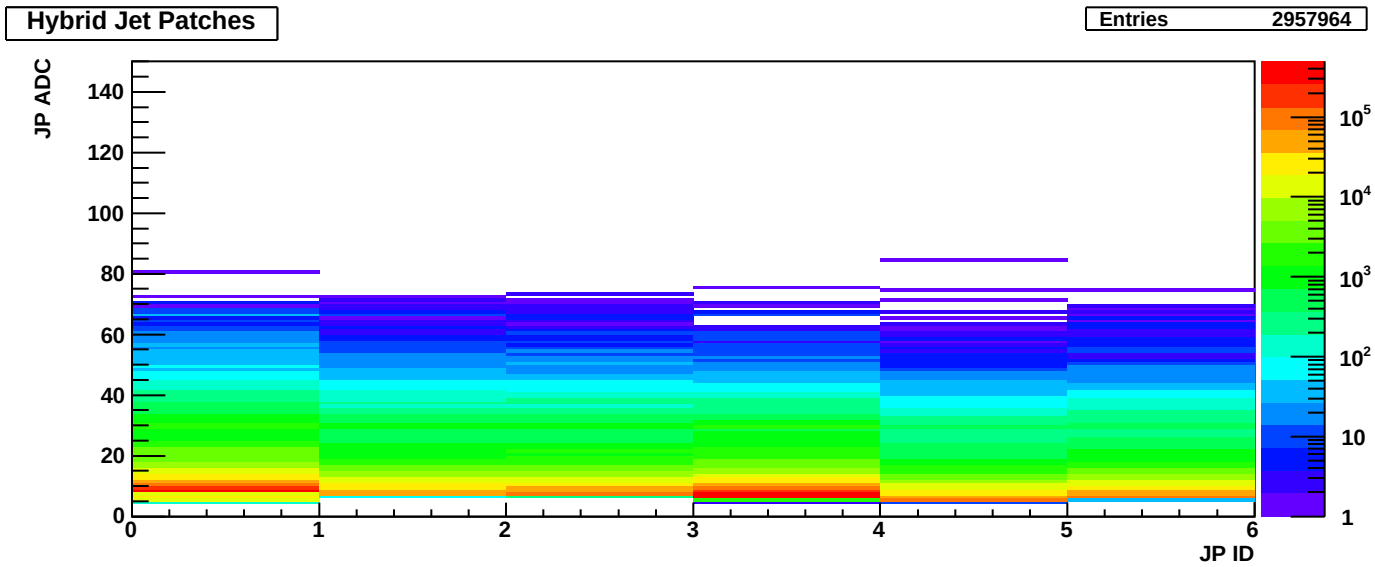

Barrel EMC L0 Input - Patch Sum
Entries 4.436946e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

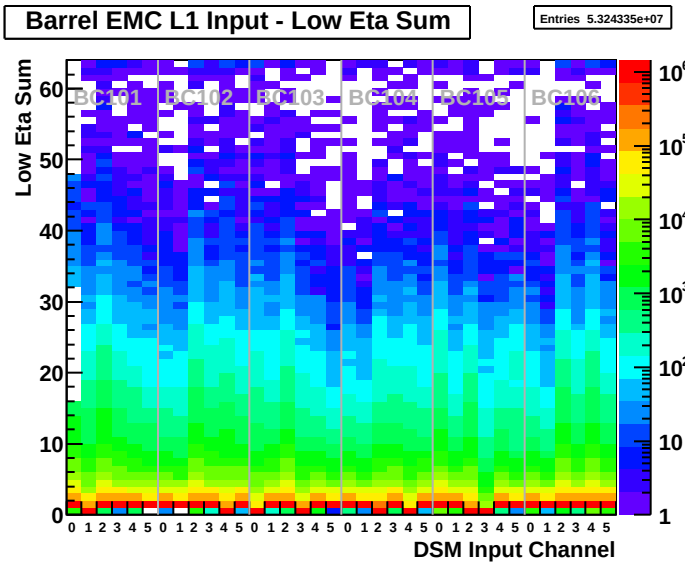

Endcap EMC L0 Input - High Tower
Entries 1.331084e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

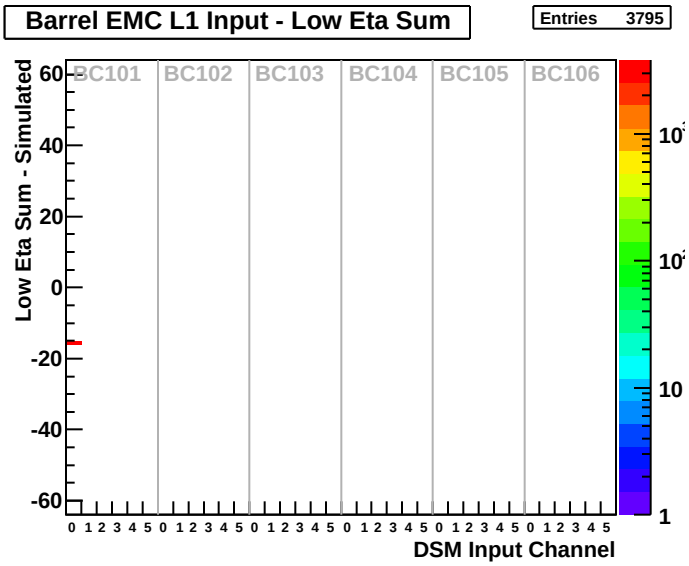

Endcap EMC L0 Input - Patch Sum
Entries 1.331084e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

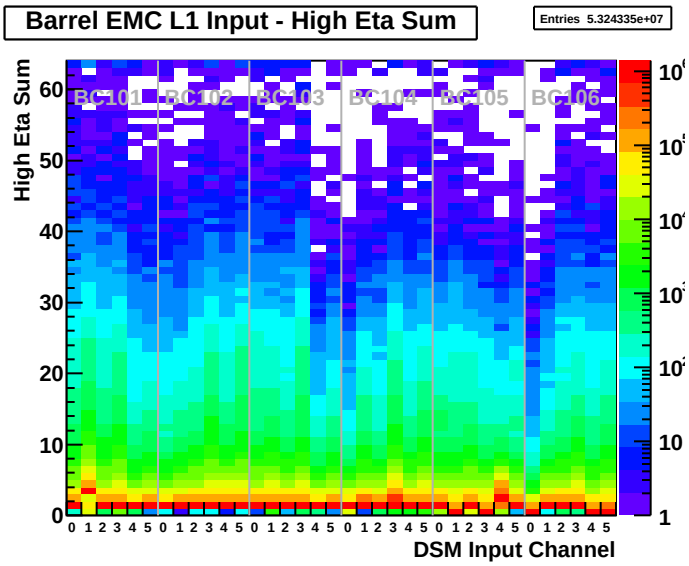

Barrel Jet Patches
Entries 2.662168e+07
JP ADC
JP ID

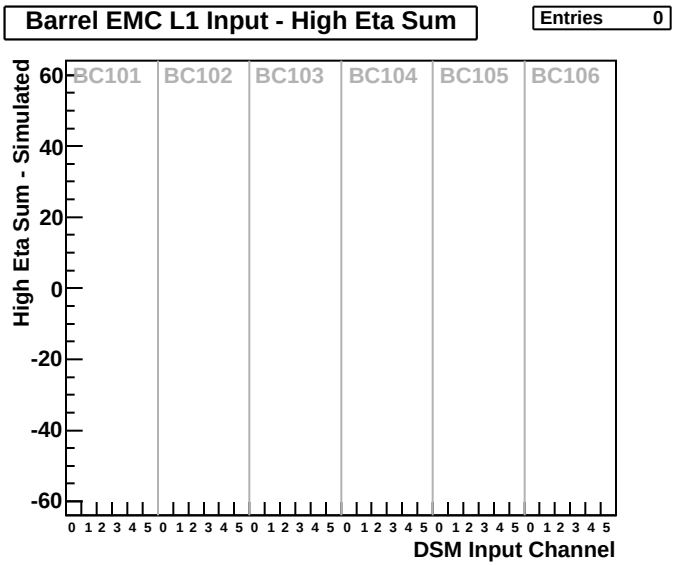

Endcap Jet Patches
Entries 8873892
JP ADC
JP ID

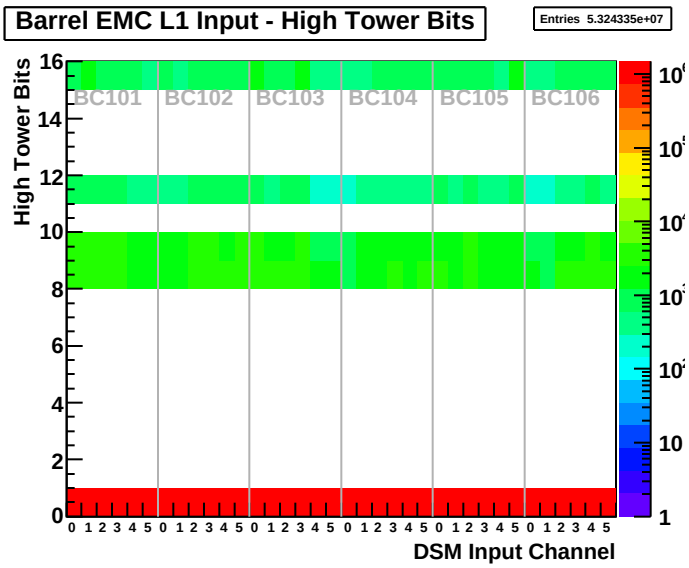

Hybrid Jet Patches
Entries 2957964
JP ADC
JP ID

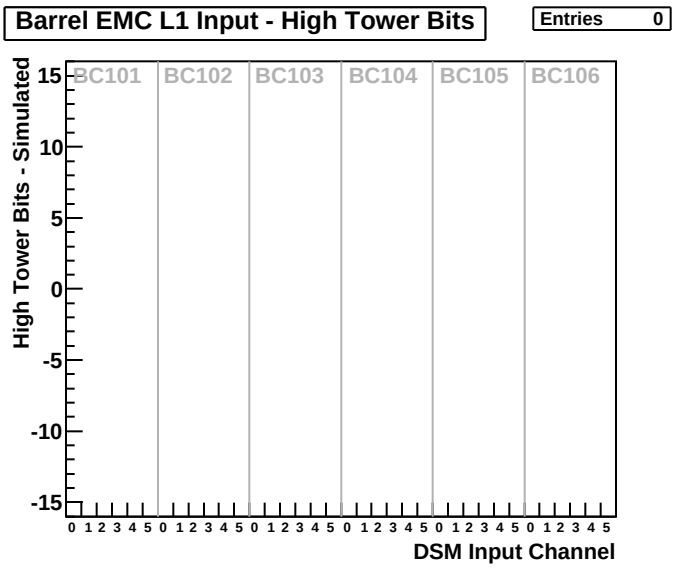

Barrel EMC L1 Input - Low Eta Sum
Entries 5.324335e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

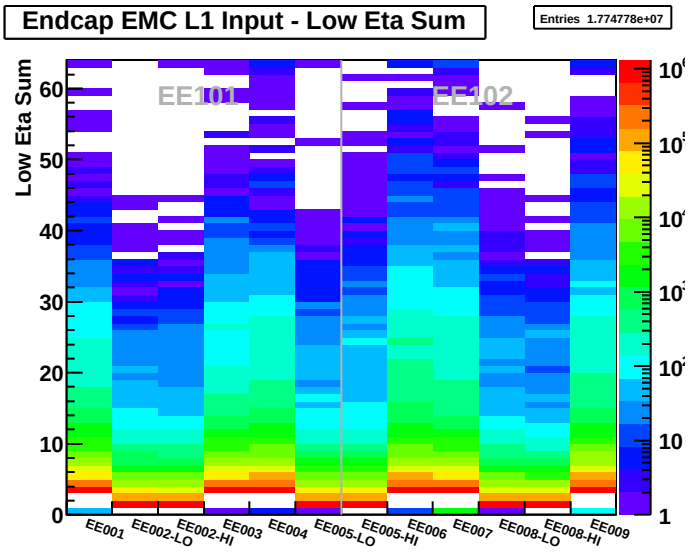

Barrel EMC L1 Input - Low Eta Sum
Entries 3795
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

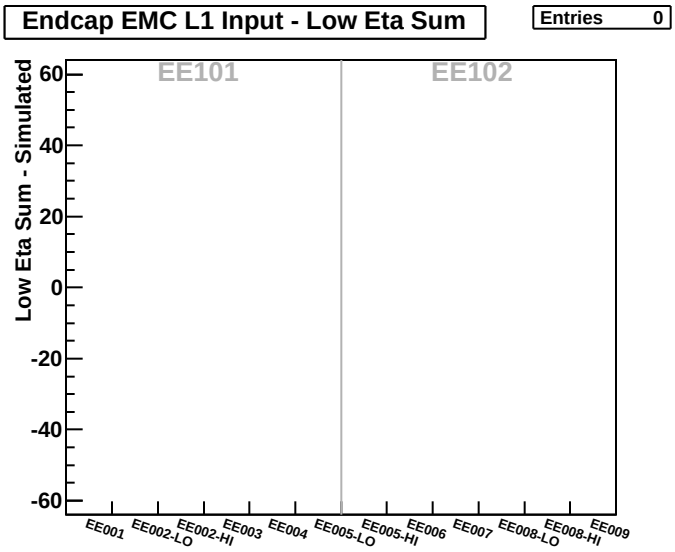

Barrel EMC L1 Input - High Eta Sum
Entries 5.324335e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

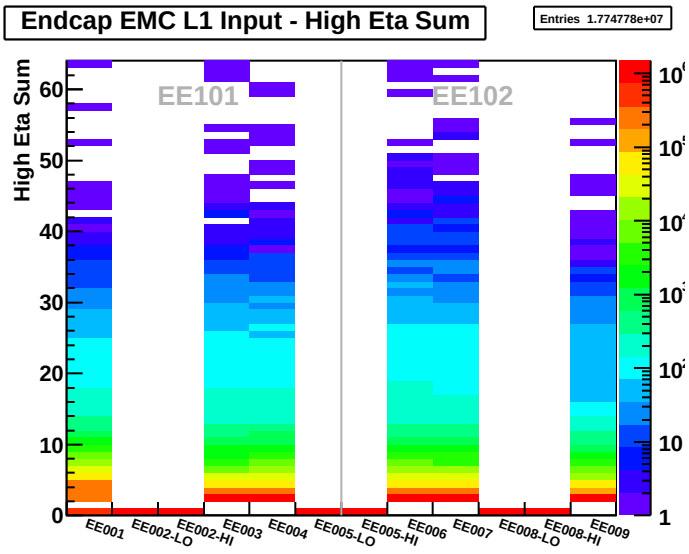

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

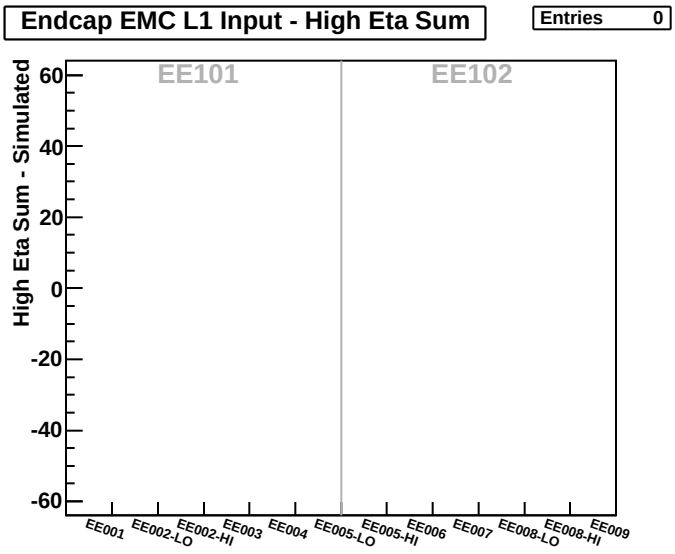

Barrel EMC L1 Input - High Tower Bits
Entries 5.324335e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

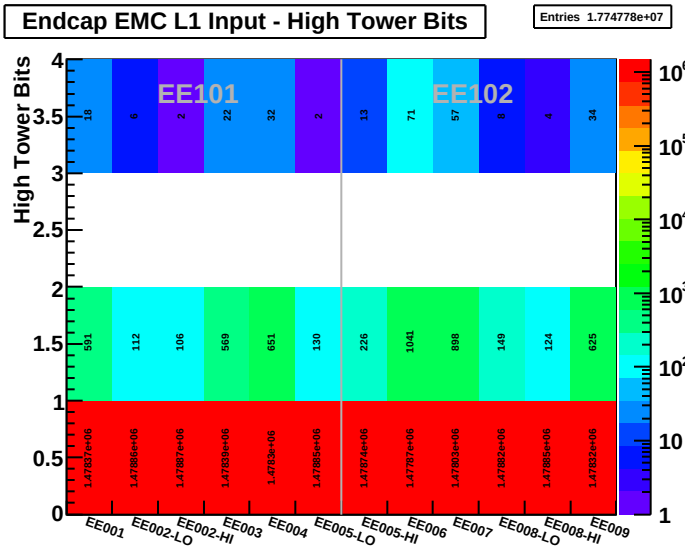

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

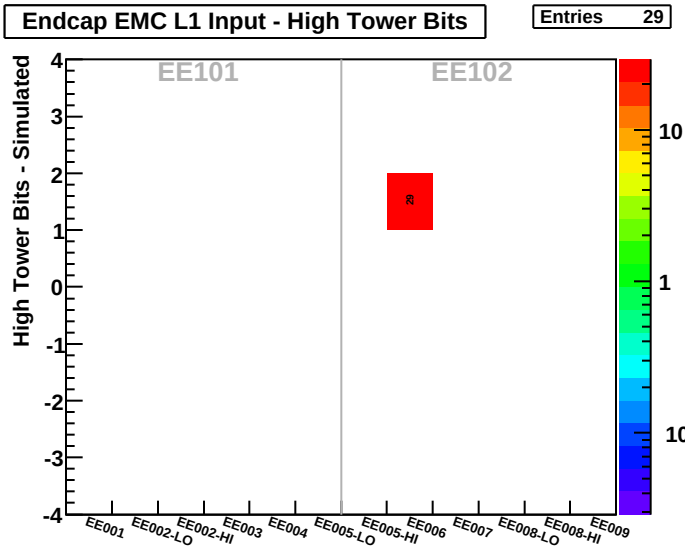

Endcap EMC L1 Input - Low Eta Sum
Entries 1.774778e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

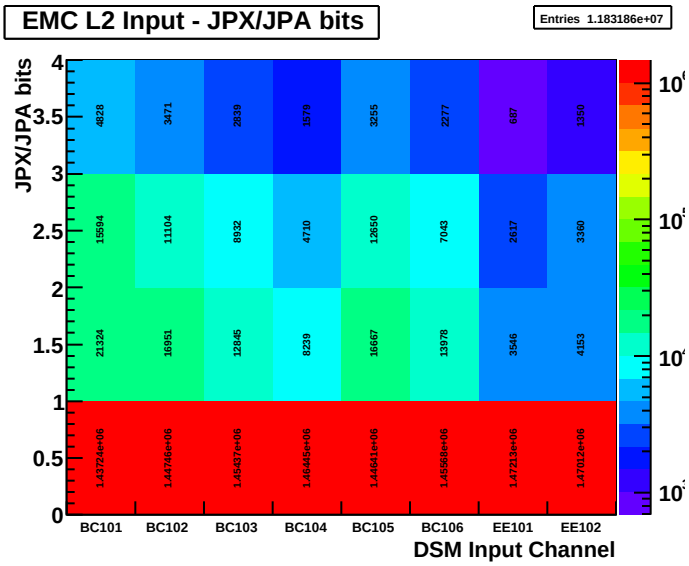

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

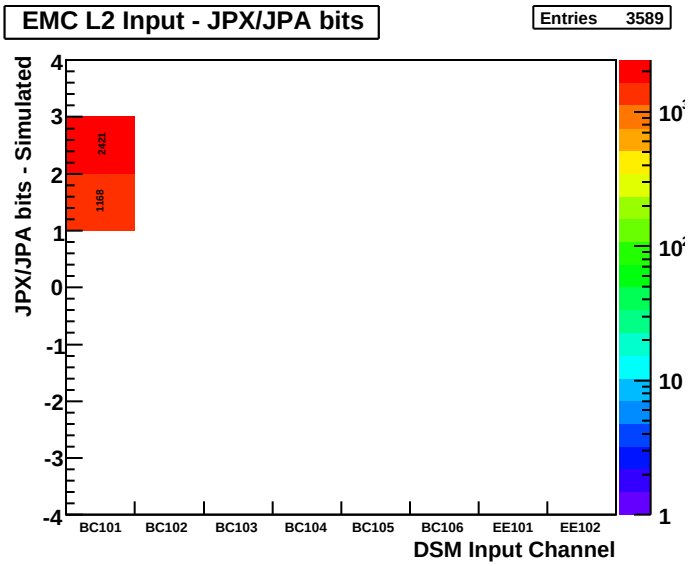

Endcap EMC L1 Input - High Eta Sum
Entries 1.774778e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

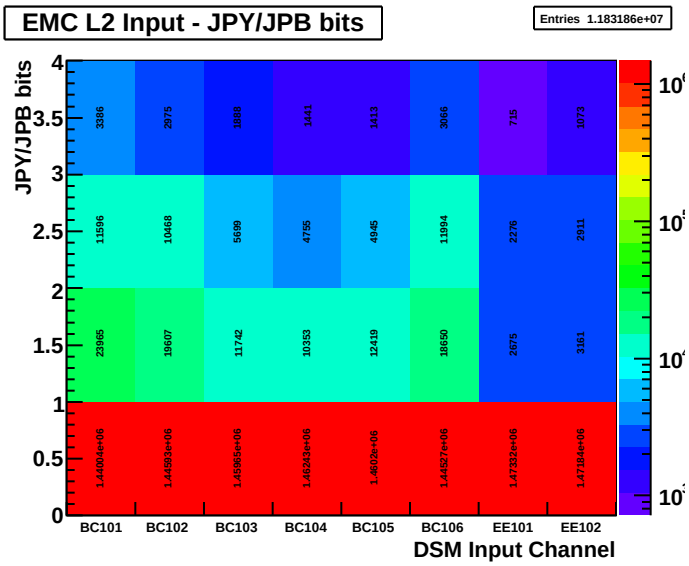

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

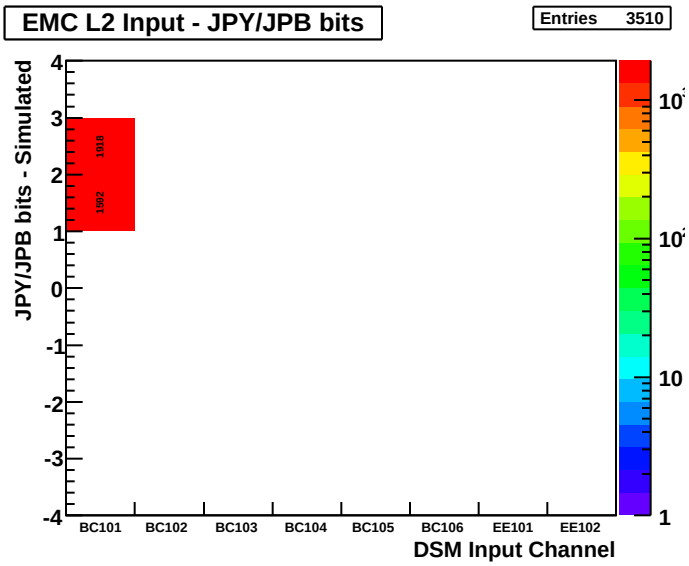

Endcap EMC L1 Input - High Tower Bits
Entries 1.774778e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

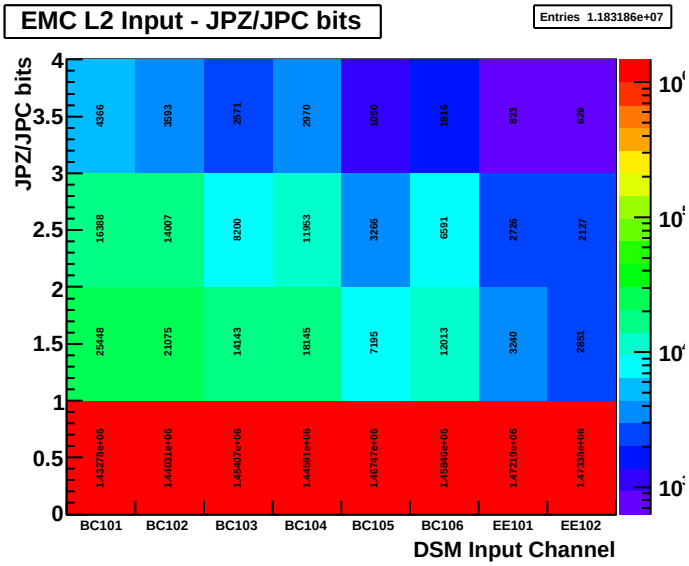

Endcap EMC L1 Input - High Tower Bits
Entries 29
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

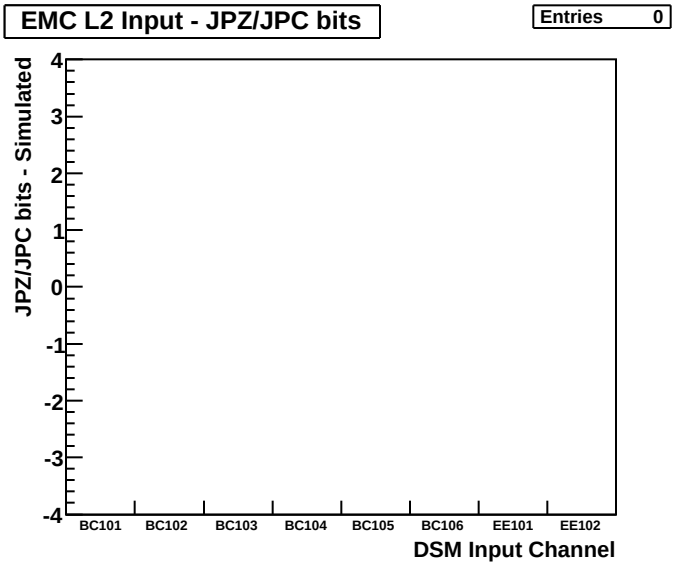

EMC L2 Input - JPX/JPA bits
Entries 1.183186e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4828 3471 2839 1579 3255 2277 687 1350
15594 11104 8932 4710 12650 7043 2617 3360
21324 16951 12845 8239 16667 13978 3546 4153
1.43724e+06 1.44746e+06 1.45437e+06 1.46445e+06 1.44641e+06 1.45968e+06 1.47213e+06 1.47012e+06

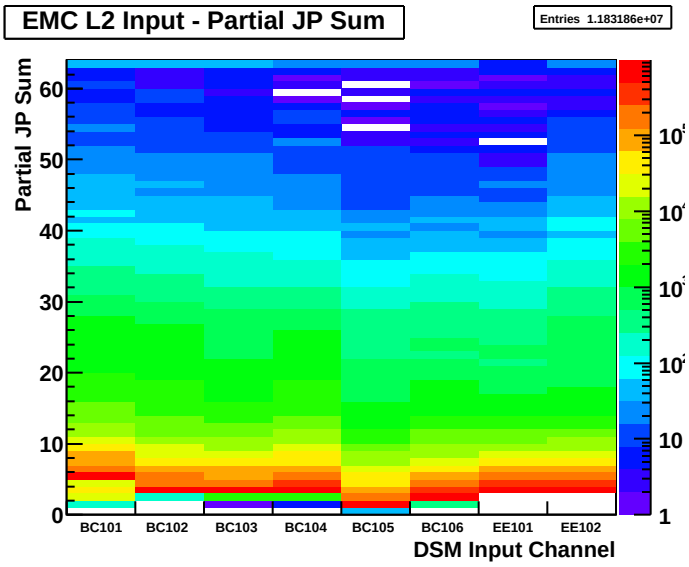

EMC L2 Input - JPX/JPA bits
Entries 3589
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2421
1168

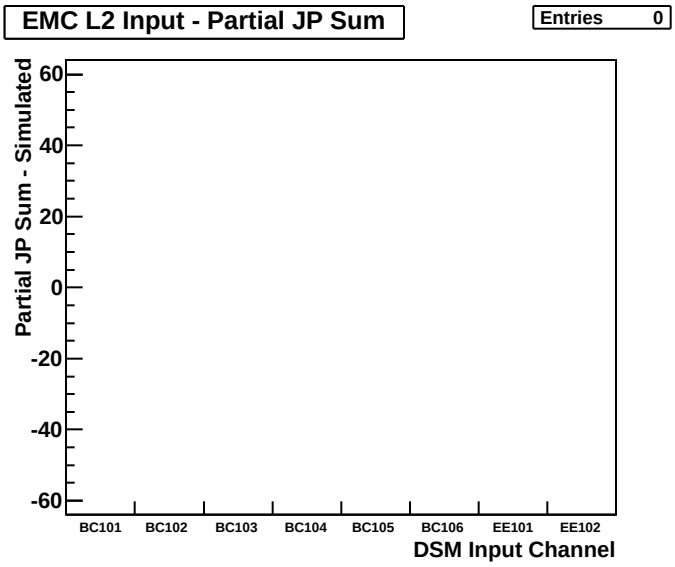

EMC L2 Input - JPY/JPB bits
Entries 1.183186e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3386 2975 1888 1441 1413 3066 715 1073
11596 10468 5699 4755 4945 11994 2276 2911
23965 19607 11742 10353 12419 18650 2675 3161
1.44004e+06 1.44593e+06 1.45865e+06 1.46243e+06 1.4602e+06 1.44527e+06 1.47332e+06 1.47194e+06

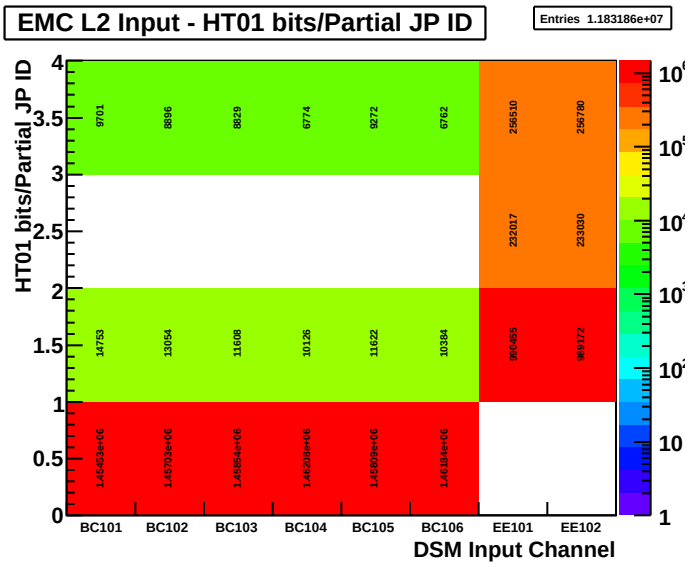

EMC L2 Input - JPY/JPB bits
Entries 3510
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1918
1592

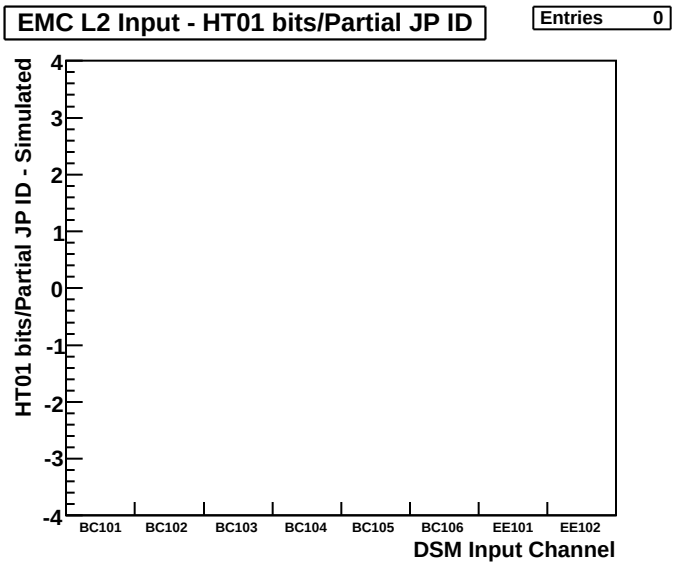

EMC L2 Input - JPZ/JPC bits
Entries 1.183186e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4366 3593 2571 2970 1050 1916 823 629
16388 14007 8200 11953 3266 6591 2728 2127
25448 21075 14443 18145 7195 12013 3240 2851
1.43278e+06 1.44031e+06 1.45407e+06 1.44591e+06 1.46747e+06 1.45846e+06 1.47219e+06 1.47338e+06

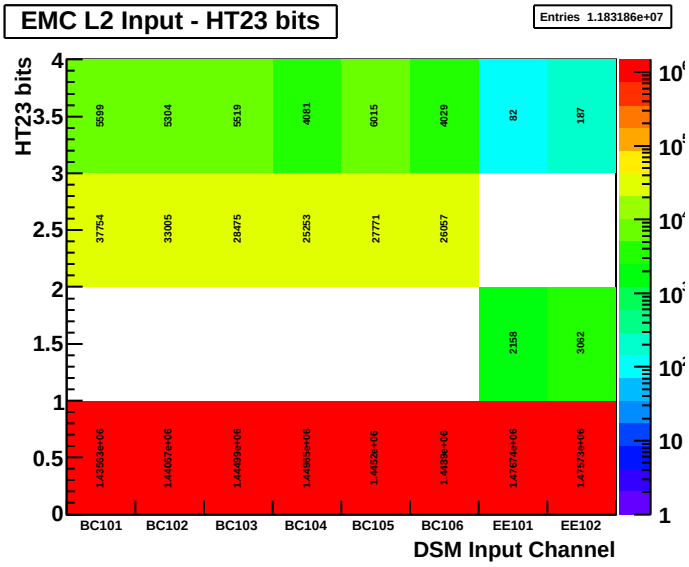

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

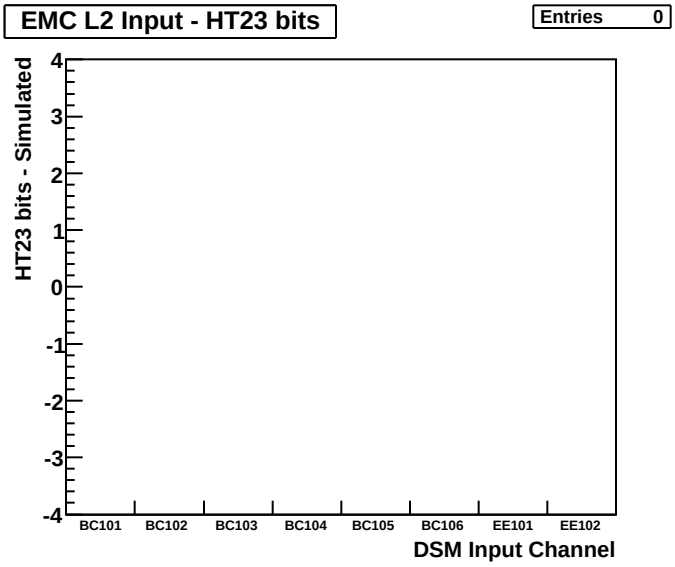

EMC L2 Input - Partial JP Sum
Entries 1.183186e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

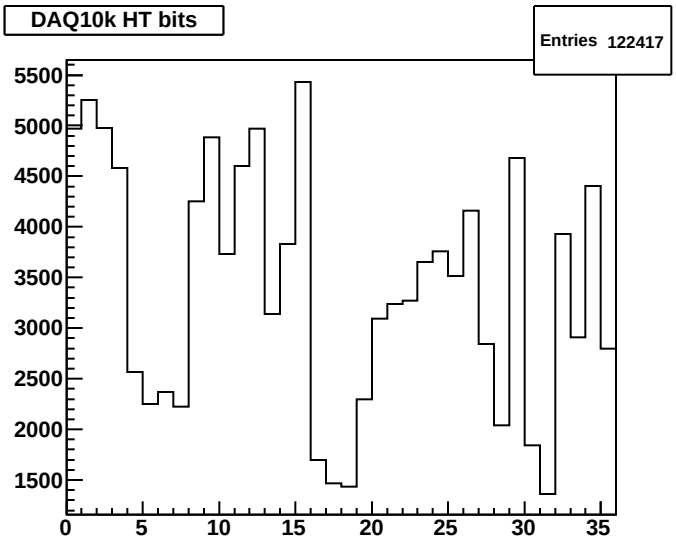

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

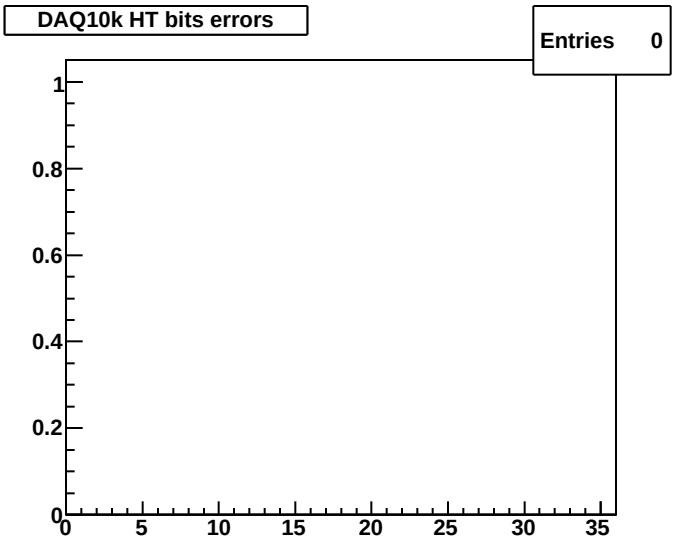

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.183186e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

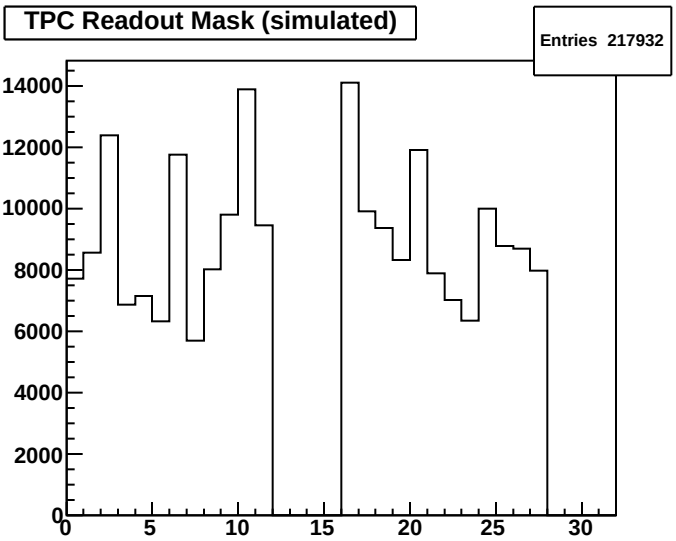

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

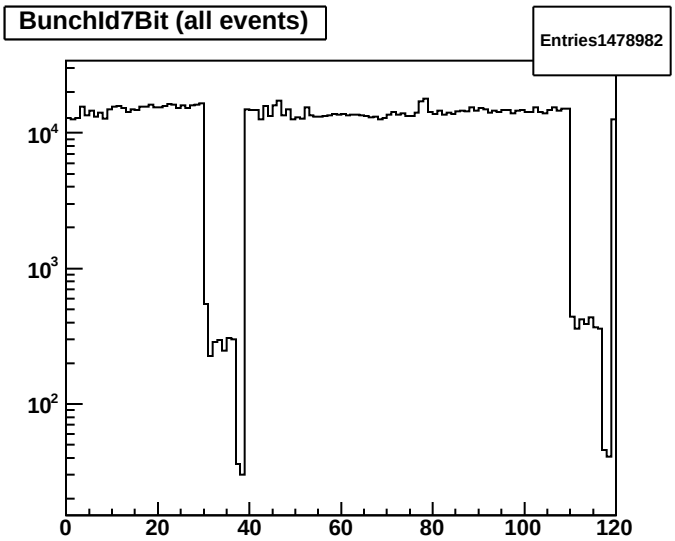

EMC L2 Input - HT23 bits
Entries 1.183186e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

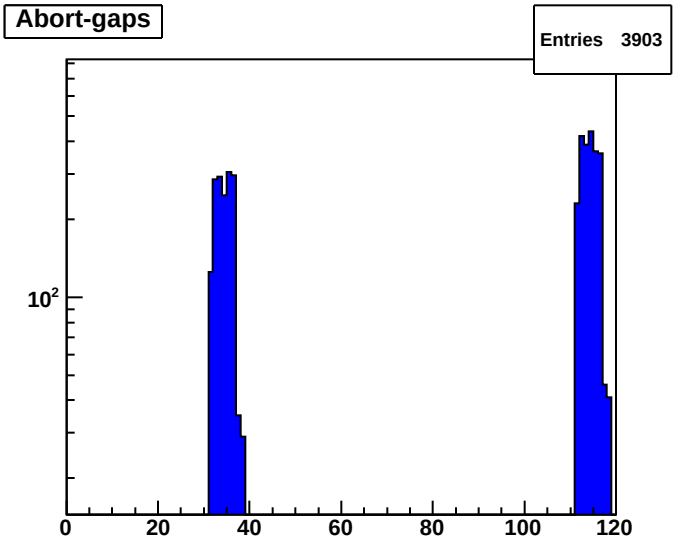

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

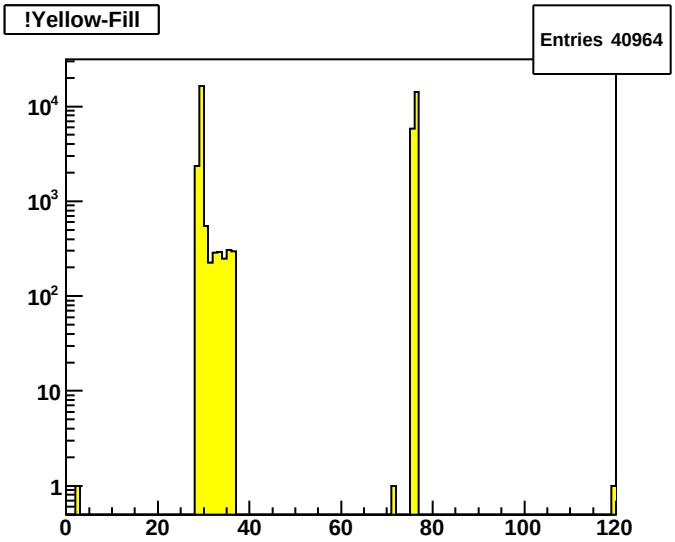

DAQ10k HT bits
Entries 122417

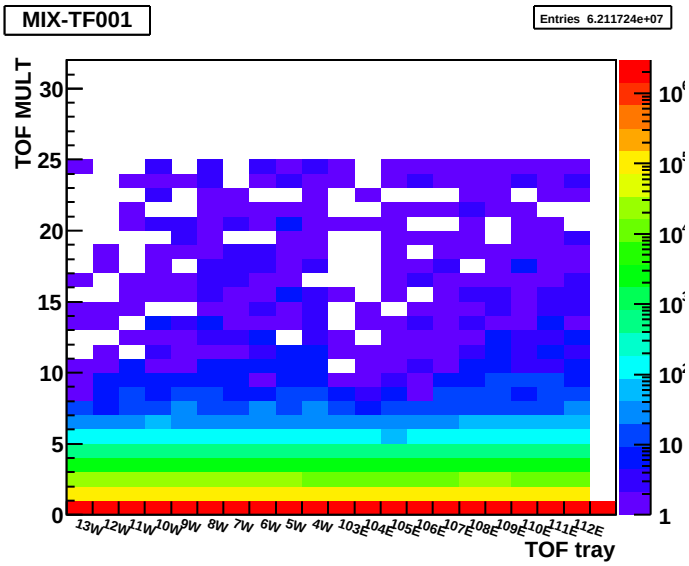

DAQ10k HT bits errors
Entries 0

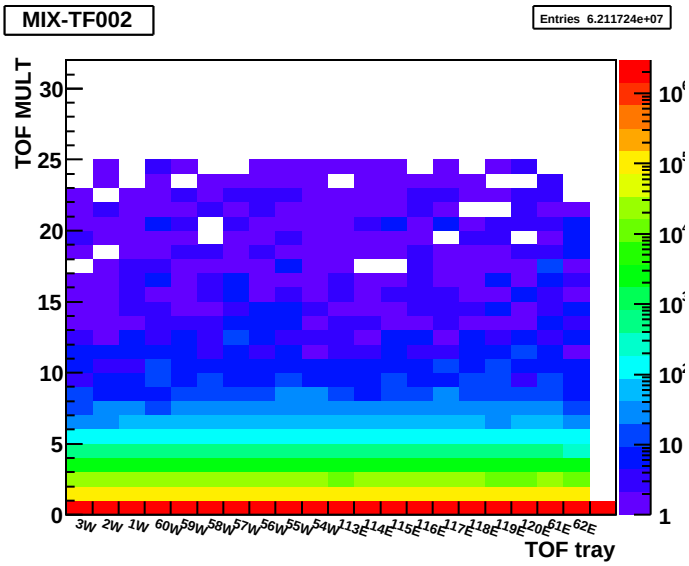

TPC Readout Mask (simulated)
Entries 217932

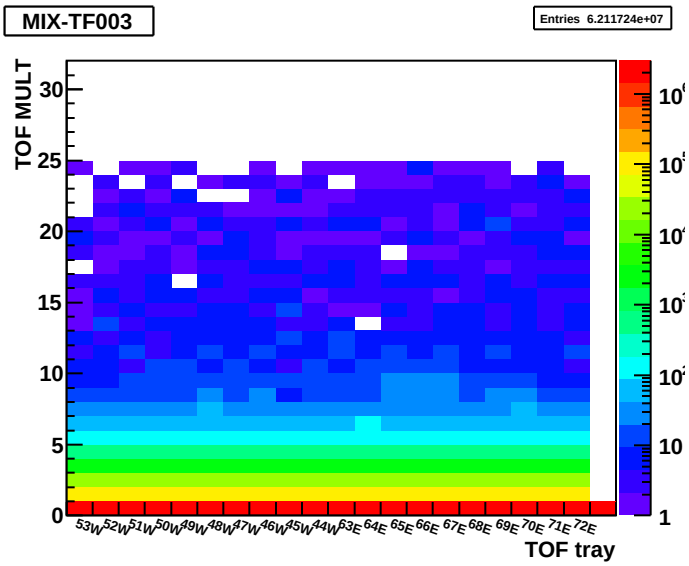

BunchId7Bit (all events)
Entries1478982

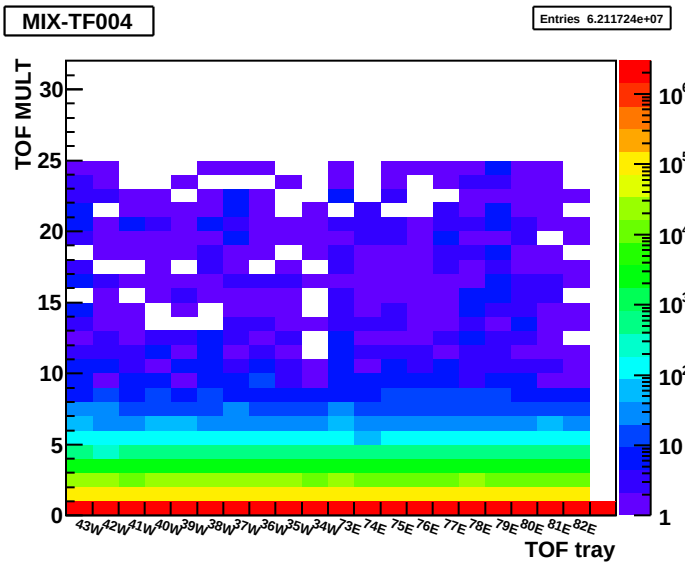

Abort-gaps
Entries 3903

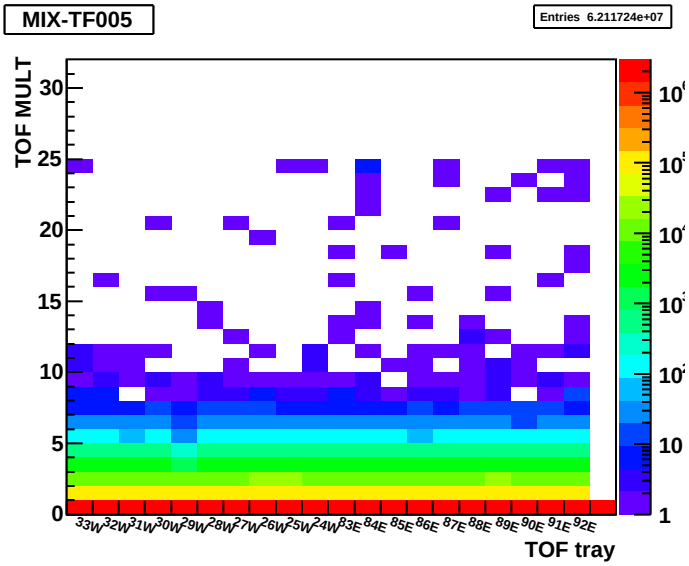

!Yellow-Fill
Entries 40964

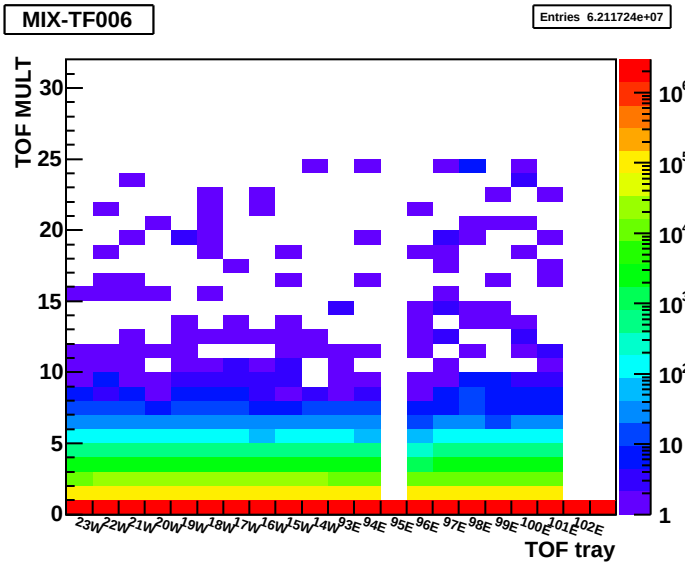

MIX-TF001
Entries 6.211724e+07
TOF MULT
TOF tray

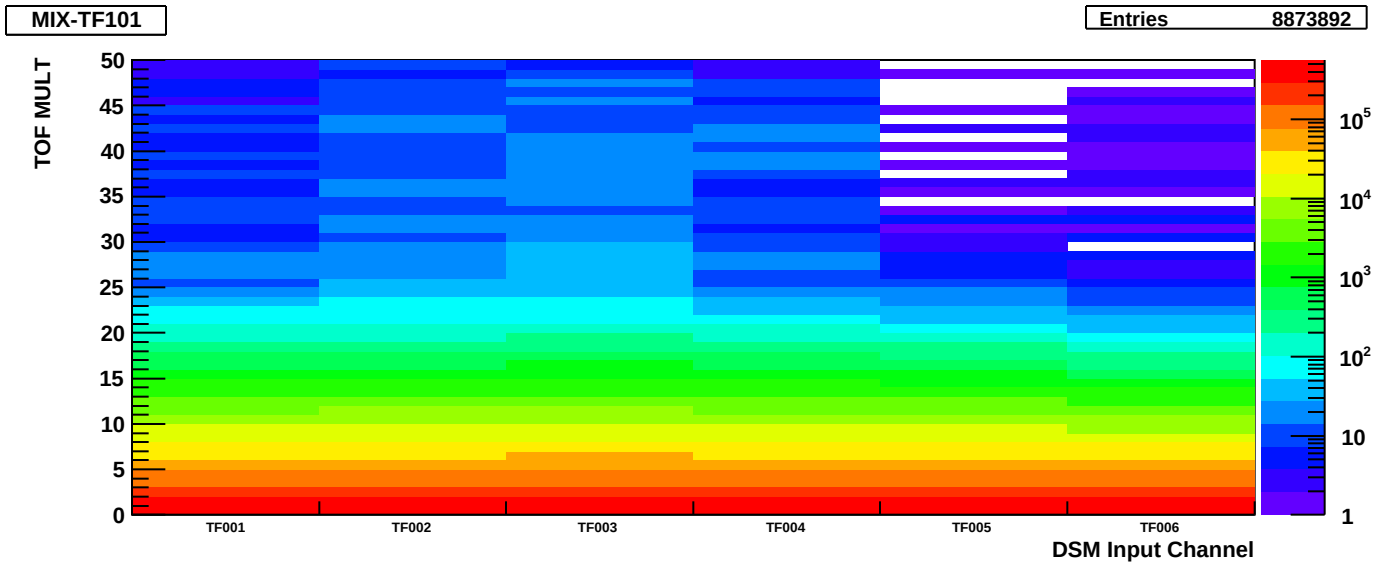

MIX-TF002
Entries 6.211724e+07
TOF MULT
TOF tray

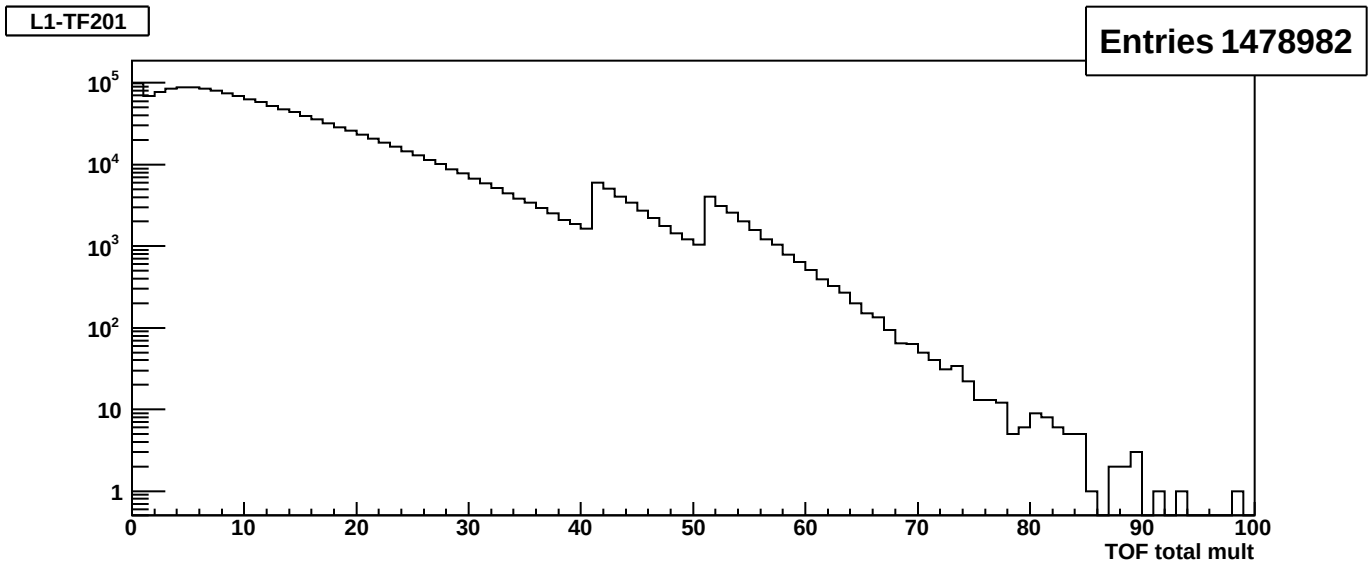

MIX-TF003
Entries 6.211724e+07
TOF MULT
TOF tray

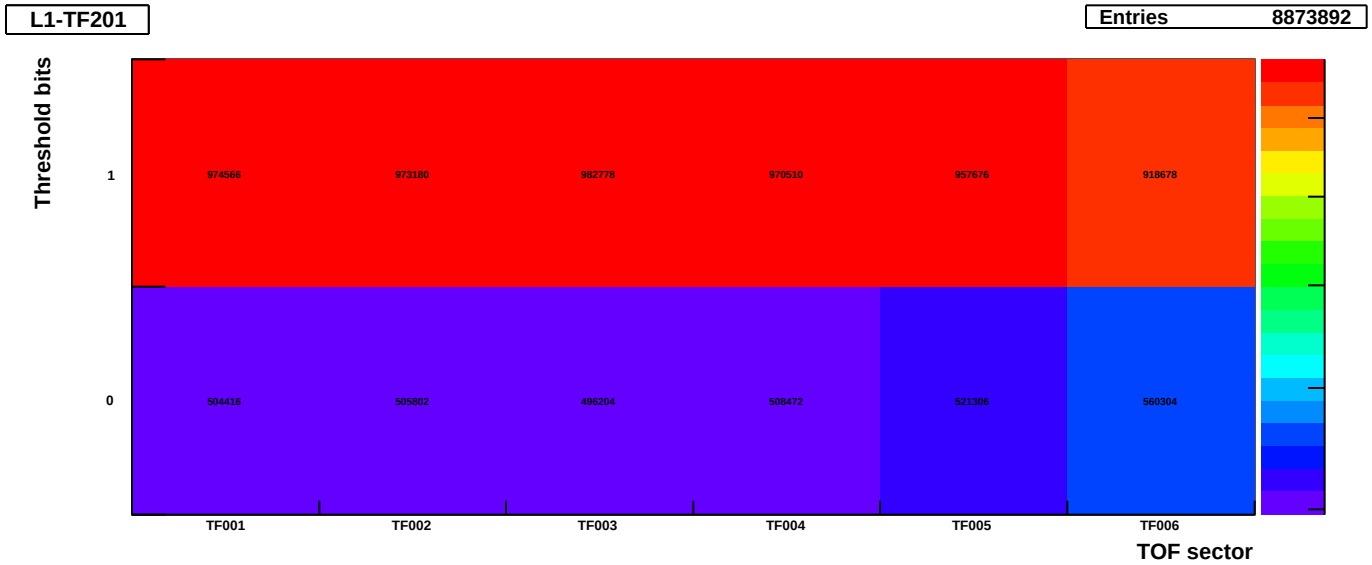

MIX-TF004
Entries 6.211724e+07
TOF MULT
TOF tray

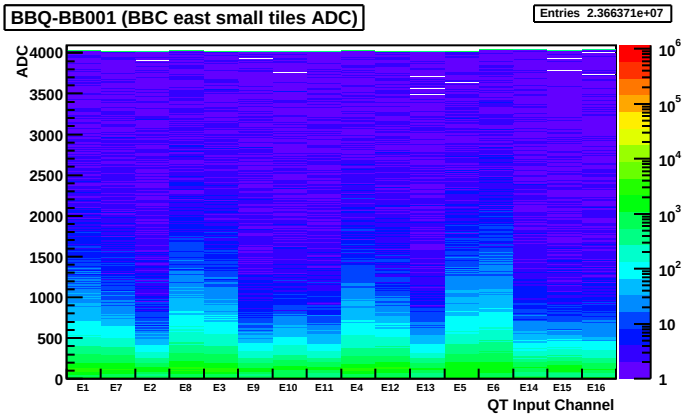

MIX-TF005
Entries 6.211724e+07
TOF MULT
TOF tray

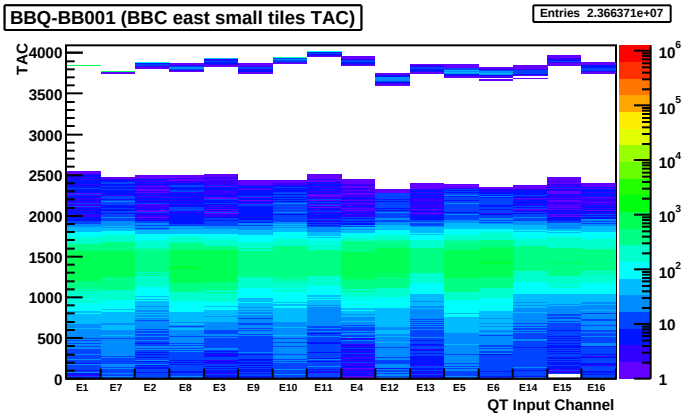

MIX-TF006
Entries 6.211724e+07
TOF MULT
TOF tray

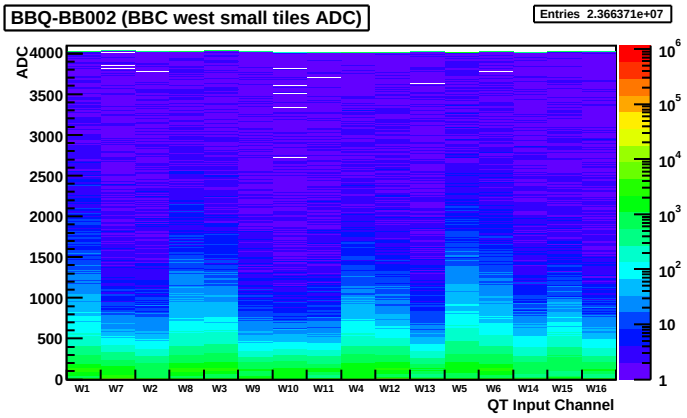

MIX-TF101
Entries 8873892
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

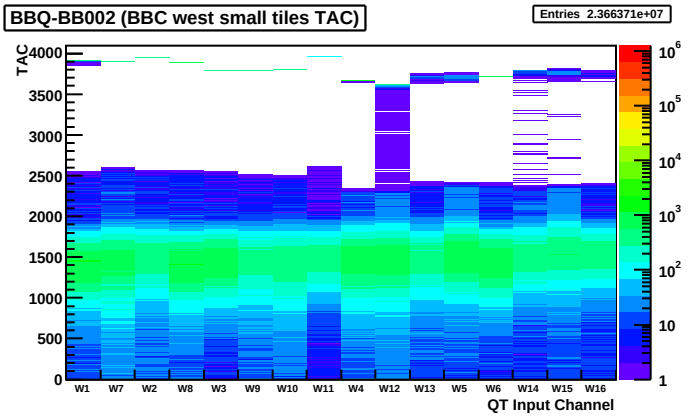

L1-TF201
Entries 1478982
TOF total mult

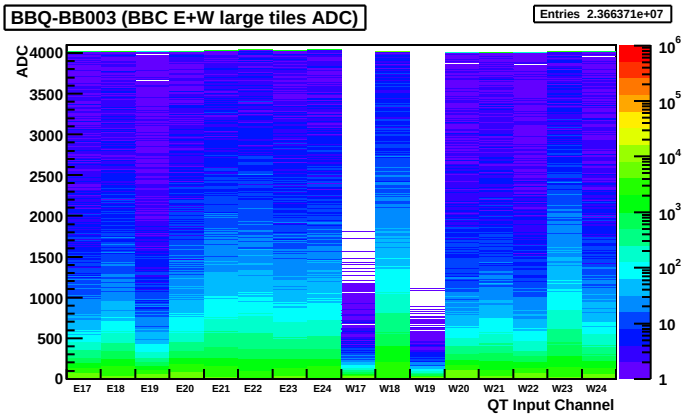

L1-TF201
Entries 8873892
Threshold bits
974566
973180
982778
970510
957676
918678
504416
505802
496204
508472
521306
560304
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

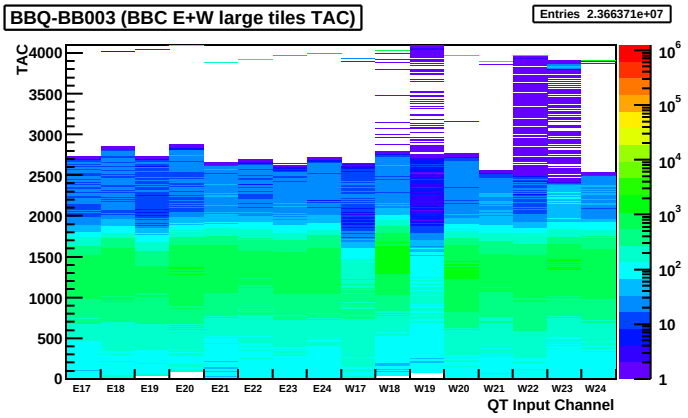

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.366371e+07
ADC
QT Input Channel

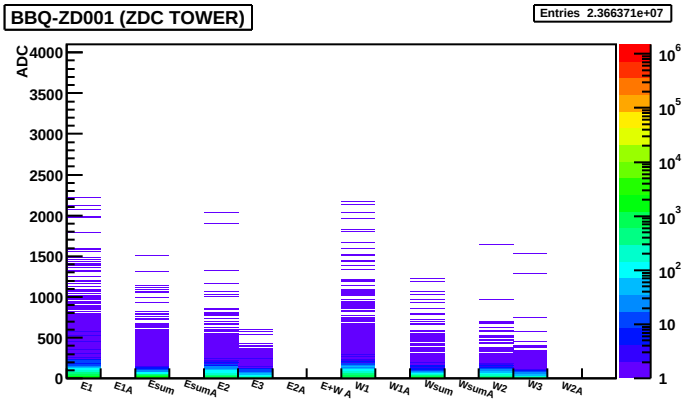

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.366371e+07
TAC
QT Input Channel

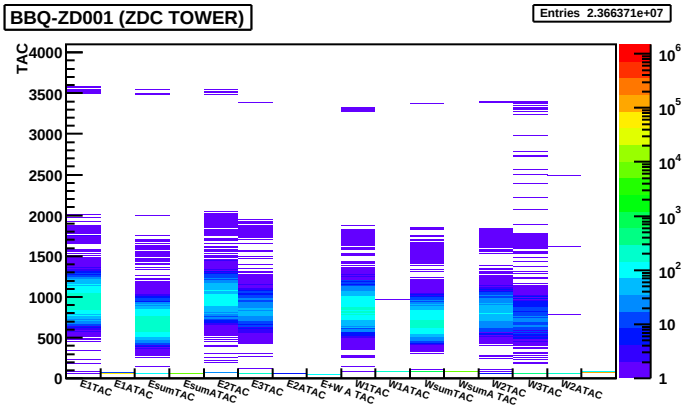

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.366371e+07
ADC
QT Input Channel

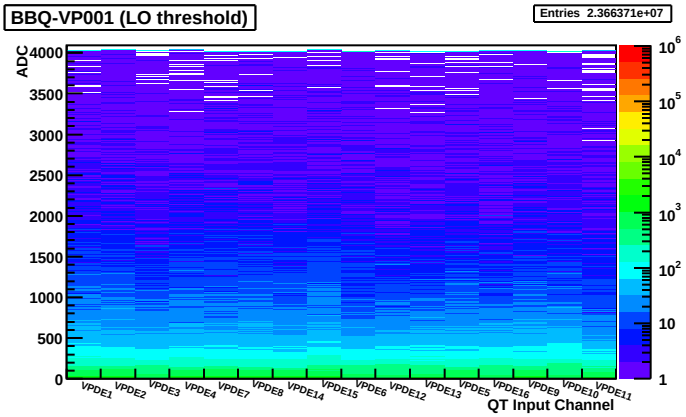

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.366371e+07
TAC
QT Input Channel

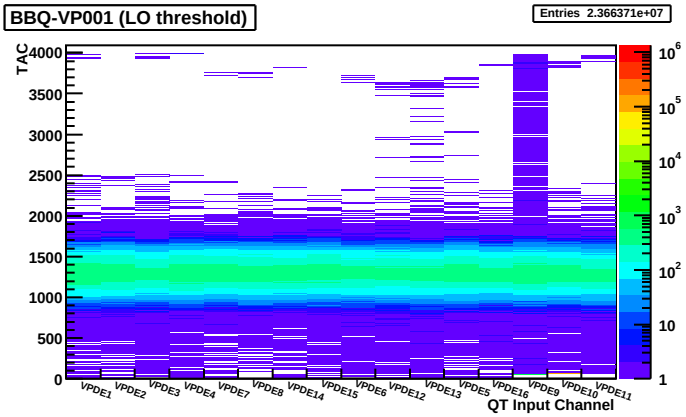

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.366371e+07
ADC
QT Input Channel

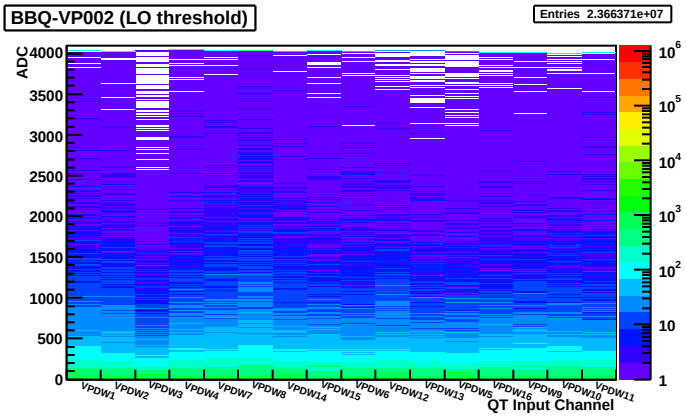

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.366371e+07
TAC
QT Input Channel

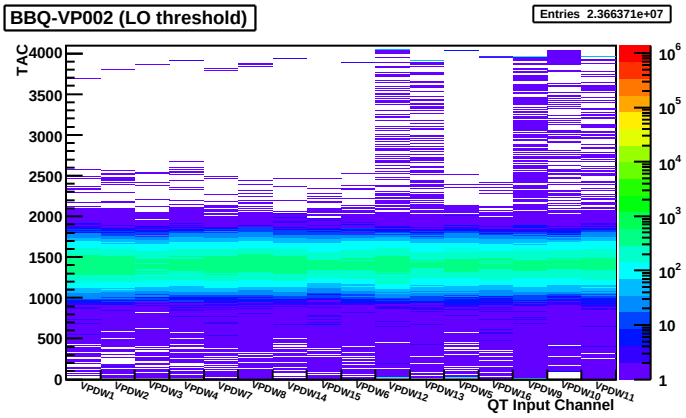

BBQ-ZD001 (ZDC TOWER)
Entries 2.366371e+07
ADC

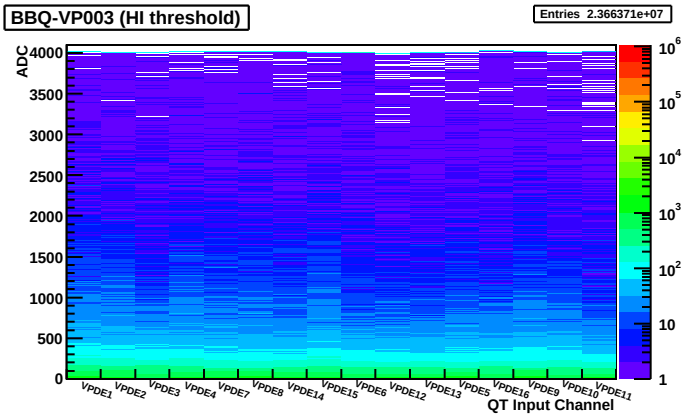

BBQ-ZD001 (ZDC TOWER)
Entries 2.366371e+07
TAC

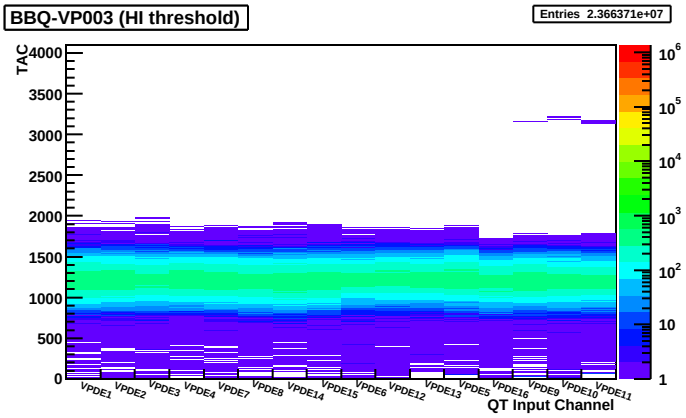

BBQ-VP001 (LO threshold)
Entries 2.366371e+07
ADC
QT Input Channel

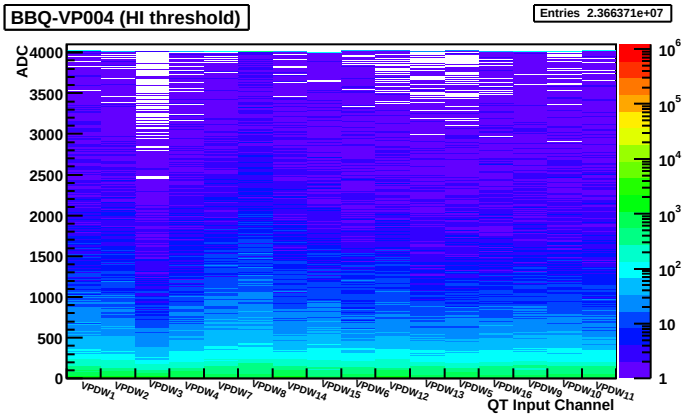

BBQ-VP001 (LO threshold)
Entries 2.366371e+07
TAC
QT Input Channel

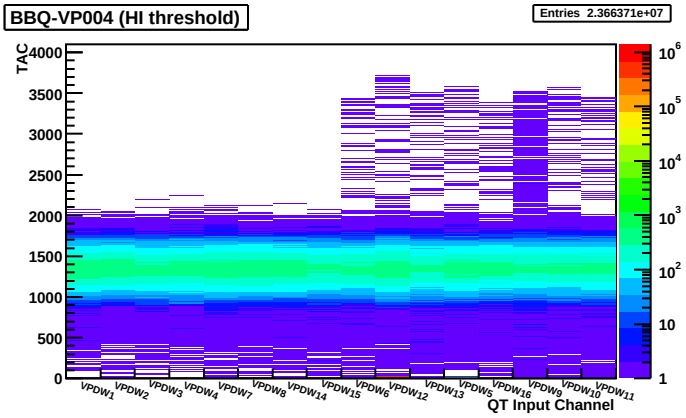

BBQ-VP002 (LO threshold)
Entries 2.366371e+07
ADC
QT Input Channel

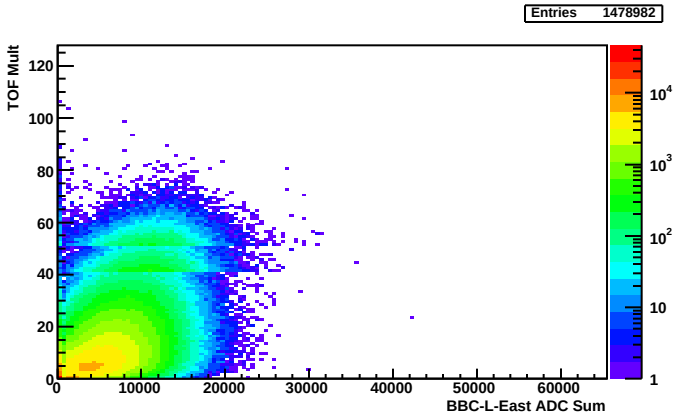

BBQ-VP002 (LO threshold)
Entries 2.366371e+07
TAC
QT Input Channel

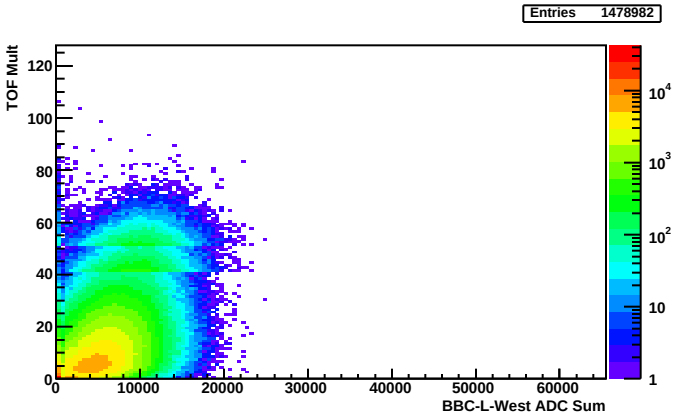

BBQ-VP003 (HI threshold)
Entries 2.366371e+07
ADC
QT Input Channel

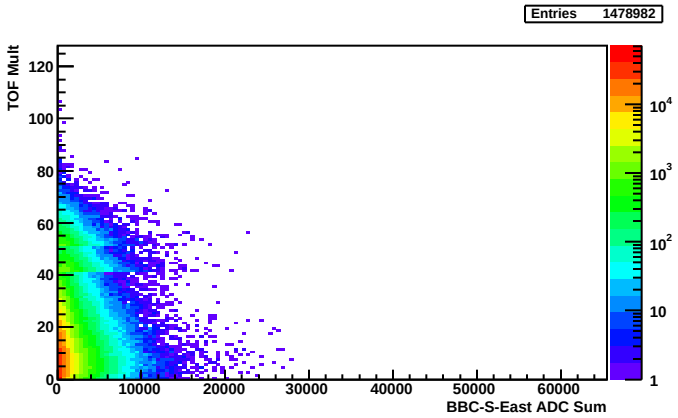

BBQ-VP003 (HI threshold)
Entries 2.366371e+07
TAC
QT Input Channel

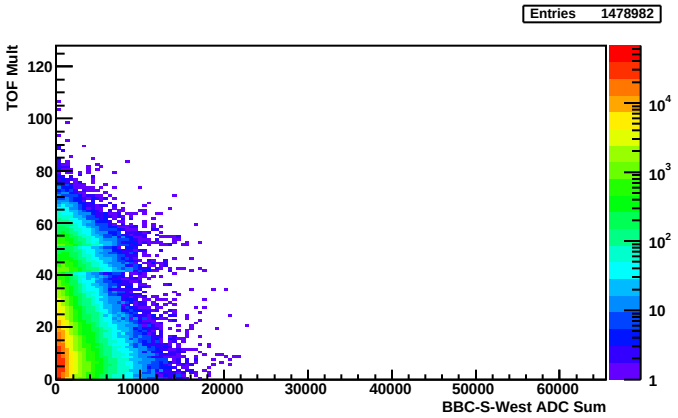

BBQ-VP004 (HI threshold)
Entries 2.366371e+07
ADC
QT Input Channel

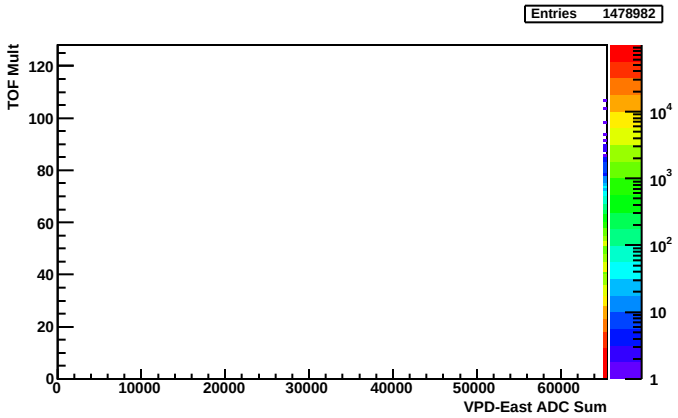

BBQ-VP004 (HI threshold)
Entries 2.366371e+07
TAC
QT Input Channel

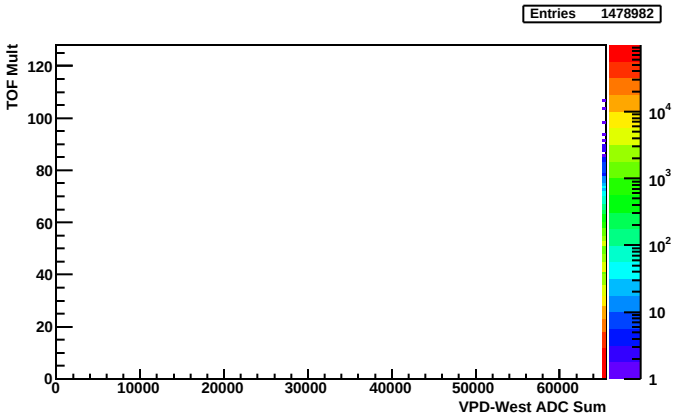

Entries 1478982
TOF Mult
BBC-L-East ADC Sum

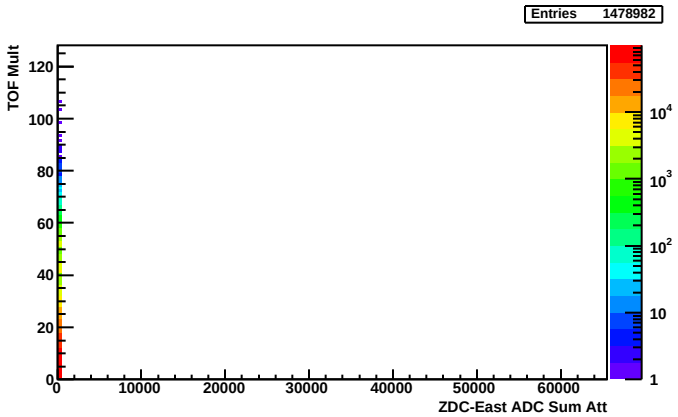

Entries 1478982
TOF Mult
BBC-L-West ADC Sum

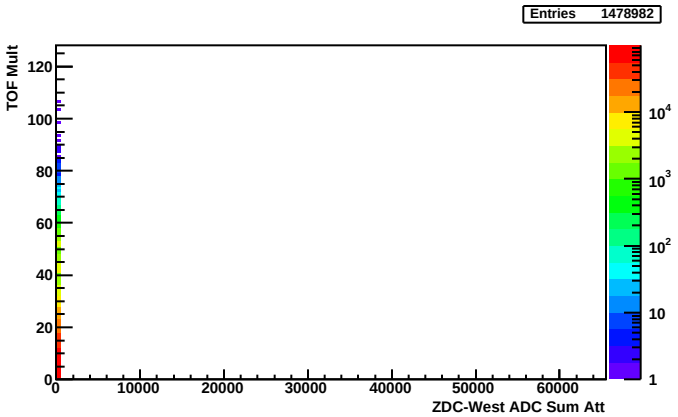

Entries 1478982
TOF Mult
BBC-S-East ADC Sum

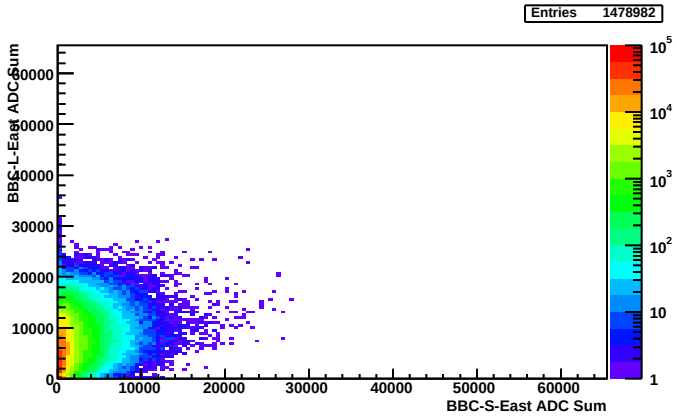

Entries 1478982
TOF Mult
BBC-S-West ADC Sum

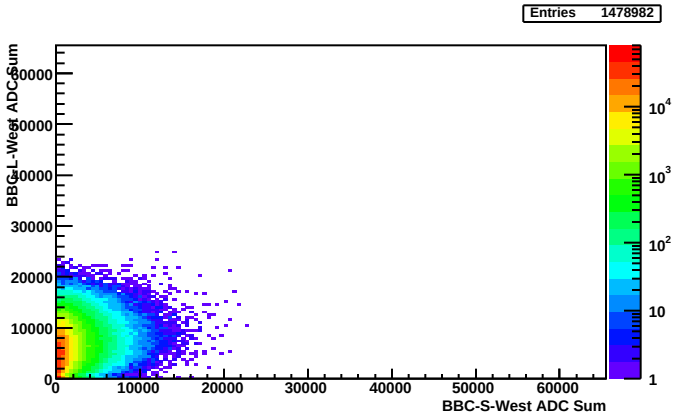

Entries 1478982
TOF Mult
VPD-East ADC Sum

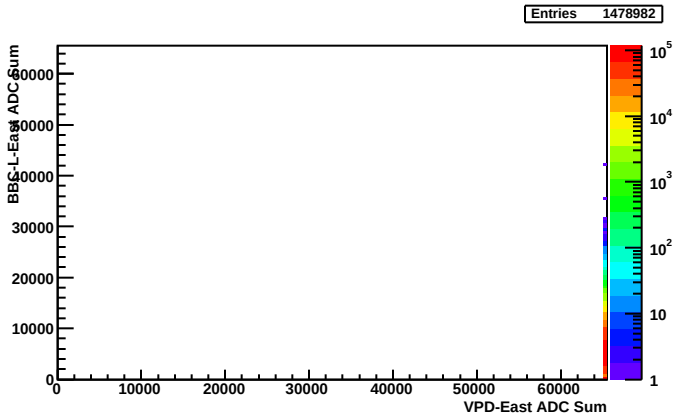

Entries 1478982
TOF Mult
VPD-West ADC Sum

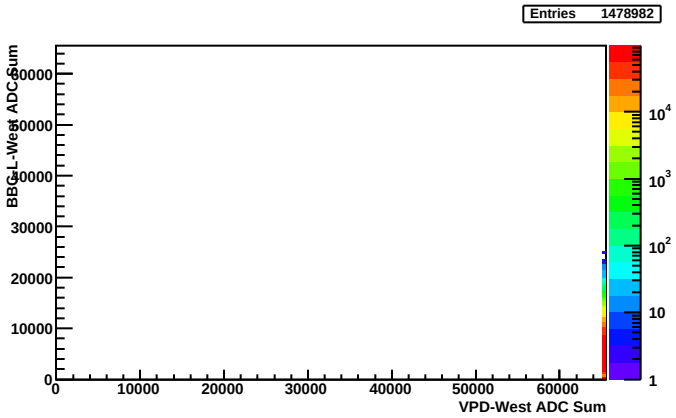

Entries 1478982
TOF Mult
ZDC-East ADC Sum Att


Entries 1478982
TOF Mult
ZDC-West ADC Sum Att


Entries 1478982
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1478982
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 1478982
BBC-L-East ADC Sum
VPD-East ADC Sum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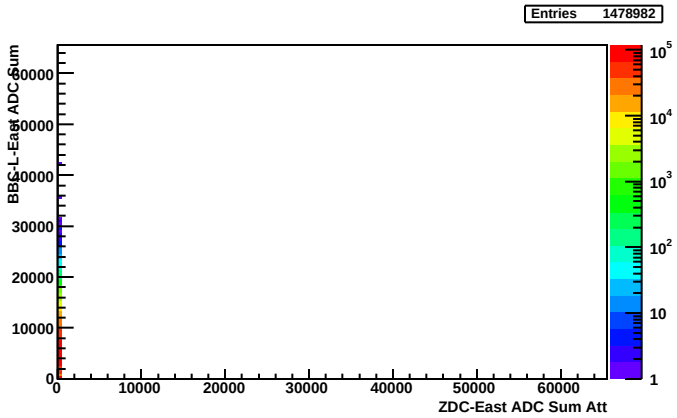
BBC-L-West ADC Sum
VPD-West ADC Sum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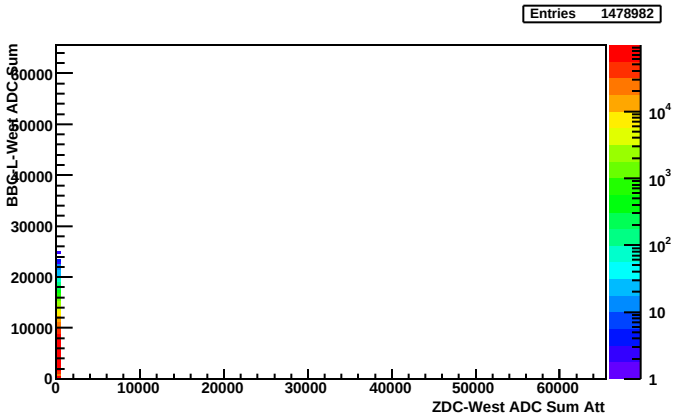
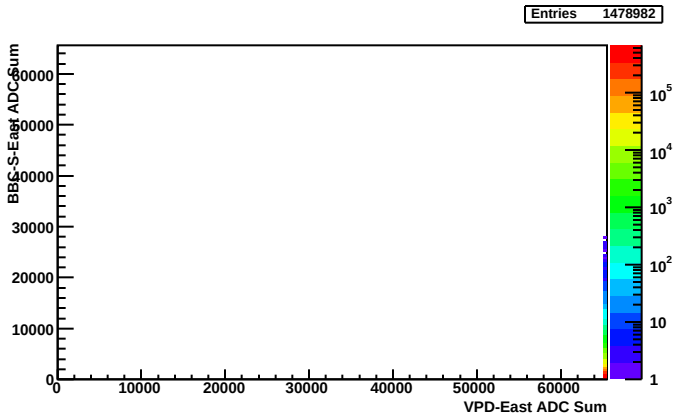
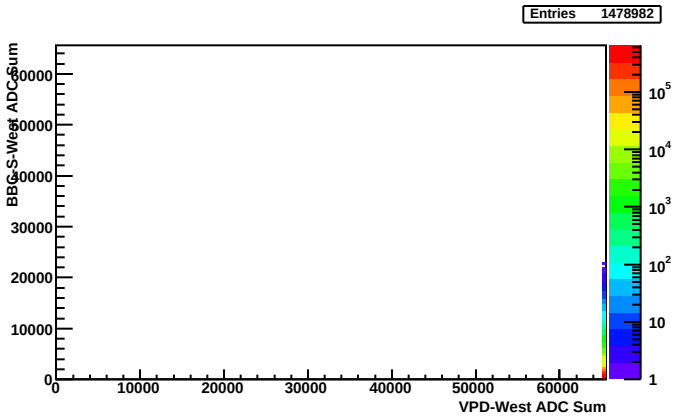
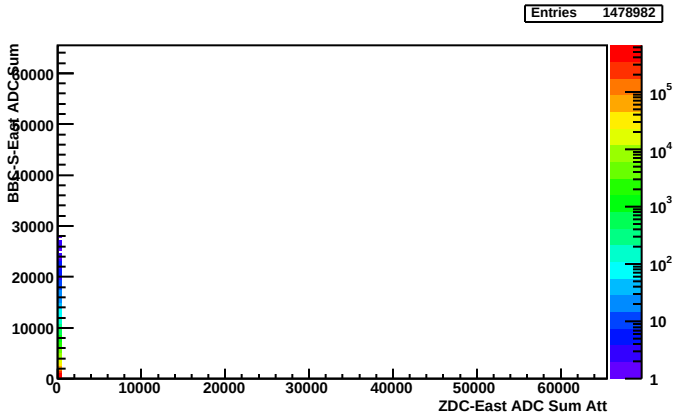
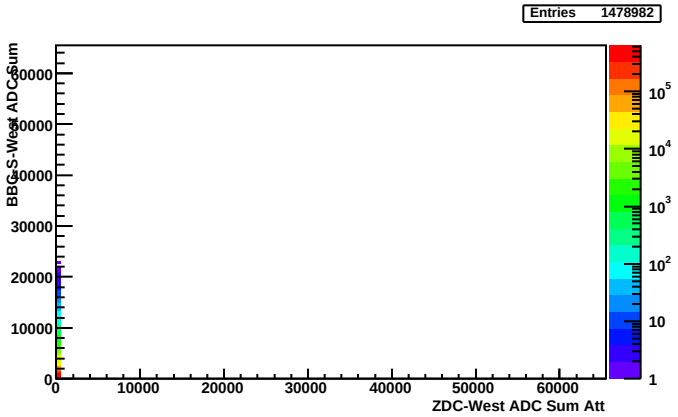
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

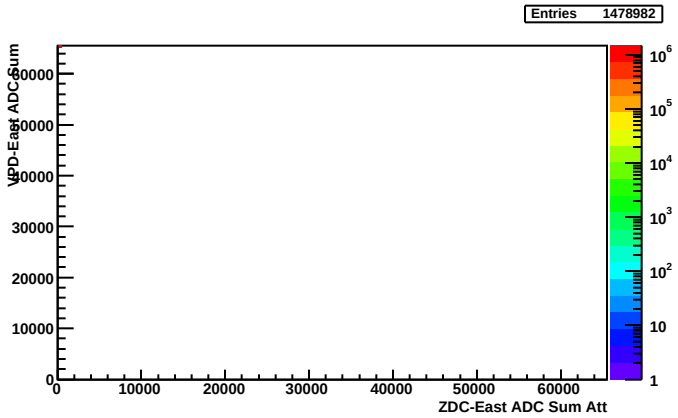

Entries 1478982
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

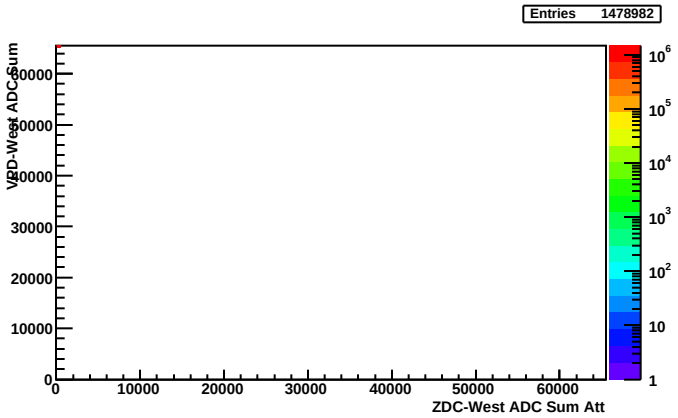

Entries 1478982
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 1478982
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 1478982
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 1478982
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1478982
VPD-East ADC Sum
ZDC-East ADC Sum Att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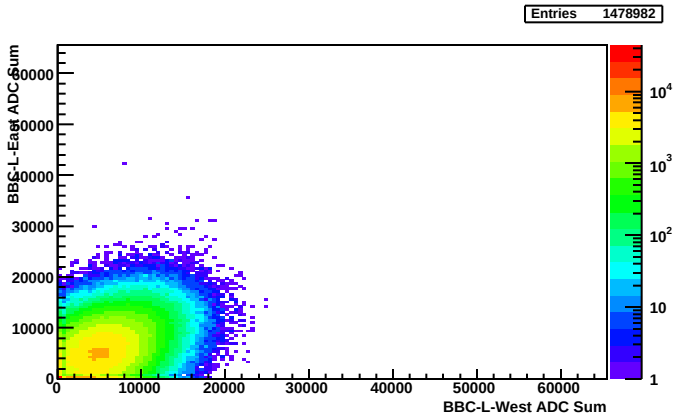
VPD-West ADC Sum
ZDC-West ADC Sum Att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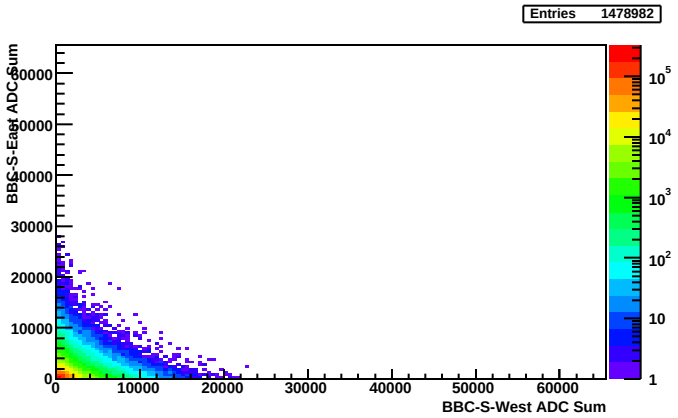
BBC-L-East ADC Sum
BBC-L-West ADC Sum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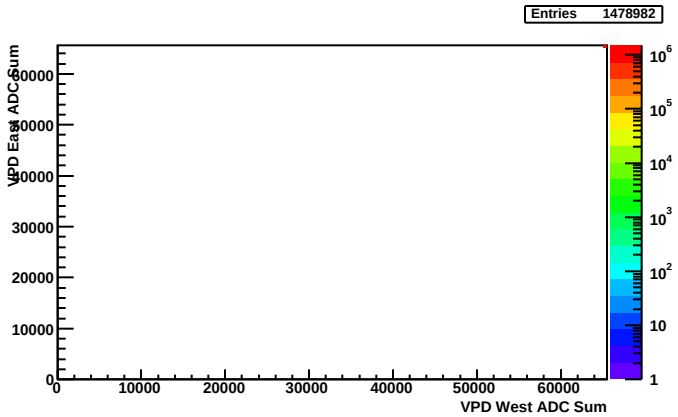
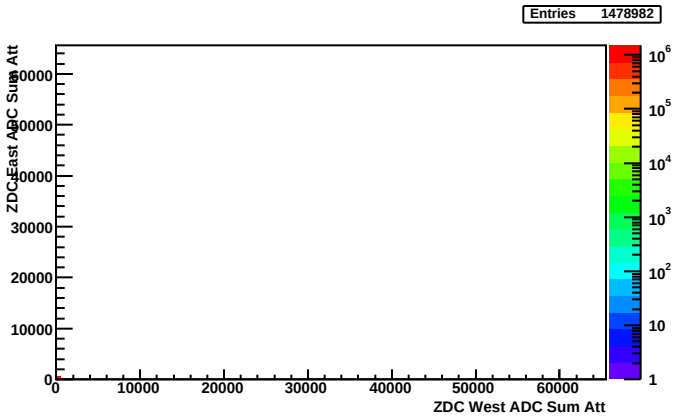
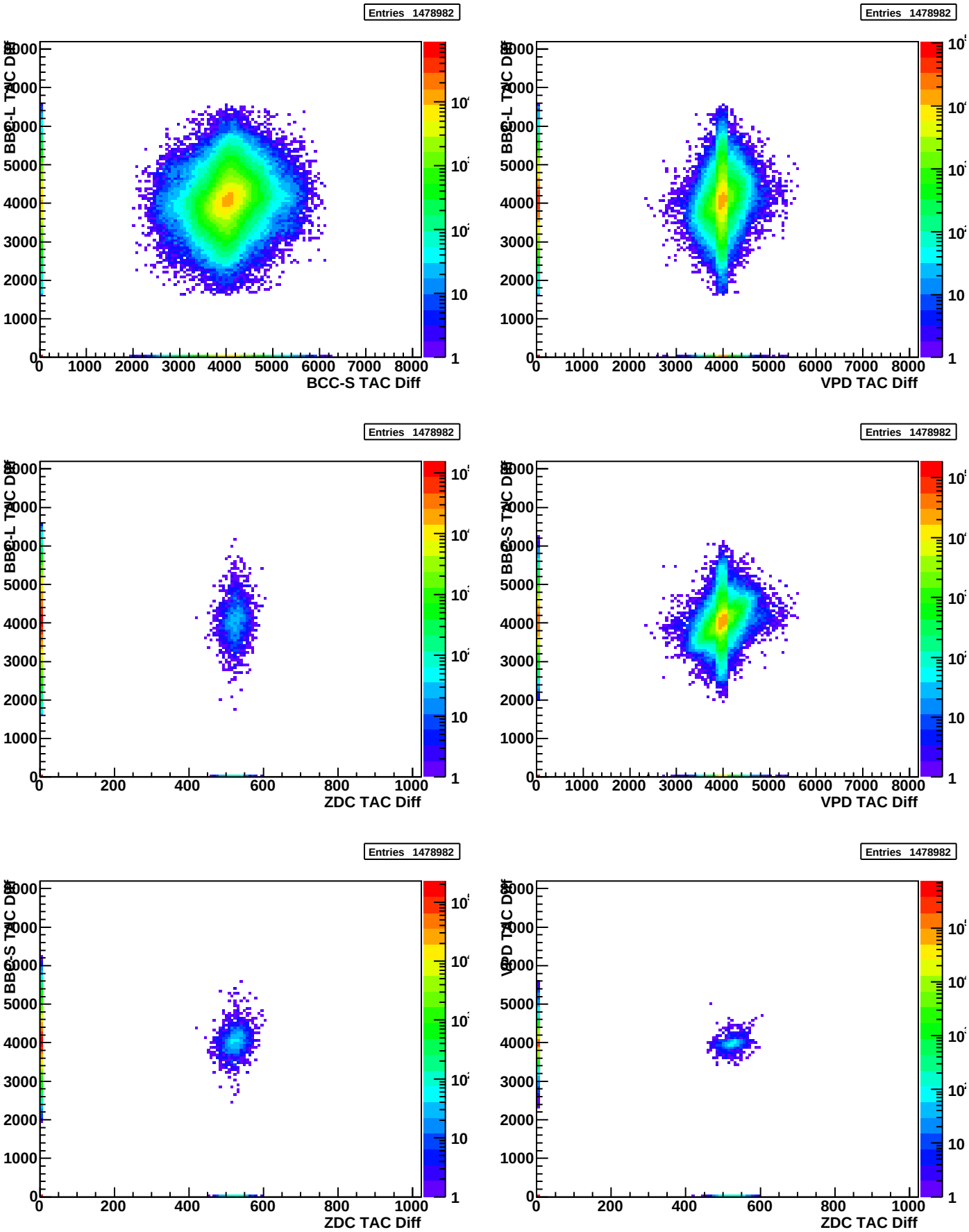
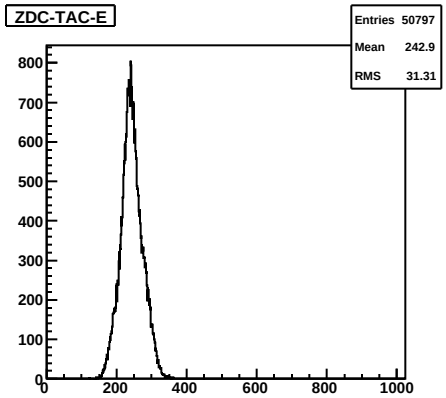
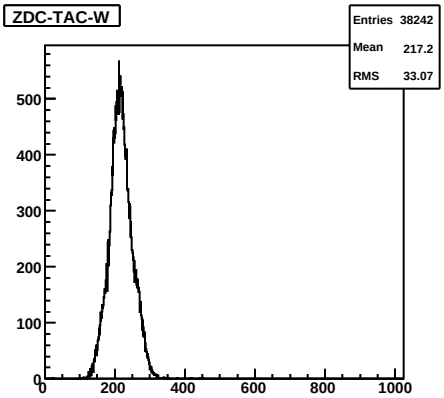
BBC-S-East ADC Sum
BBC-S-West ADC Sum

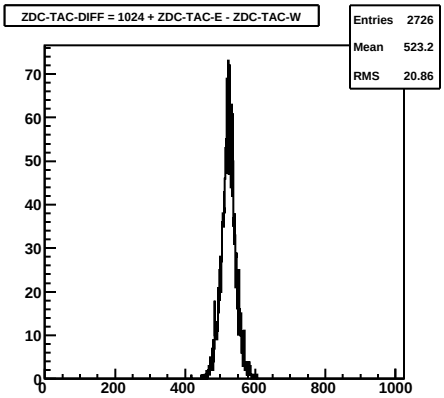

Entries 1478982
VPD East ADC Sum
VPD West ADC Sum

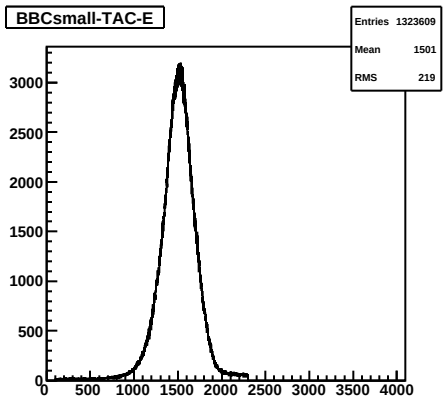

Entries 1478982
ZDC East ADC Sum Att
ZDC West ADC Sum Att

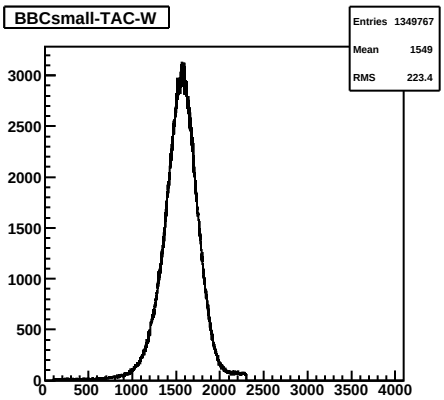

Entries 1478982
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1478982
BBC-L TAC Diff
VPD TAC Diff
Entries 1478982
BBC-L TAC Diff
ZDC TAC Diff
Entries 1478982
BBC-S TAC Diff
VPD TAC Diff
Entries 1478982
BBC-S TAC Diff
ZDC TAC Diff
Entries 1478982
VPD TAC Diff
ZDC TAC Diff

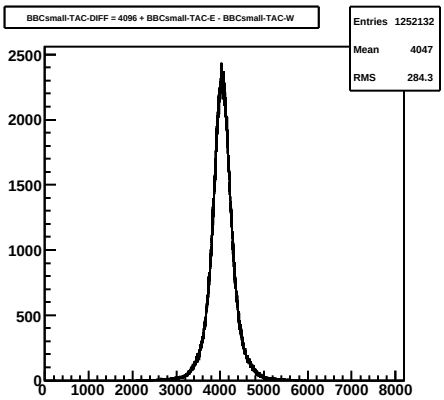

ZDC-TAC-E
Entries 50797
Mean 242.9
RMS 31.31

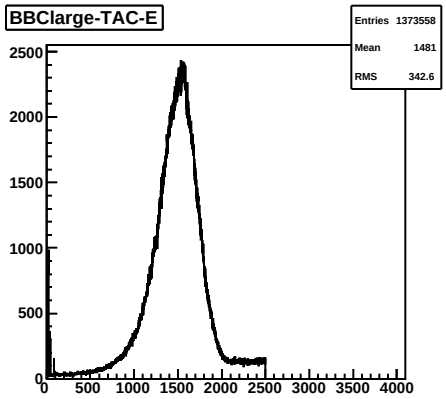

ZDC-TAC-W
Entries 38242
Mean 217.2
RMS 33.07

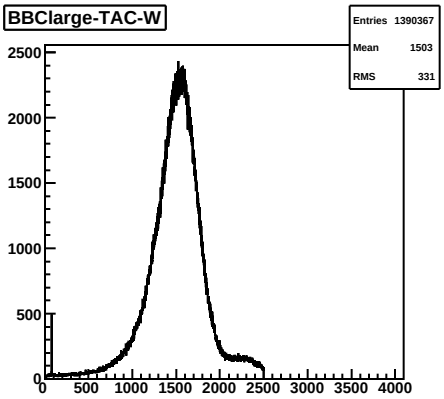

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2726
Mean 523.2
RMS 20.86

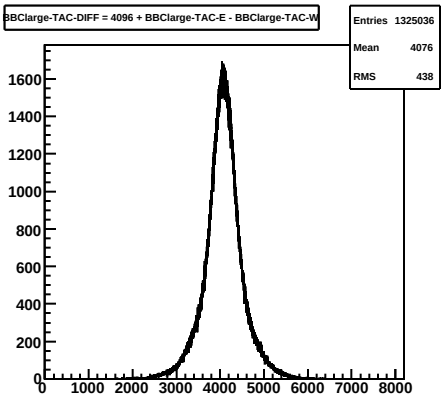

BBCsmall-TAC-E
Entries 1323609
Mean 1501
RMS 219

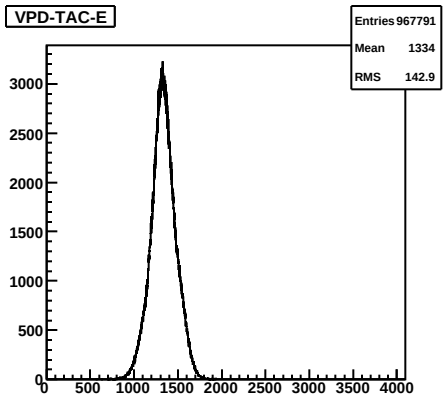

BBCsmall-TAC-W
Entries 1349767
Mean 1549
RMS 223.4

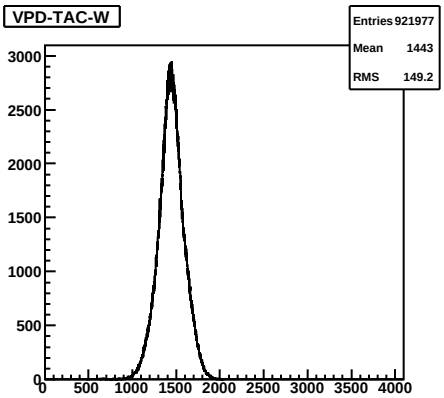

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1252132
Mean 4047
RMS 284.3

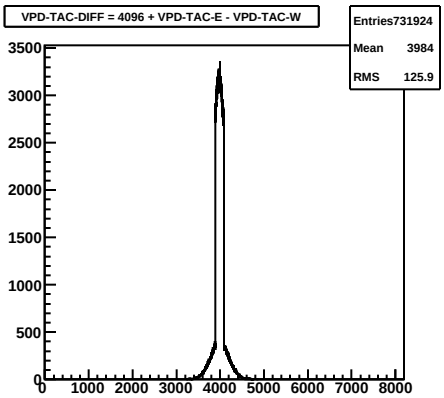

BBClarge-TAC-E
Entries 1373558
Mean 1481
RMS 342.6

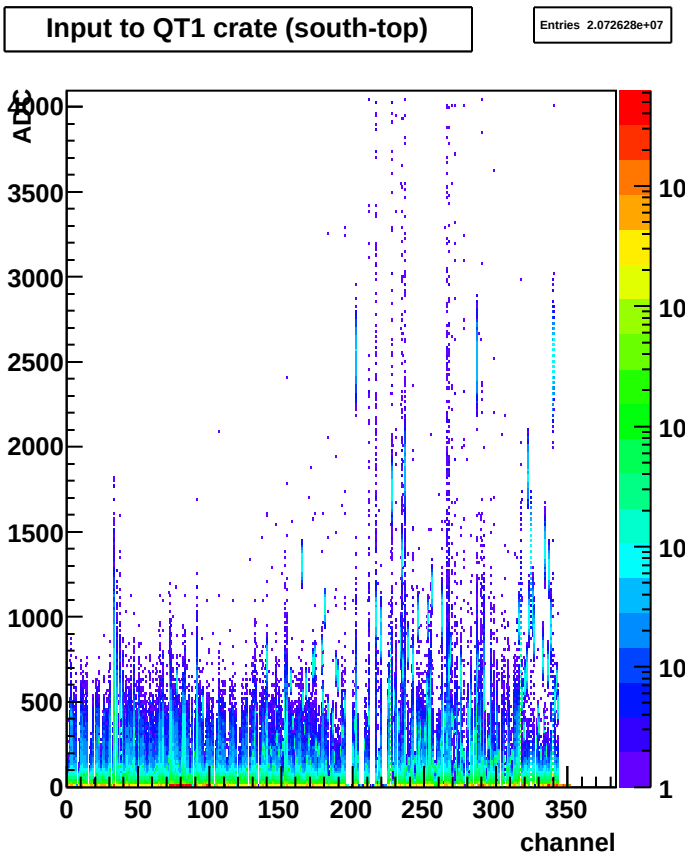

BBClarge-TAC-W
Entries 1390367
Mean 1503
RMS 331

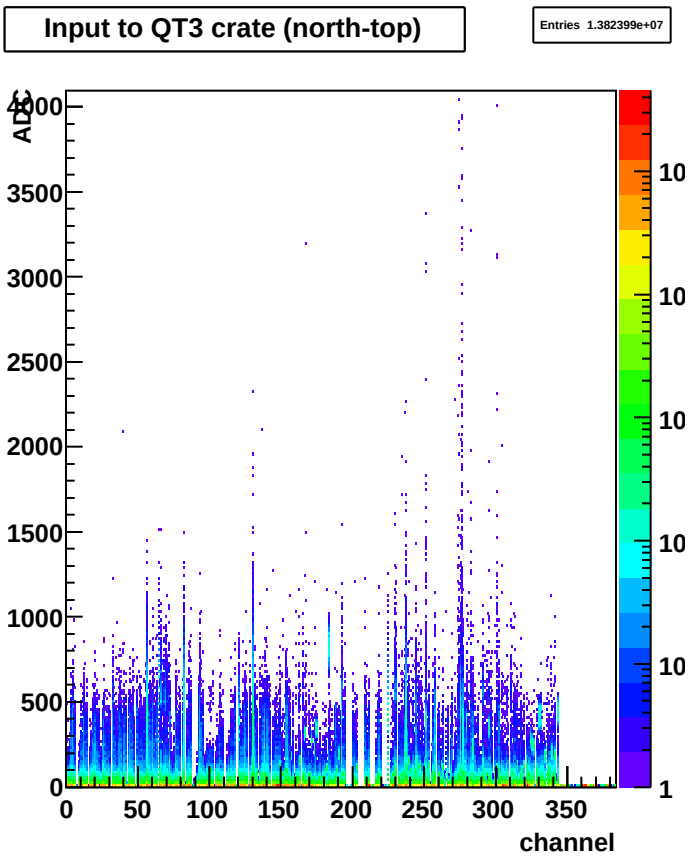

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1325036
Mean 4076
RMS 438

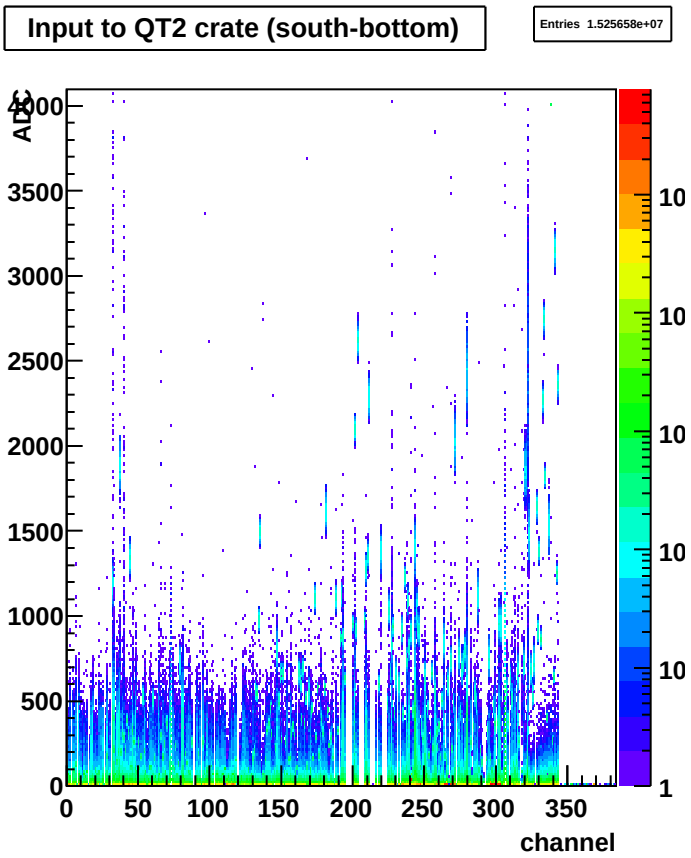

VPD-TAC-E
Entries 967791
Mean 1334
RMS 142.9

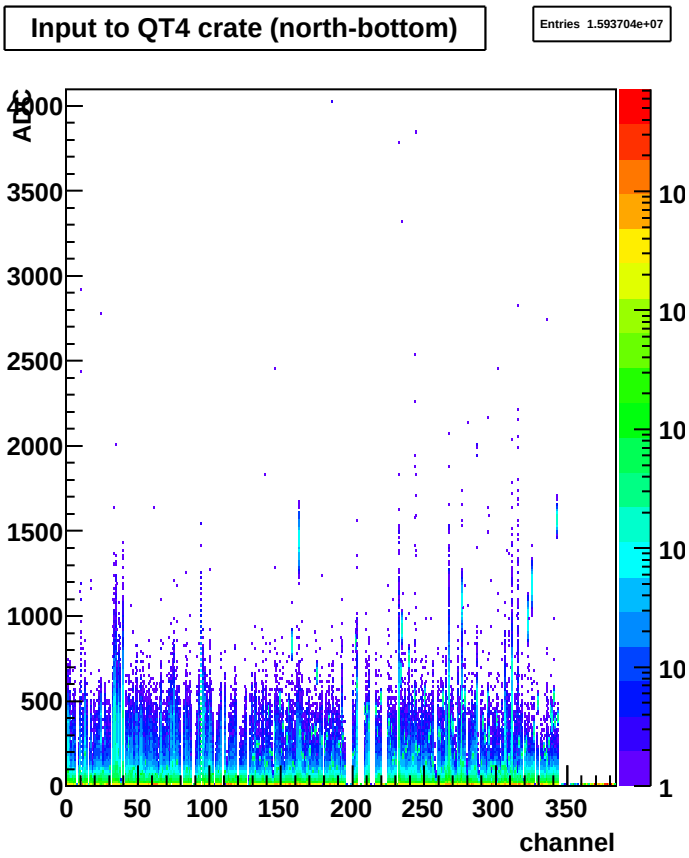

VPD-TAC-W
Entries 921977
Mean 1443
RMS 149.2

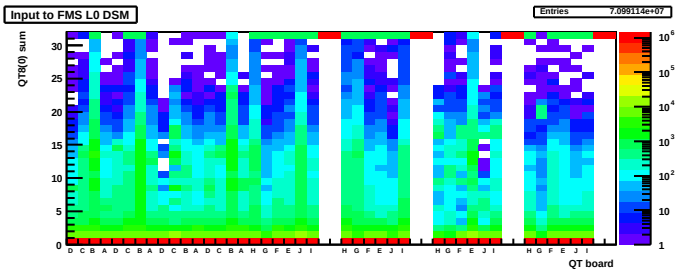

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 731924
Mean 3984
RMS 125.9

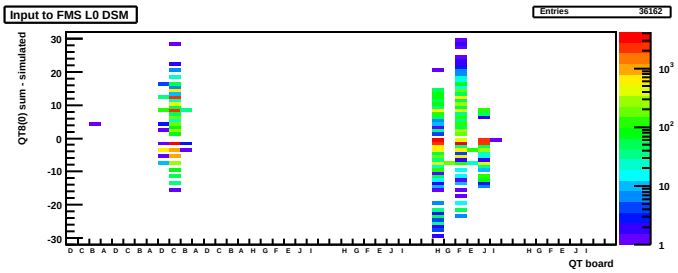

Input to QT1 crate (south-top)
Entries 2.072628e+07
ADC
channel

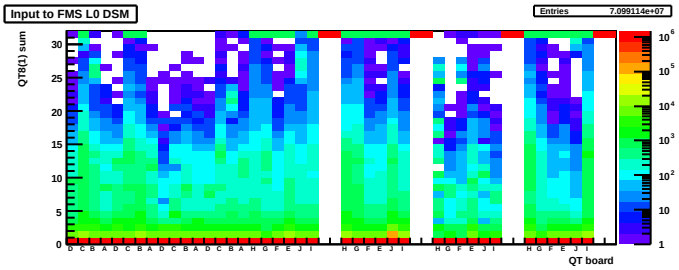

Input to QT3 crate (north-top)
Entries 1.382399e+07
ADC
channel

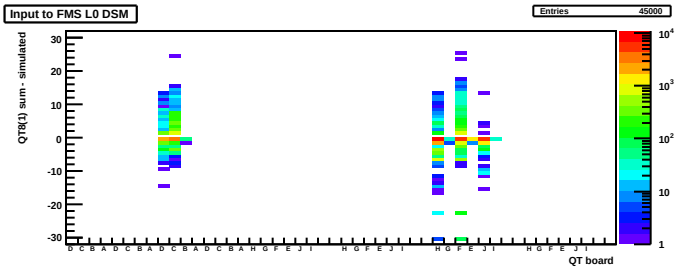

Input to QT2 crate (south-bottom)
Entries 1.525658e+07
ADC
channel

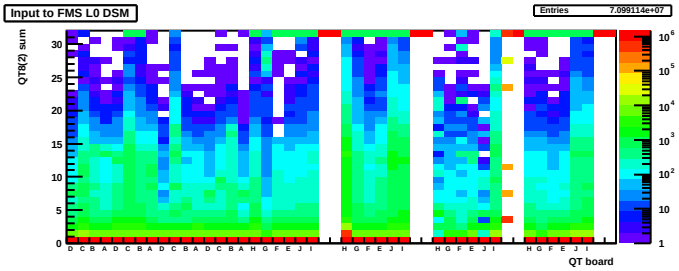

Input to QT4 crate (north-bottom)
Entries 1.593704e+07
ADC
channel

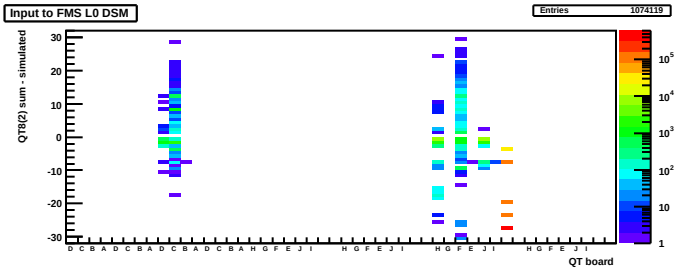

Input to FMS L0 DSM
Entries 7.099114e+07
QT8(0) sum
QT board

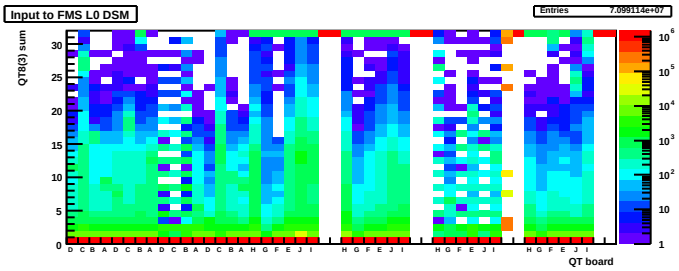

Input to FMS L0 DSM
Entries 36162
QT8(0) sum - simulated
QT board

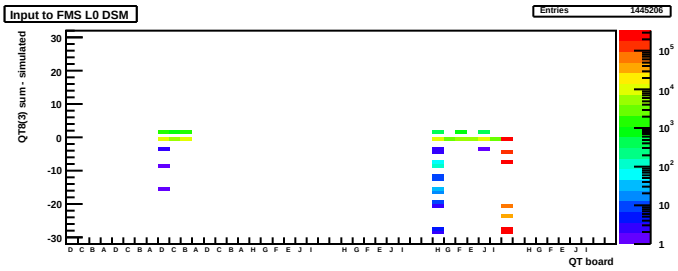

Input to FMS L0 DSM
Entries 7.099114e+07
QT8(1) sum
QT board

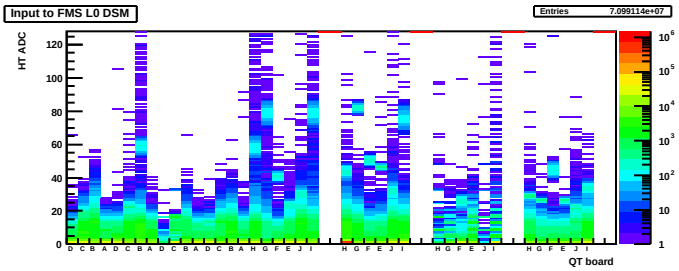

Input to FMS L0 DSM
Entries 45000
QT8(1) sum - simulated
QT board

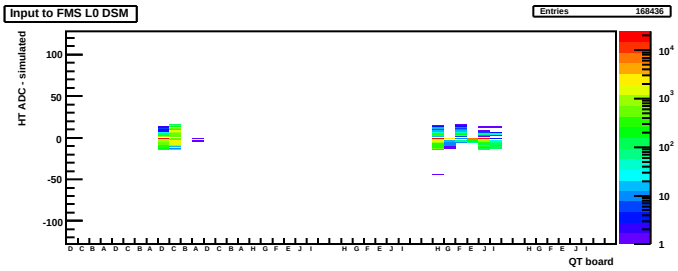

Input to FMS L0 DSM
Entries 7.099114e+07
QT8(2) sum
QT board

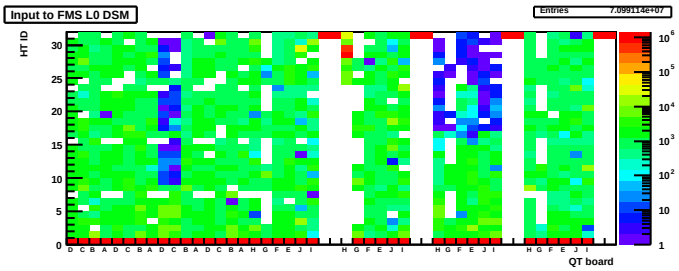

Input to FMS L0 DSM
Entries 1074119
QT8(2) sum - simulated
QT board

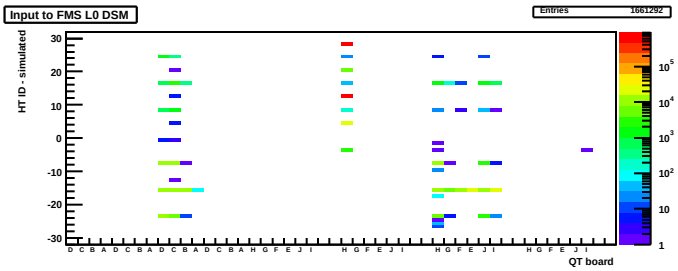

Input to FMS L0 DSM
Entries 7.099114e+07
QT8(3) sum
QT board

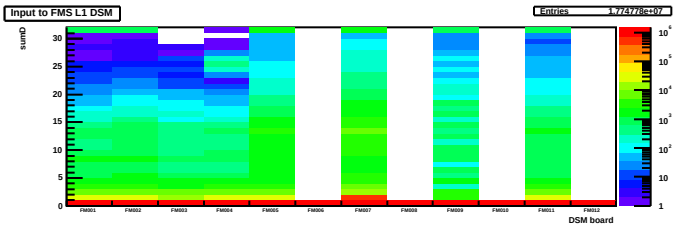

Input to FMS L0 DSM
Entries 1445206
QT8(3) sum - simulated
QT board

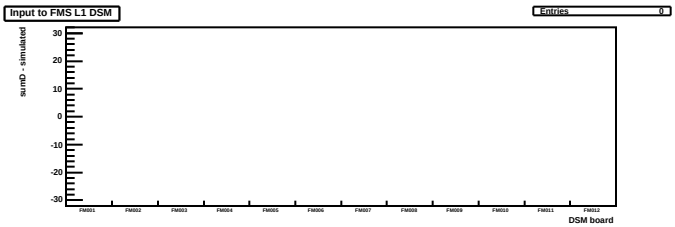

Input to FMS L0 DSM
Entries 7.099114e+07
HT ADC
QT board

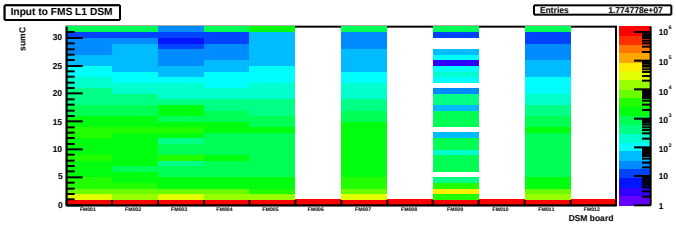

Input to FMS L0 DSM
Entries 168436
HT ADC - simulated
QT board

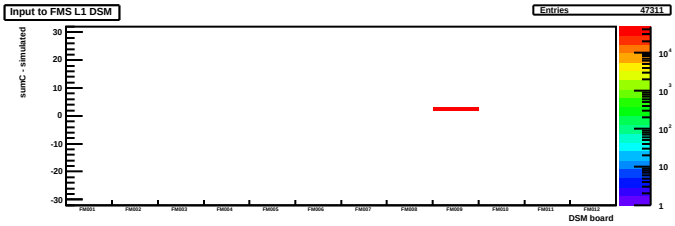

Input to FMS L0 DSM
Entries 7.099114e+07
HT ID
QT board

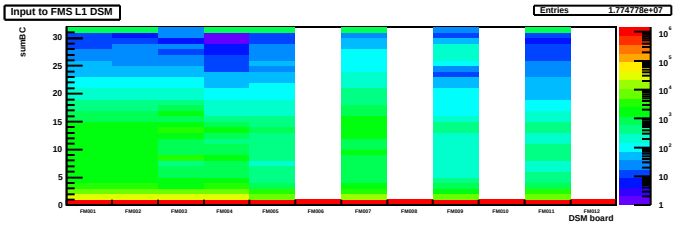

Input to FMS L0 DSM
Entries 1661292
HT ID - simulated
QT board

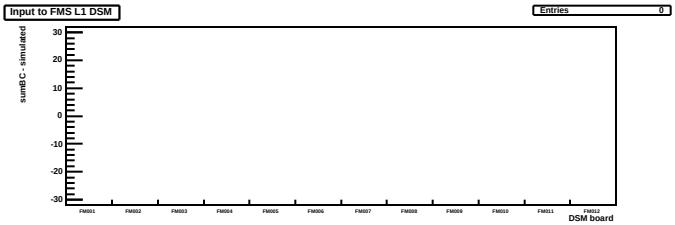

Input to FMS L1 DSM
Entries 1.774778e+07
sumD
DSM board

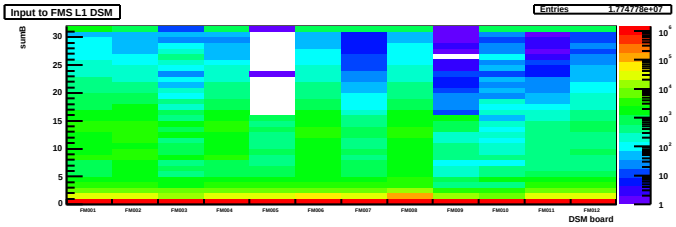

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

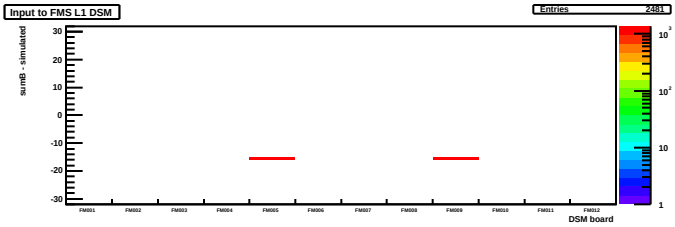

Input to FMS L1 DSM
Entries 1.774778e+07
sumC
DSM board

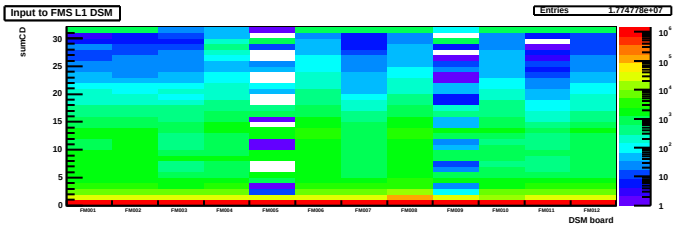

Input to FMS L1 DSM
Entries 47311
sumC - simulated
DSM board

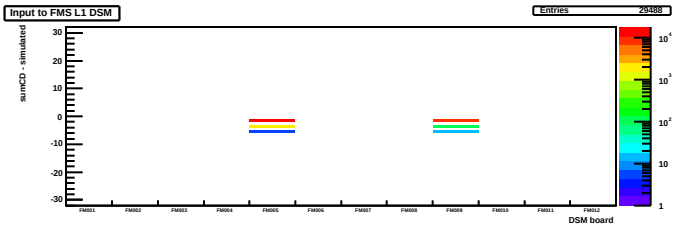

Input to FMS L1 DSM
Entries 1.774778e+07
sumBC
DSM board

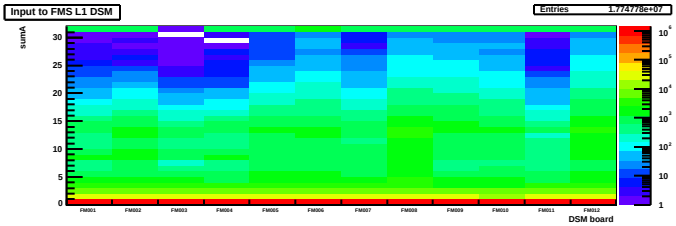

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

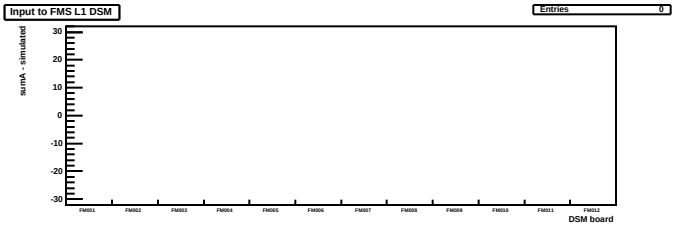

Input to FMS L1 DSM
Entries 1.774778e+07
sumB
DSM board

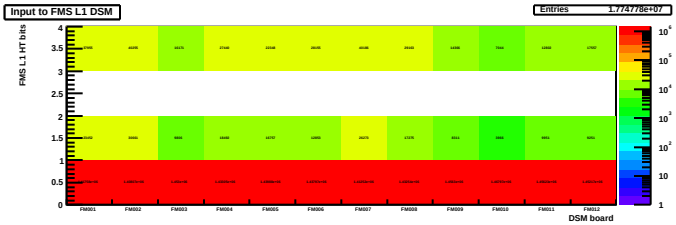

Input to FMS L1 DSM
Entries 2481
sumB - simulated
DSM board

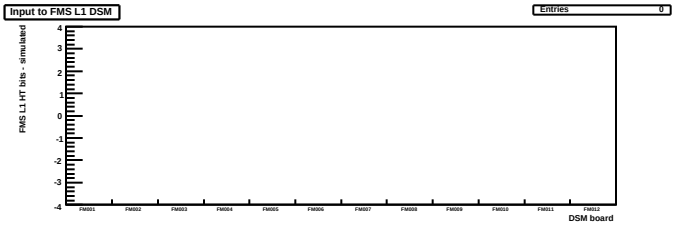

Input to FMS L1 DSM
Entries 1.774778e+07
sumCD
DSM board

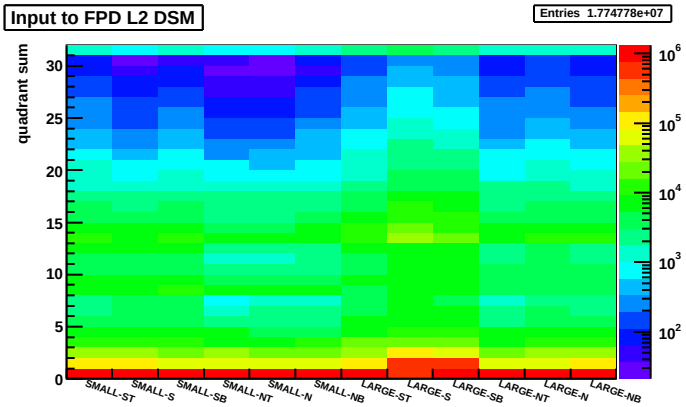

Input to FMS L1 DSM
Entries 29488
sumCD - simulated
DSM board

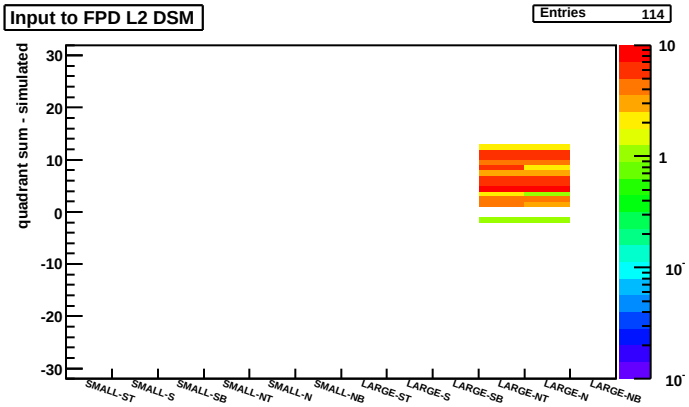

Input to FMS L1 DSM
Entries 1.774778e+07
sumA
DSM board

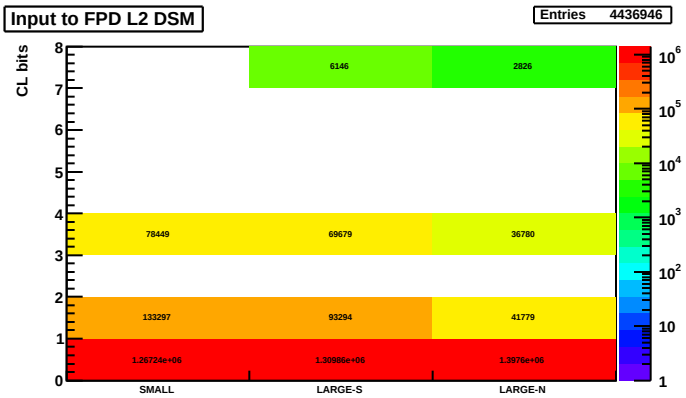

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

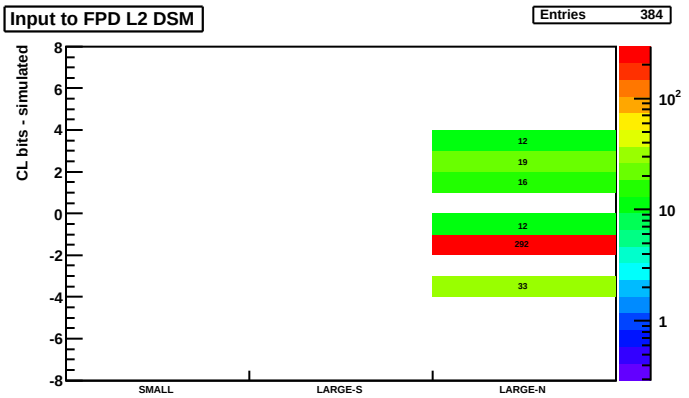

Input to FMS L1 DSM
Entries 1.774778e+07
FMS L1 HT bits
DSM board

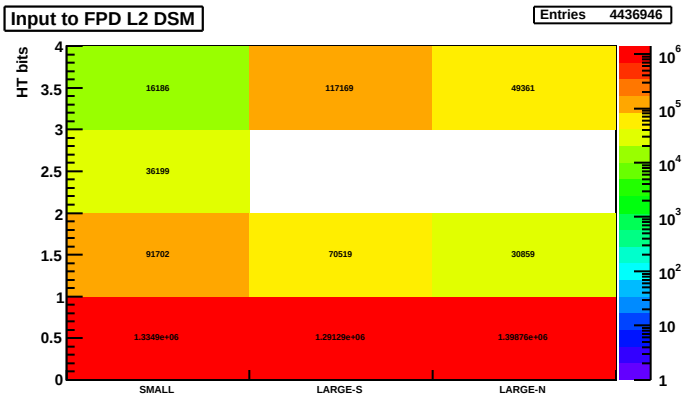

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

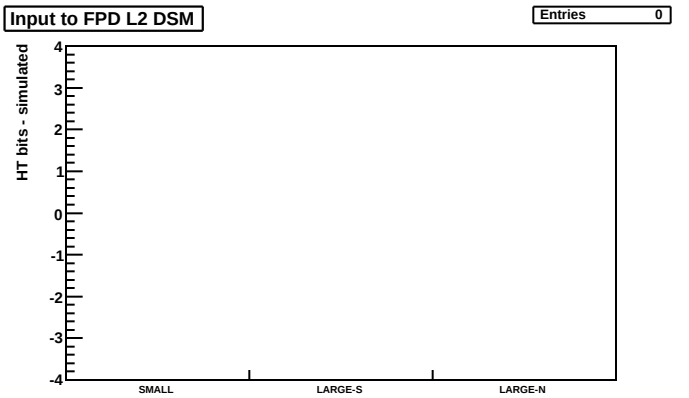

Input to FPD L2 DSM
Entries 1.774778e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

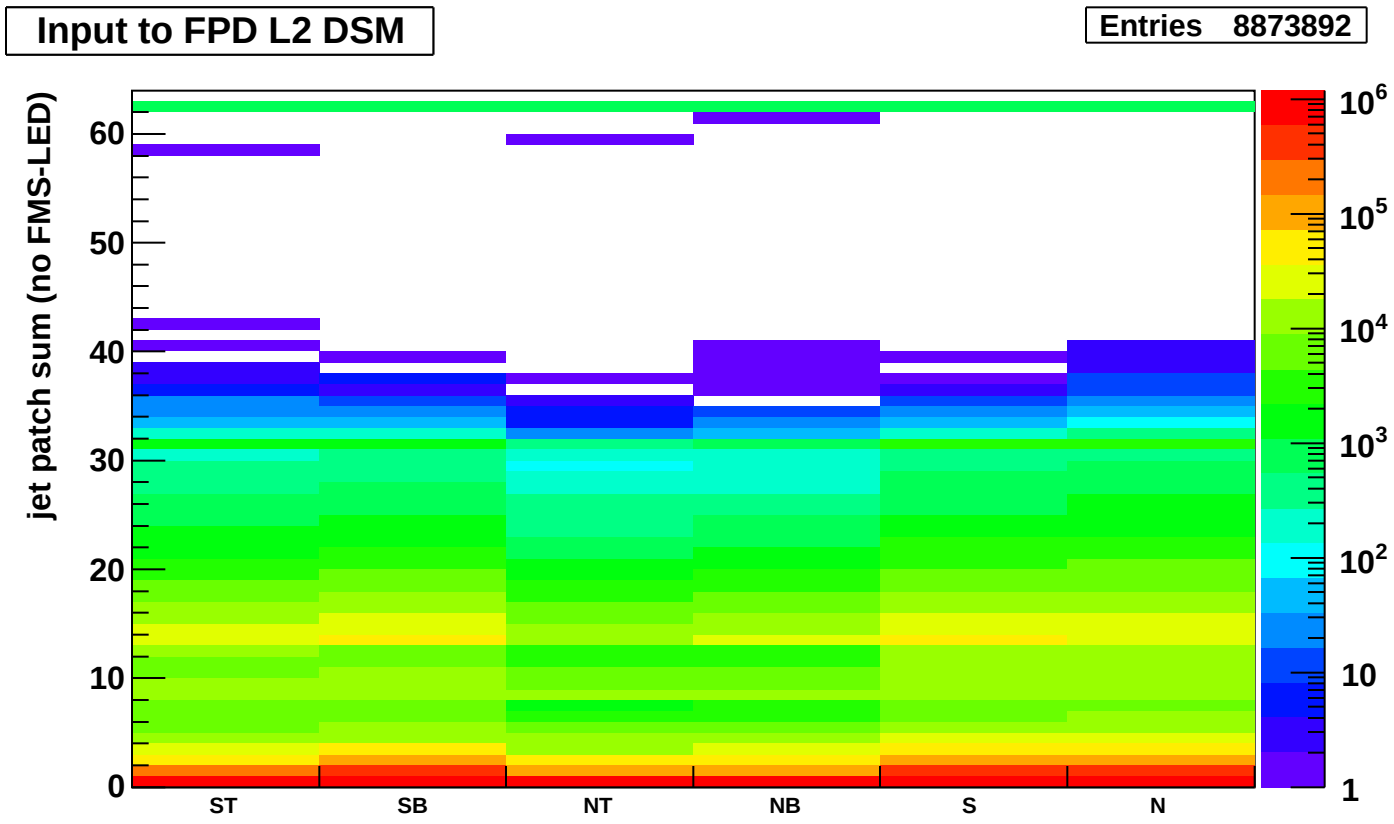

Input to FPD L2 DSM
Entries 114
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

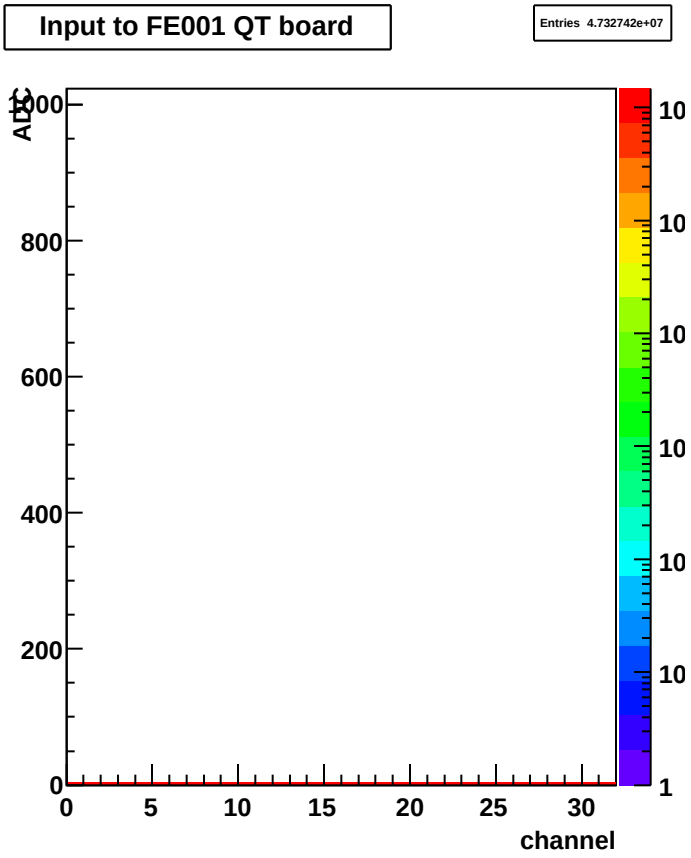

Input to FPD L2 DSM
Entries 4436946
CL bits
6146
2826
78449
69679
36780
133297
93294
41779
1.26724e+06
1.30986e+06
1.3976e+06
SMALL
LARGE-S
LARGE-N

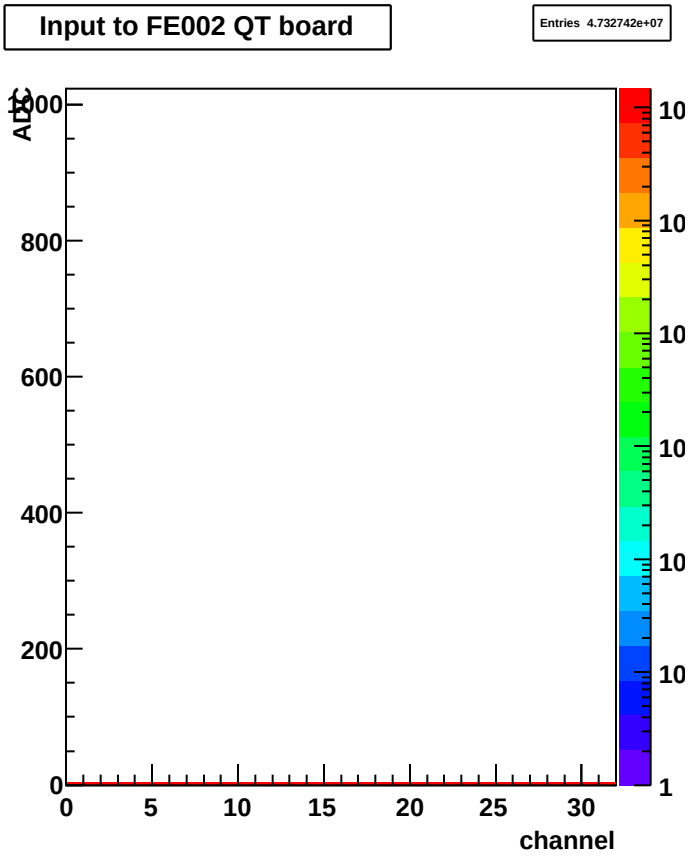

Input to FPD L2 DSM
Entries 384
CL bits - simulated
12
19
16
12
292
33
SMALL
LARGE-S
LARGE-N

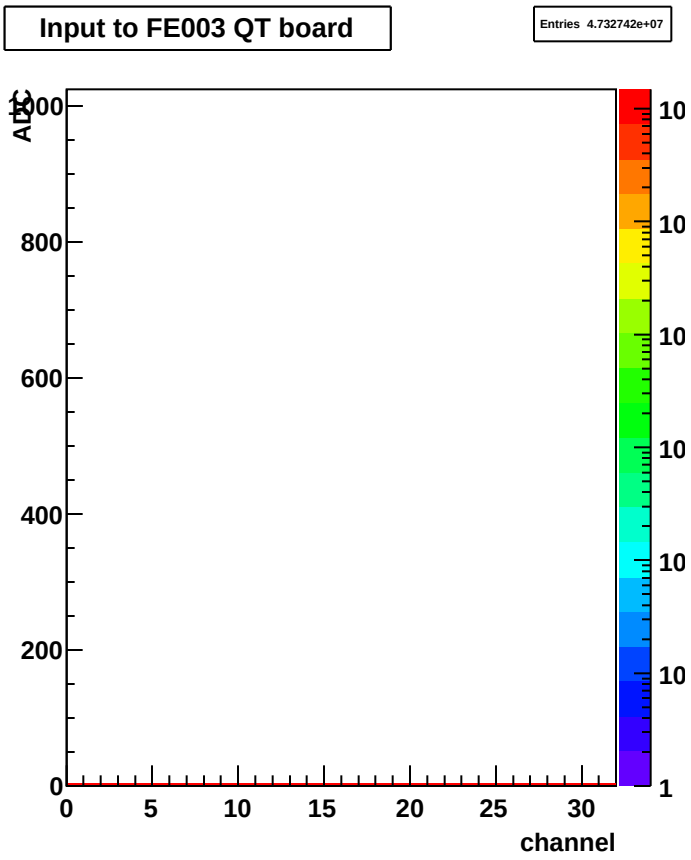

Input to FPD L2 DSM
Entries 4436946
HT bits
16186
117169
49361
36199
91702
70519
30859
1.3349e+06
1.29129e+06
1.39876e+06
SMALL
LARGE-S
LARGE-N

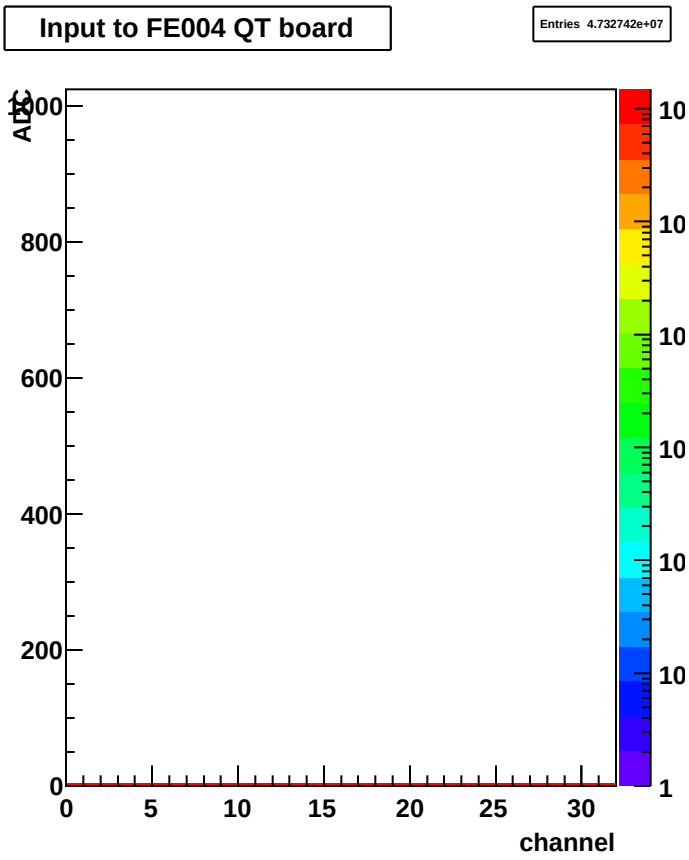

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

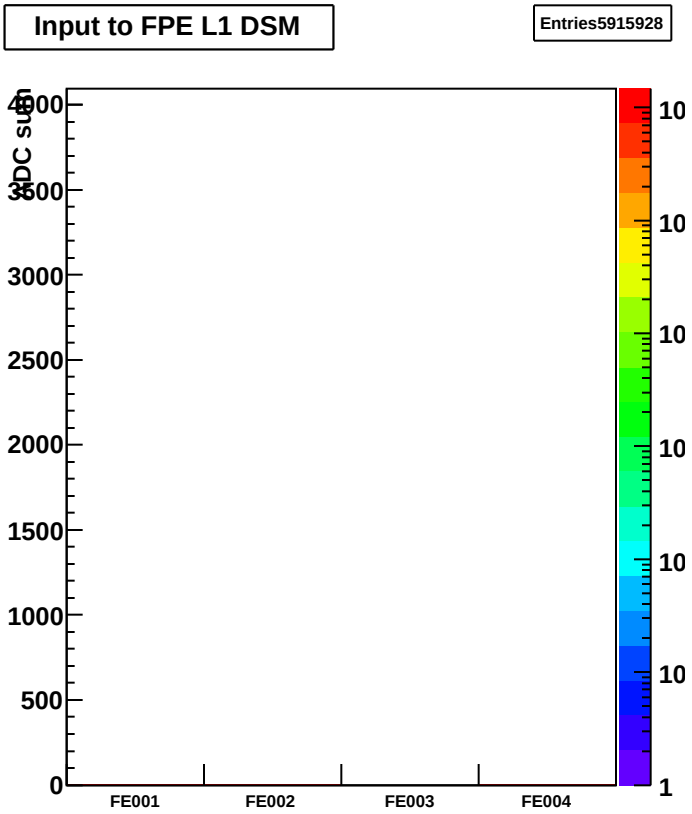

Input to FPD L2 DSM
Entries 8873892
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

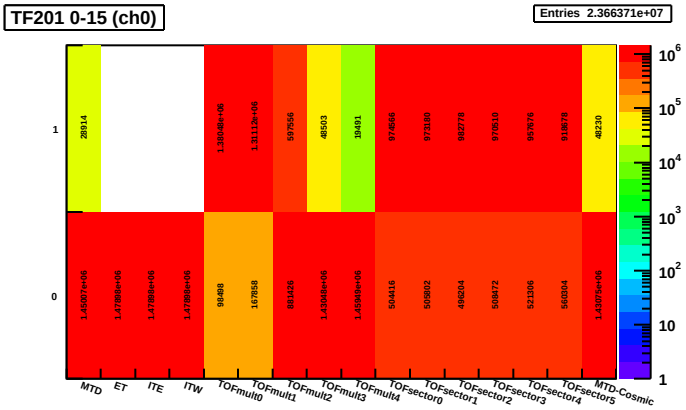

Input to FE001 QT board
Entries 4.732742e+07
ADC
channel

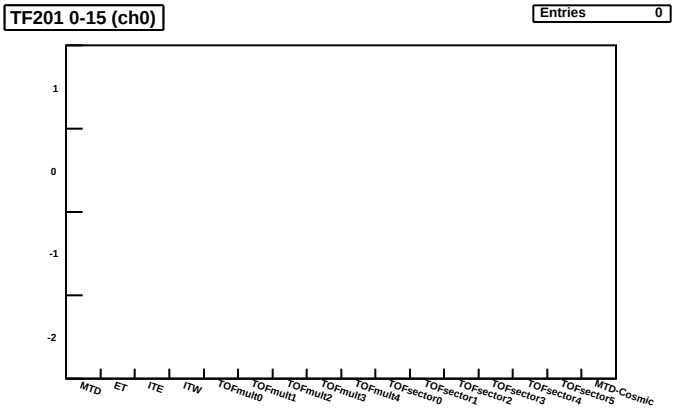

Input to FE002 QT board
Entries 4.732742e+07
ADC
channel

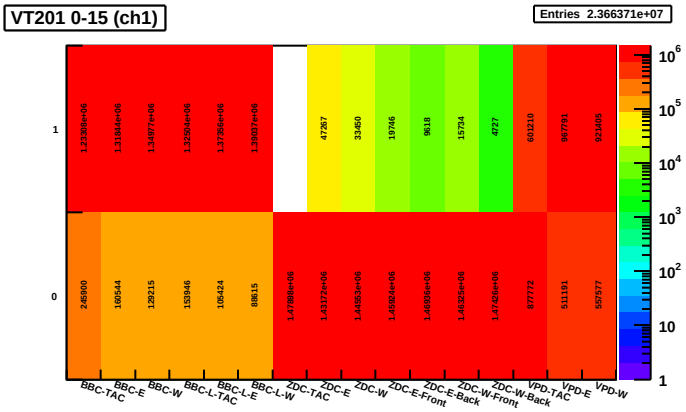

Input to FE003 QT board
Entries 4.732742e+07
ADC
channel


Input to FE004 QT board
Entries 4.732742e+07
ADC
channel


Input to FPE L1 DSM
Entries5915928
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 2.366371e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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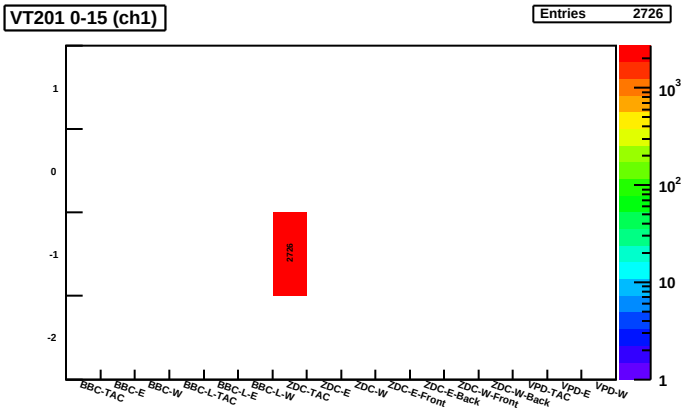
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2726
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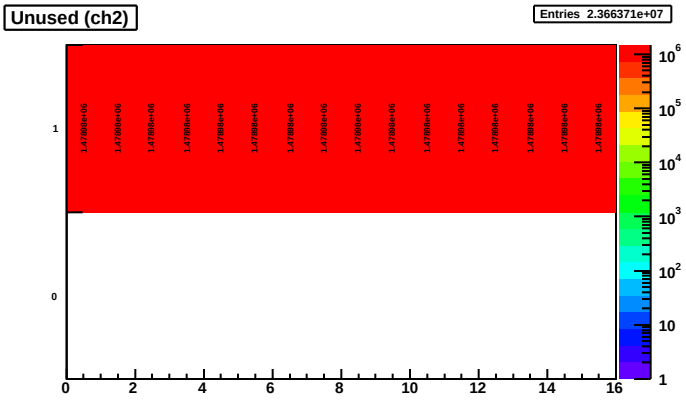
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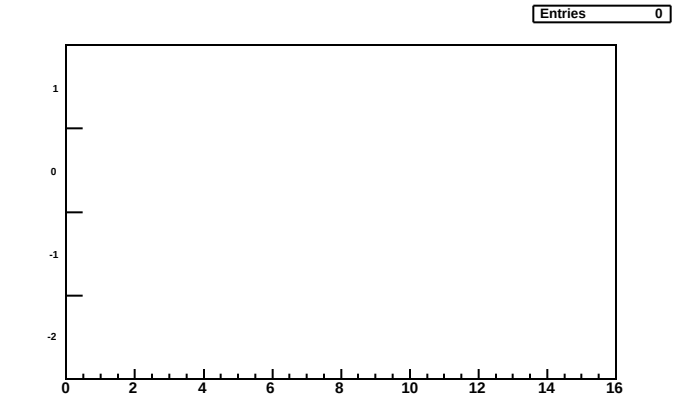
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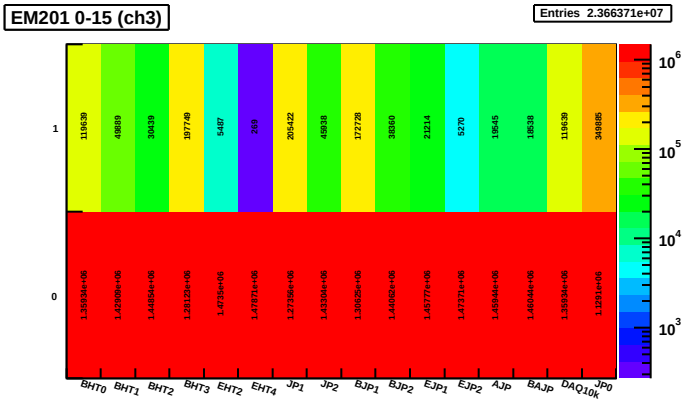
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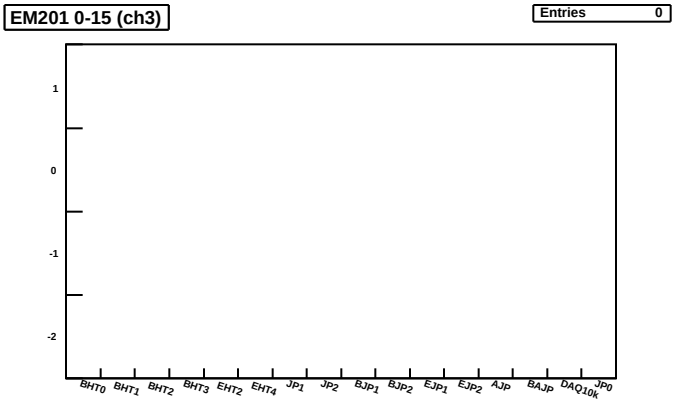
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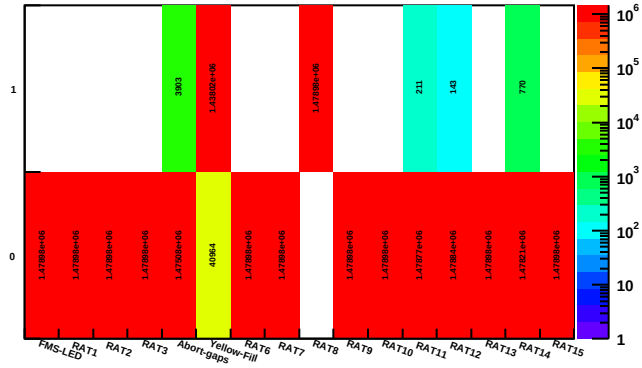
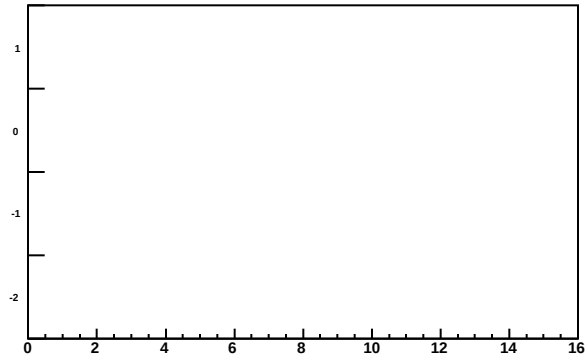
RAT board (ch4)





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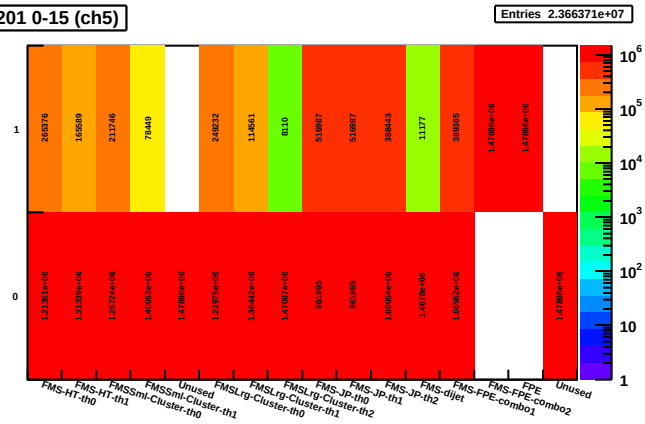

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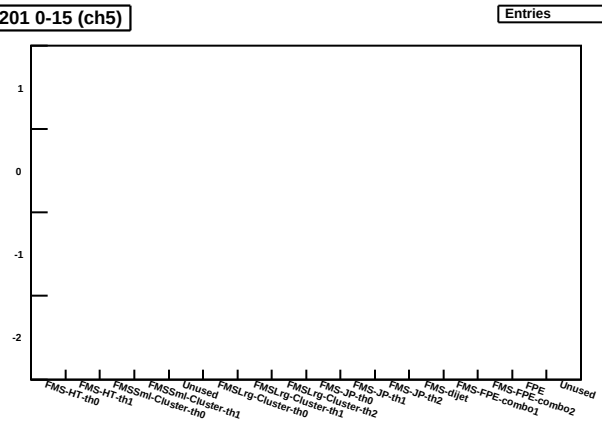
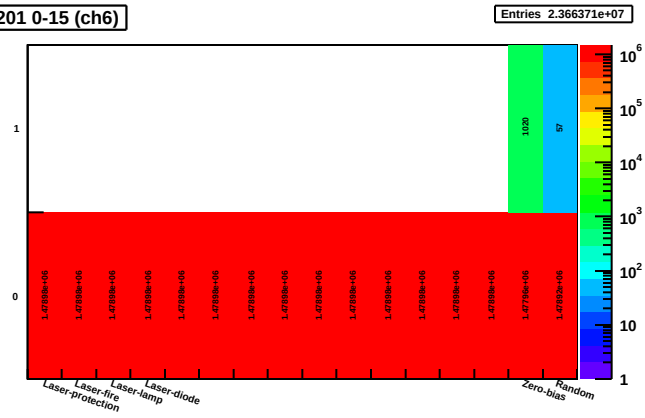
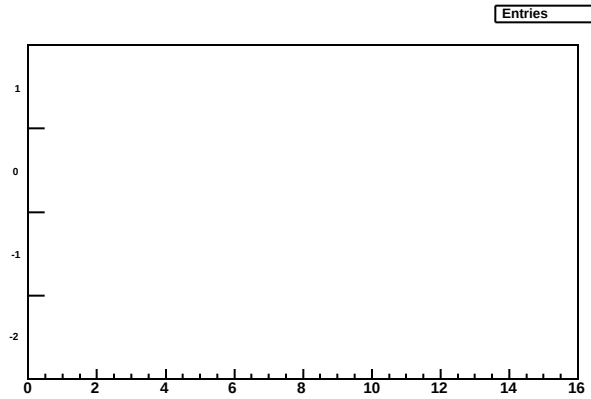
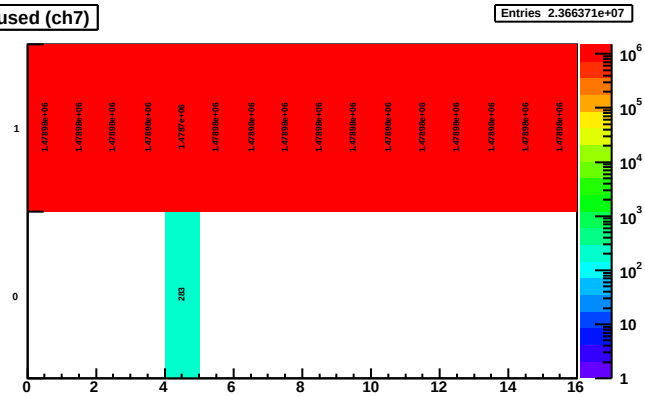
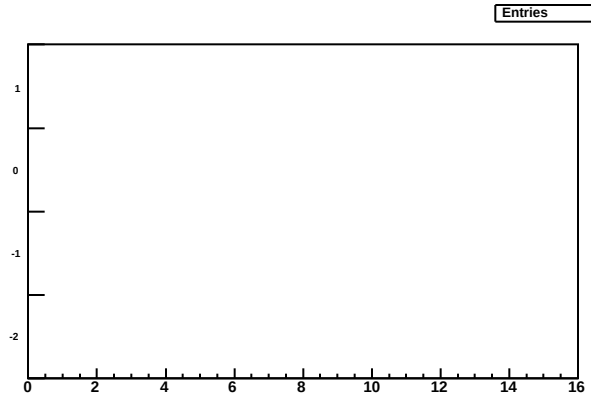


ST201 0-15 (ch6)





Unused (ch7)