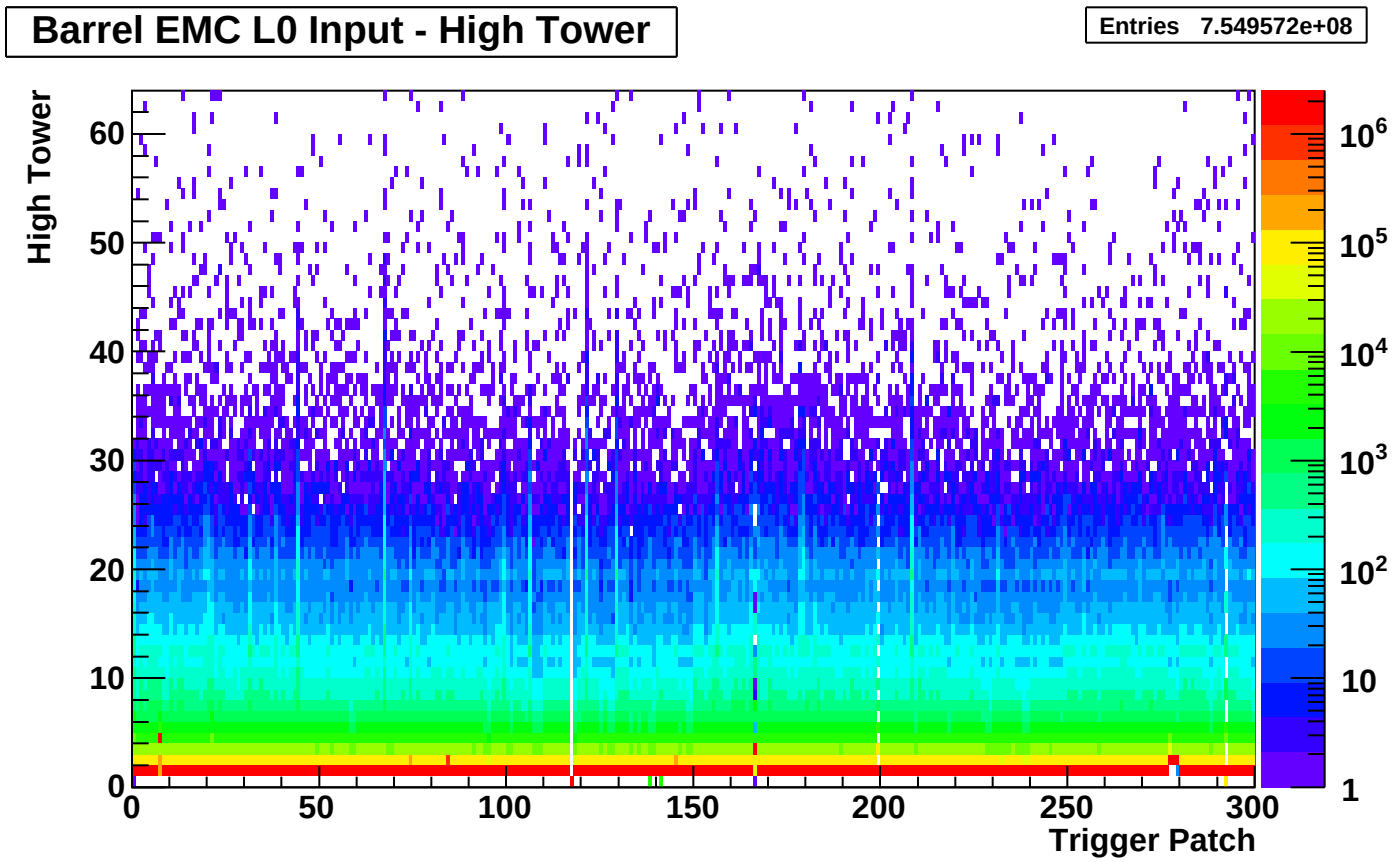
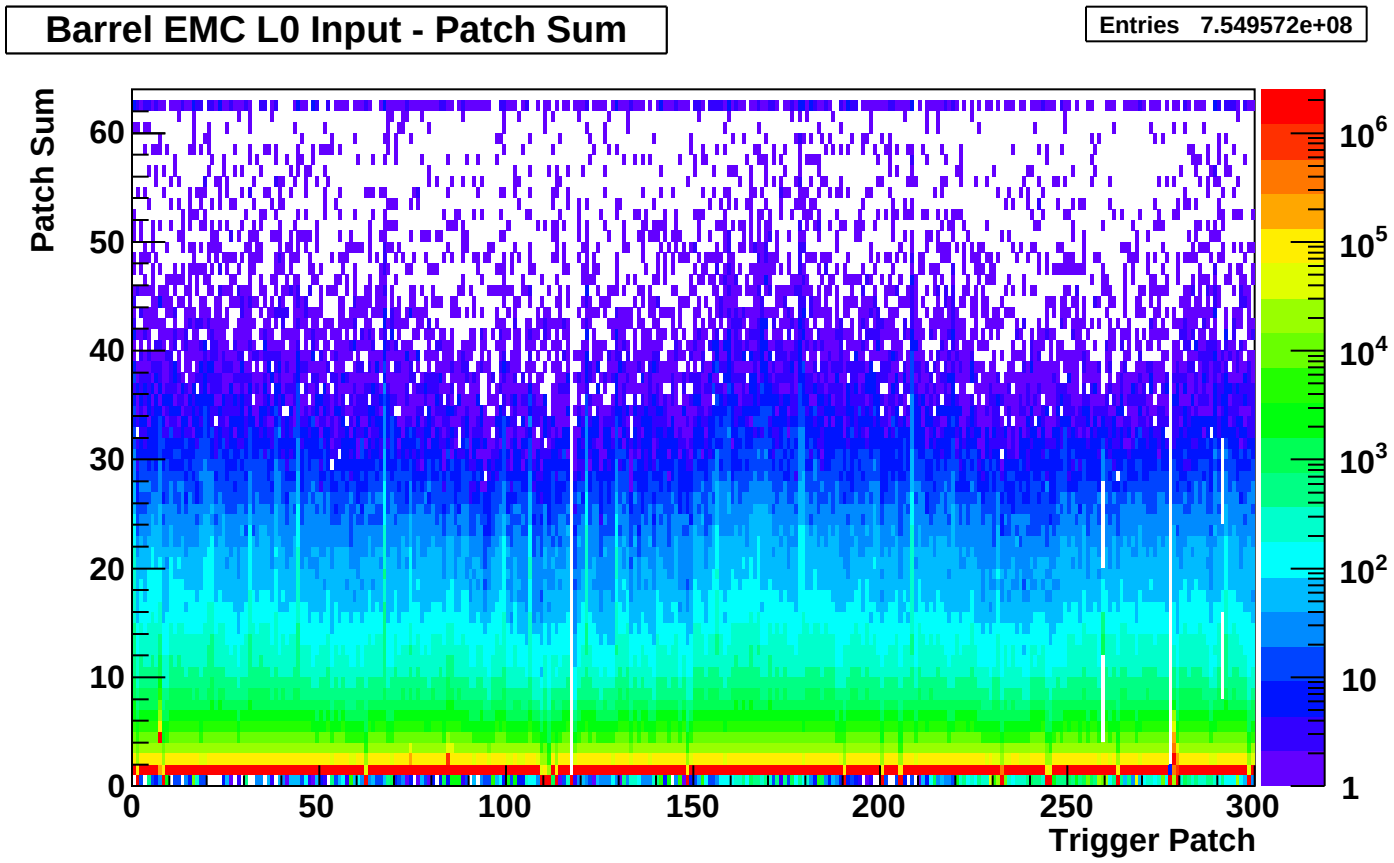
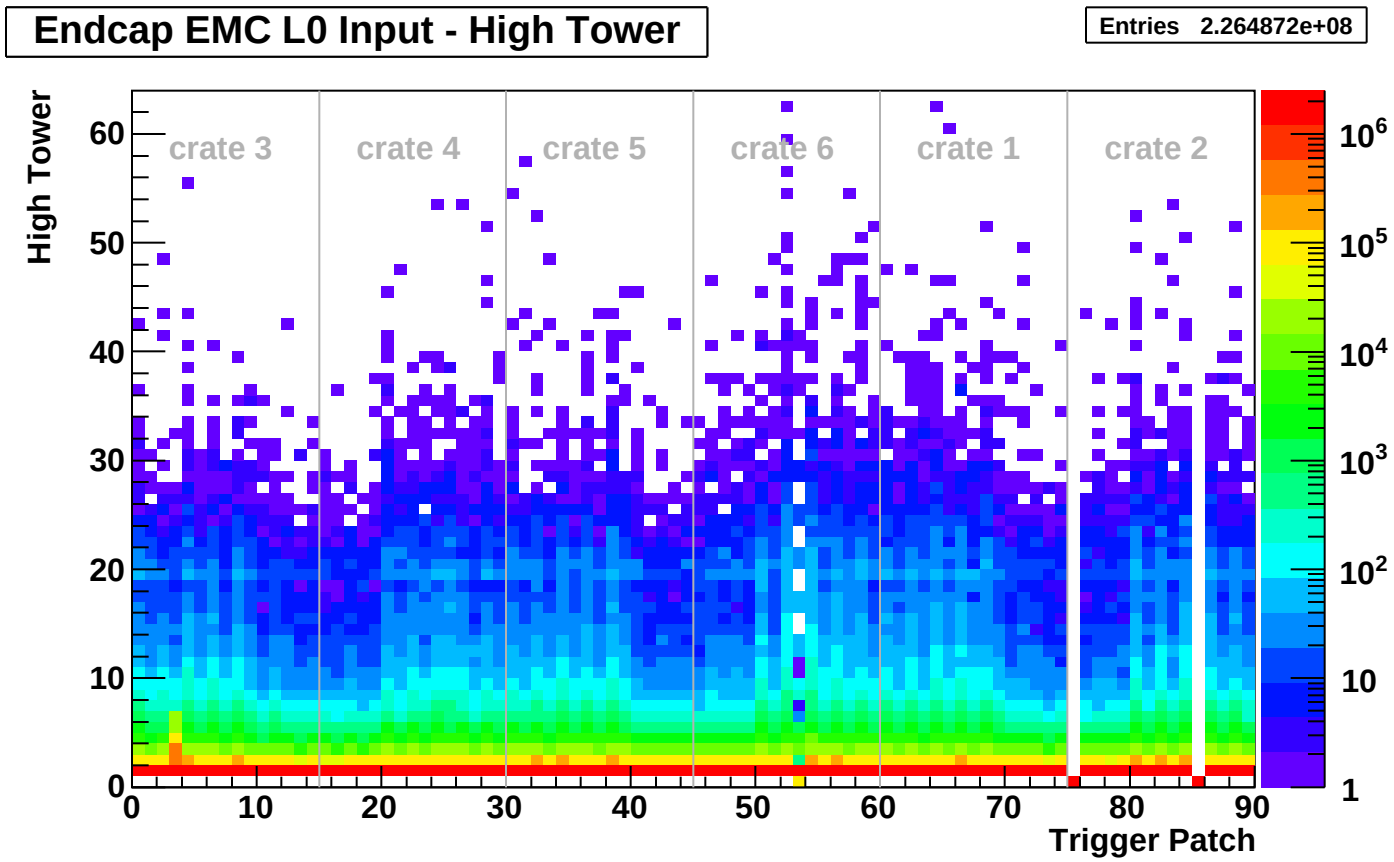
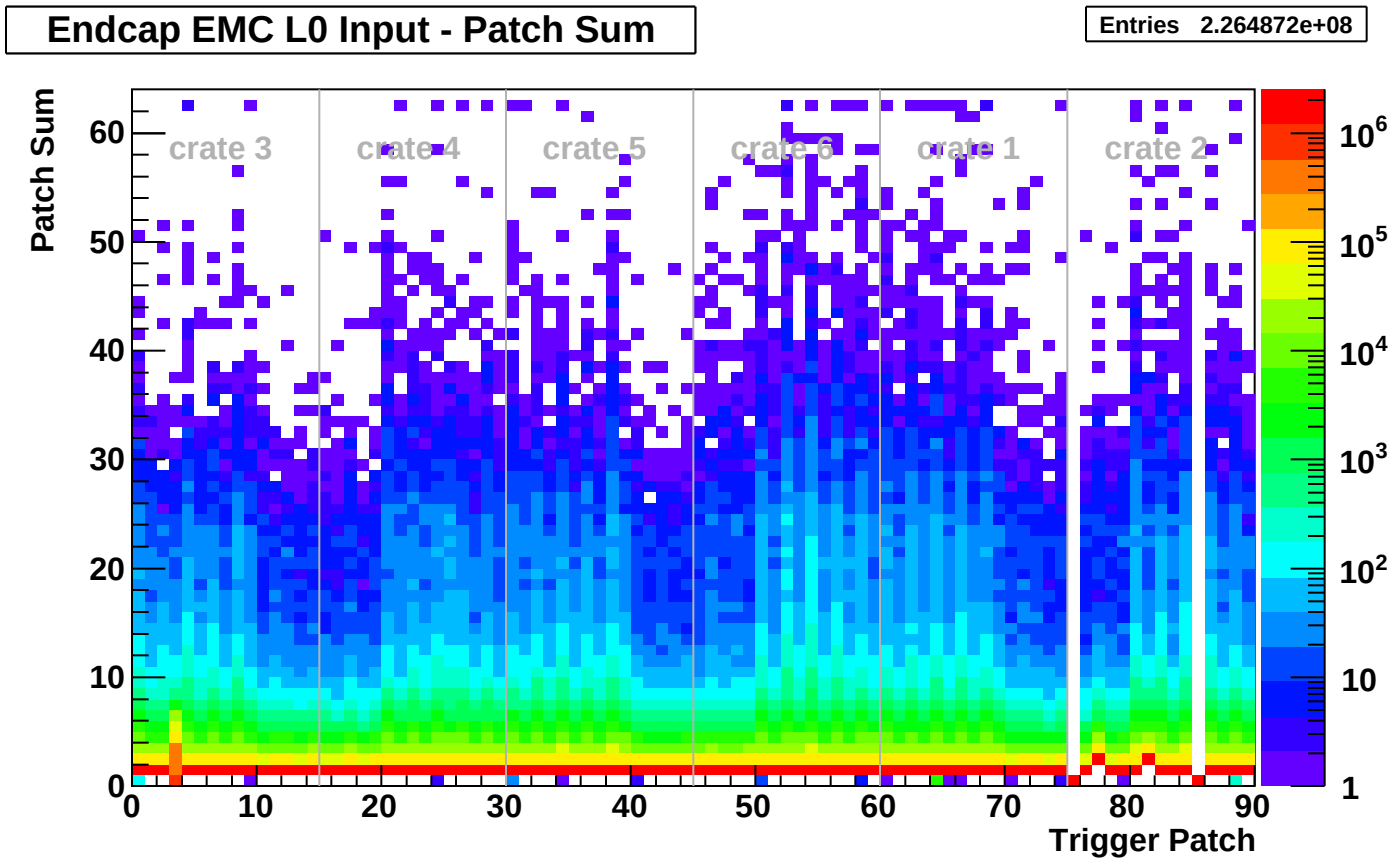
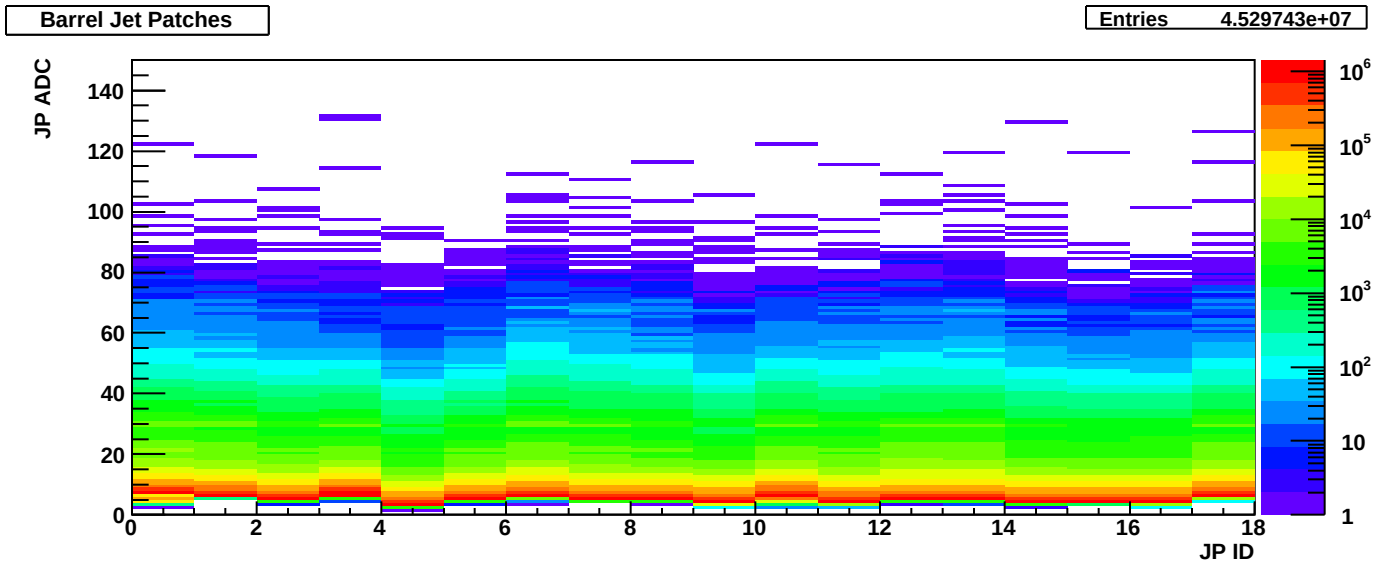
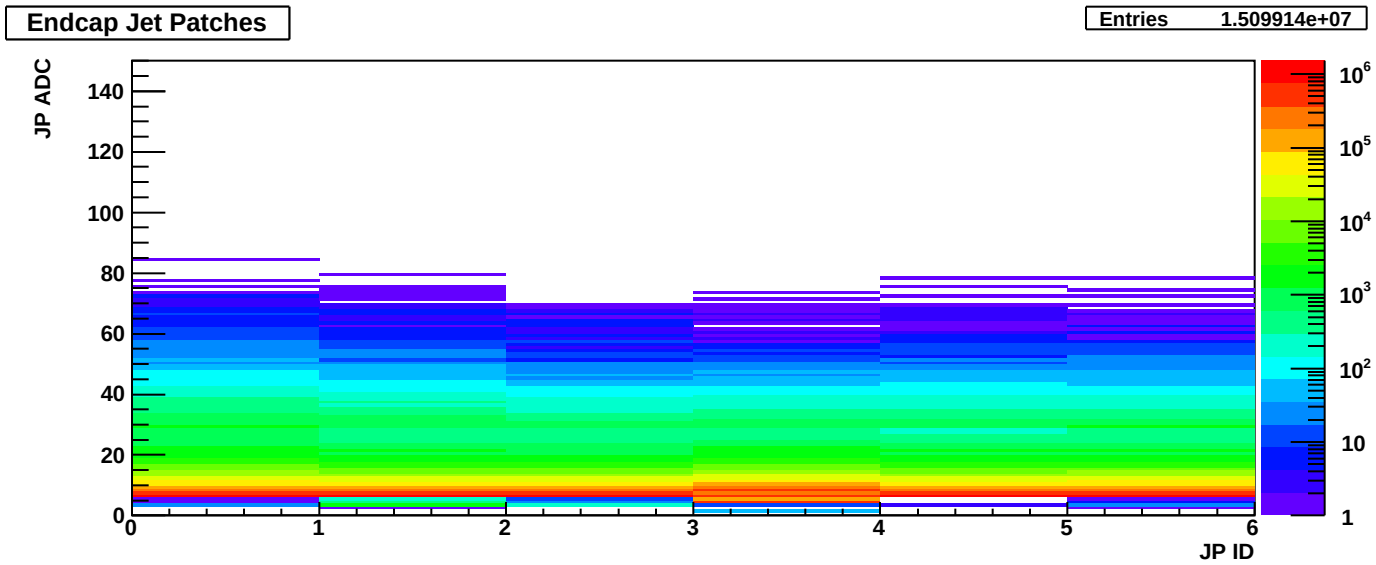

Barrel EMC L0 Input - High Tower
Entries 7.549572e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

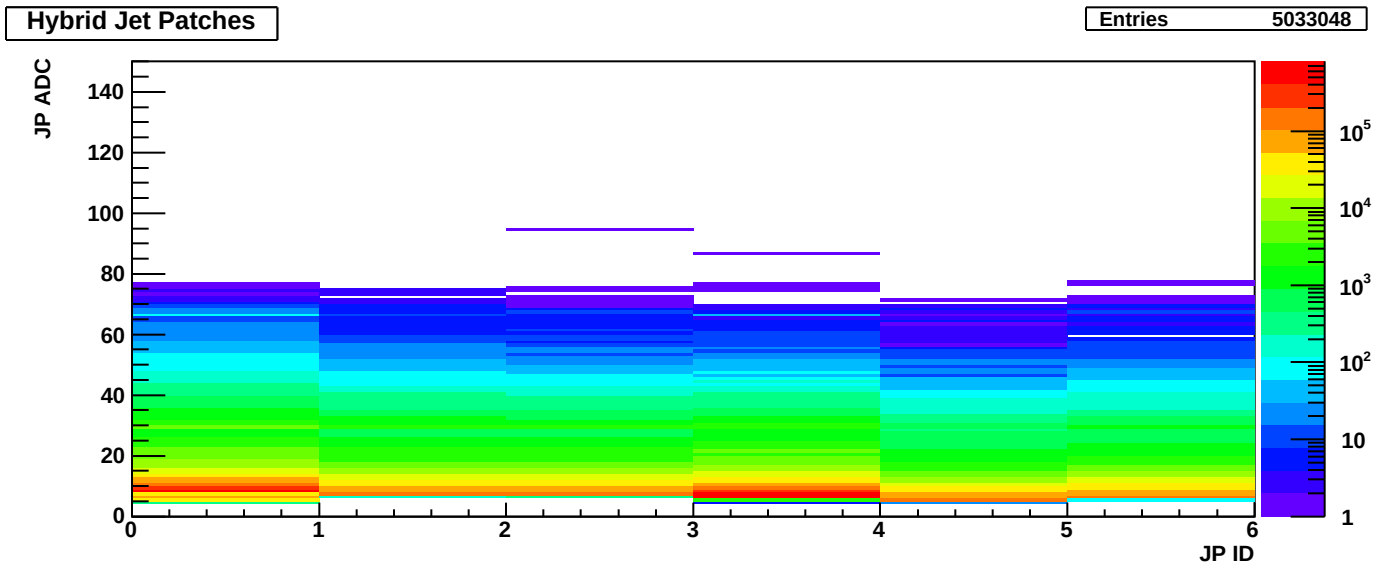

Barrel EMC L0 Input - Patch Sum
Entries 7.549572e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

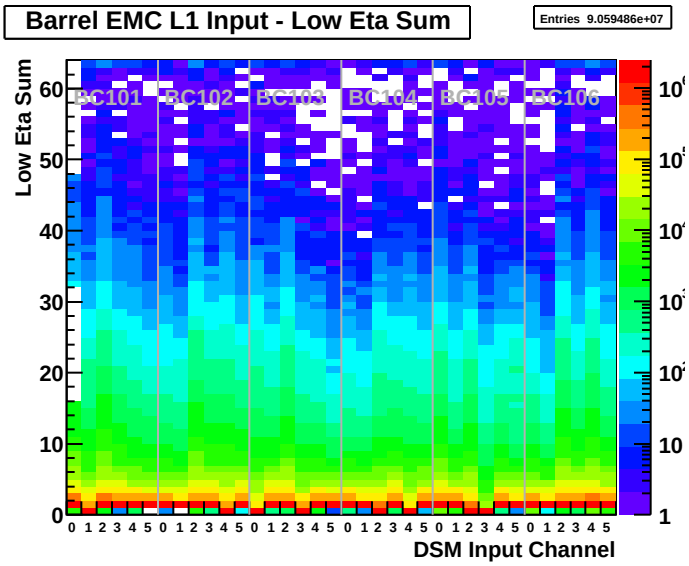

Endcap EMC L0 Input - High Tower
Entries 2.264872e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

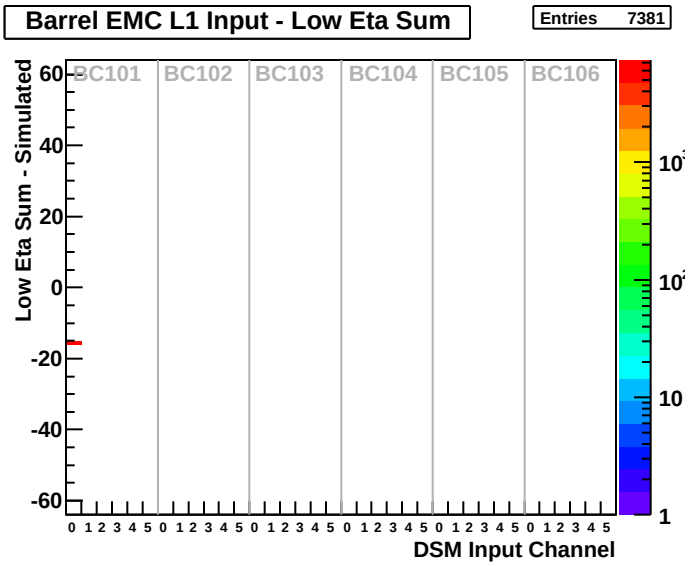

Endcap EMC L0 Input - Patch Sum
Entries 2.264872e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

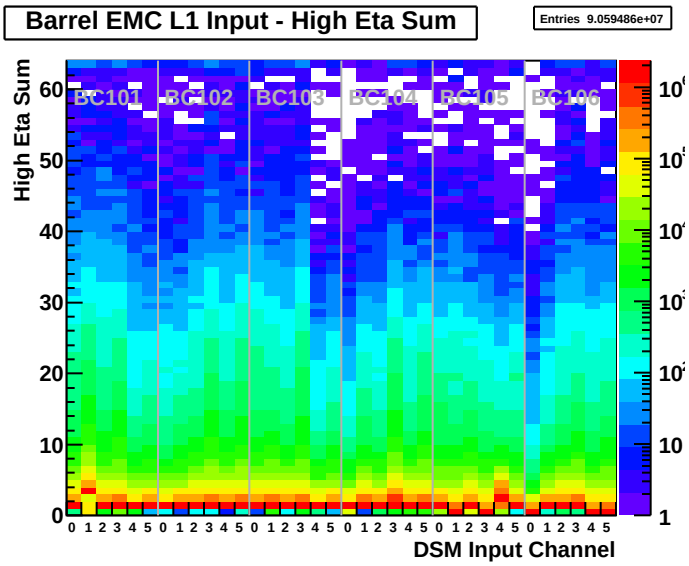

Barrel Jet Patches
Entries 4.529743e+07
JP ADC
JP ID

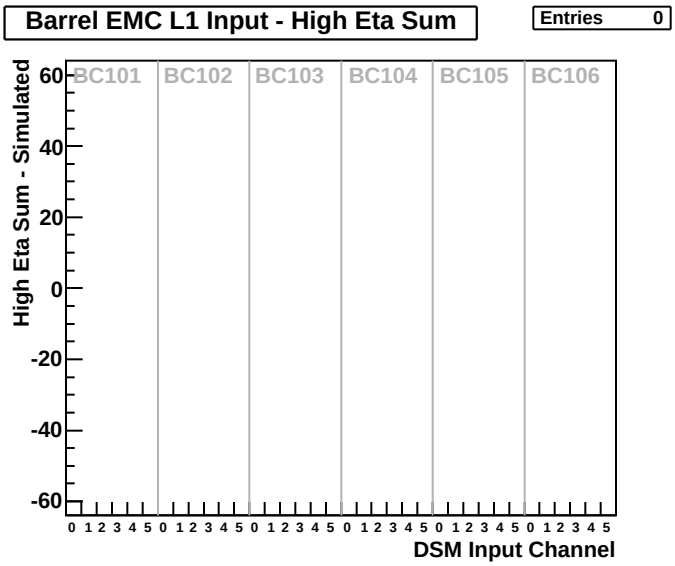

Endcap Jet Patches
Entries 1.509914e+07
JP ADC
JP ID

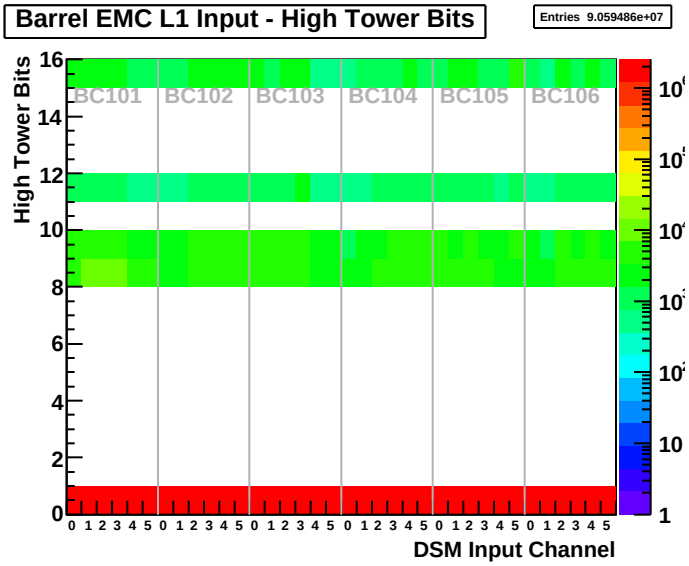

Hybrid Jet Patches
Entries 5033048
JP ADC
JP ID

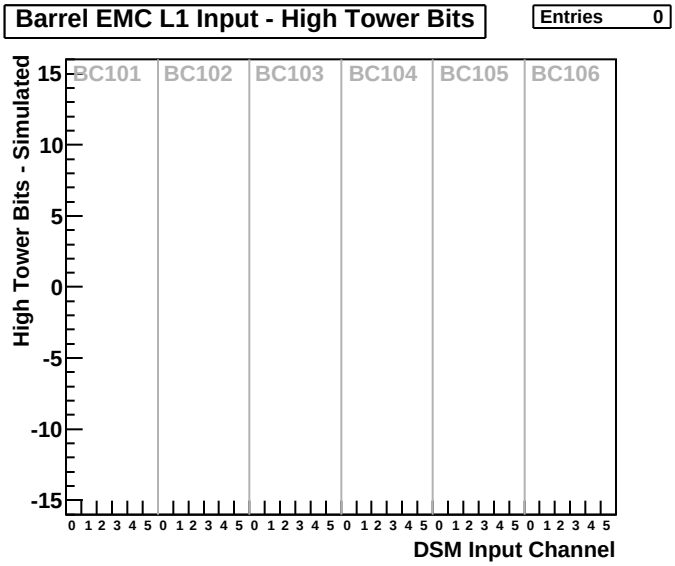

Barrel EMC L1 Input - Low Eta Sum
Entries 9.059486e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

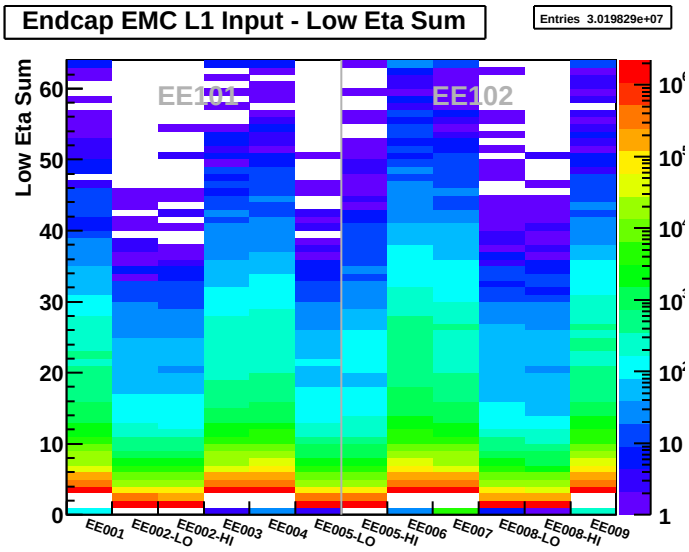

Barrel EMC L1 Input - Low Eta Sum
Entries 7381
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

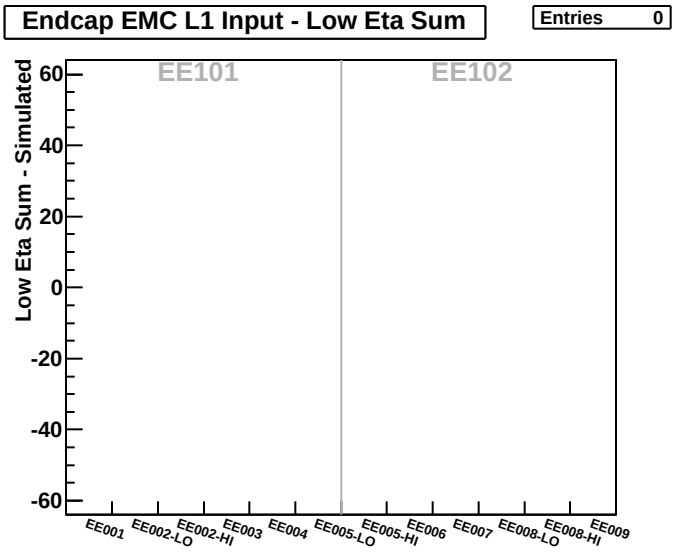

Barrel EMC L1 Input - High Eta Sum
Entries 9.059486e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

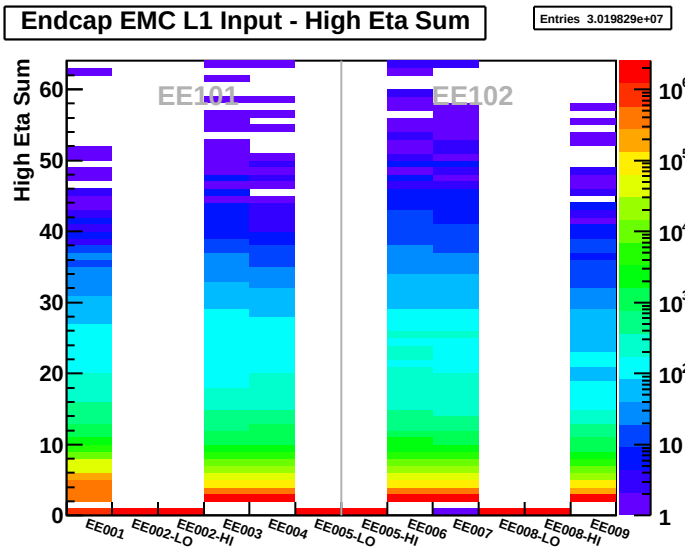

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

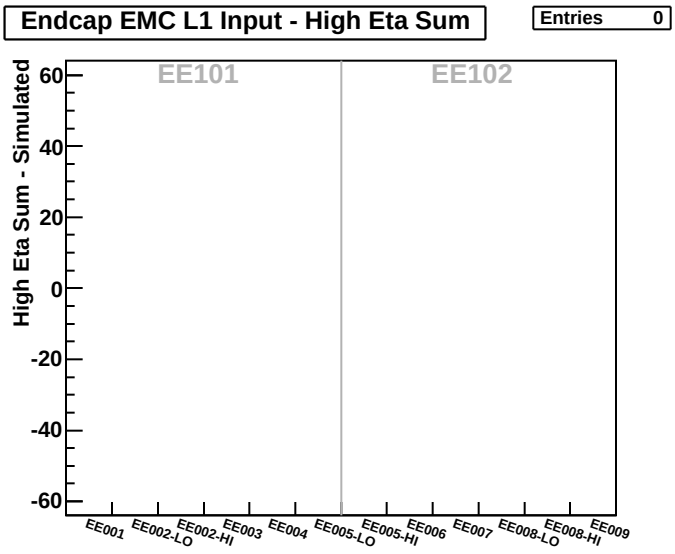

Barrel EMC L1 Input - High Tower Bits
Entries 9.059486e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

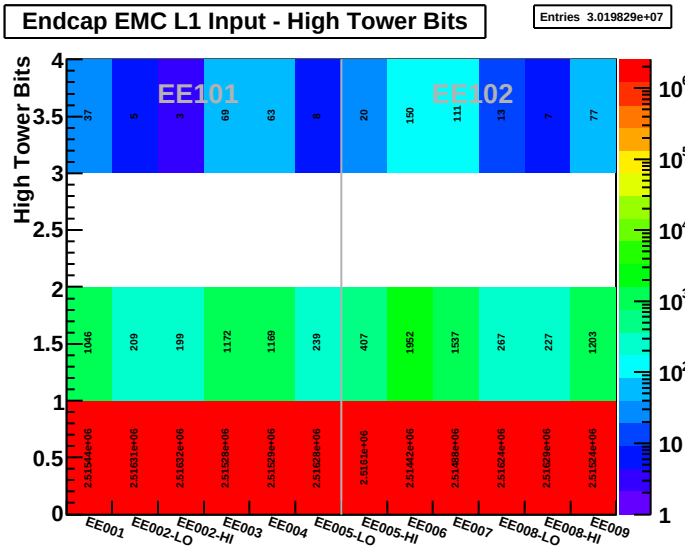

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

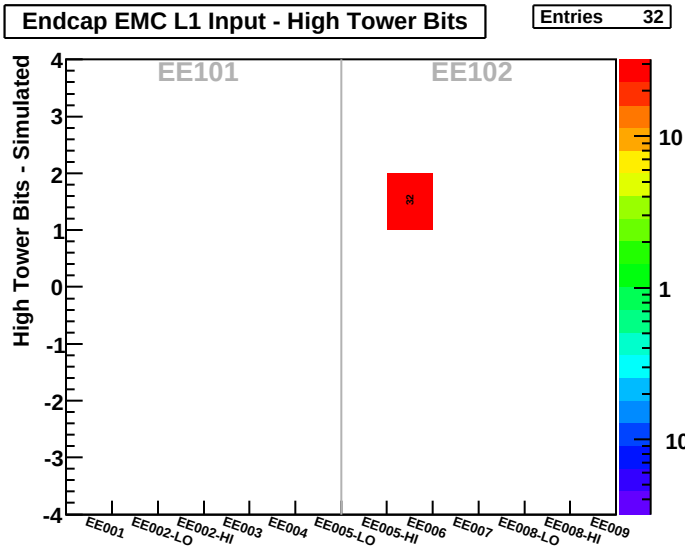

Endcap EMC L1 Input - Low Eta Sum
Entries 3.019829e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

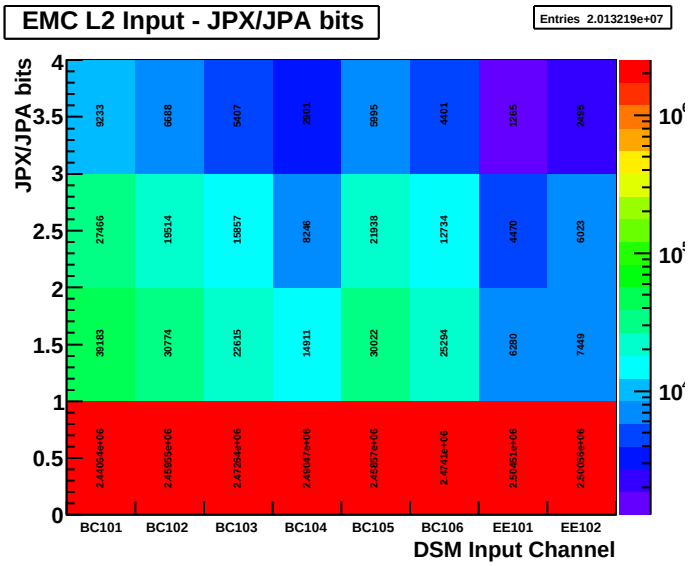

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

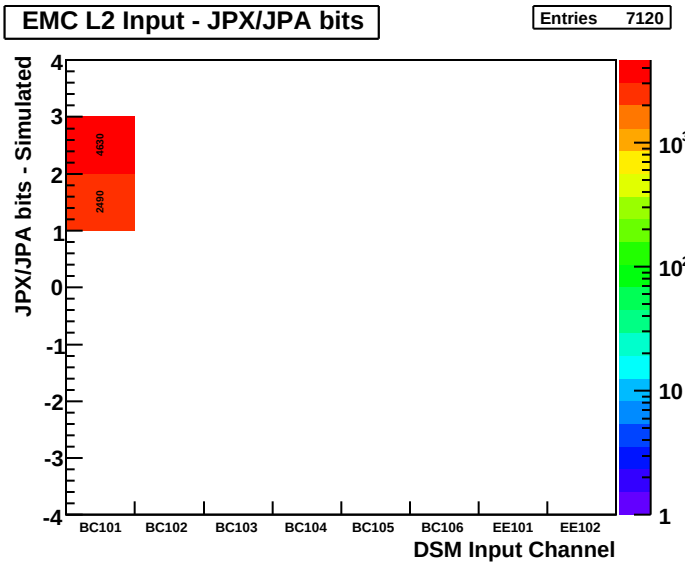

Endcap EMC L1 Input - High Eta Sum
Entries 3.019829e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

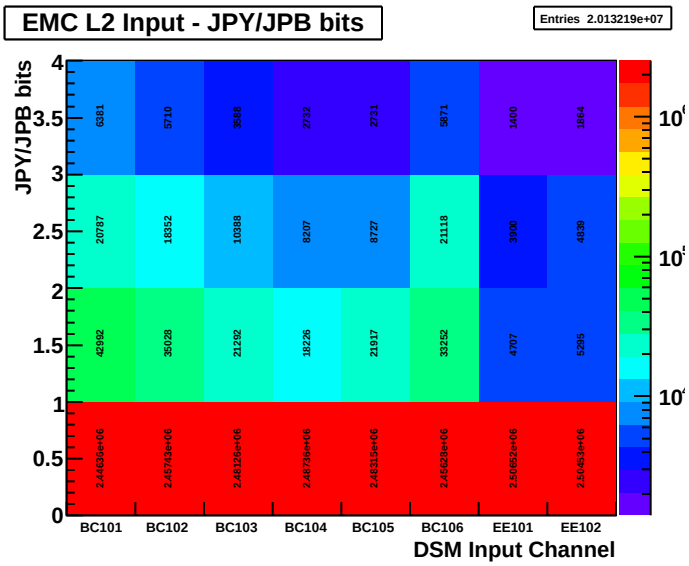

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

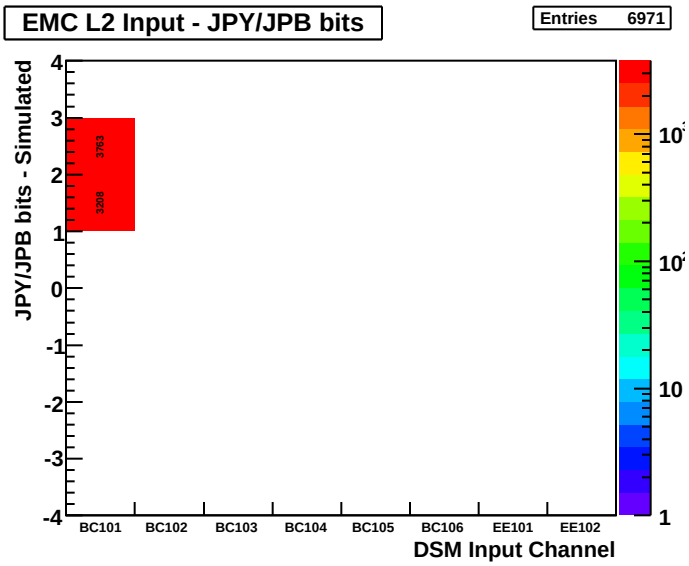

Endcap EMC L1 Input - High Tower Bits
Entries 3.019829e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

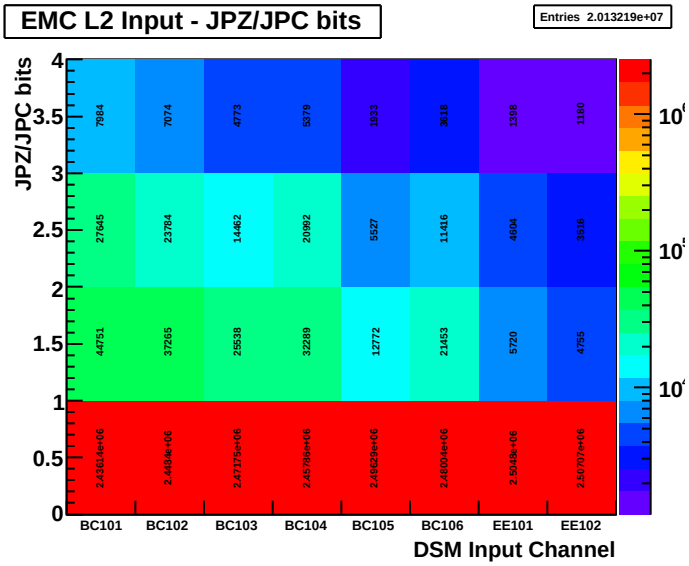

Endcap EMC L1 Input - High Tower Bits
Entries 32
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

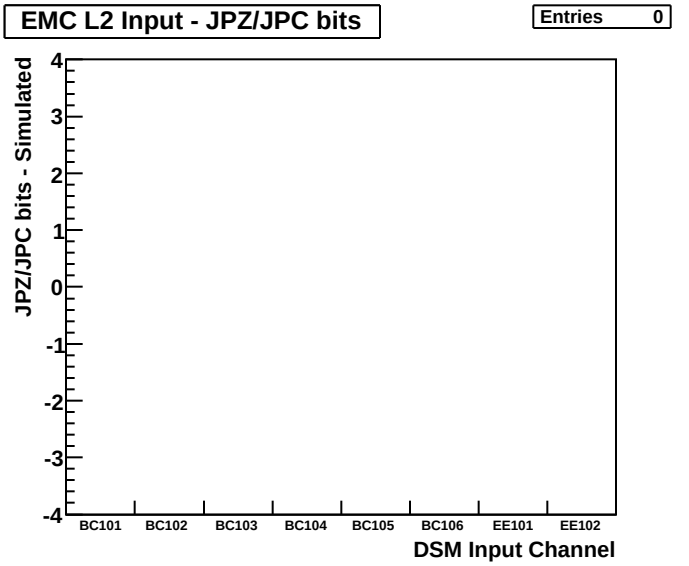

EMC L2 Input - JPX/JPA bits
Entries 2.013219e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
9233 6688 5407 2901 5995 4401 1265 2495
27466 19514 15857 8246 21938 12734 4470 6023
39183 30774 22615 14911 30022 25294 6280 7449
2.44064e+06 2.45955e+06 2.47264e+06 2.49047e+06 2.45857e+06 2.4741e+06 2.50451e+06 2.50056e+06

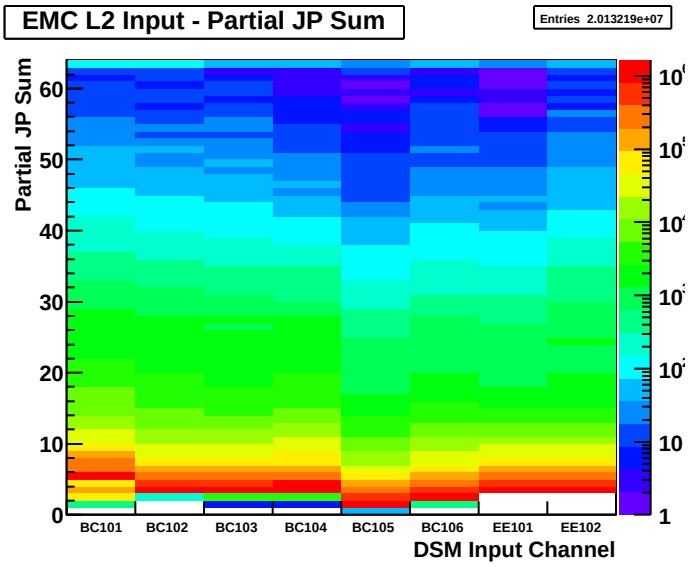

EMC L2 Input - JPX/JPA bits
Entries 7120
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4630
2490

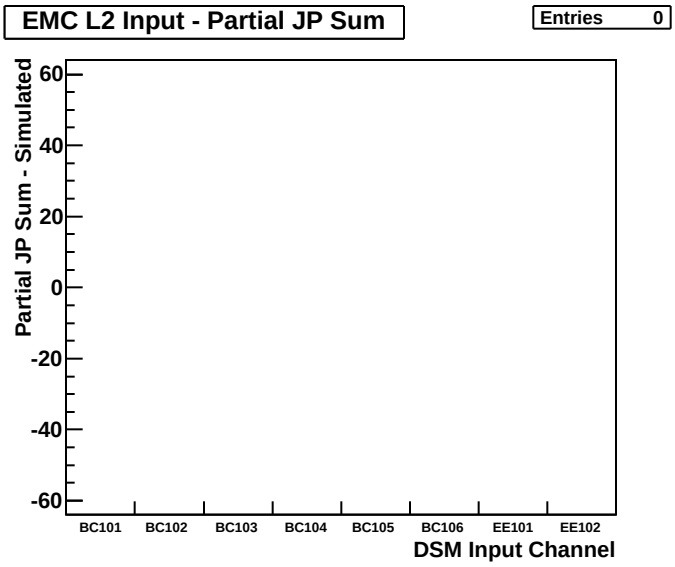

EMC L2 Input - JPY/JPB bits
Entries 2.013219e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6381 5710 3588 2732 2731 5871 1400 1864
20787 18352 10388 8207 8727 21118 3900 4839
42992 35028 21292 18226 21917 33252 4707 5295
2.44636e+06 2.45743e+06 2.48126e+06 2.48736e+06 2.48315e+06 2.45628e+06 2.50652e+06 2.50453e+06

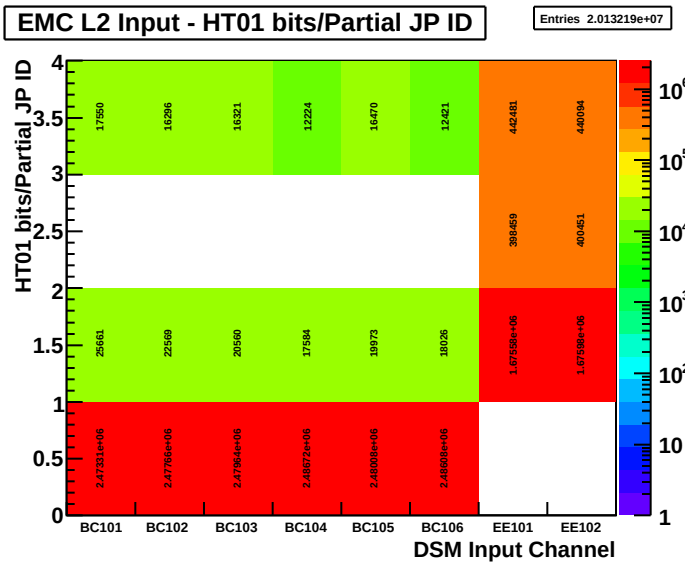

EMC L2 Input - JPY/JPB bits
Entries 6971
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3763
3208

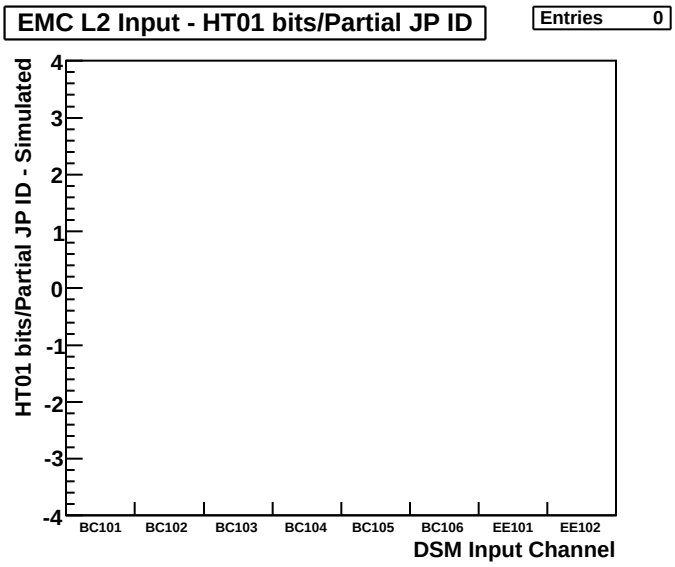

EMC L2 Input - JPZ/JPC bits
Entries 2.013219e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
7984 7074 4773 5379 1933 3618 1398 1180
27645 23784 14462 20992 5527 11416 4604 3516
44751 37265 25538 32289 12772 21453 5720 4755
2.43614e+06 2.448e+06 2.47175e+06 2.45786e+06 2.49629e+06 2.48004e+06 2.5048e+06 2.50707e+06

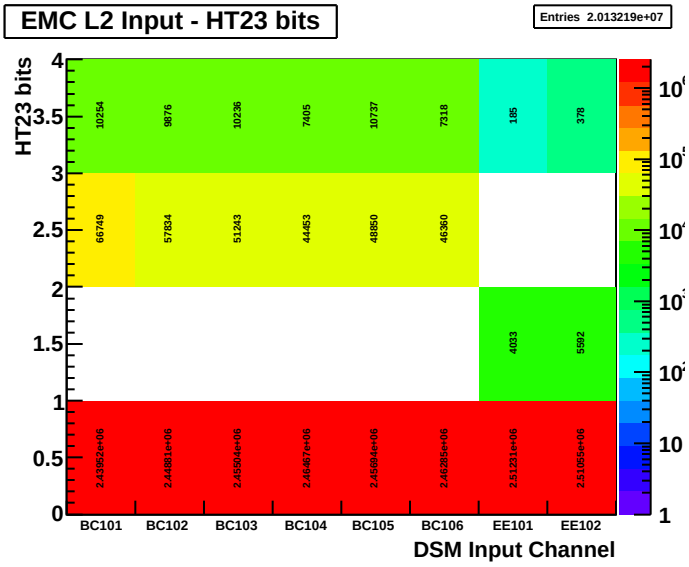

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

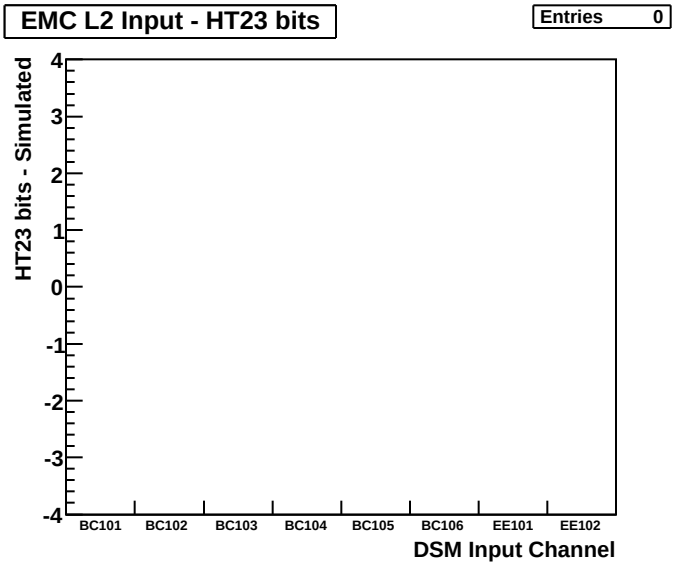

EMC L2 Input - Partial JP Sum
Entries 2.013219e+07
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

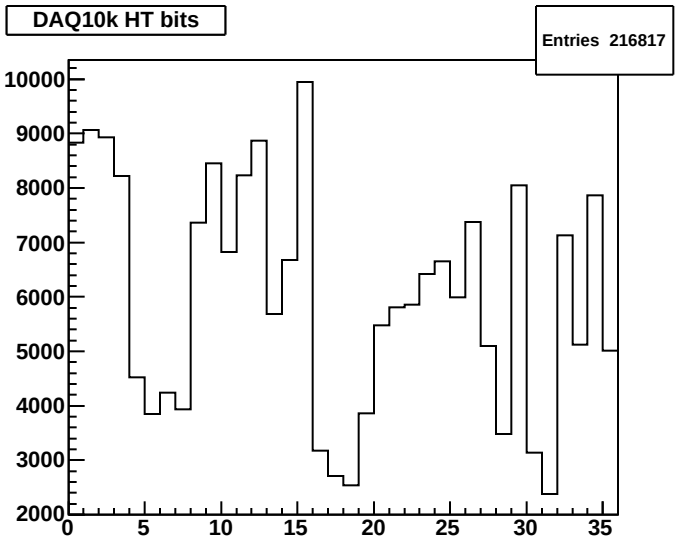

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

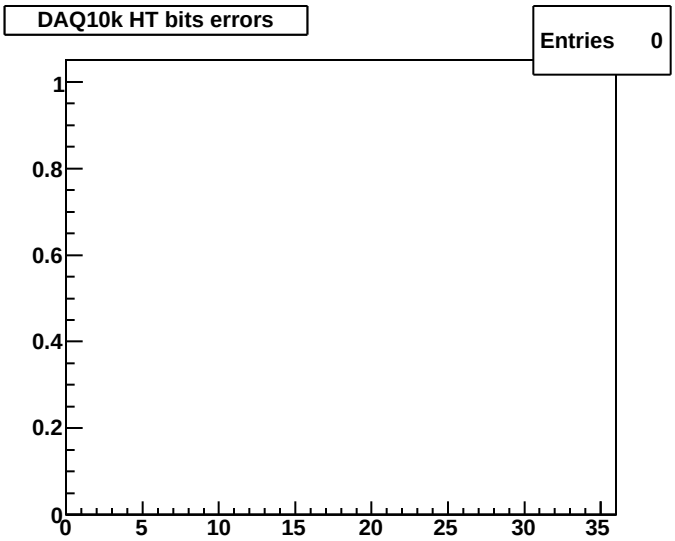

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2.013219e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

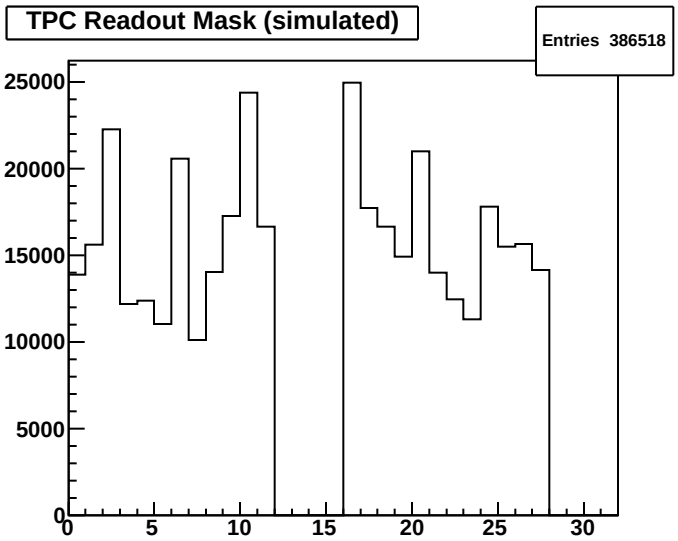

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

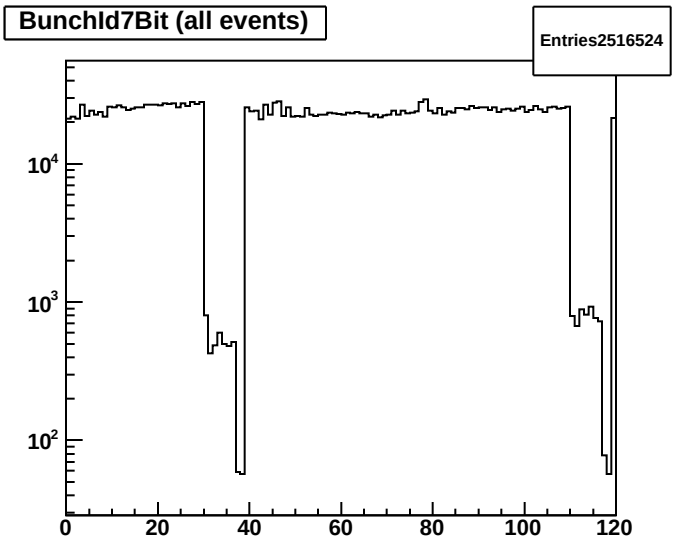

EMC L2 Input - HT23 bits
Entries 2.013219e+07
HT23 bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

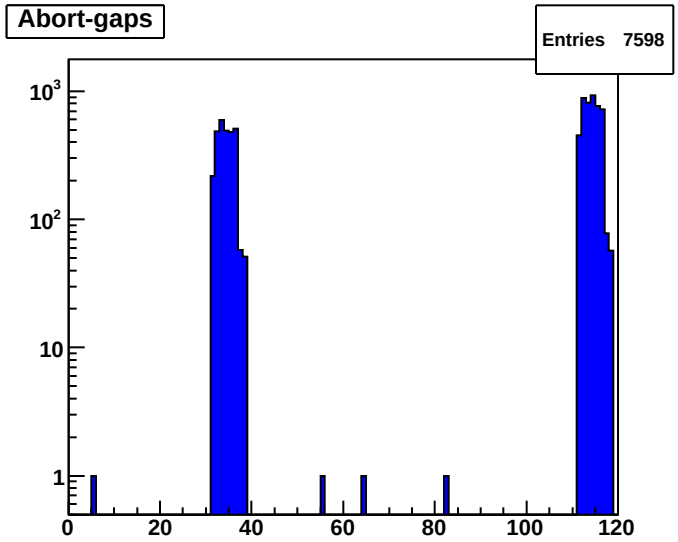

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

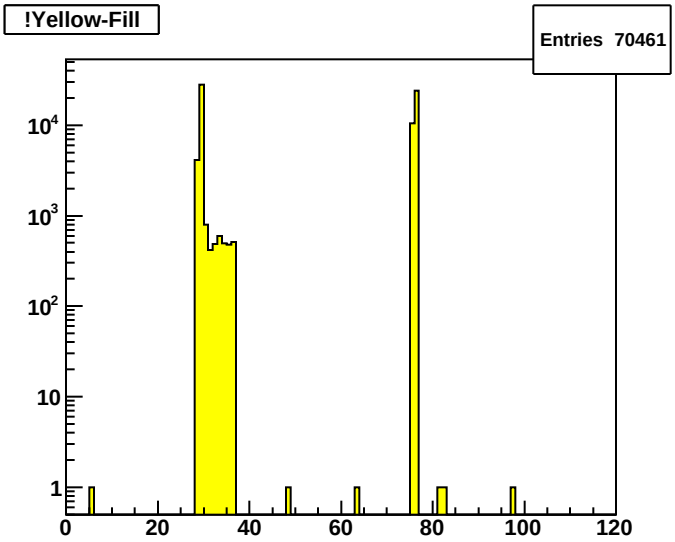

DAQ10k HT bits
Entries 216817

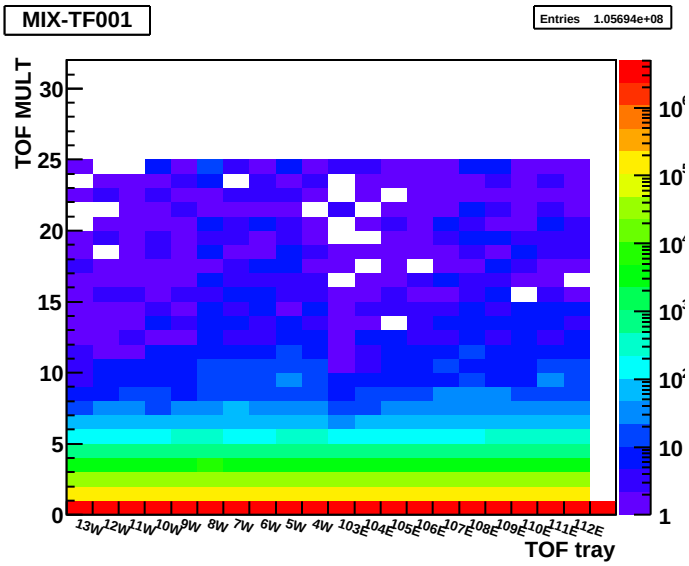

DAQ10k HT bits errors
Entries 0

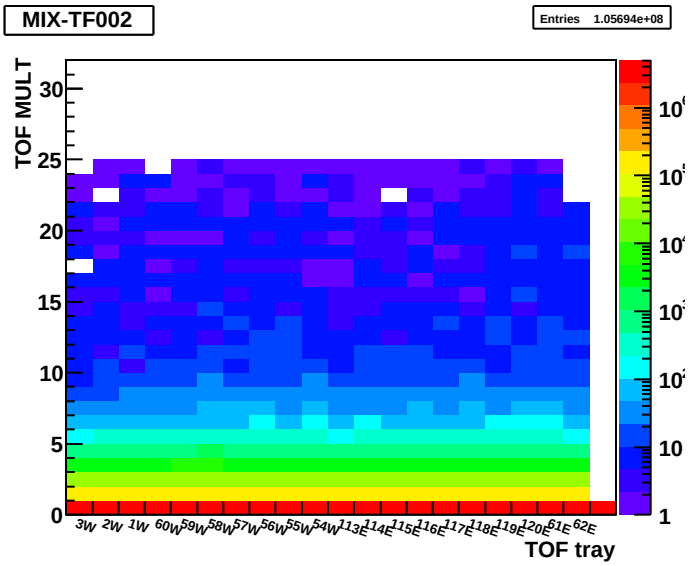

TPC Readout Mask (simulated)
Entries 386518

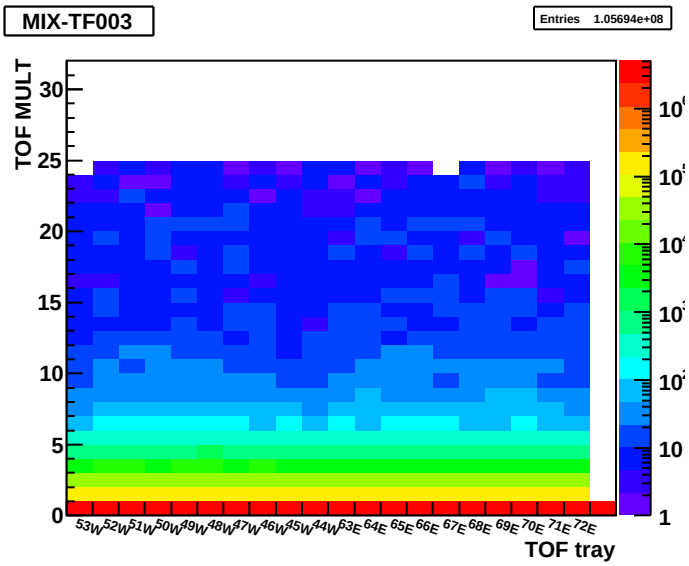

BunchId7Bit (all events)
Entries2516524

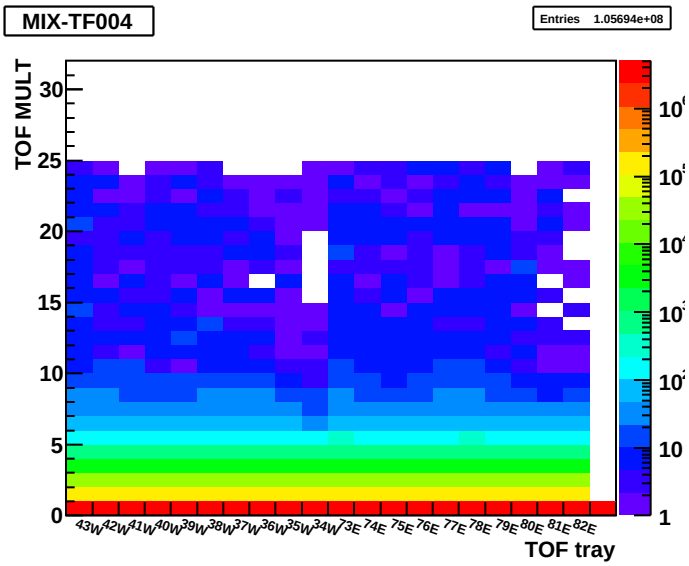

Abort-gaps
Entries 7598

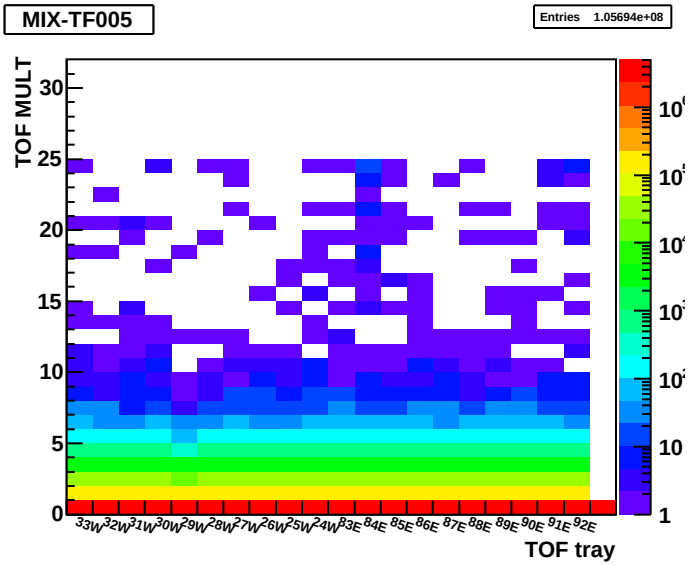

!Yellow-Fill
Entries 70461

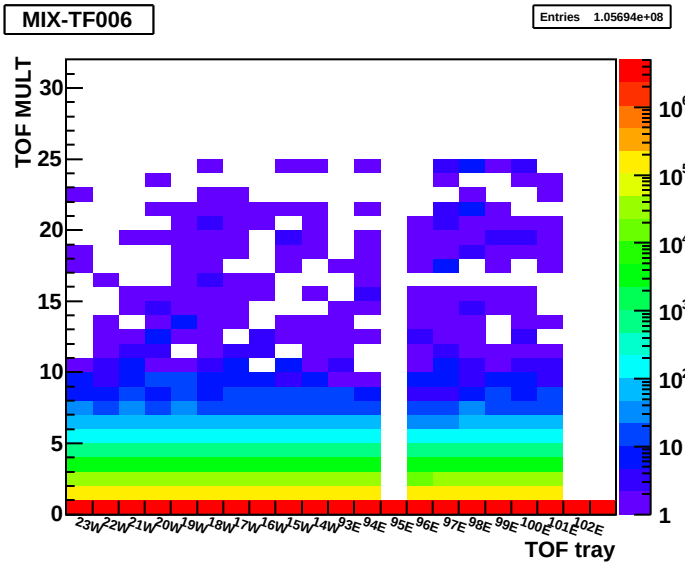

MIX-TF001
Entries 1.05694e+08
TOF MULT
TOF tray

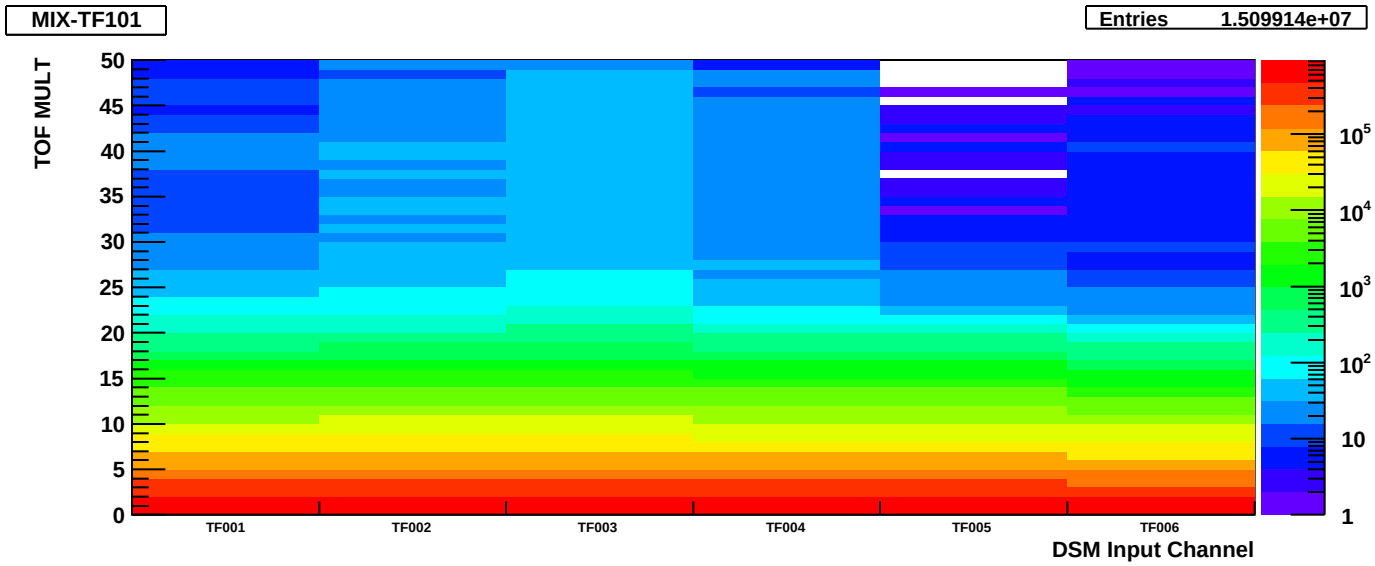

MIX-TF002
Entries 1.05694e+08
TOF MULT
TOF tray

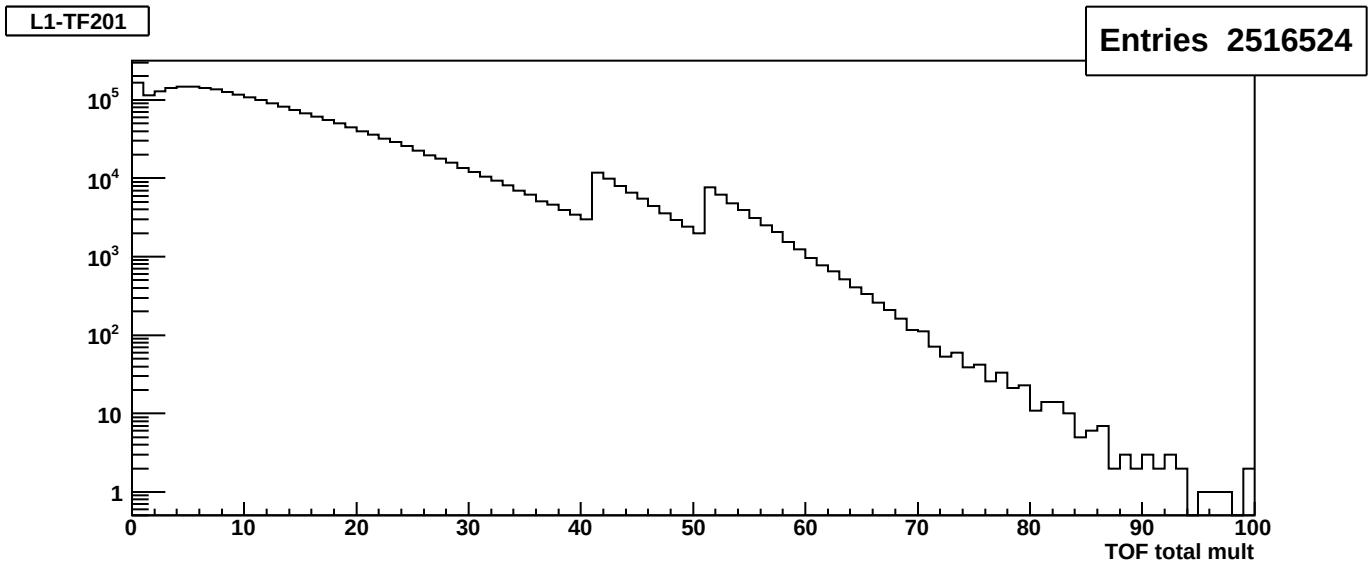

MIX-TF003
Entries 1.05694e+08
TOF MULT
TOF tray

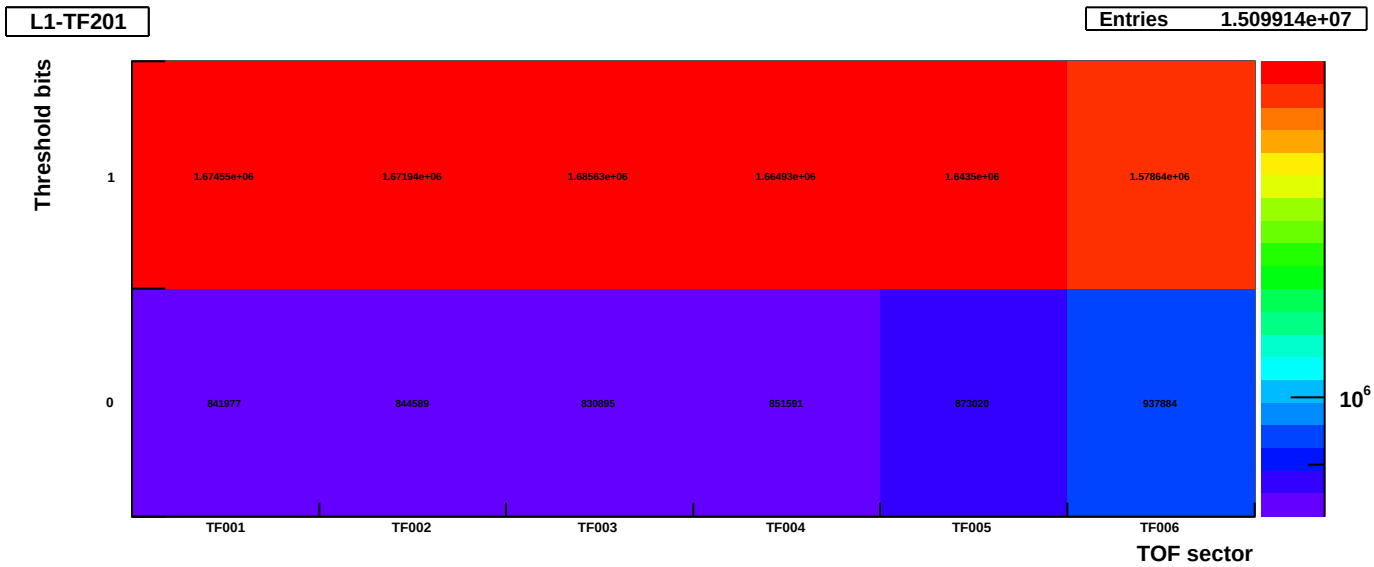

MIX-TF004
Entries 1.05694e+08
TOF MULT
TOF tray

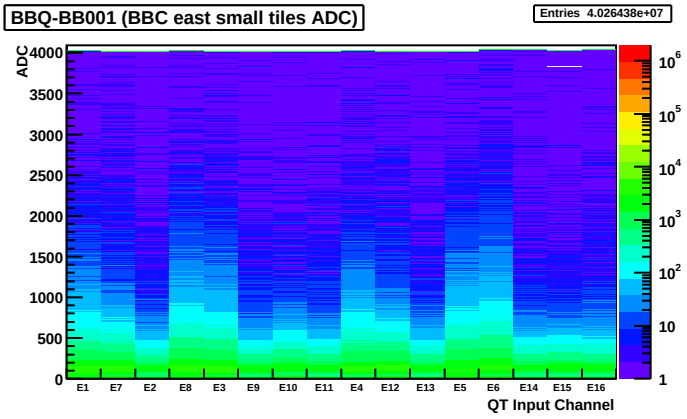

MIX-TF005
Entries 1.05694e+08
TOF MULT
TOF tray

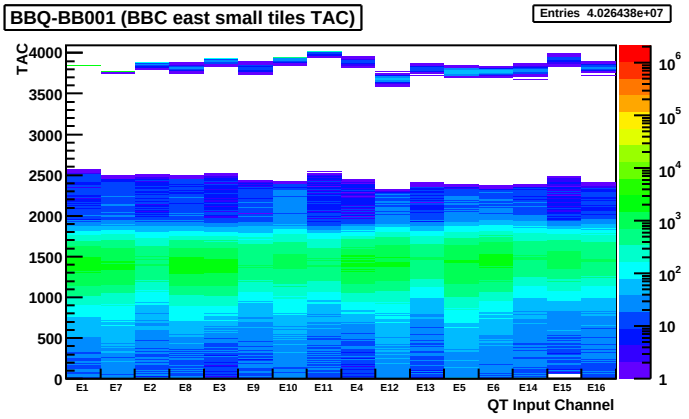

MIX-TF006
Entries 1.05694e+08
TOF MULT
TOF tray

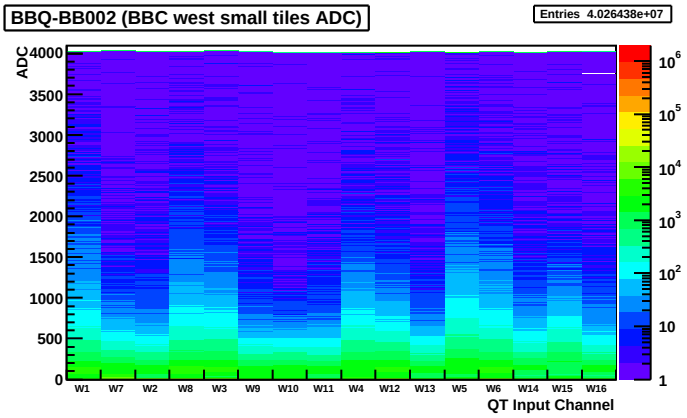

MIX-TF101
Entries 1.509914e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

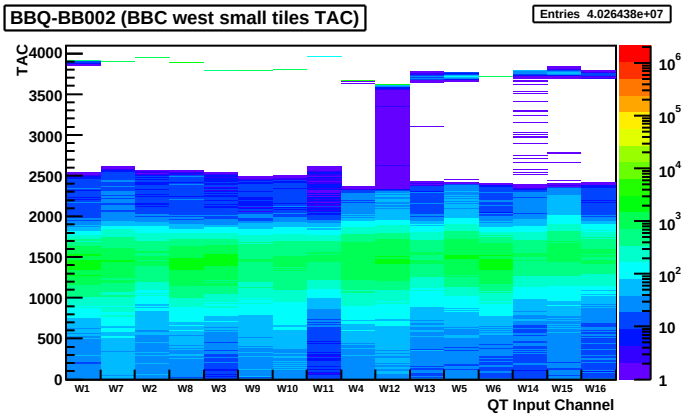

L1-TF201
Entries 2516524
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

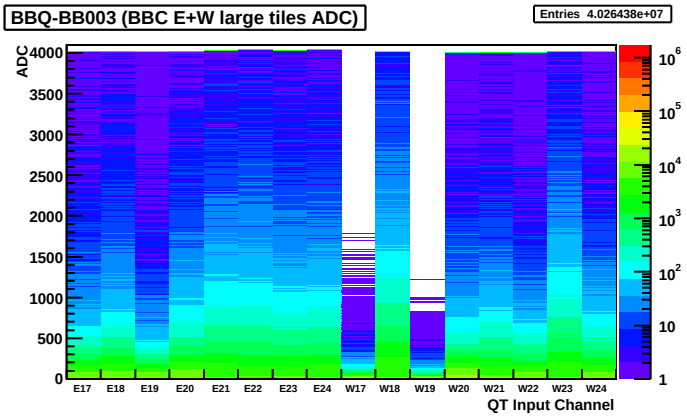

L1-TF201
Entries 1.509914e+07
Threshold bits
1
0
1.67455e+06
1.67194e+06
1.68563e+06
1.66493e+06
1.6435e+06
1.57864e+06
841977
844589
830895
851591
873020
937884
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

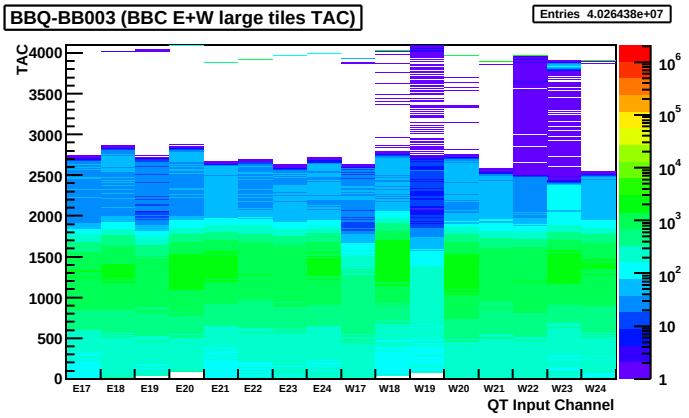

BBQ-BB001 (BBC east small tiles ADC)
Entries 4.026438e+07
ADC
QT Input Channel

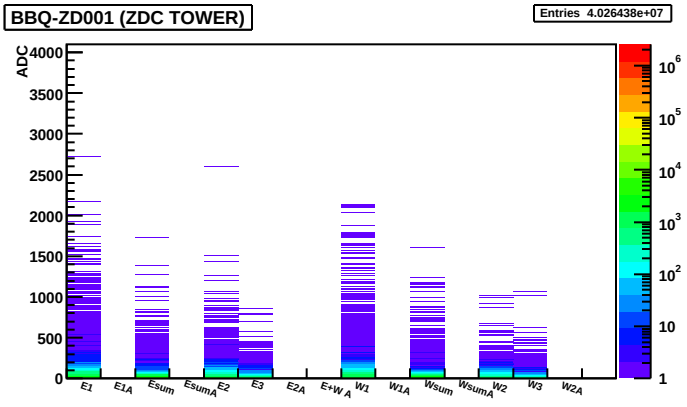

BBQ-BB001 (BBC east small tiles TAC)
Entries 4.026438e+07
TAC
QT Input Channel

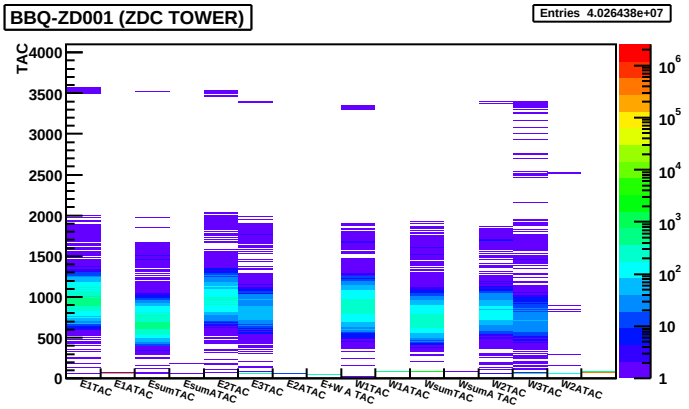

BBQ-BB002 (BBC west small tiles ADC)
Entries 4.026438e+07
ADC
QT Input Channel

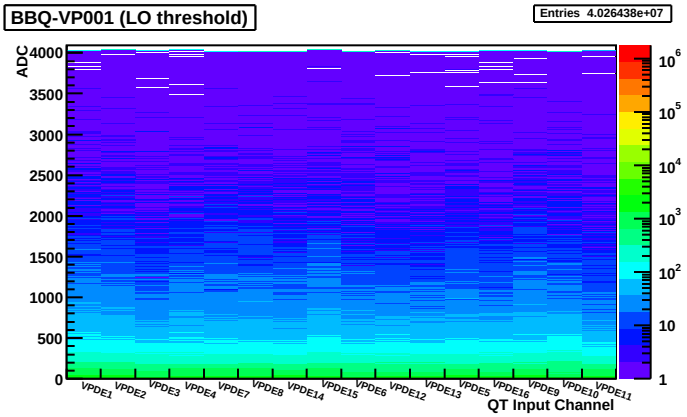

BBQ-BB002 (BBC west small tiles TAC)
Entries 4.026438e+07
TAC
QT Input Channel

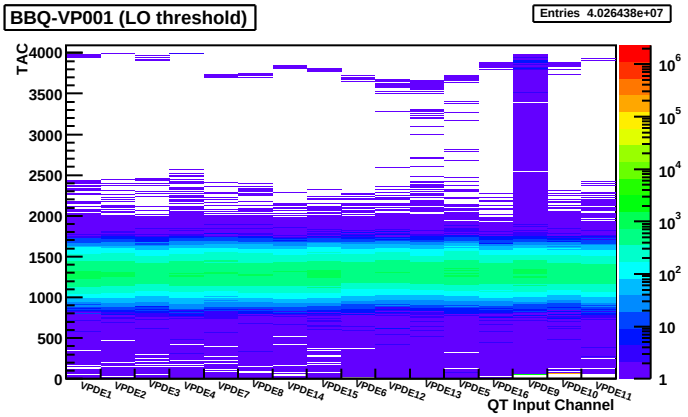

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4.026438e+07
ADC
QT Input Channel

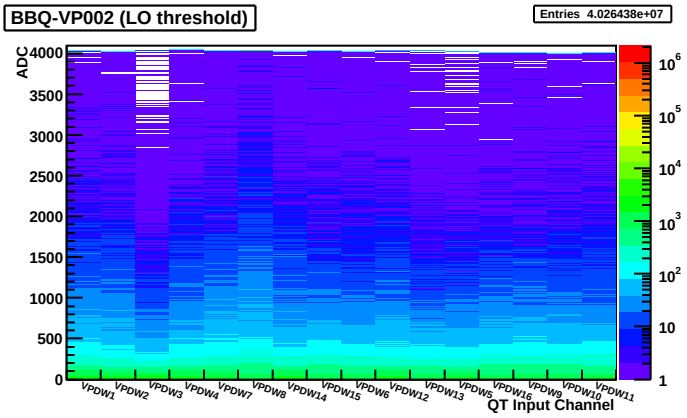

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4.026438e+07
TAC
QT Input Channel

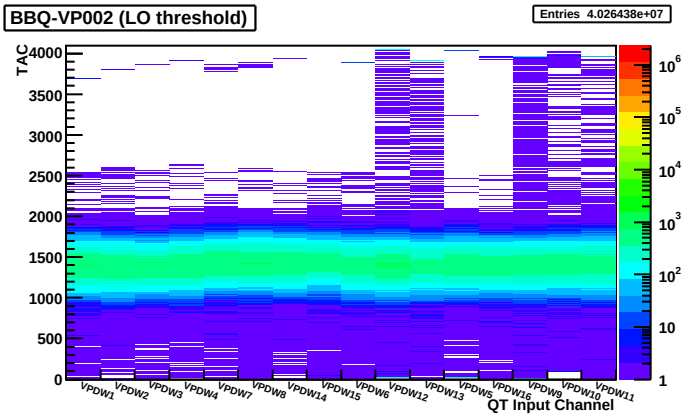

BBQ-ZD001 (ZDC TOWER)
Entries 4.026438e+07
ADC

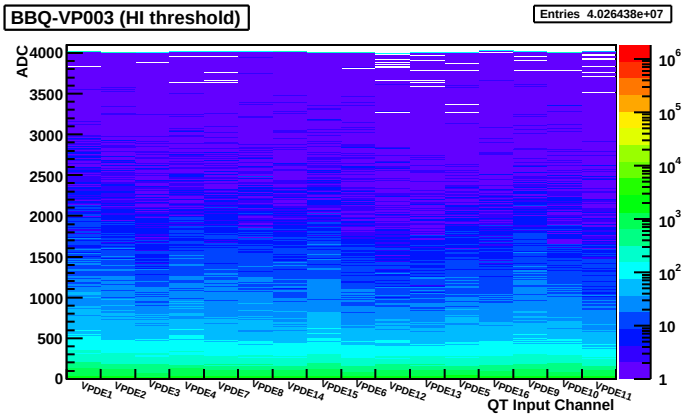

BBQ-ZD001 (ZDC TOWER)
Entries 4.026438e+07
TAC

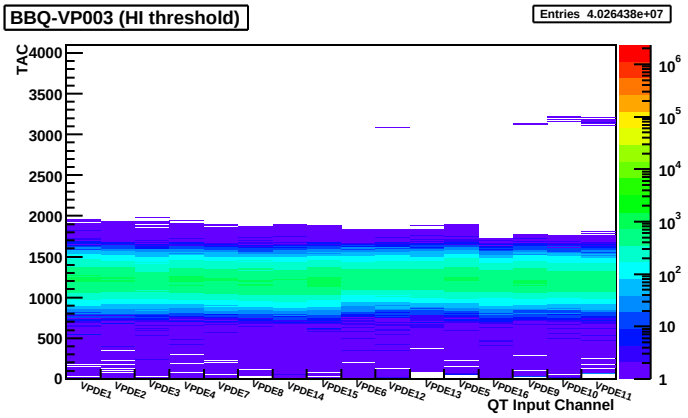

BBQ-VP001 (LO threshold)
Entries 4.026438e+07
ADC
QT Input Channel

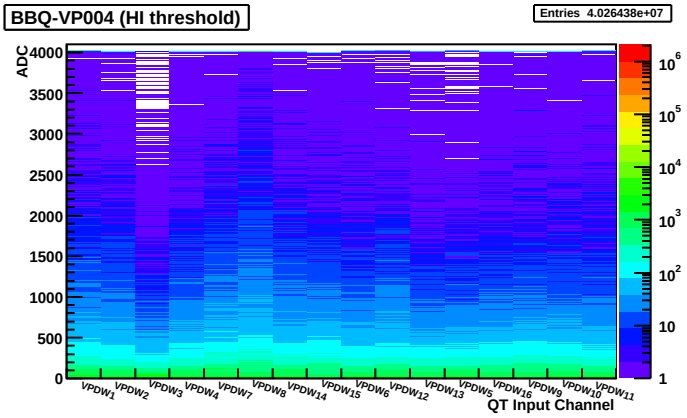

BBQ-VP001 (LO threshold)
Entries 4.026438e+07
TAC
QT Input Channel

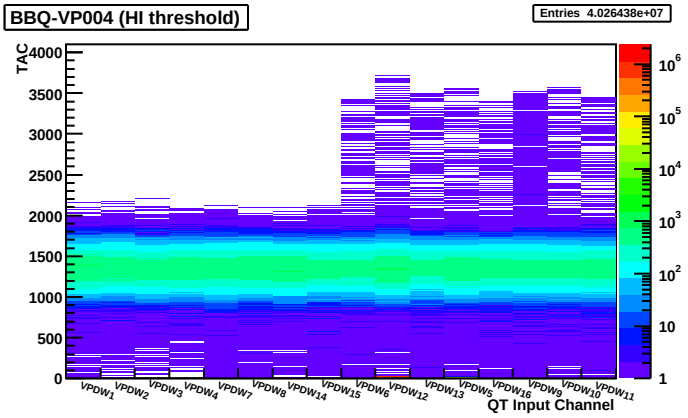

BBQ-VP002 (LO threshold)
Entries 4.026438e+07
ADC
QT Input Channel

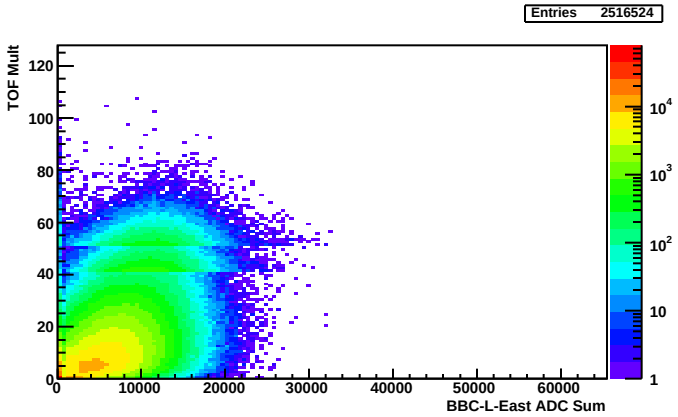

BBQ-VP002 (LO threshold)
Entries 4.026438e+07
TAC
QT Input Channel

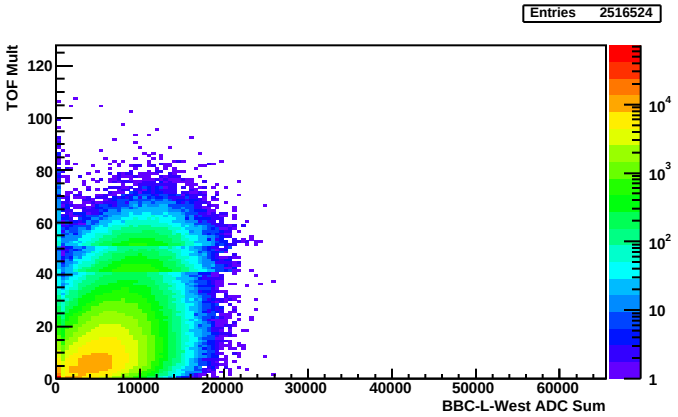

BBQ-VP003 (HI threshold)
Entries 4.026438e+07
ADC
QT Input Channel

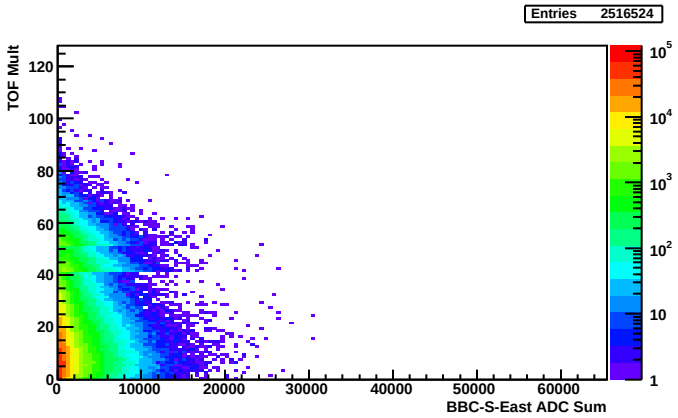

BBQ-VP003 (HI threshold)
Entries 4.026438e+07
TAC
QT Input Channel

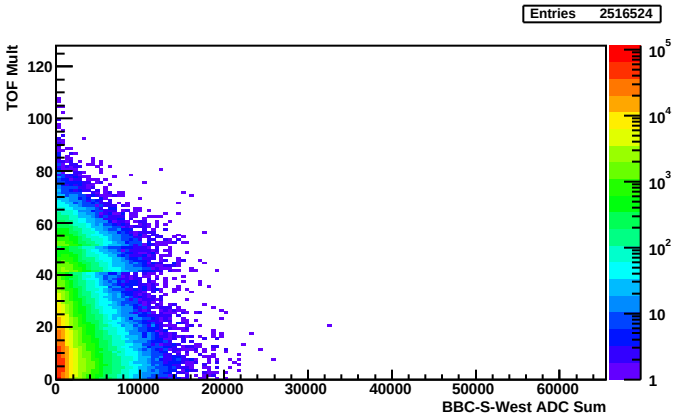

BBQ-VP004 (HI threshold)
Entries 4.026438e+07
ADC
QT Input Channel

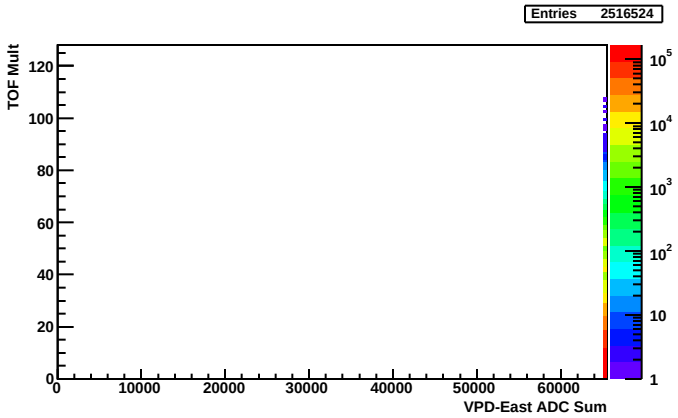

BBQ-VP004 (HI threshold)
Entries 4.026438e+07
TAC
QT Input Channel

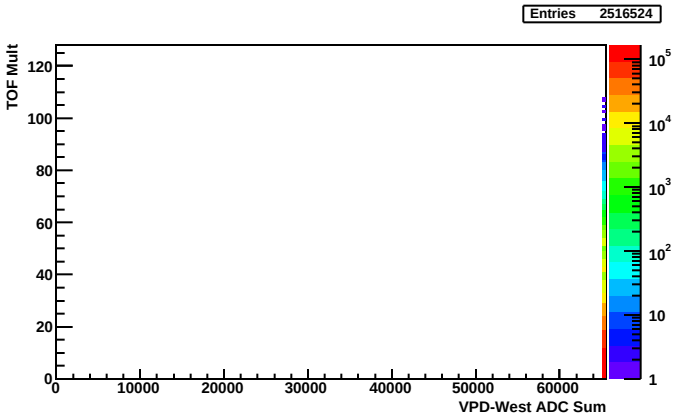

Entries 2516524
TOF Mult
BBC-L-East ADC Sum

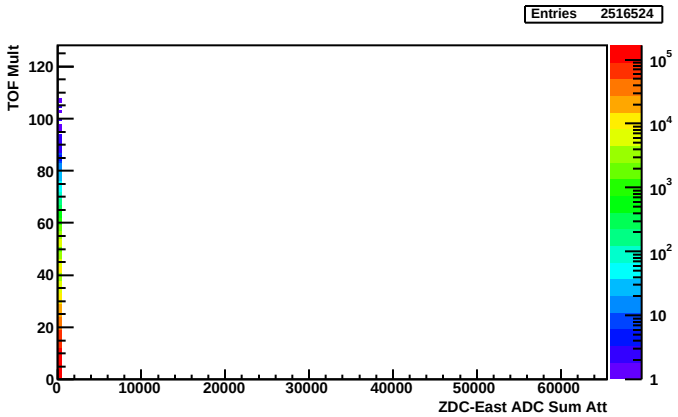

Entries 2516524
TOF Mult
BBC-L-West ADC Sum

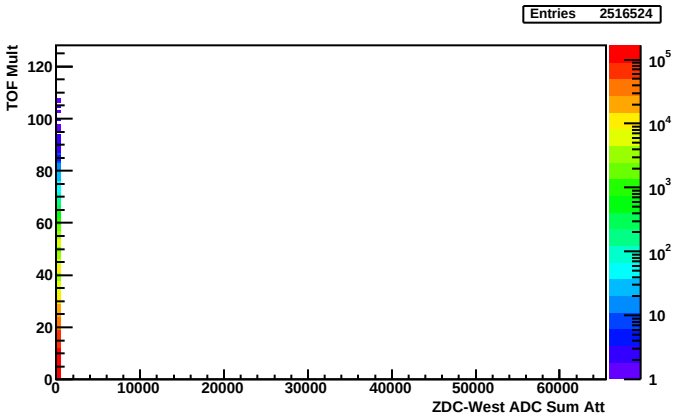

Entries 2516524
TOF Mult
BBC-S-East ADC Sum

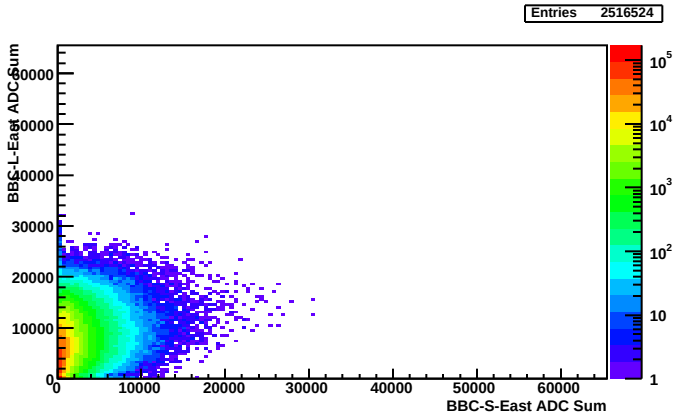

Entries 2516524
TOF Mult
BBC-S-West ADC Sum

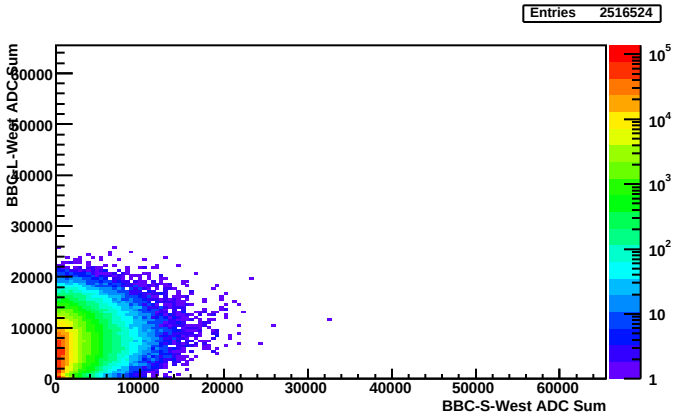

Entries 2516524
TOF Mult
VPD-East ADC Sum

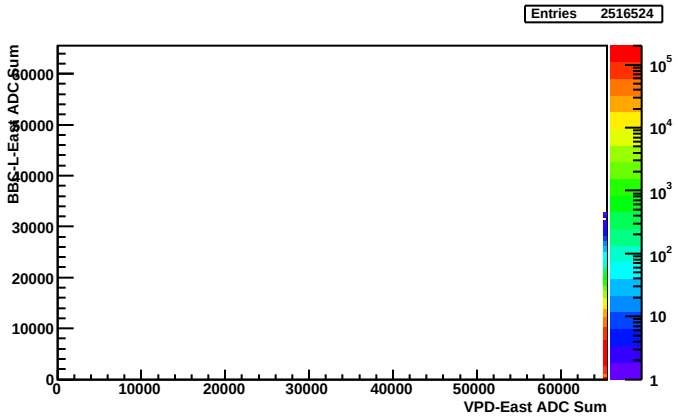

Entries 2516524
TOF Mult
VPD-West ADC Sum

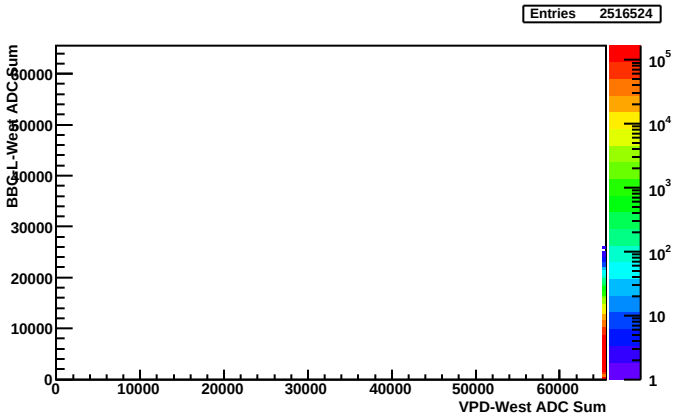

Entries 2516524
TOF Mult
ZDC-East ADC Sum Att

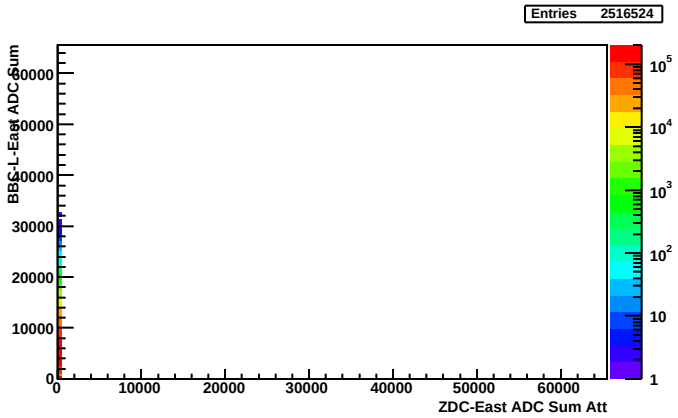

Entries 2516524
TOF Mult
ZDC-West ADC Sum Att

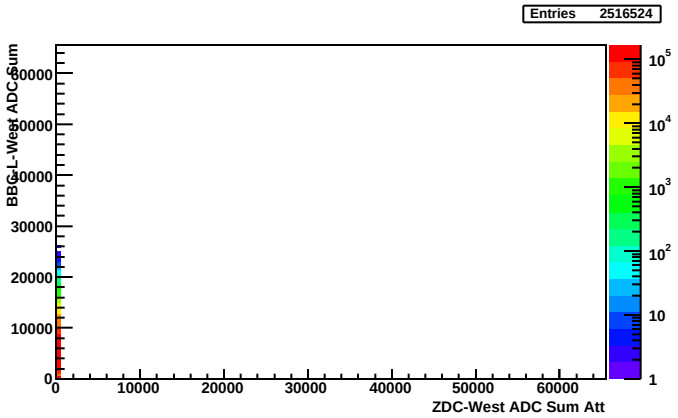

Entries 2516524
BBC-L-East ADC Sum
BBC-S-East ADC Sum

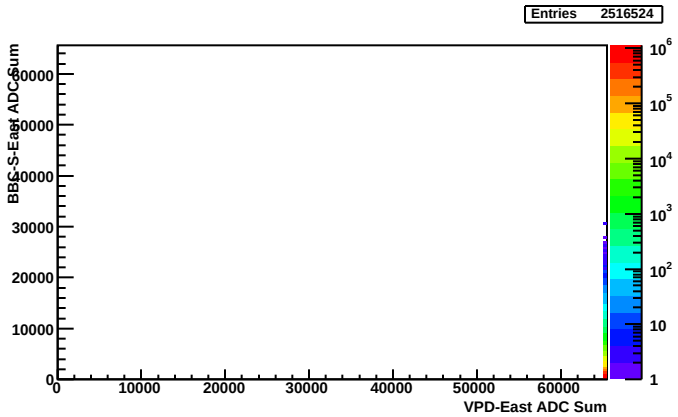

Entries 2516524
BBC-L-West ADC Sum
BBC-S-West ADC Sum

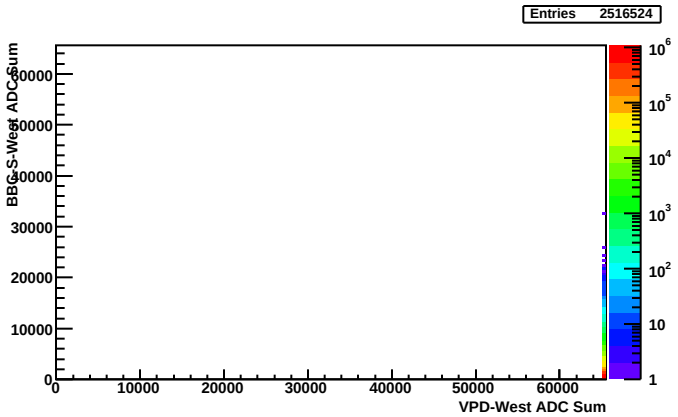

Entries 2516524
BBC-L-East ADC Sum
VPD-East ADC Sum

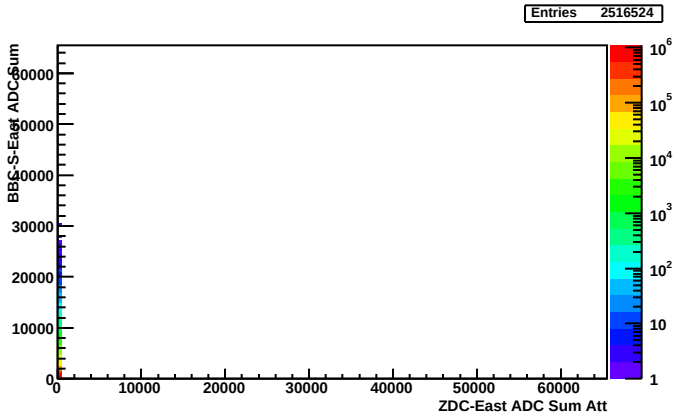

Entries 2516524
BBC-L-West ADC Sum
VPD-West ADC Sum

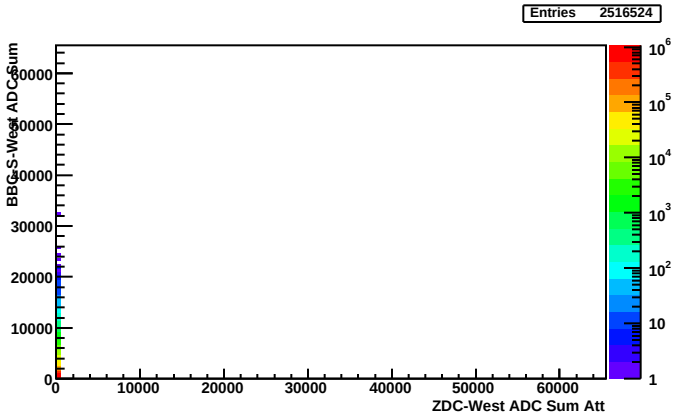

Entries 2516524
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

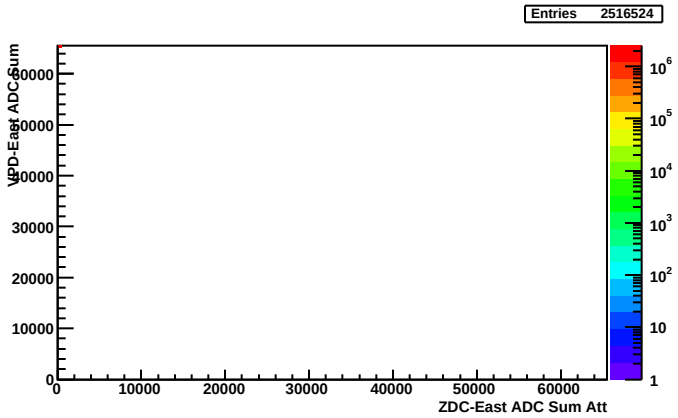

Entries 2516524
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

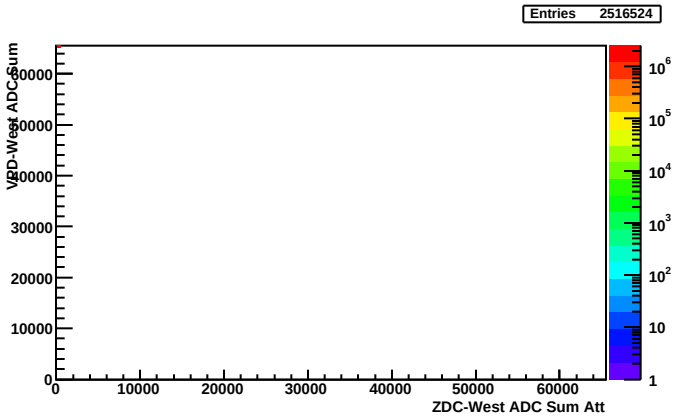

Entries 2516524
BBC-S-East ADC Sum
VPD-East ADC Sum

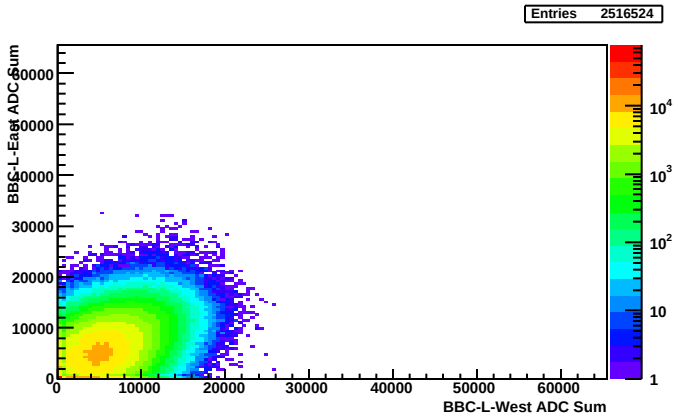

Entries 2516524
BBC-S-West ADC Sum
VPD-West ADC Sum

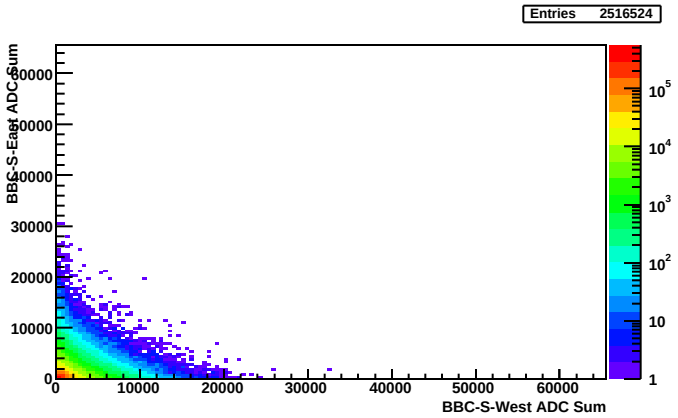

Entries 2516524
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

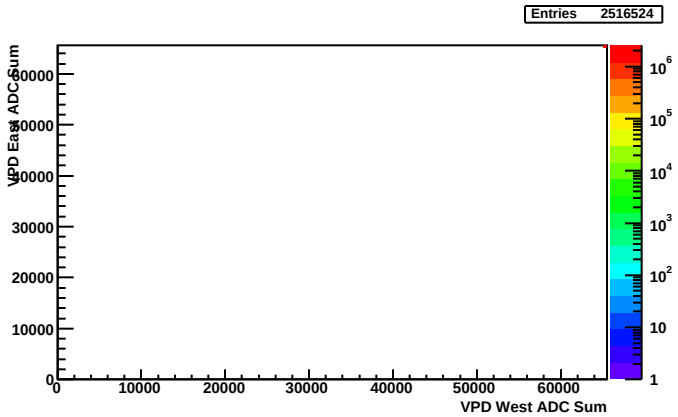

Entries 2516524
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

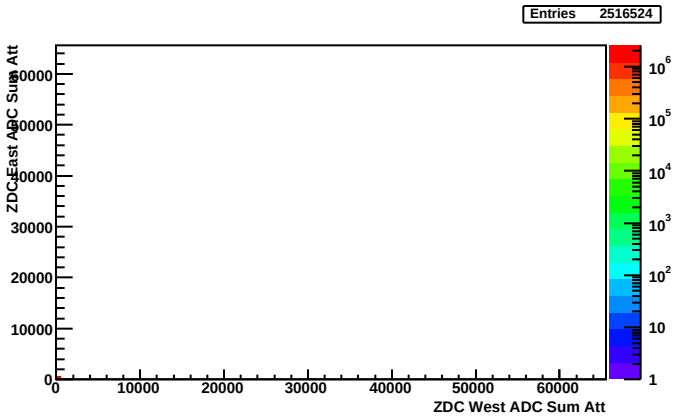

Entries 2516524
VPD-East ADC Sum
ZDC-East ADC Sum Att

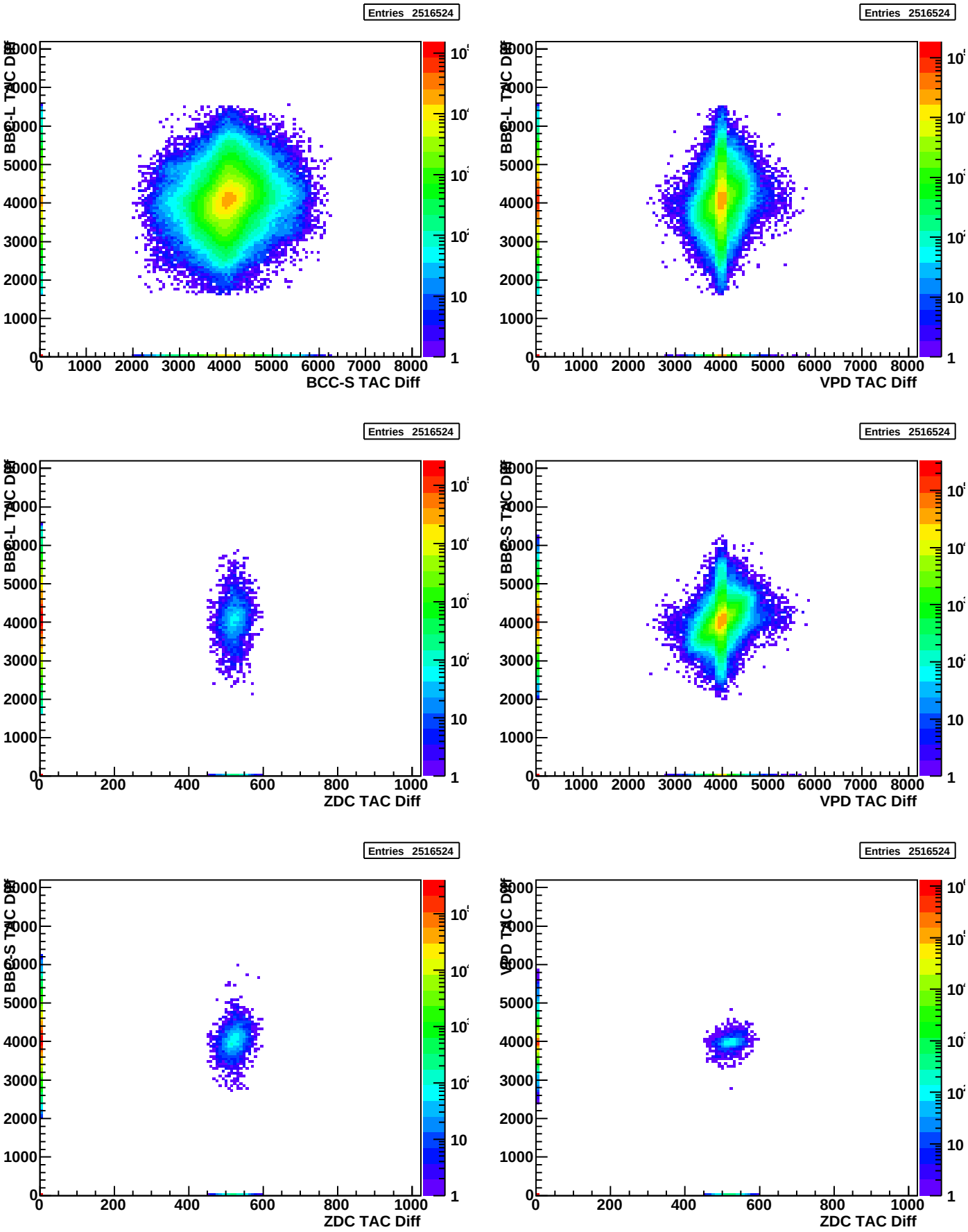

Entries 2516524
VPD-West ADC Sum
ZDC-West ADC Sum Att

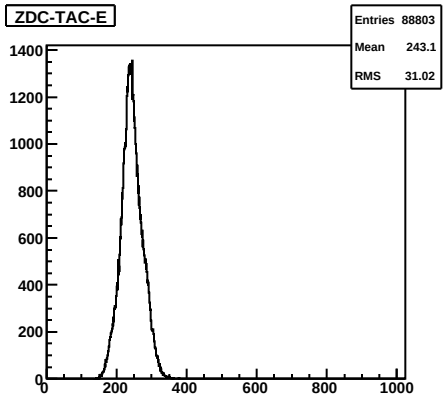

Entries 2516524
BBC-L-East ADC Sum
BBC-L-West ADC Sum

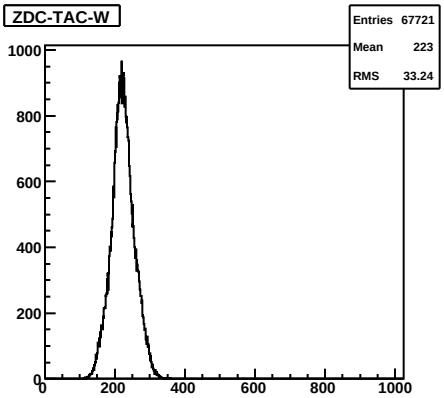

Entries 2516524
BBC-S-East ADC Sum
BBC-S-West ADC Sum

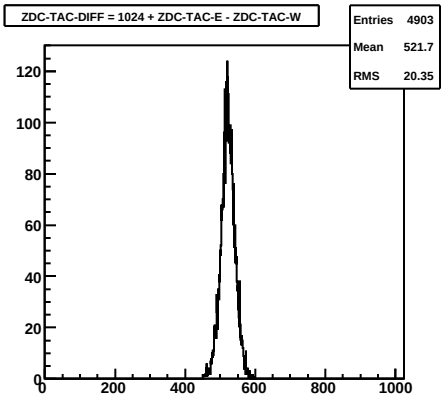

Entries 2516524
VPD East ADC Sum
VPD West ADC Sum

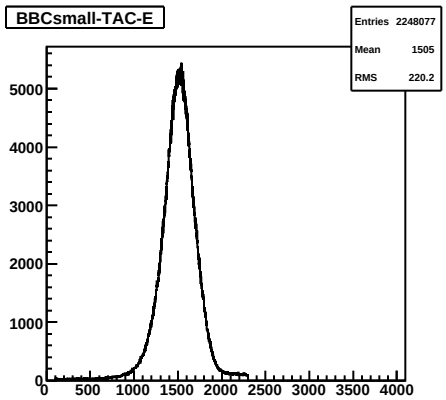

Entries 2516524
ZDC East ADC Sum Att
ZDC West ADC Sum Att

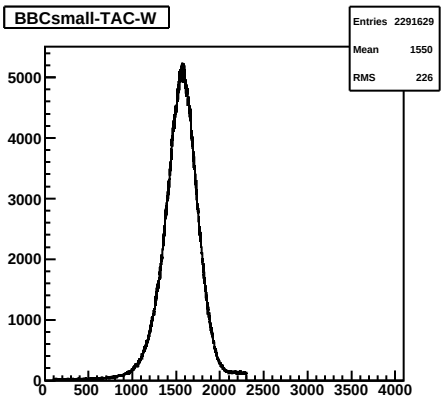

Entries 2516524
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2516524
BBC-L TAC Diff
VPD TAC Diff
Entries 2516524
BBC-L TAC Diff
ZDC TAC Diff
Entries 2516524
BBC-S TAC Diff
VPD TAC Diff
Entries 2516524
BBC-S TAC Diff
ZDC TAC Diff
Entries 2516524
VPD TAC Diff
ZDC TAC Diff

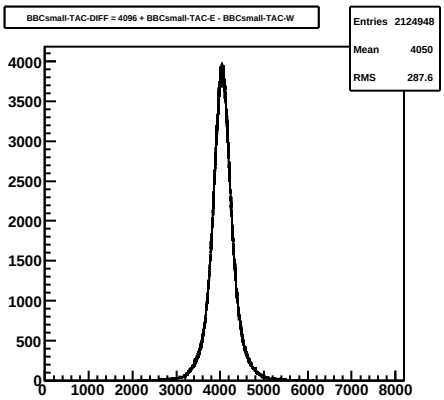

ZDC-TAC-E
Entries 88803
Mean 243.1
RMS 31.02

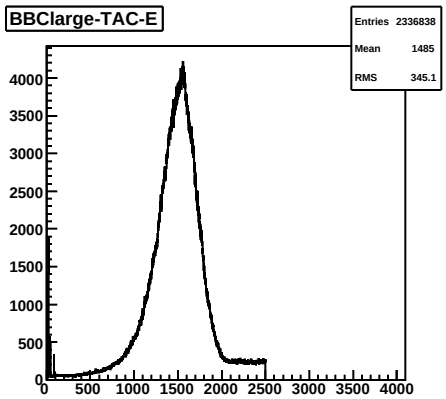

ZDC-TAC-W
Entries 67721
Mean 223
RMS 33.24

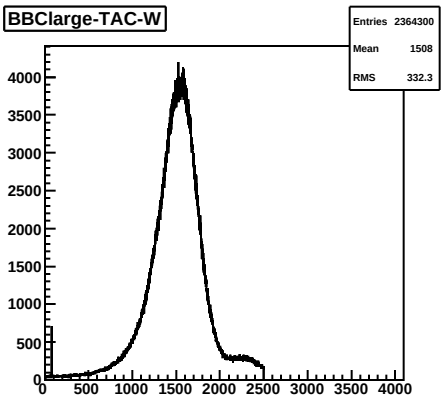

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 4903
Mean 521.7
RMS 20.35

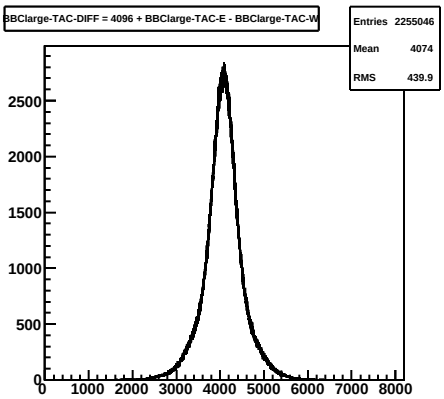

BBCsmall-TAC-E
Entries 2248077
Mean 1505
RMS 220.2

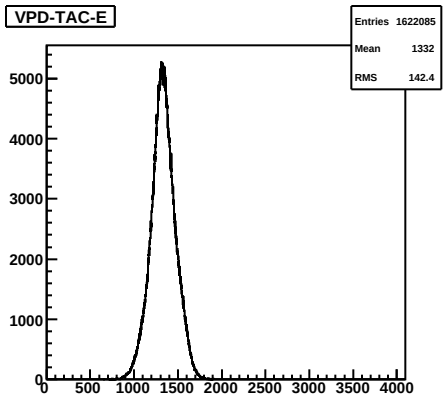

BBCsmall-TAC-W
Entries 2291629
Mean 1550
RMS 226

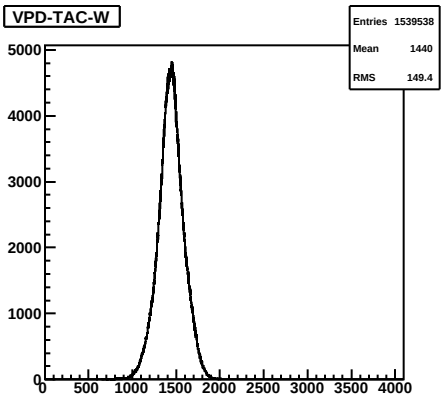

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2124948
Mean 4050
RMS 287.6

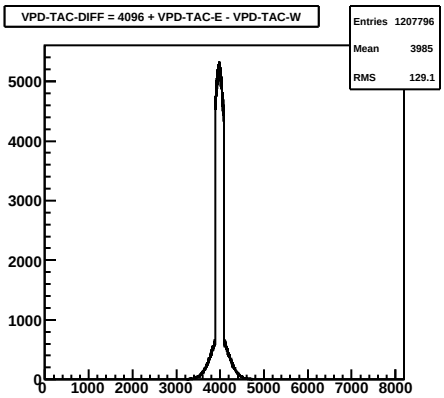

BBClarge-TAC-E
Entries 2336838
Mean 1485
RMS 345.1

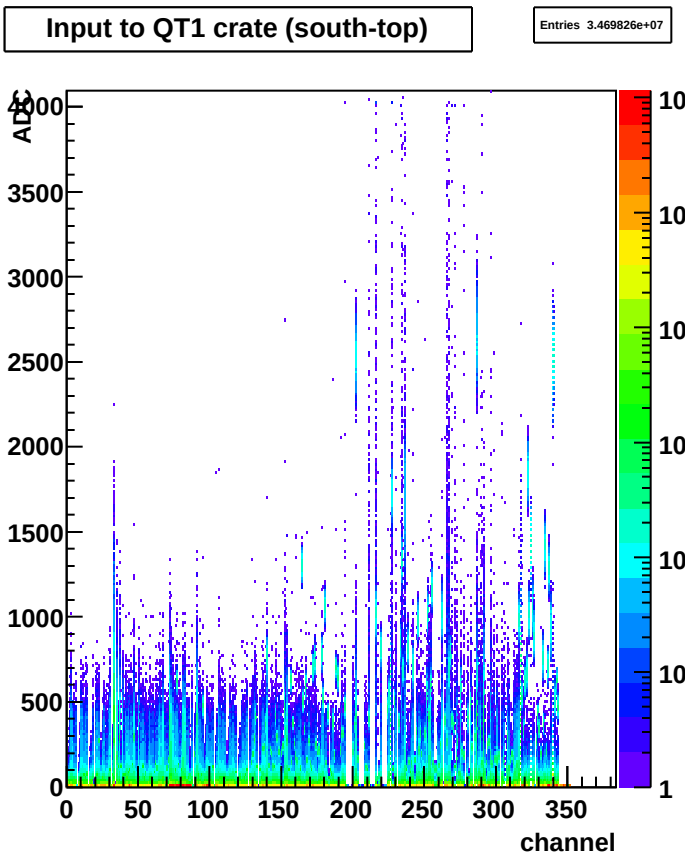

BBClarge-TAC-W
Entries 2364300
Mean 1508
RMS 332.3

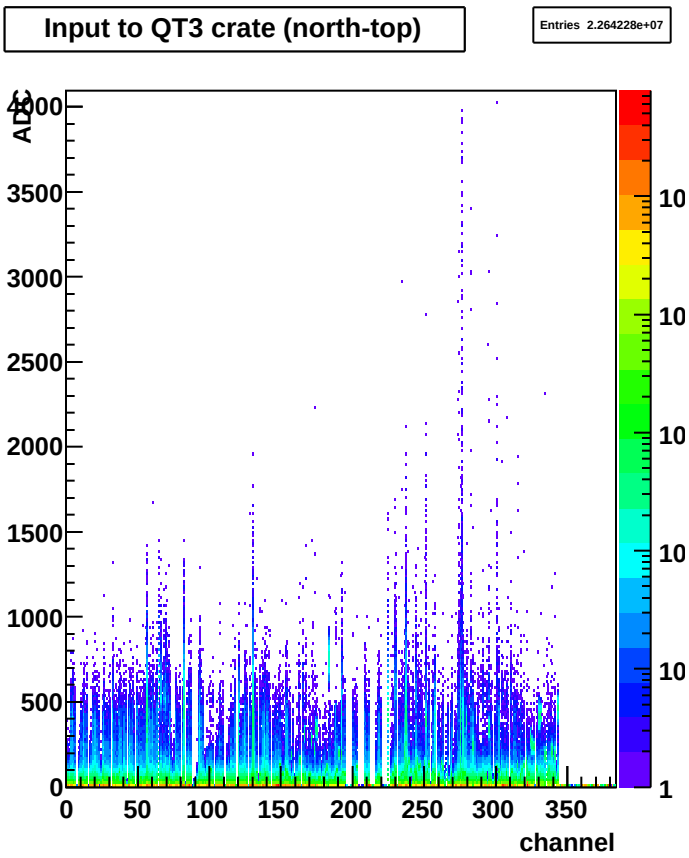

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2255046
Mean 4074
RMS 439.9

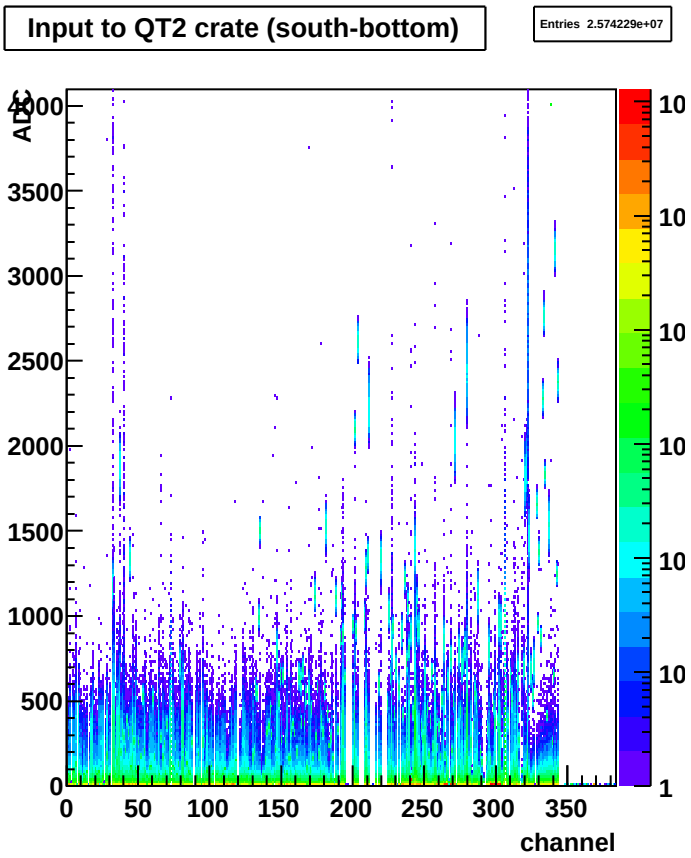

VPD-TAC-E
Entries 1622085
Mean 1332
RMS 142.4

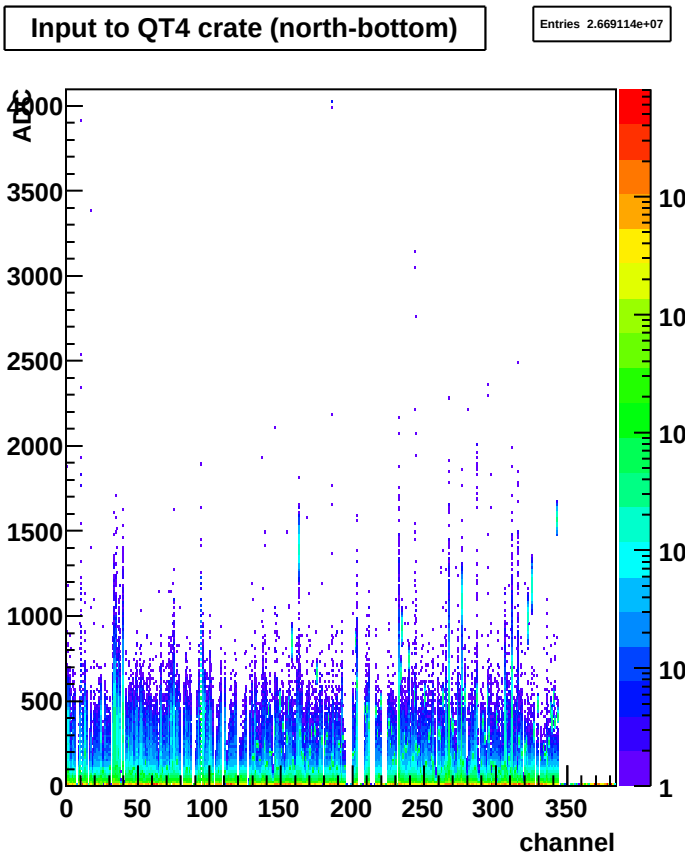

VPD-TAC-W
Entries 1539538
Mean 1440
RMS 149.4

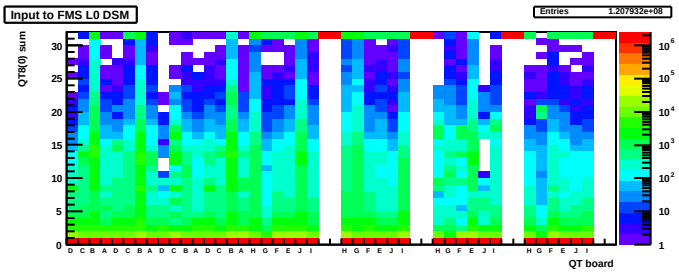

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1207796
Mean 3985
RMS 129.1

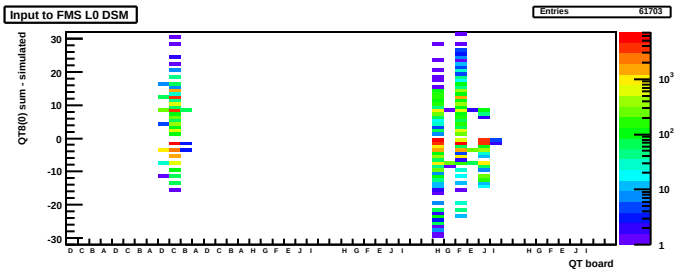

Input to QT1 crate (south-top)
Entries 3.469826e+07
ADC
channel

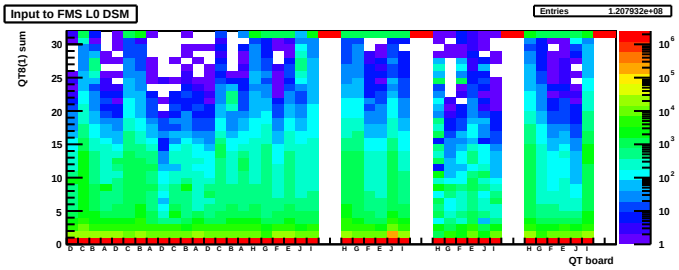

Input to QT3 crate (north-top)
Entries 2.264228e+07
ADC
channel

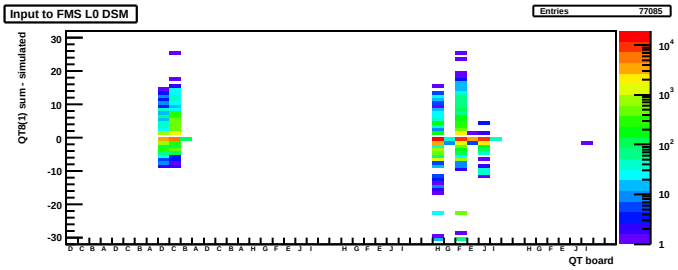

Input to QT2 crate (south-bottom)
Entries 2.574229e+07
ADC
channel

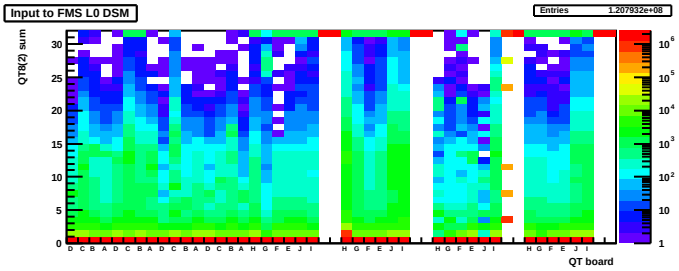

Input to QT4 crate (north-bottom)
Entries 2.669114e+07
ADC
channel

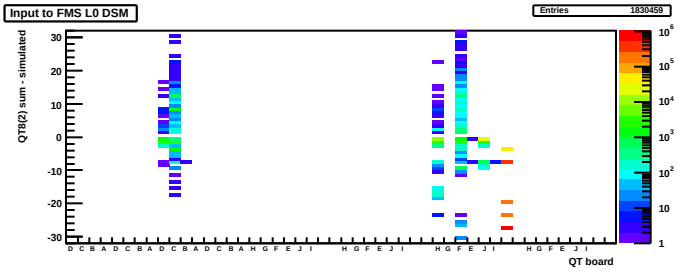

Input to FMS L0 DSM
Entries 1.207932e+08
QT8(0) sum
QT board

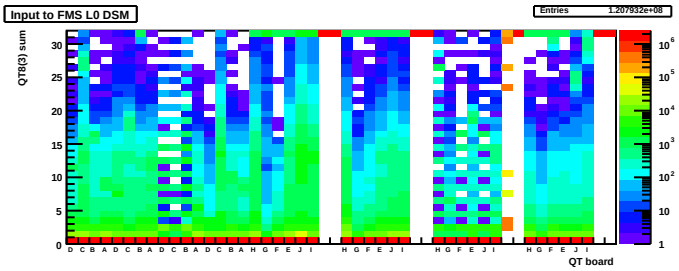

Input to FMS L0 DSM
Entries 61703
QT8(0) sum - simulated
QT board

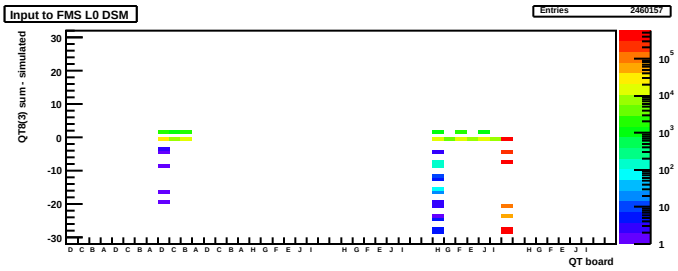

Input to FMS L0 DSM
Entries 1.207932e+08
QT8(1) sum
QT board

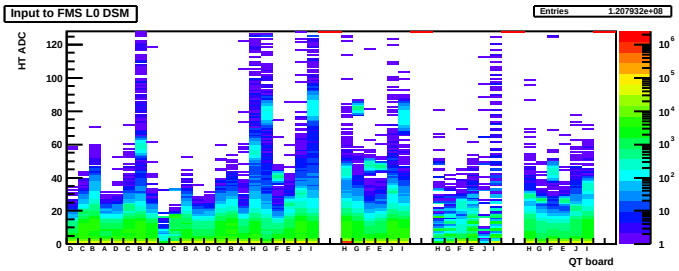

Input to FMS L0 DSM
Entries 77085
QT8(1) sum - simulated
QT board

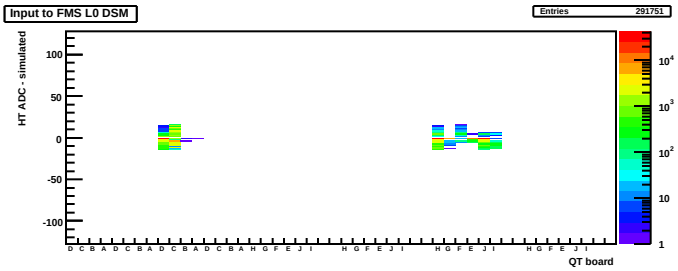

Input to FMS L0 DSM
Entries 1.207932e+08
QT8(2) sum
QT board

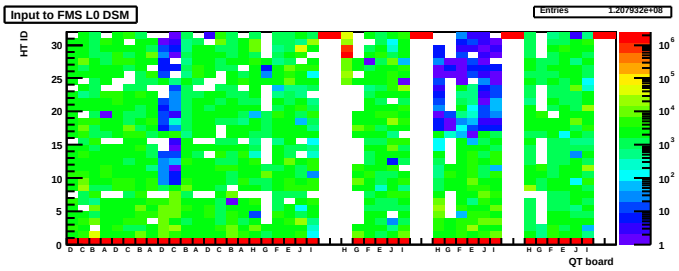

Input to FMS L0 DSM
Entries 1830459
QT8(2) sum - simulated
QT board

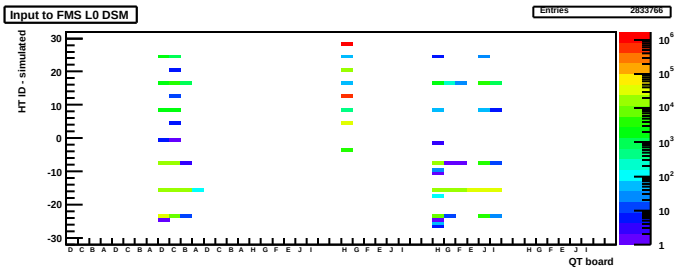

Input to FMS L0 DSM
Entries 1.207932e+08
QT8(3) sum
QT board

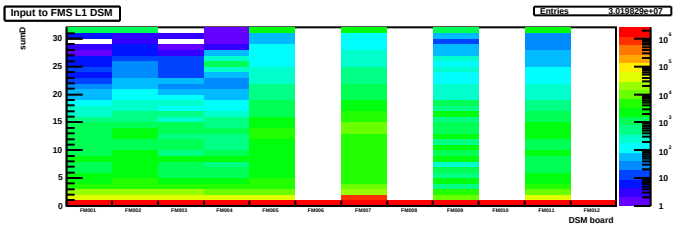

Input to FMS L0 DSM
Entries 2460157
QT8(3) sum - simulated
QT board

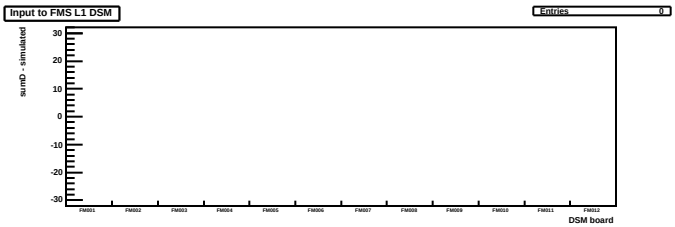

Input to FMS L0 DSM
Entries 1.207932e+08
HT ADC
QT board

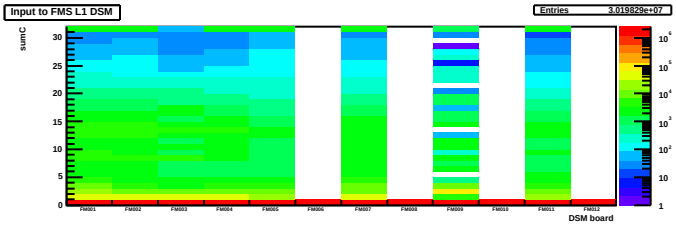

Input to FMS L0 DSM
Entries 291751
HT ADC - simulated
QT board

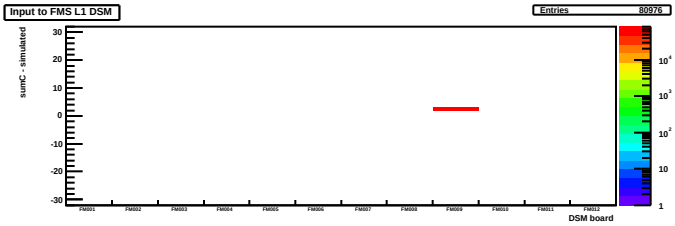

Input to FMS L0 DSM
Entries 1.207932e+08
HT ID
QT board

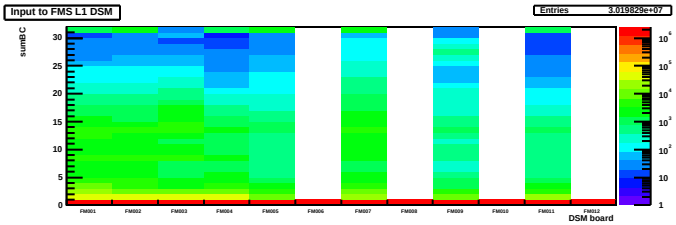

Input to FMS L0 DSM
Entries 2833766
HT ID - simulated
QT board

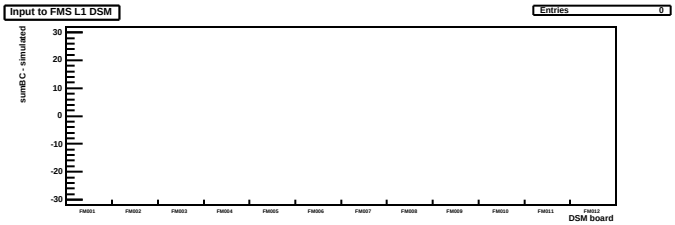

Input to FMS L1 DSM
Entries 3.019829e+07
sumD
DSM board

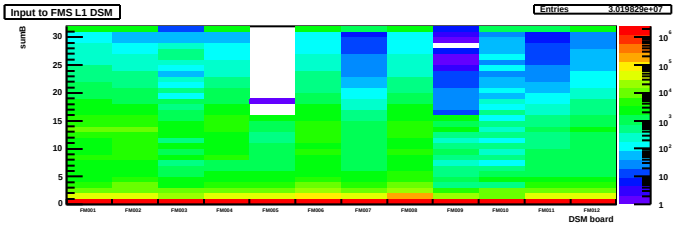

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

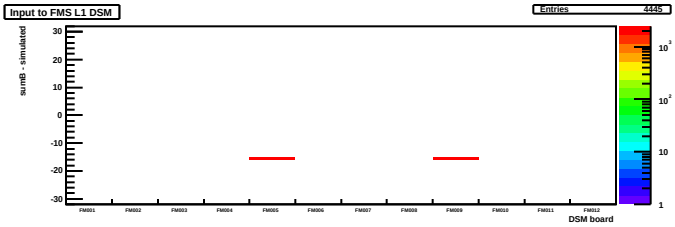

Input to FMS L1 DSM
Entries 3.019829e+07
sumC
DSM board

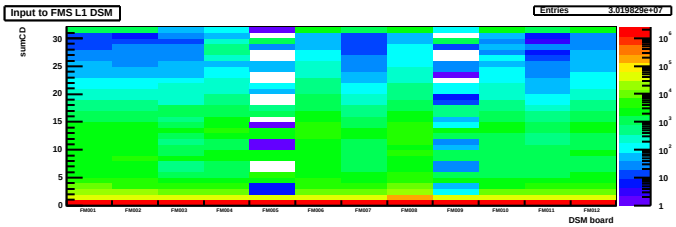

Input to FMS L1 DSM
Entries 80976
sumC - simulated
DSM board

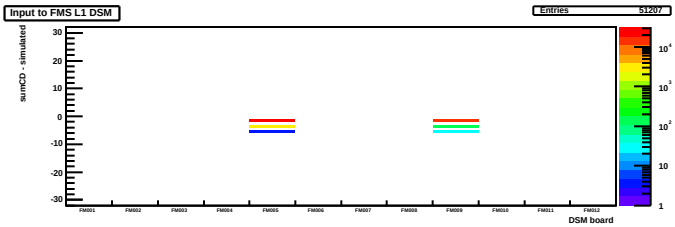

Input to FMS L1 DSM
Entries 3.019829e+07
sumBC
DSM board

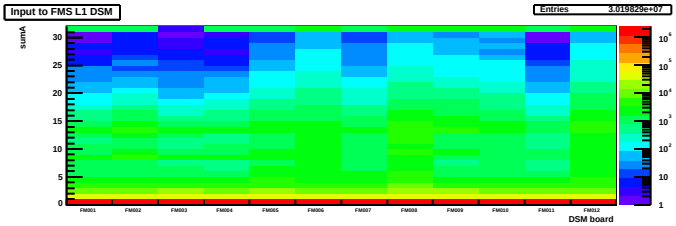

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

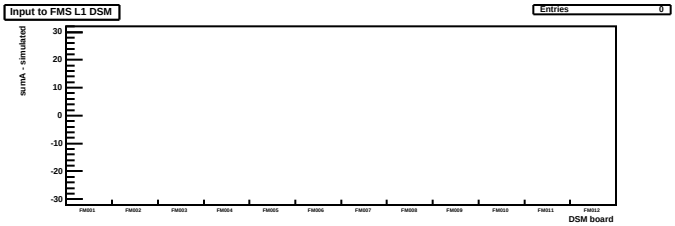

Input to FMS L1 DSM
Entries 3.019829e+07
sumB
DSM board

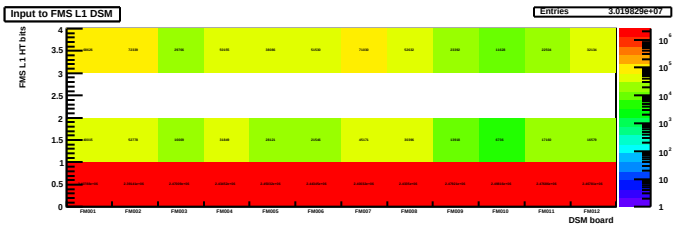

Input to FMS L1 DSM
Entries 4445
sumB - simulated
DSM board

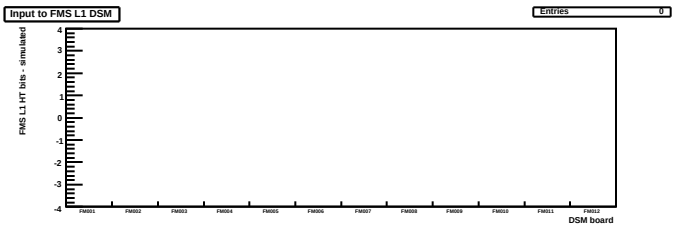

Input to FMS L1 DSM
Entries 3.019829e+07
sumCD
DSM board

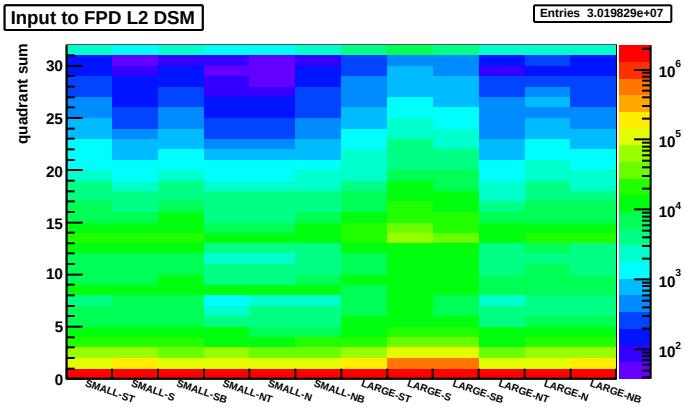

Input to FMS L1 DSM
Entries 51207
sumCD - simulated
DSM board

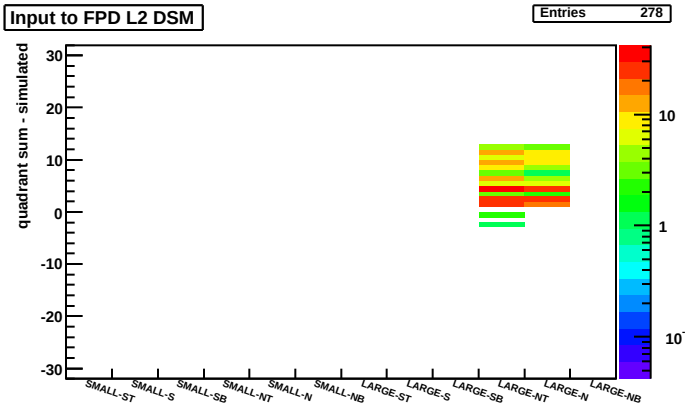

Input to FMS L1 DSM
Entries 3.019829e+07
sumA
DSM board

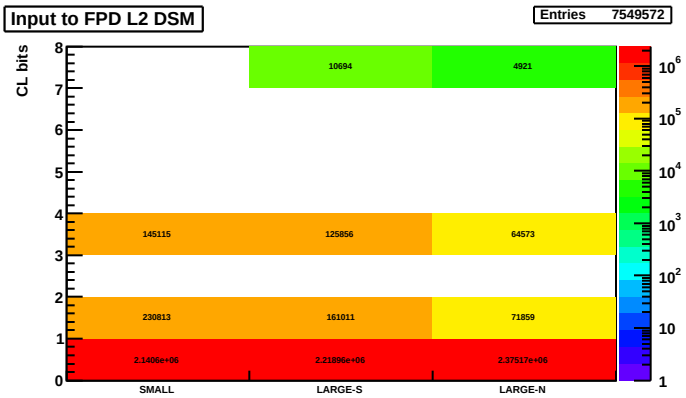

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

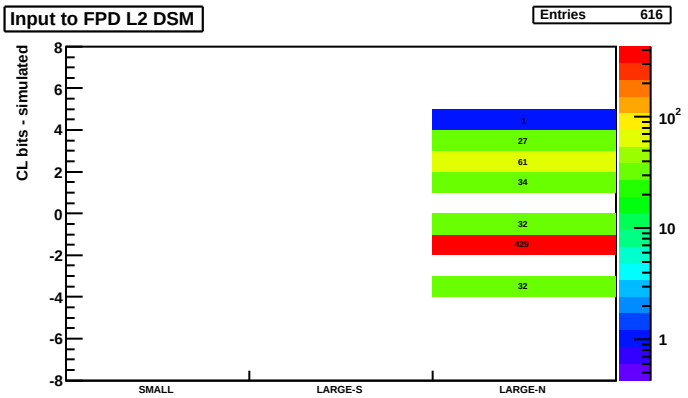

Input to FMS L1 DSM
Entries 3.019829e+07
FMS L1 HT bits
DSM board

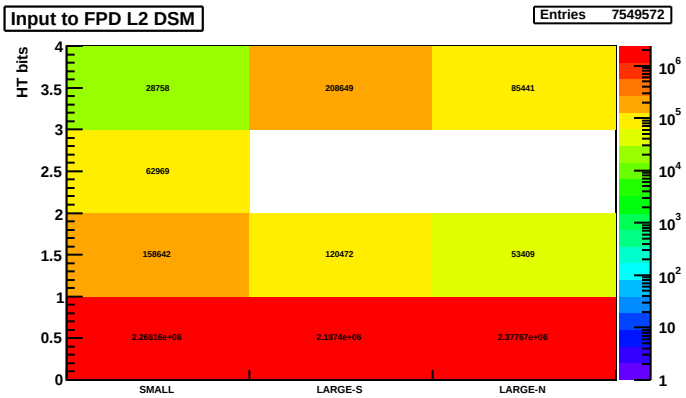

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

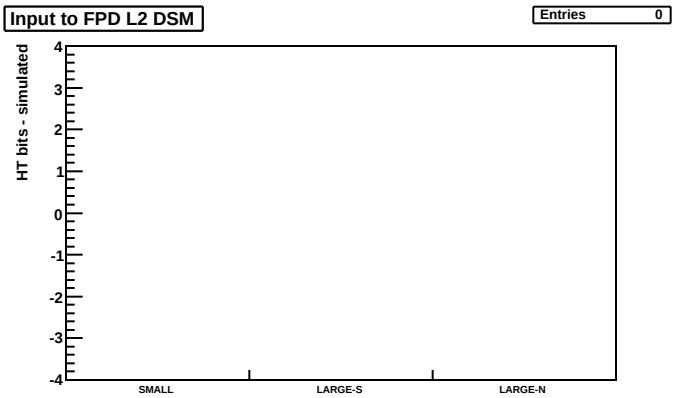

Input to FPD L2 DSM
Entries 3.019829e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

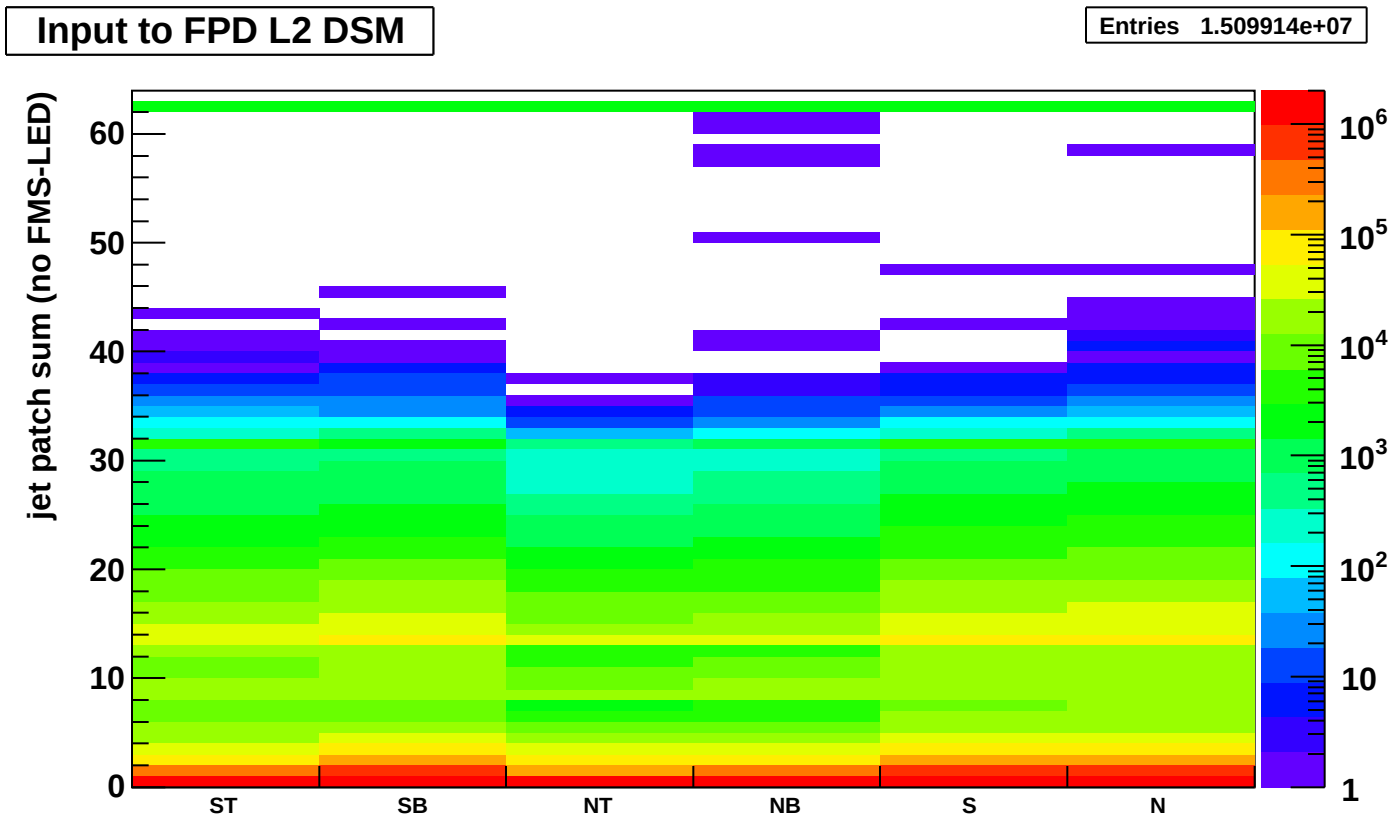

Input to FPD L2 DSM
Entries 278
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

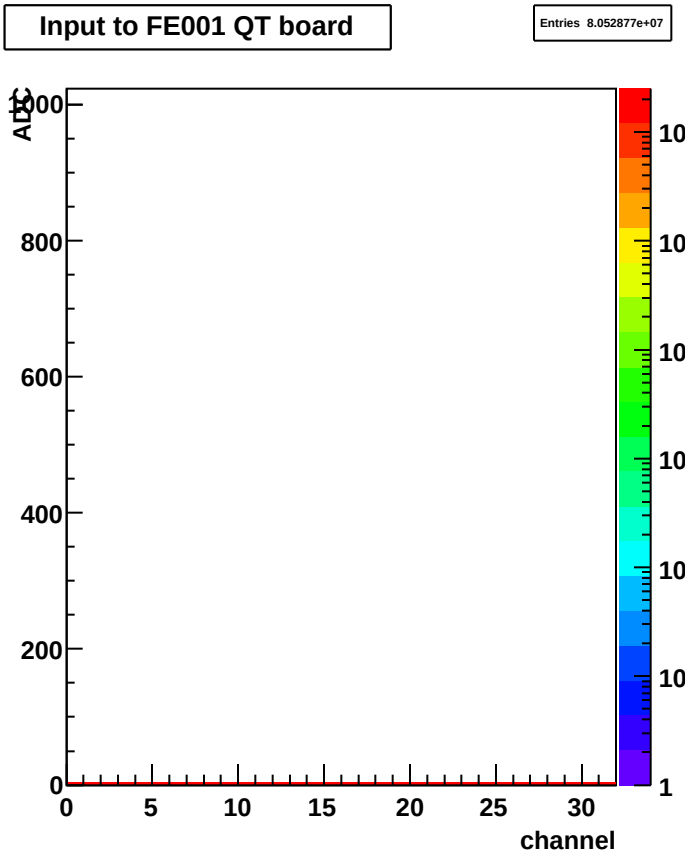

Input to FPD L2 DSM
Entries 7549572
CL bits
10694
4921
145115
125856
64573
230813
161011
71859
2.1406e+06
2.21896e+06
2.37517e+06
SMALL
LARGE-S
LARGE-N

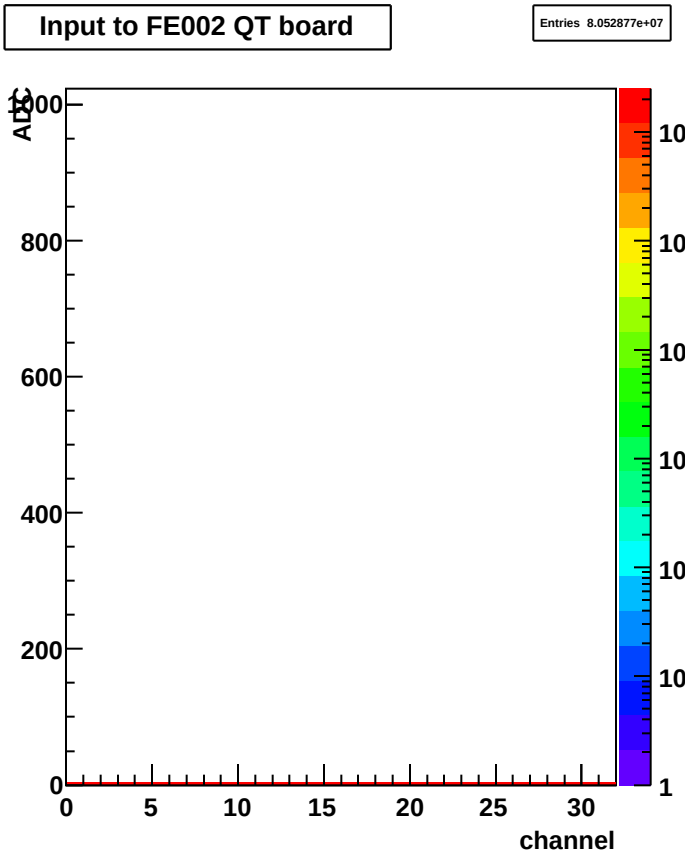

Input to FPD L2 DSM
Entries 616
CL bits - simulated
1
27
61
34
32
429
32
SMALL
LARGE-S
LARGE-N

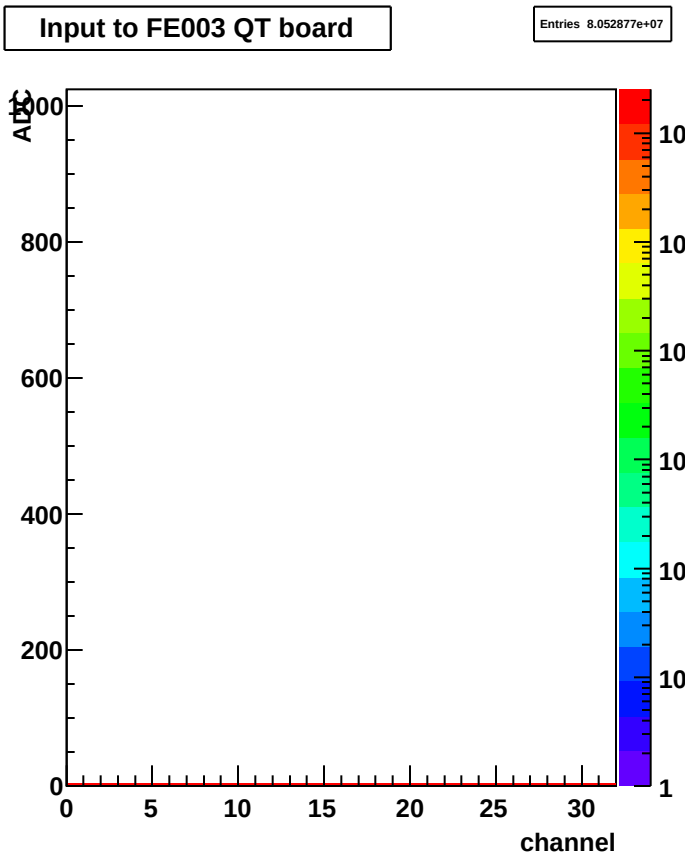

Input to FPD L2 DSM
Entries 7549572
HT bits
28758
208649
85441
62969
158642
120472
53409
2.26616e+06
2.1874e+06
2.37767e+06
SMALL
LARGE-S
LARGE-N

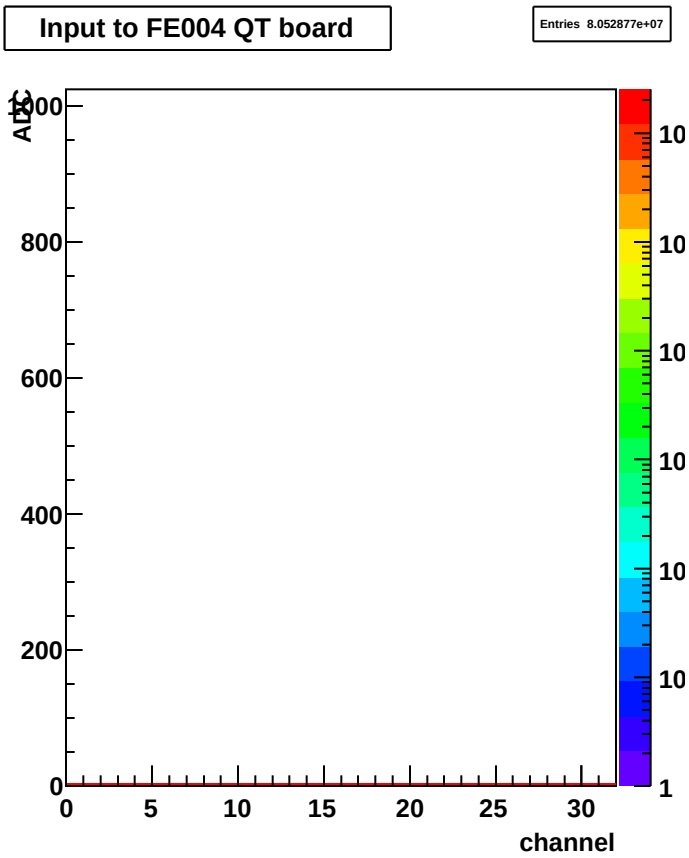

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

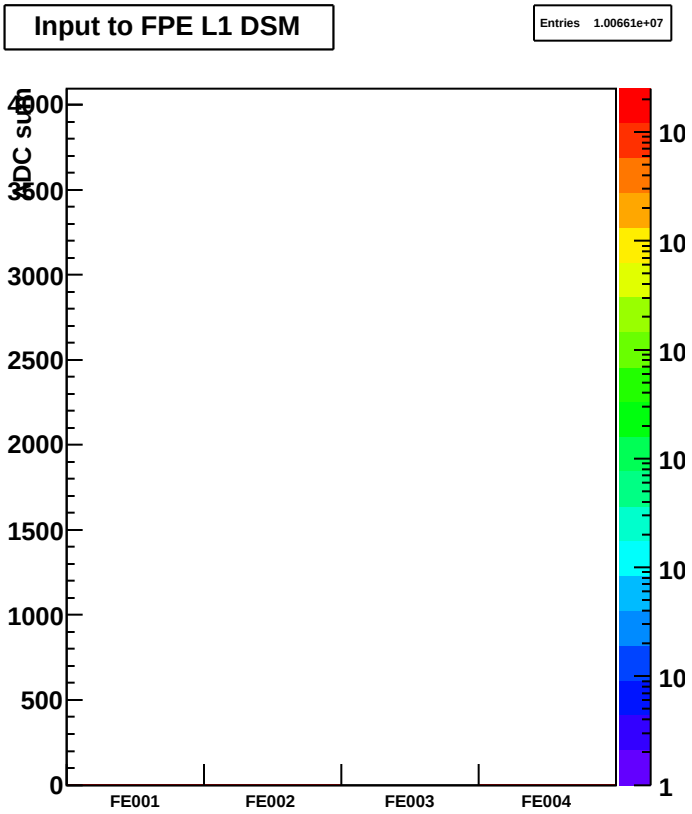

Input to FPD L2 DSM
Entries 1.509914e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 8.052877e+07
ADC
channel

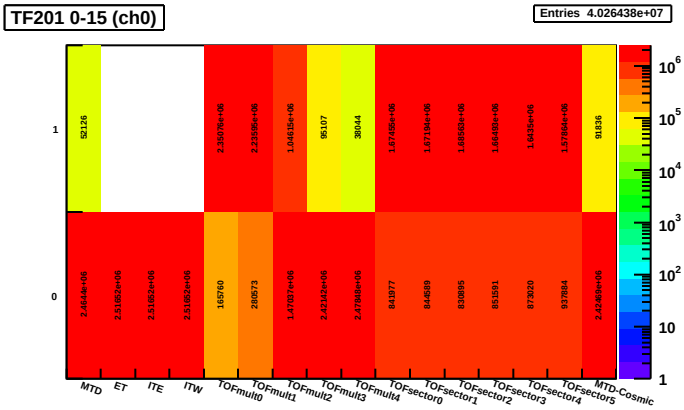

Input to FE002 QT board
Entries 8.052877e+07
ADC
channel


Input to FE003 QT board
Entries 8.052877e+07
ADC
channel


Input to FE004 QT board
Entries 8.052877e+07
ADC
channel


Input to FPE L1 DSM
Entries 1.00661e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

TF201 0-15 (ch0)



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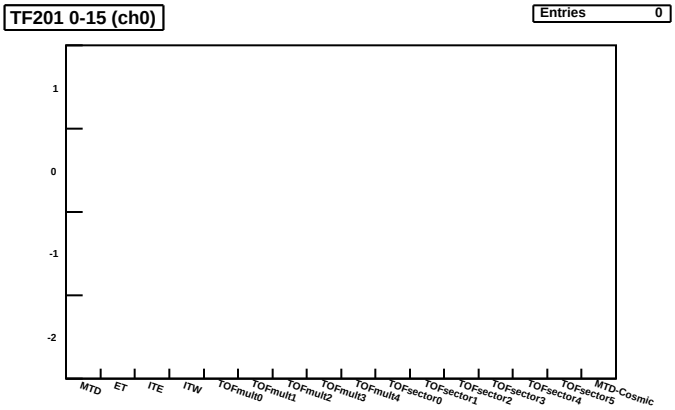

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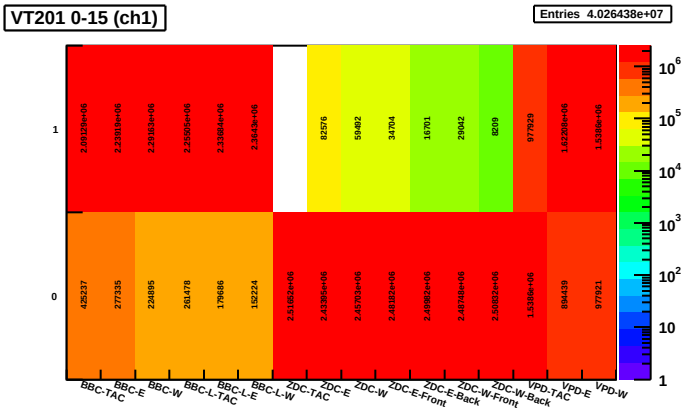

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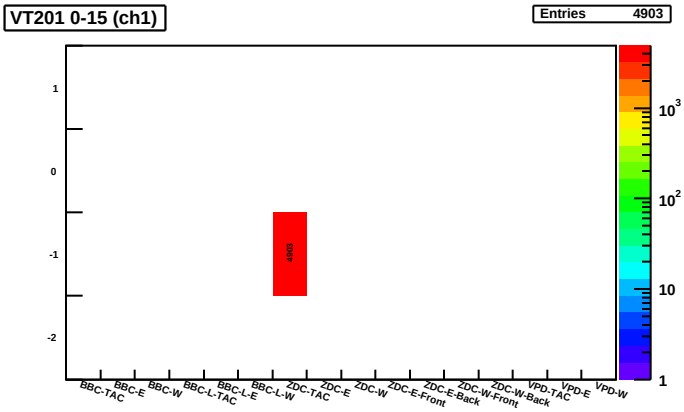

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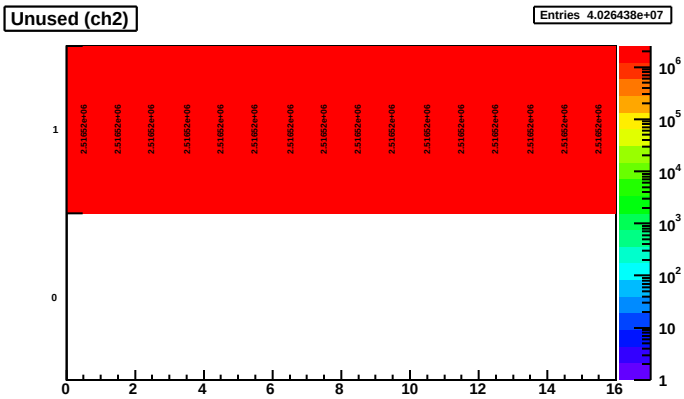

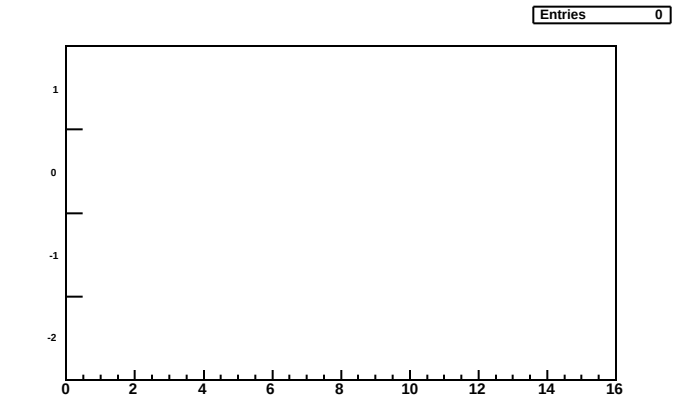

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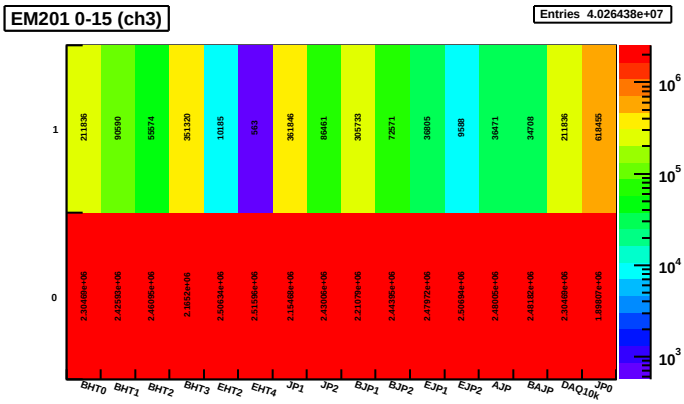

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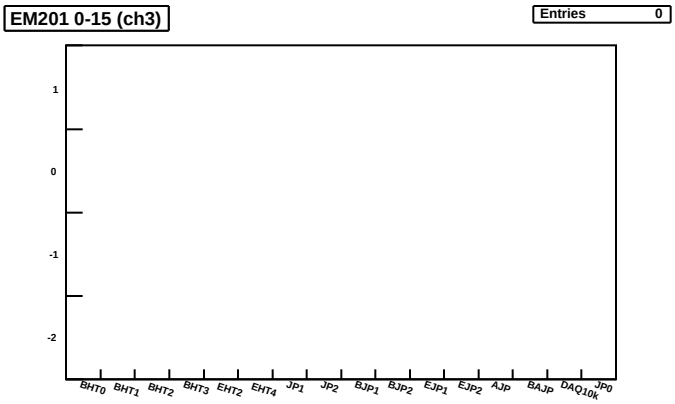
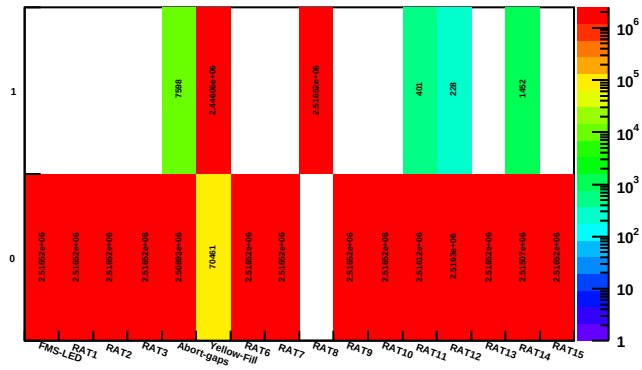
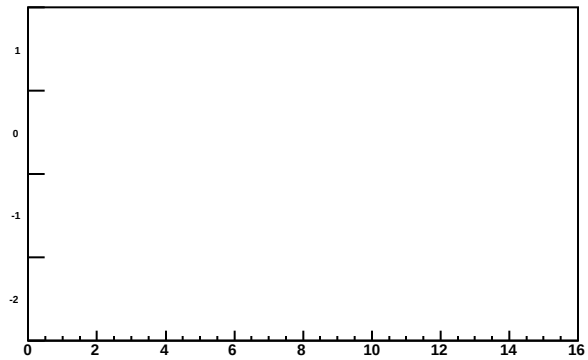


RAT board (ch4)





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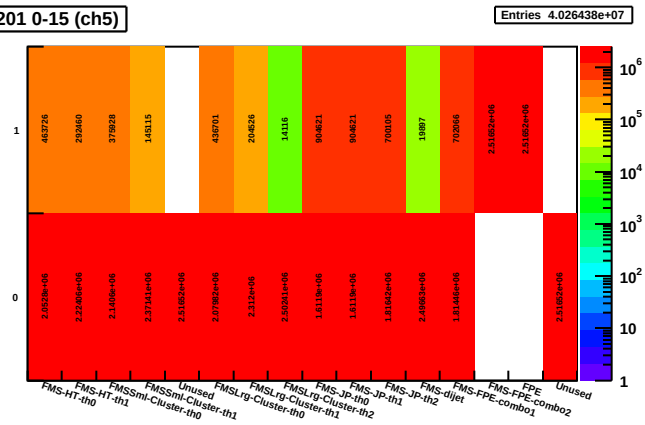

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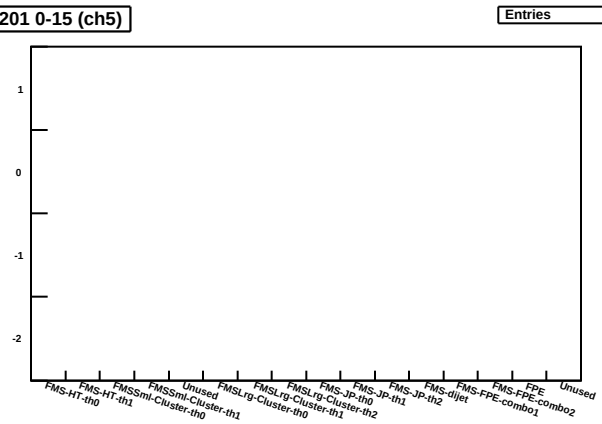

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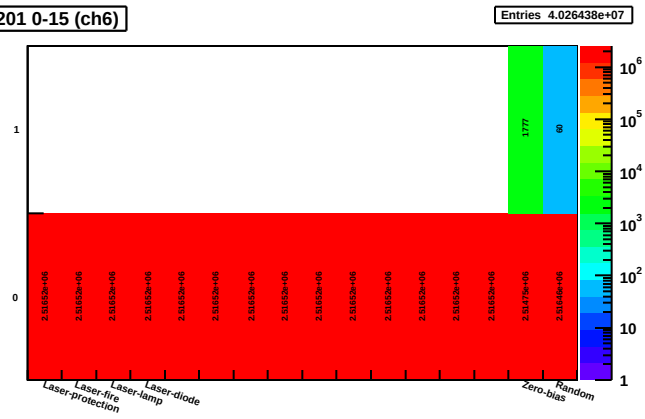

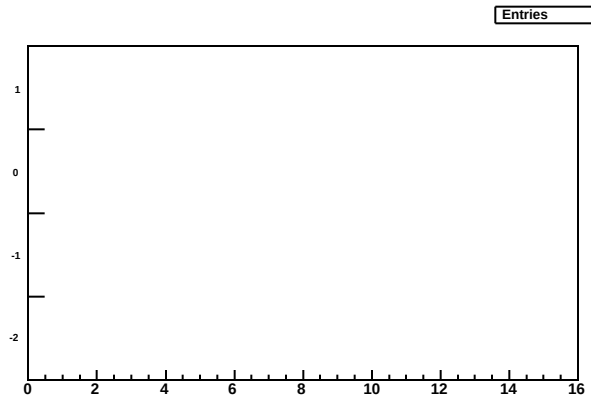
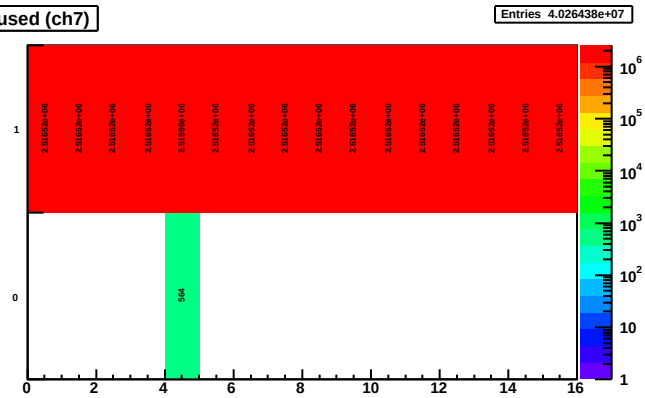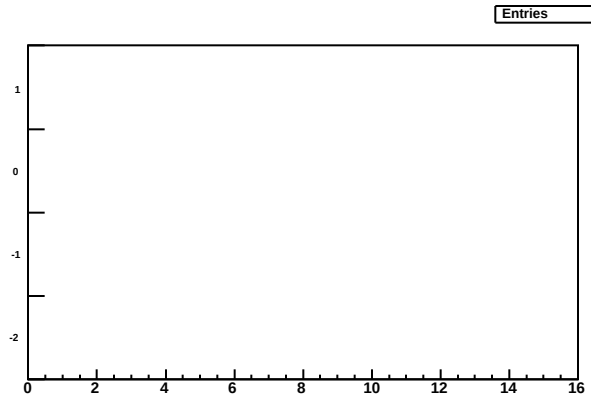
Unused (ch7)