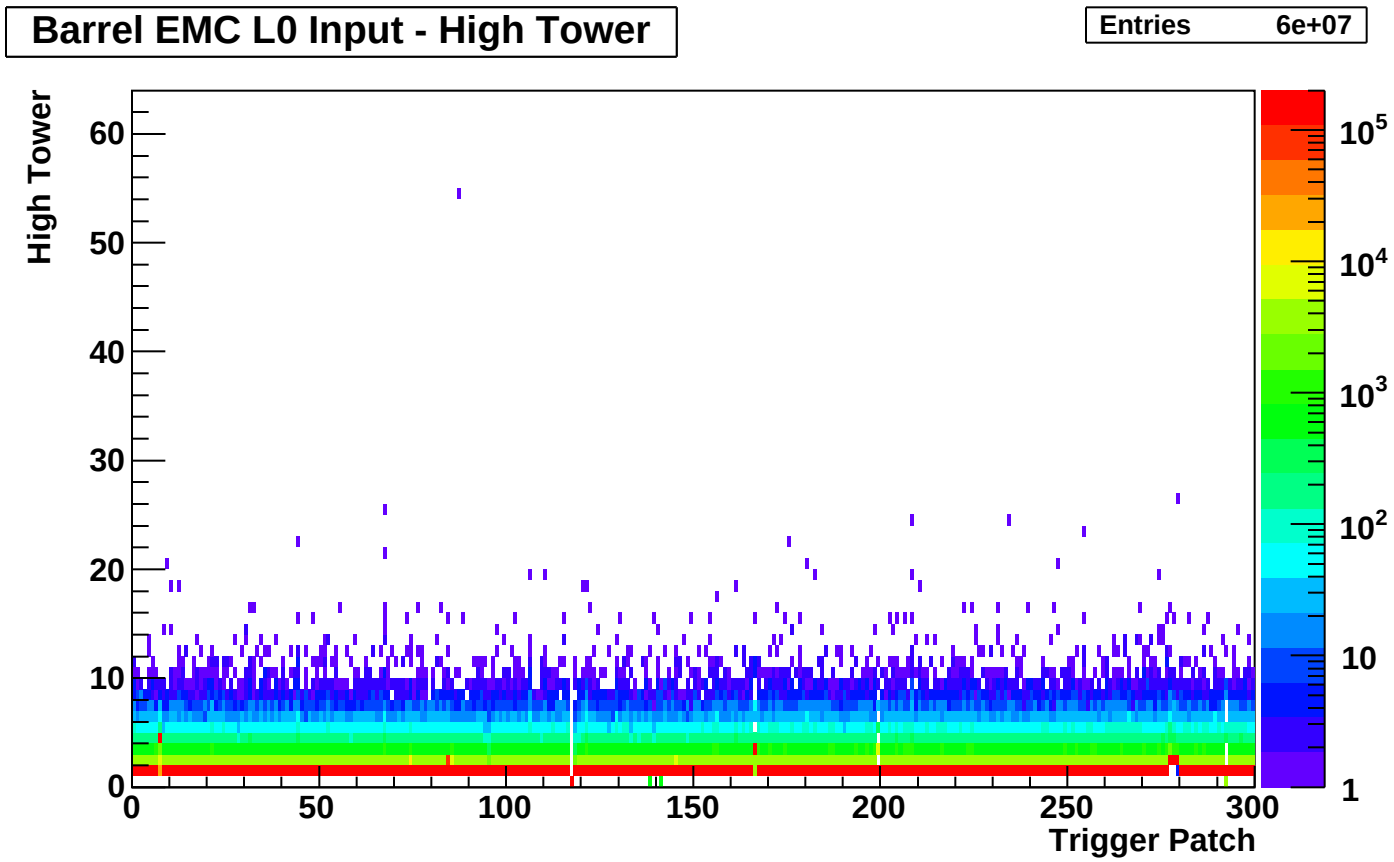
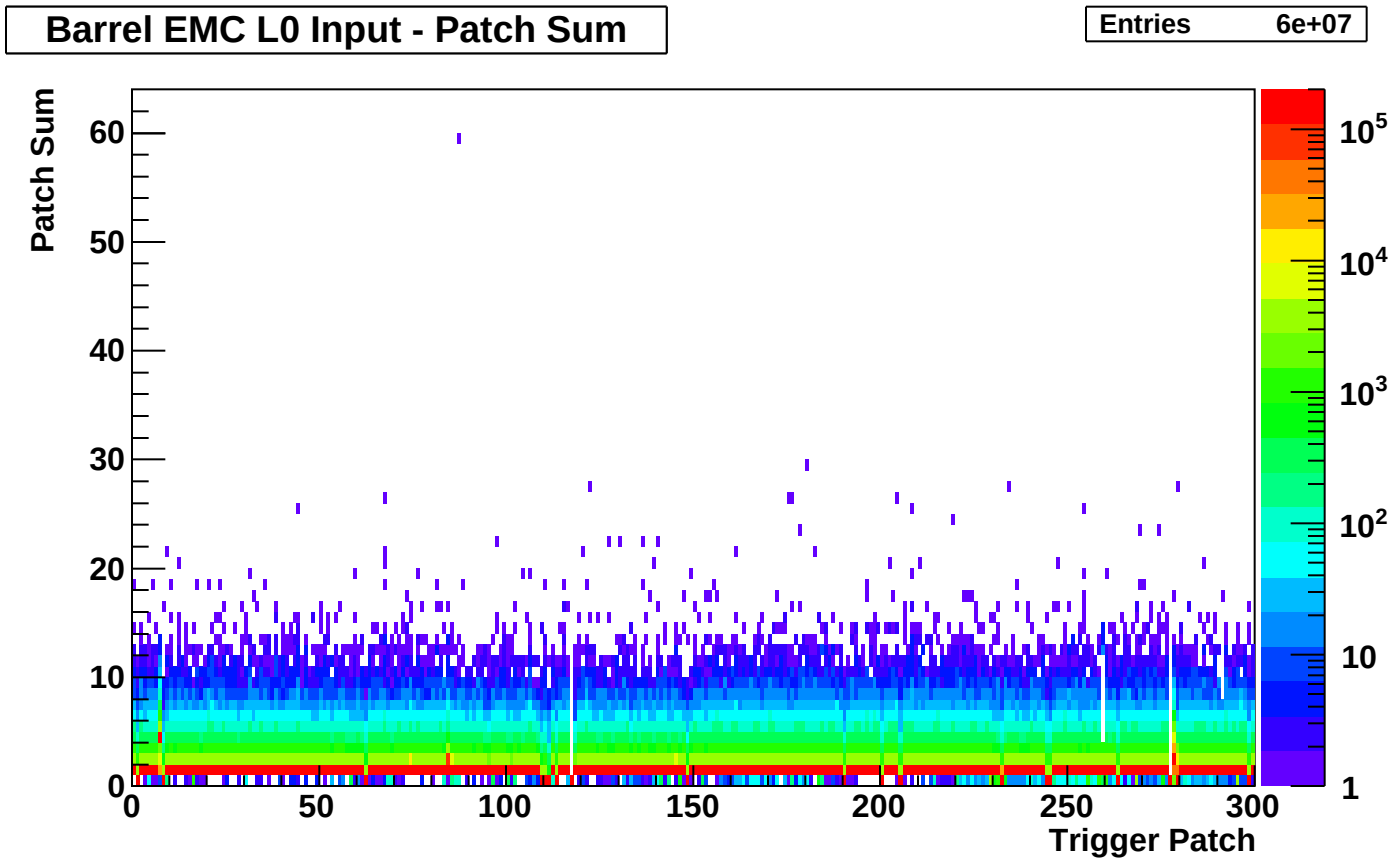
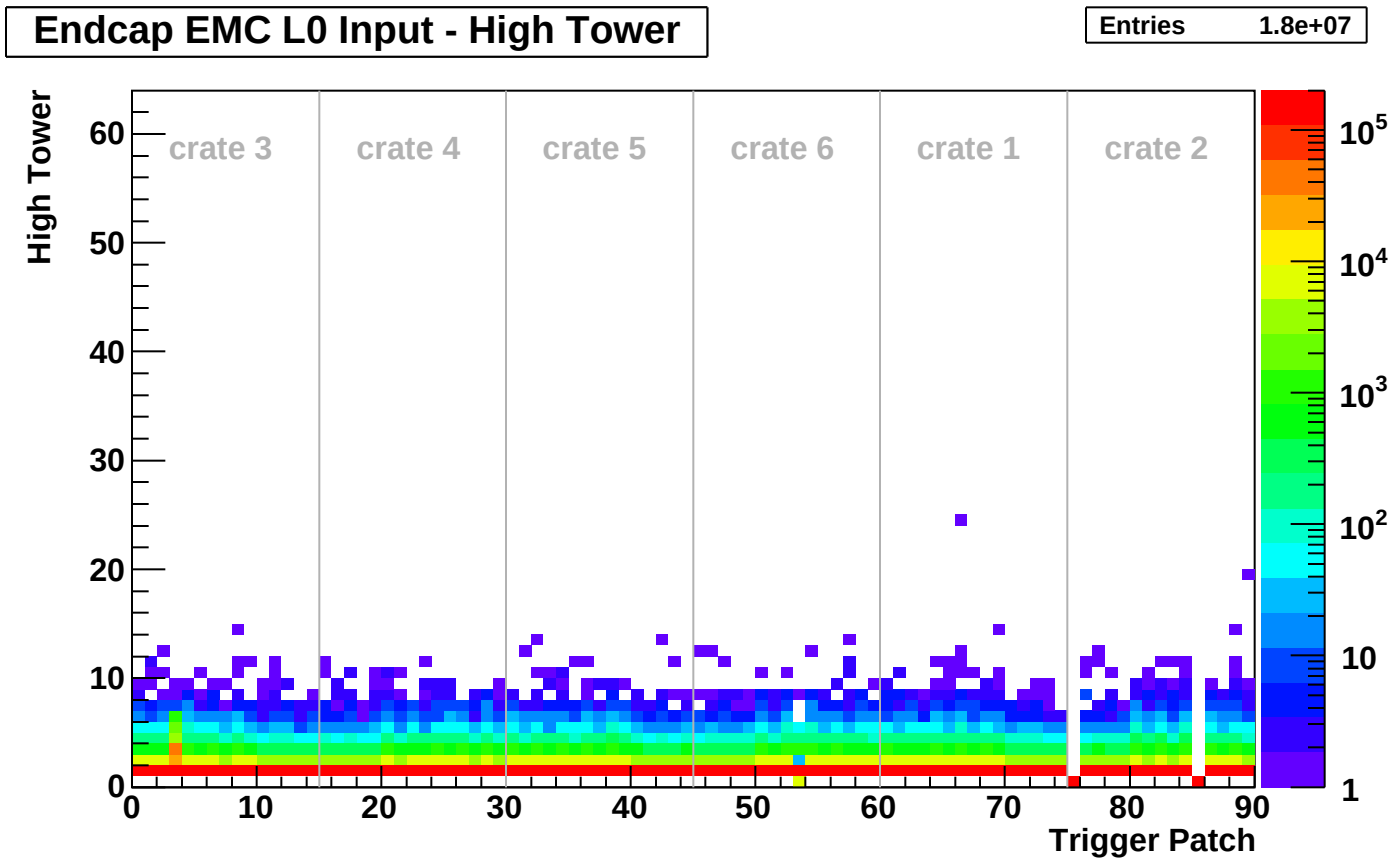
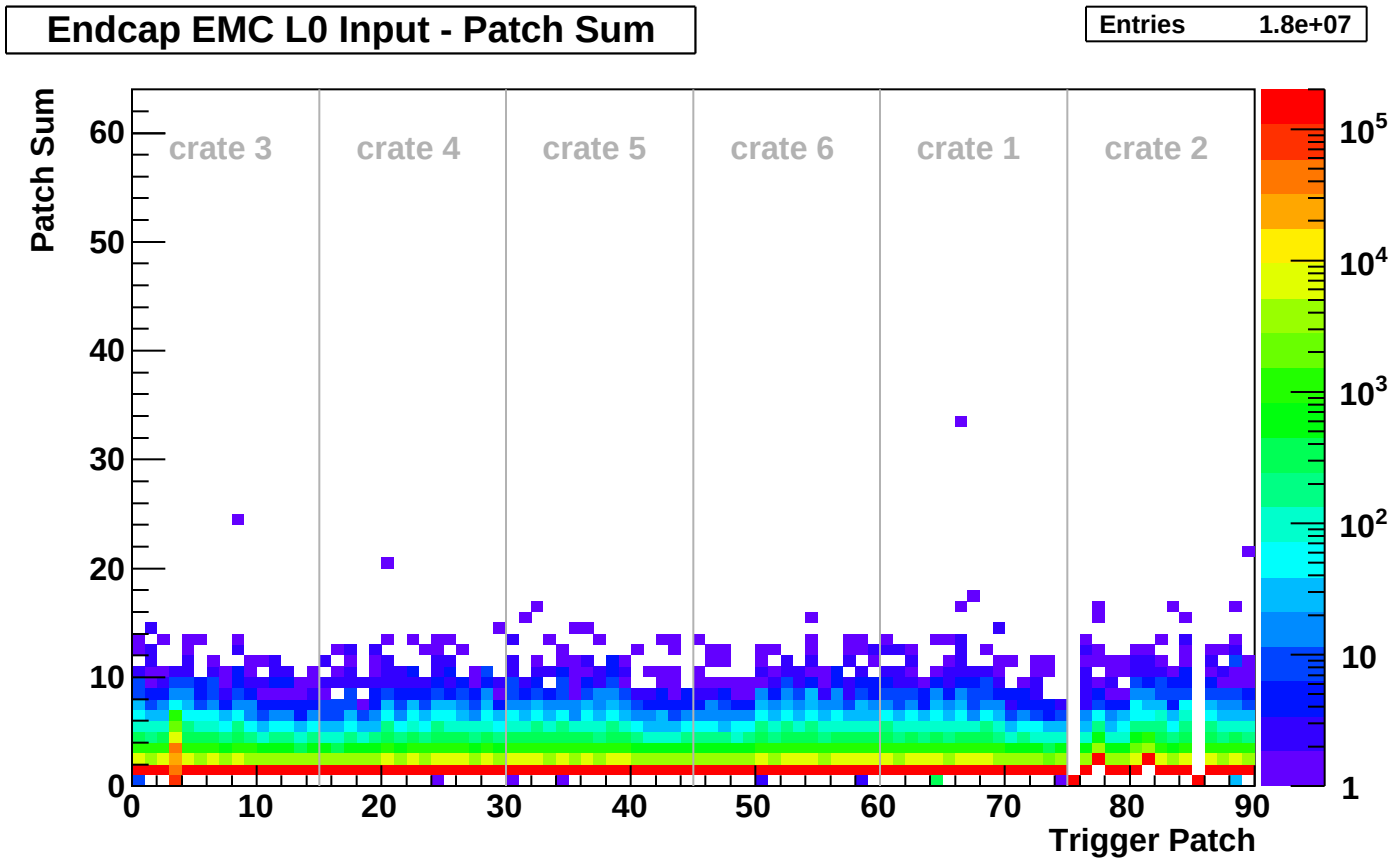
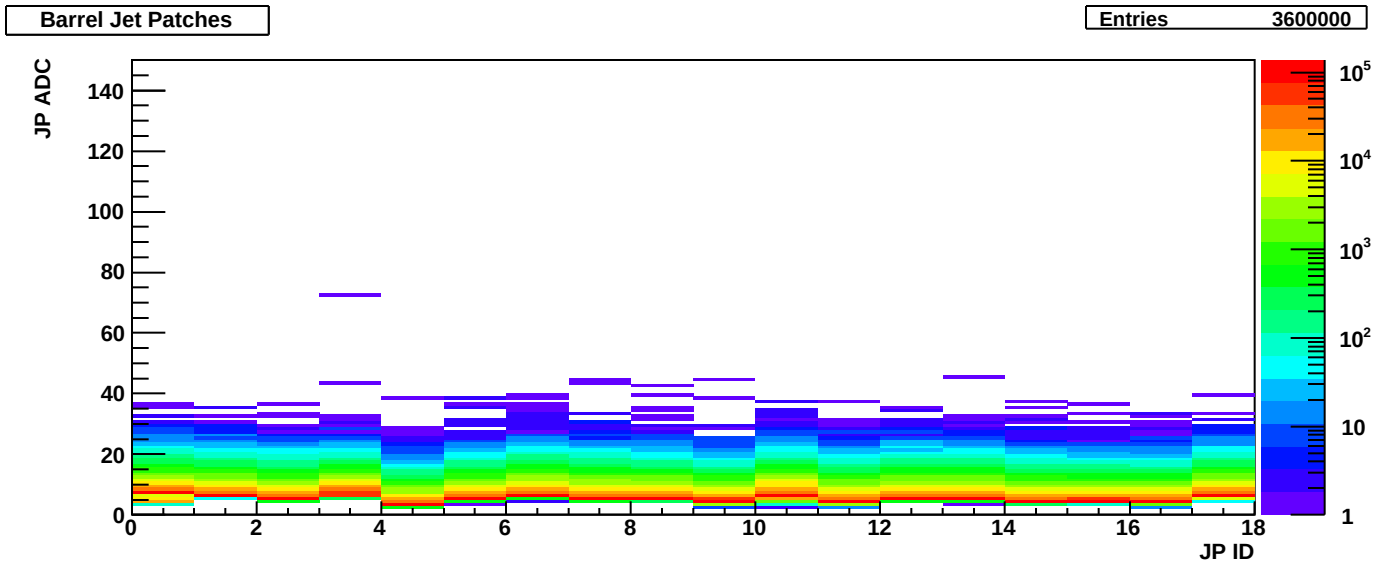
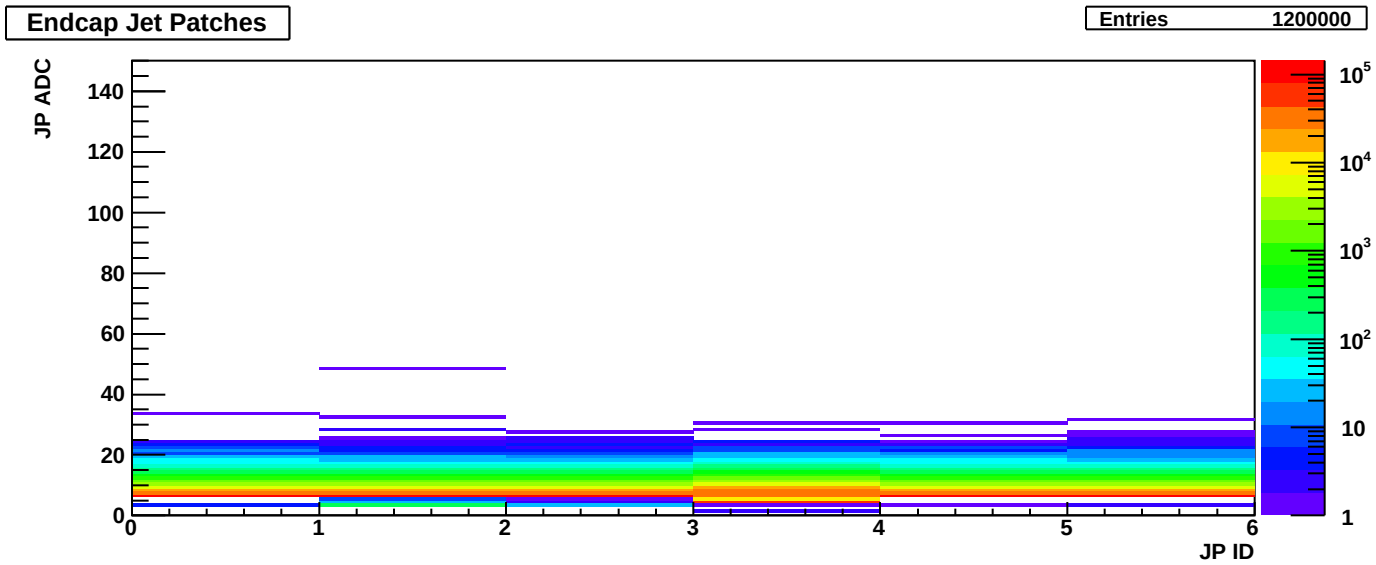

Barrel EMC L0 Input - High Tower
Entries 6e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

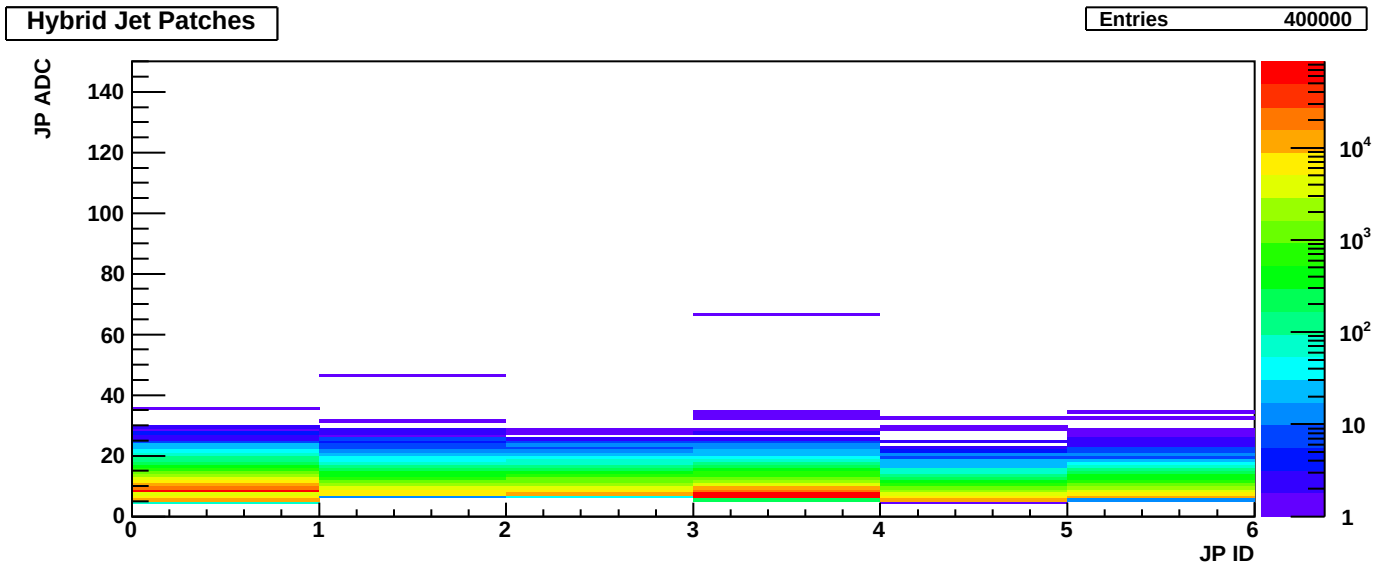

Barrel EMC L0 Input - Patch Sum
Entries 6e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

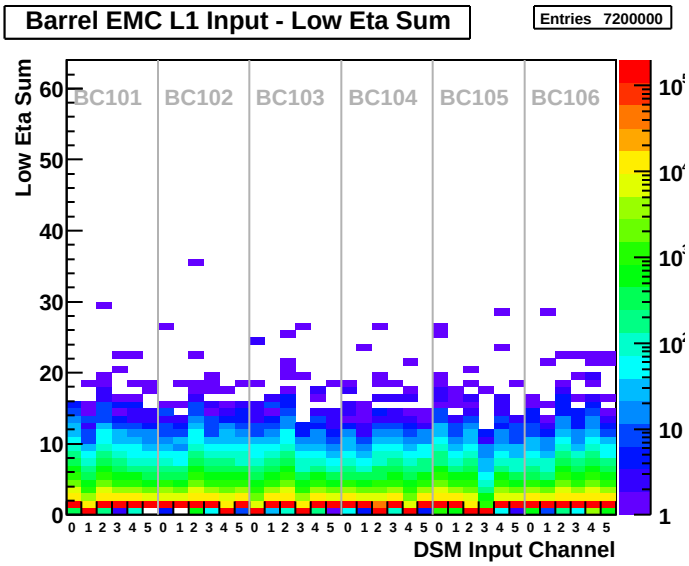

Endcap EMC L0 Input - High Tower
Entries 1.8e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

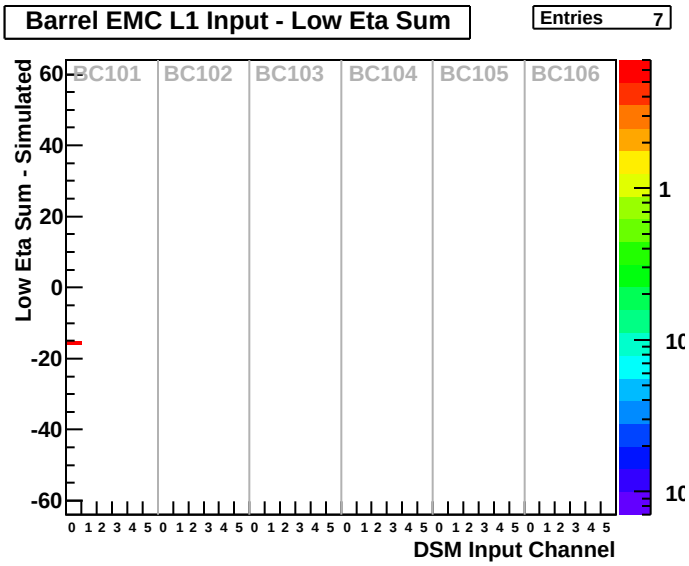

Endcap EMC L0 Input - Patch Sum
Entries 1.8e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

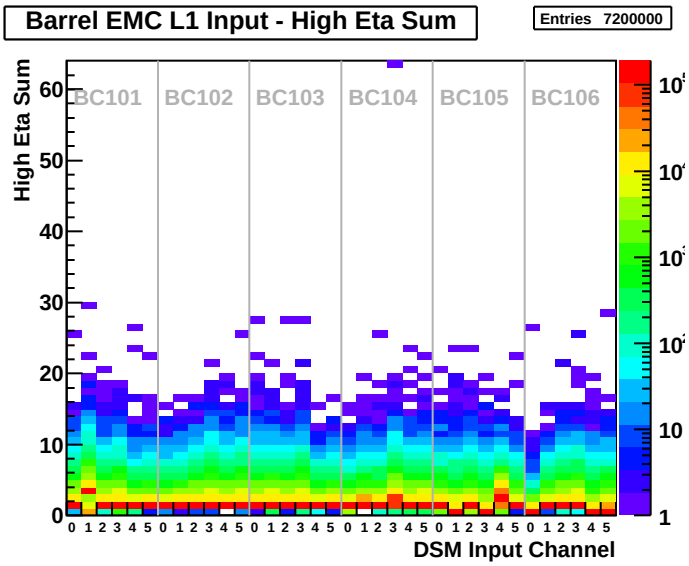

Barrel Jet Patches
Entries 3600000
JP ADC
JP ID

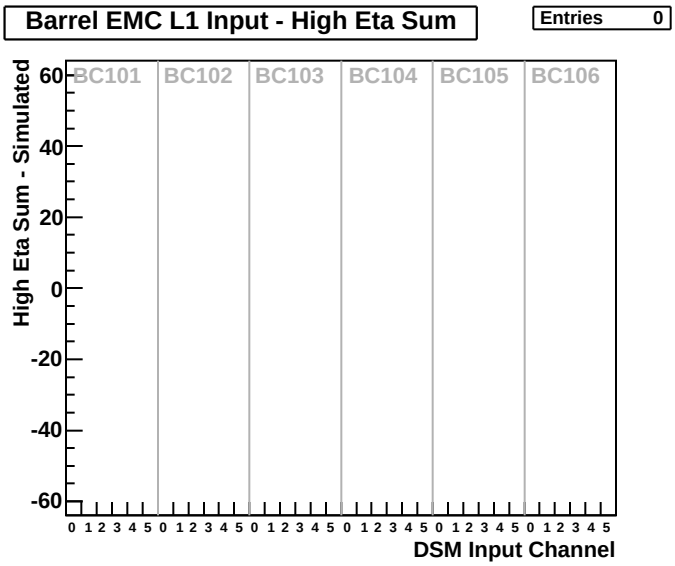

Endcap Jet Patches
Entries 1200000
JP ADC
JP ID

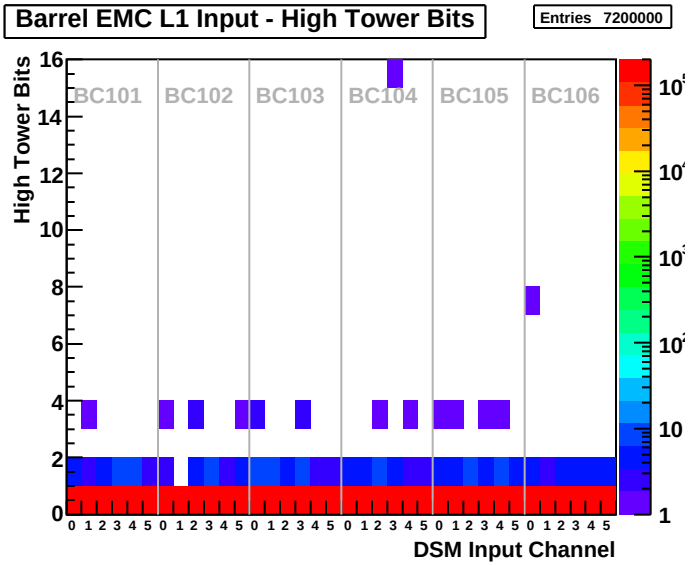

Hybrid Jet Patches
Entries 400000
JP ADC
JP ID

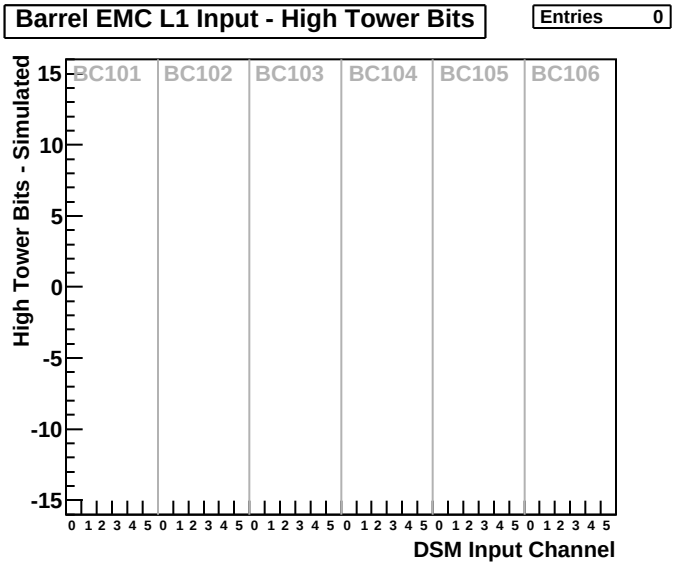

Barrel EMC L1 Input - Low Eta Sum
Entries 7200000
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

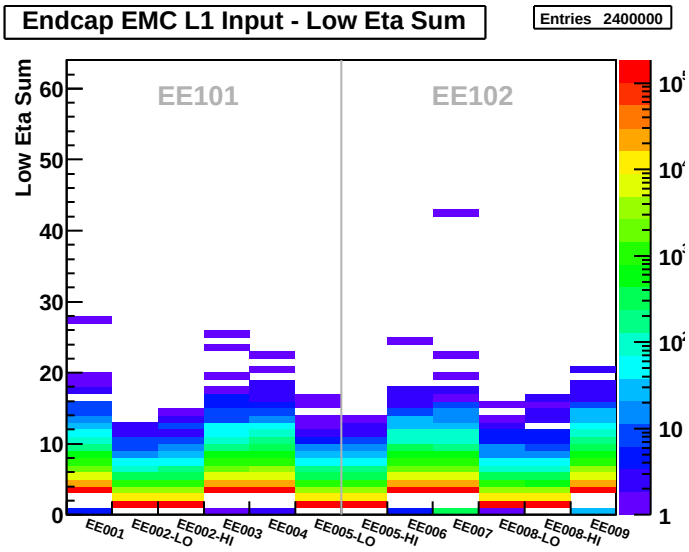

Barrel EMC L1 Input - Low Eta Sum
Entries 7
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

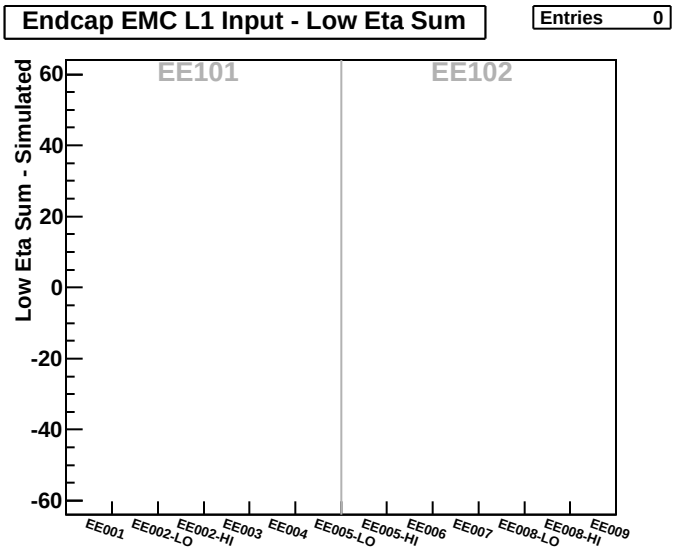

Barrel EMC L1 Input - High Eta Sum
Entries 7200000
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

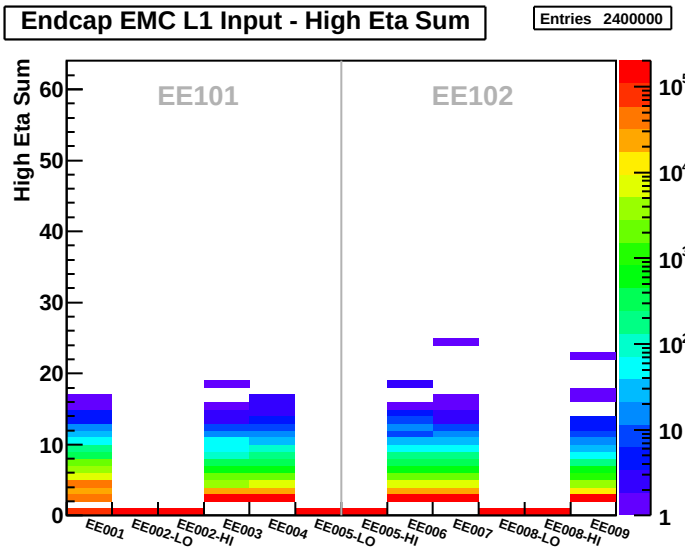

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

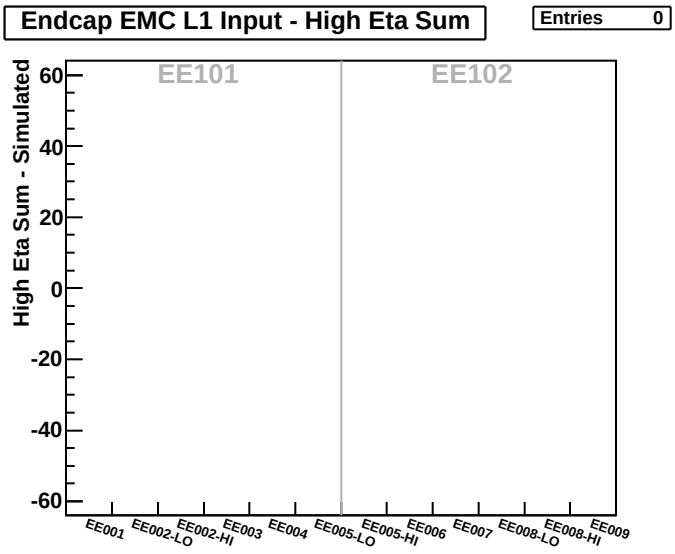

Barrel EMC L1 Input - High Tower Bits
Entries 7200000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

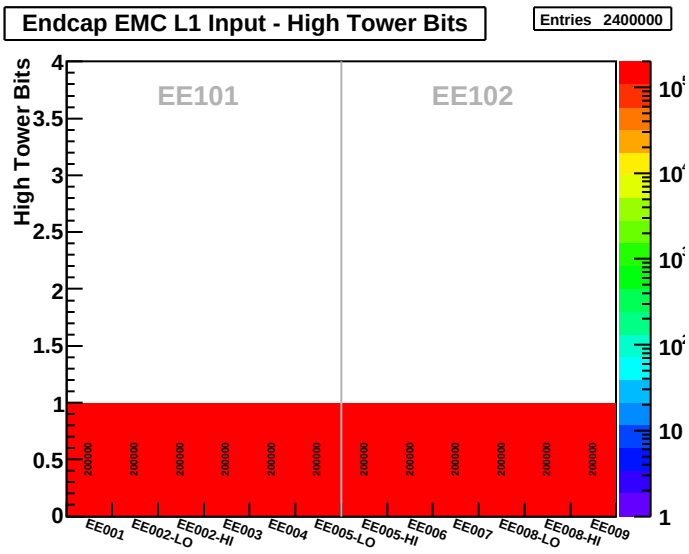

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

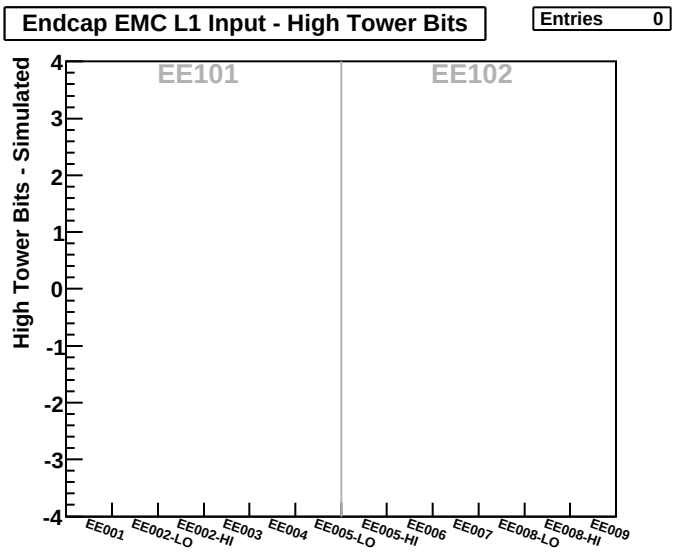

Endcap EMC L1 Input - Low Eta Sum
Entries 2400000
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

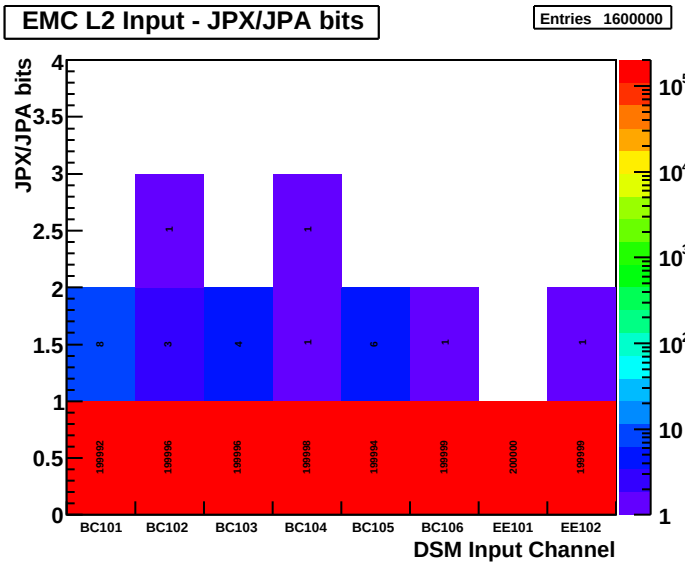

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

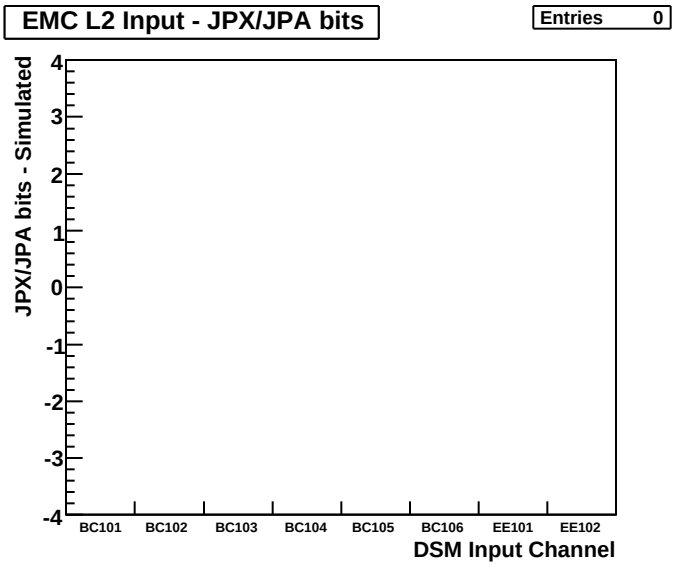

Endcap EMC L1 Input - High Eta Sum
Entries 2400000
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

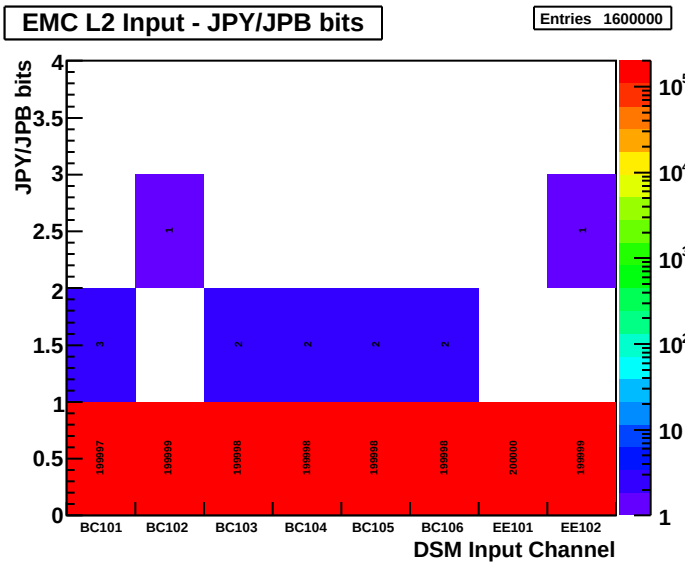

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

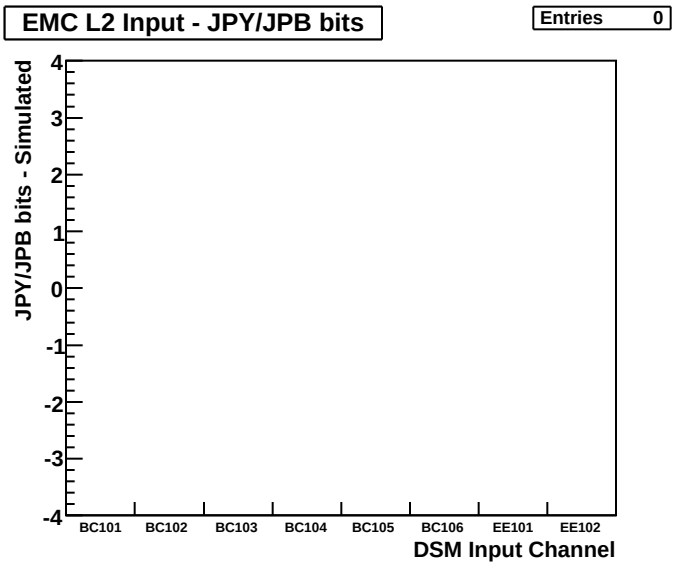

Endcap EMC L1 Input - High Tower Bits
Entries 2400000
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

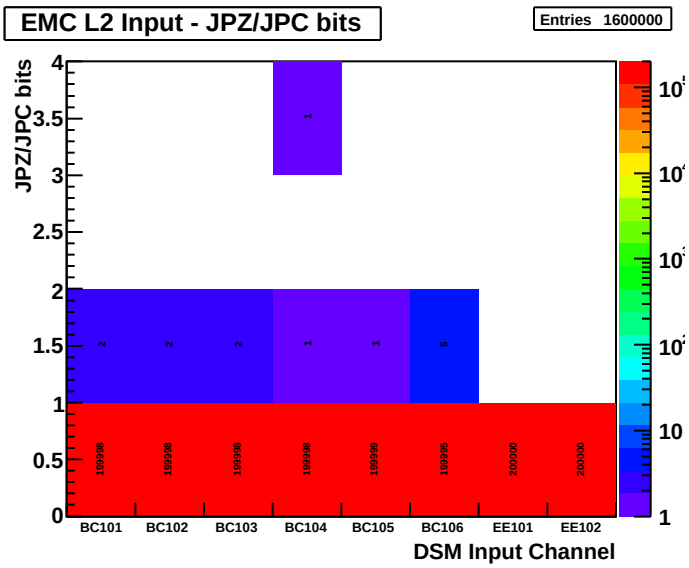

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

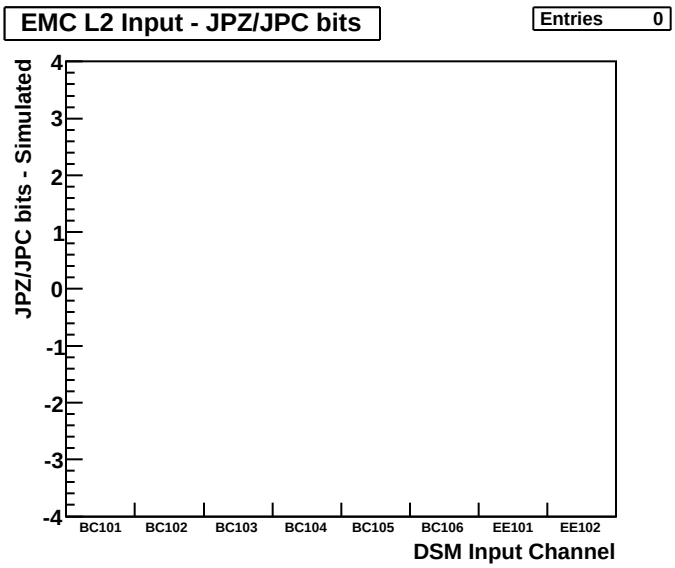

EMC L2 Input - JPX/JPA bits
Entries 1600000
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

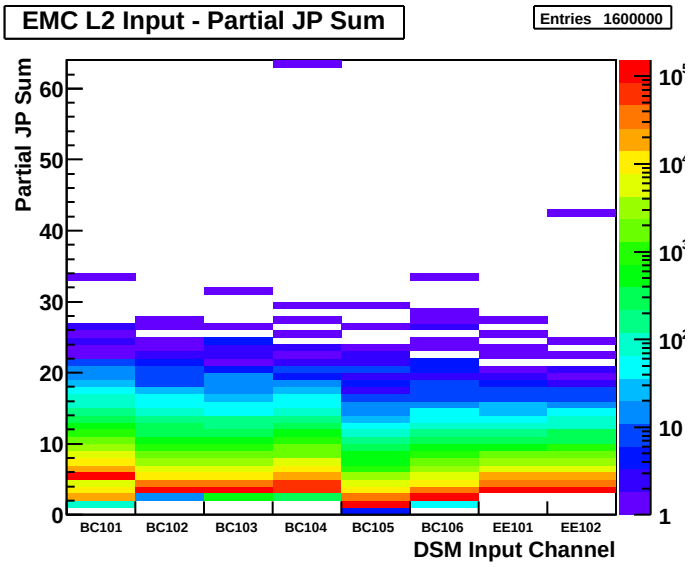

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

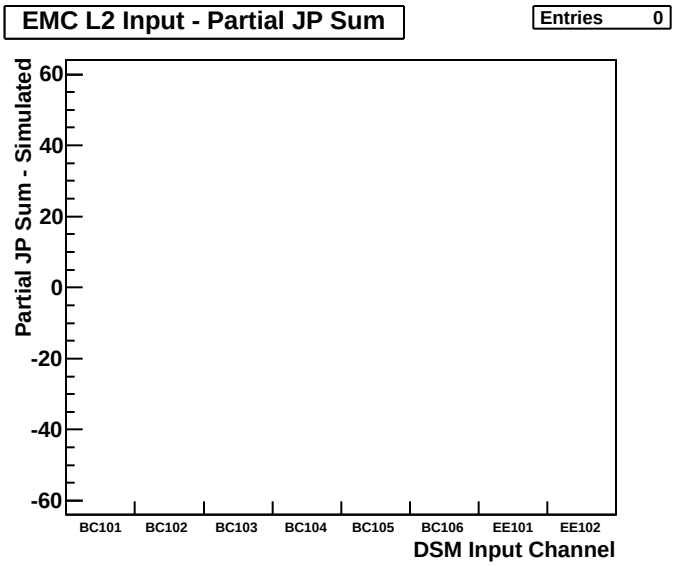

EMC L2 Input - JPY/JPB bits
Entries 1600000
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

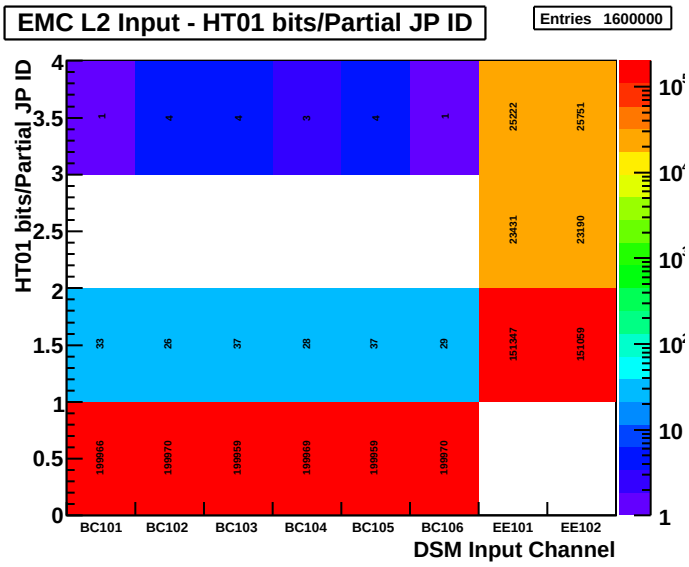

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

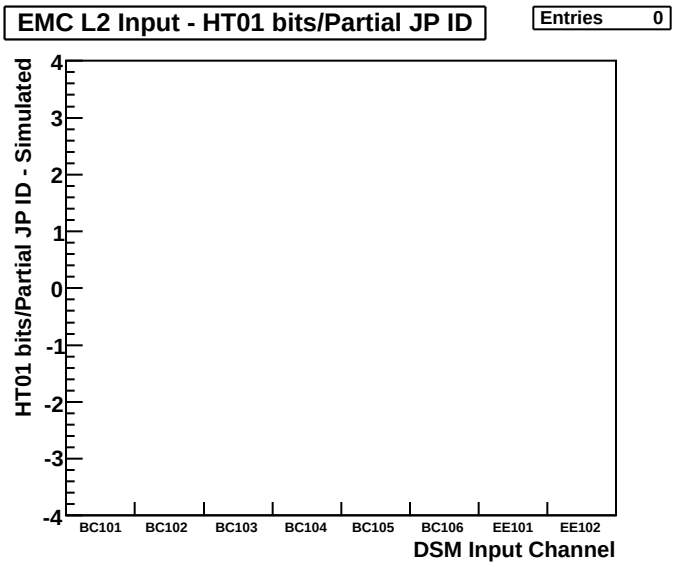

EMC L2 Input - JPZ/JPC bits
Entries 1600000
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

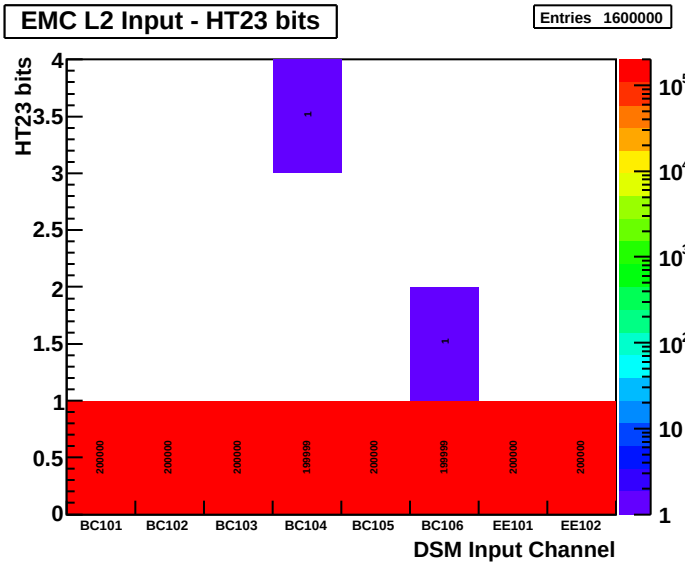

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

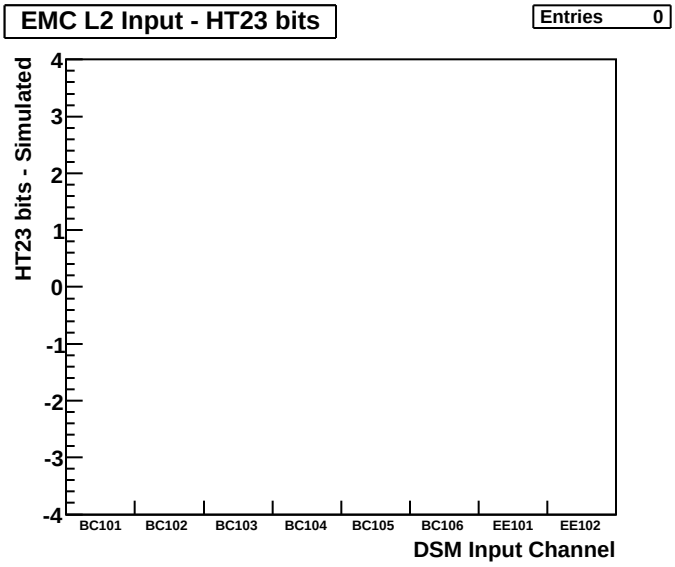

EMC L2 Input - Partial JP Sum
Entries 1600000
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

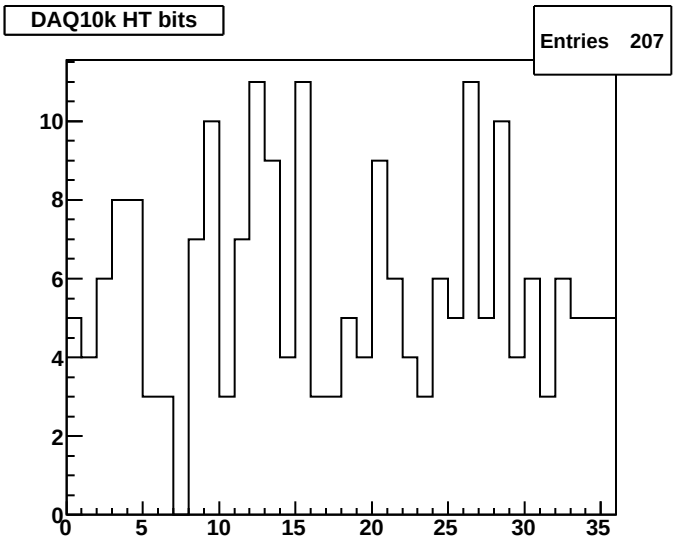

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

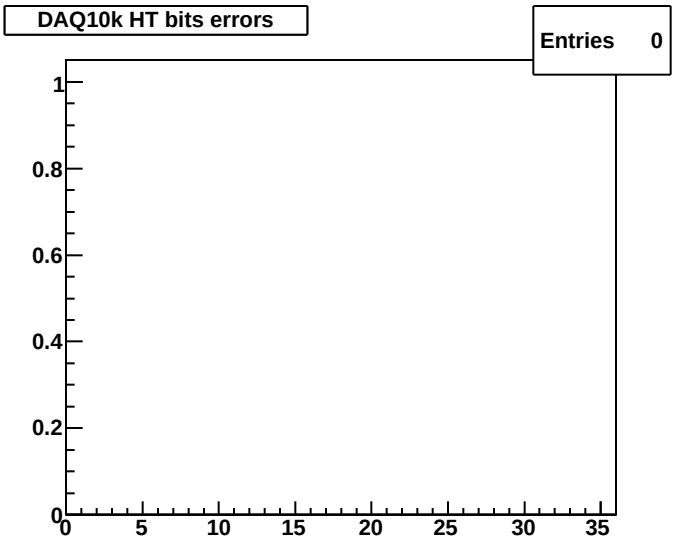

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1600000
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

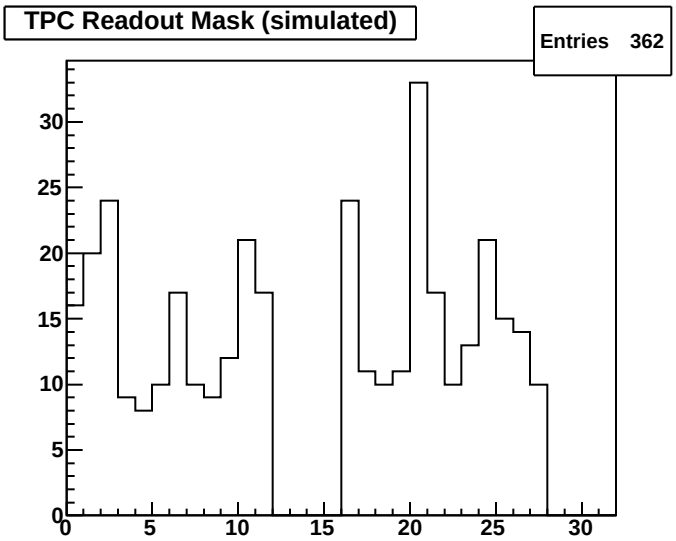

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

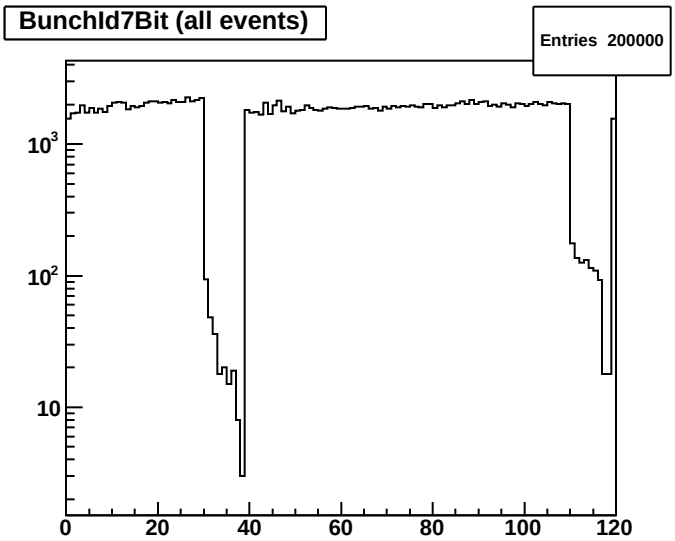

EMC L2 Input - HT23 bits
Entries 1600000
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

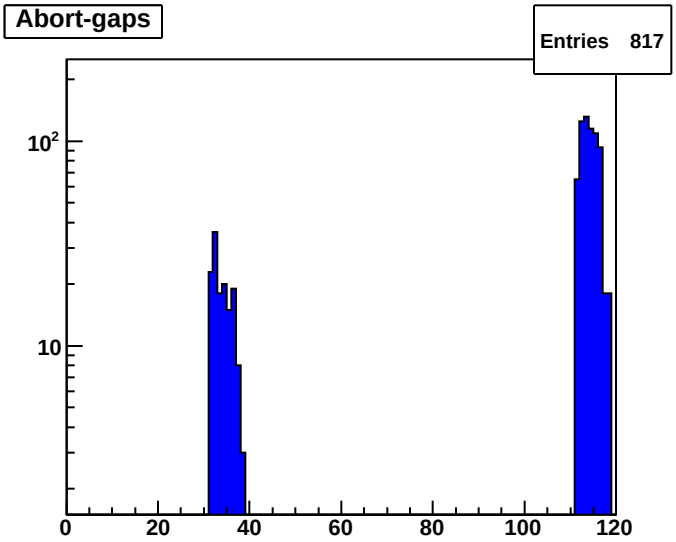

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

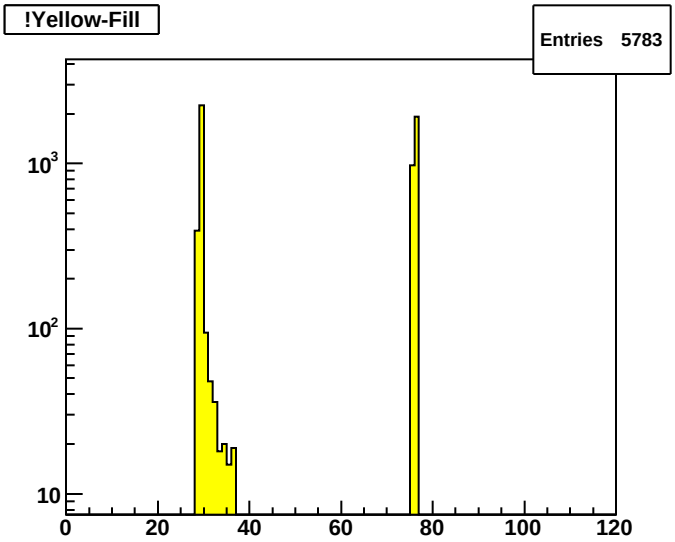

DAQ10k HT bits
Entries 207

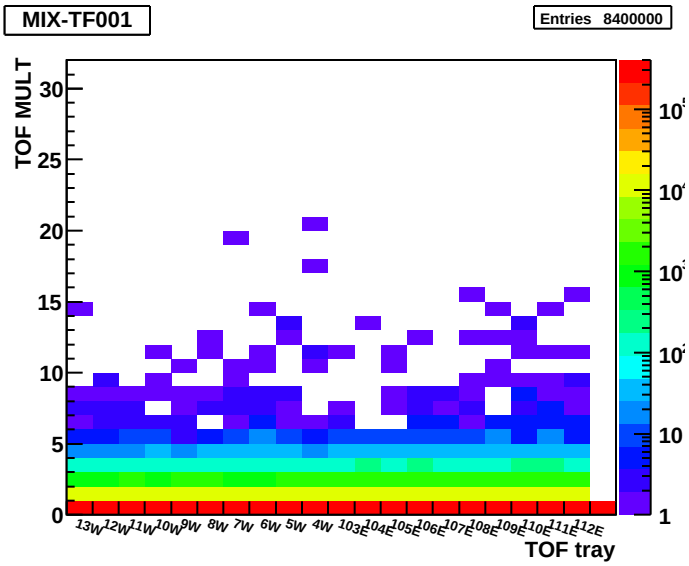

DAQ10k HT bits errors
Entries 0

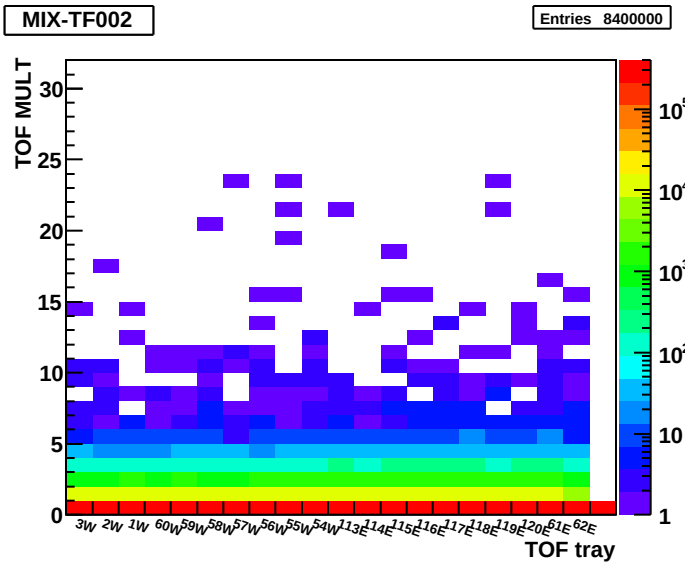

TPC Readout Mask (simulated)
Entries 362

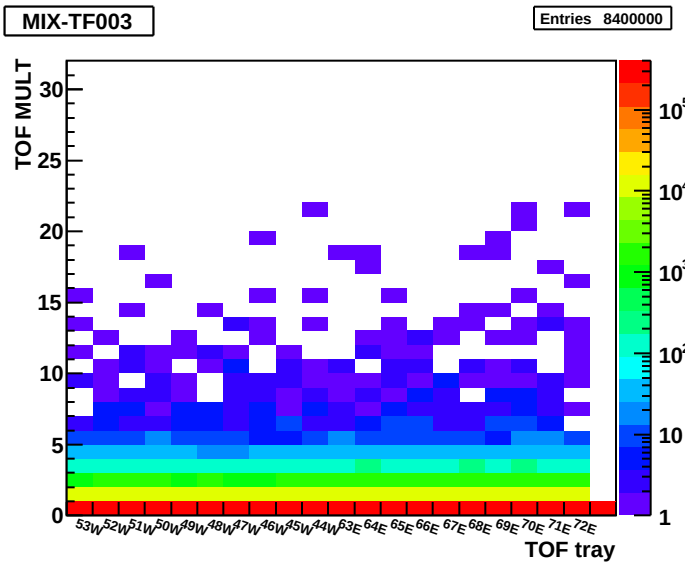

BunchId7Bit (all events)
Entries 200000

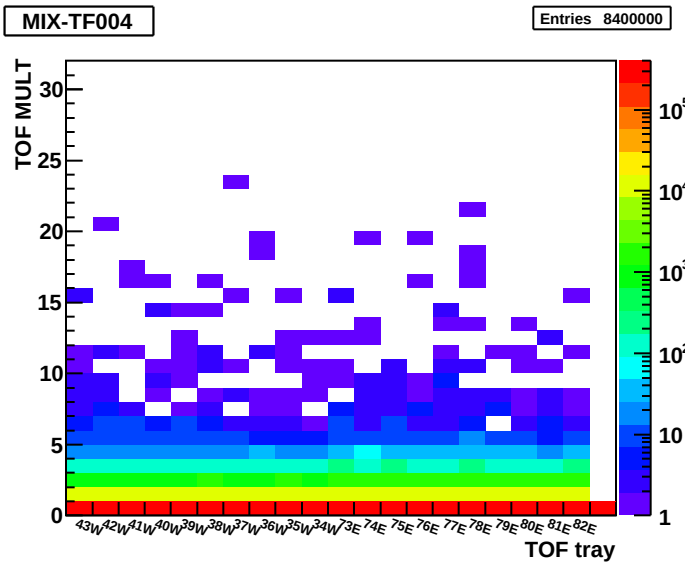

Abort-gaps
Entries 817

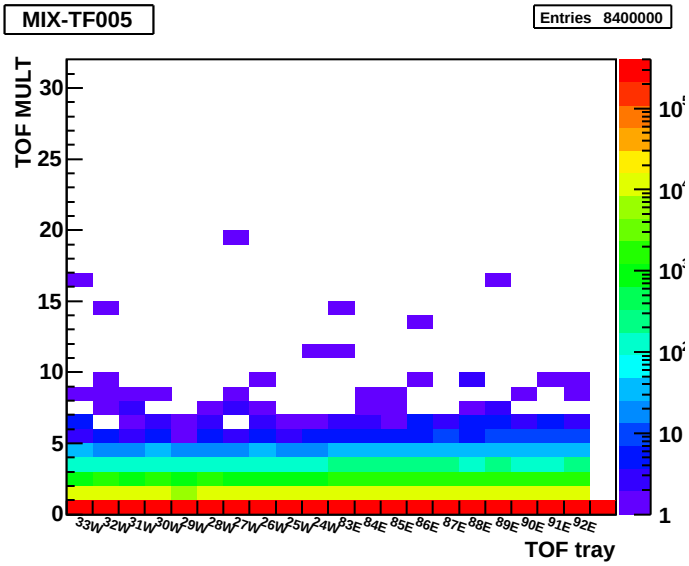

!Yellow-Fill
Entries 5783

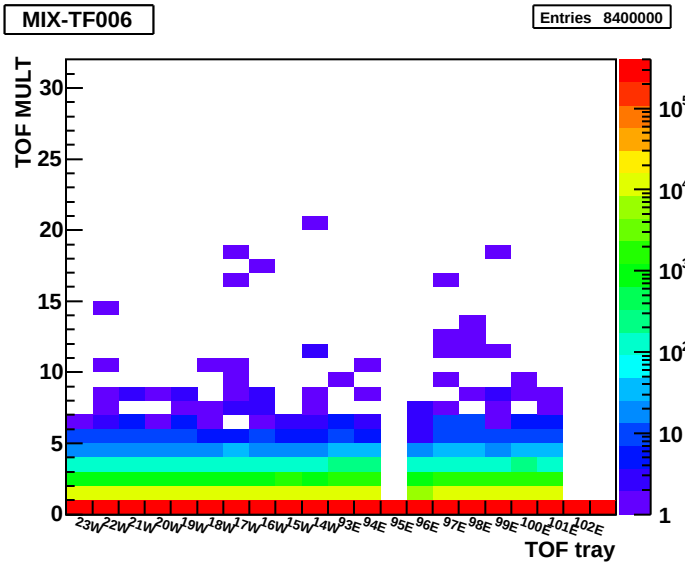

MIX-TF001
Entries 8400000
TOF MULT
TOF tray

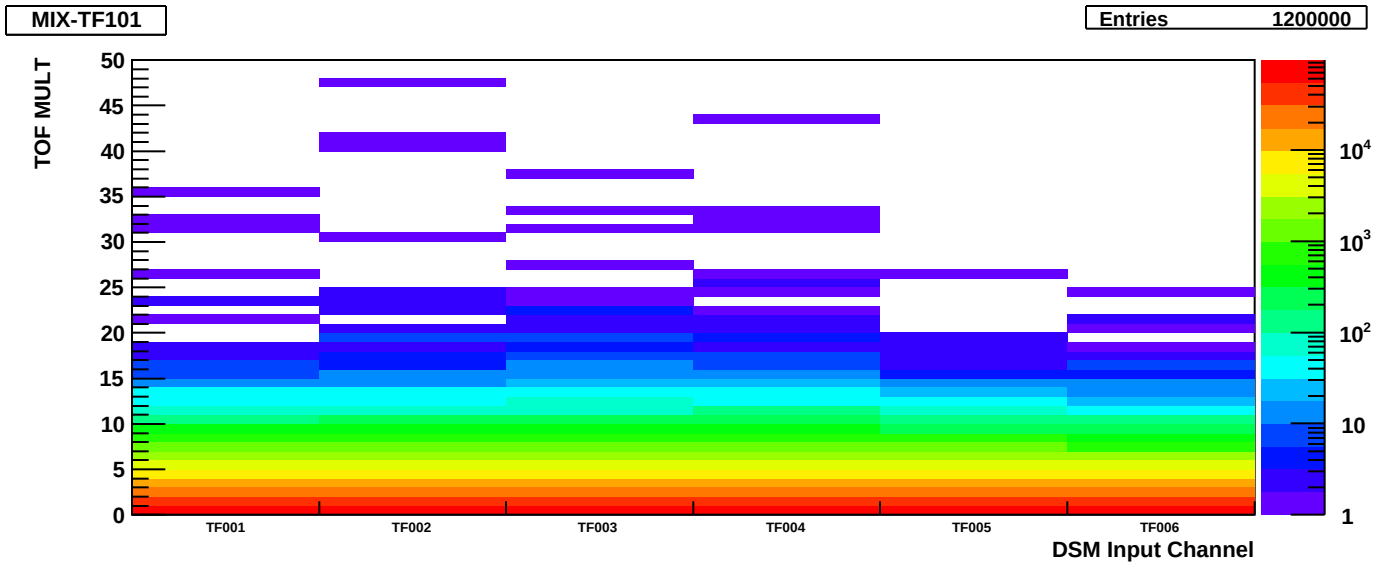

MIX-TF002
Entries 8400000
TOF MULT
TOF tray

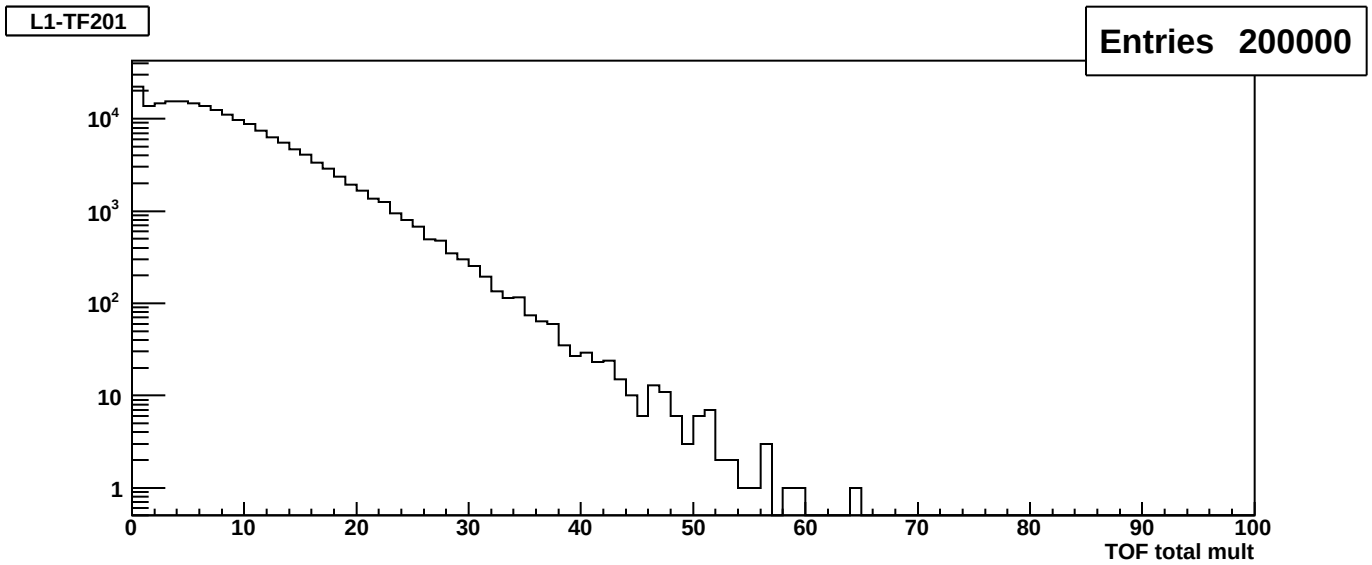

MIX-TF003
Entries 8400000
TOF MULT
TOF tray

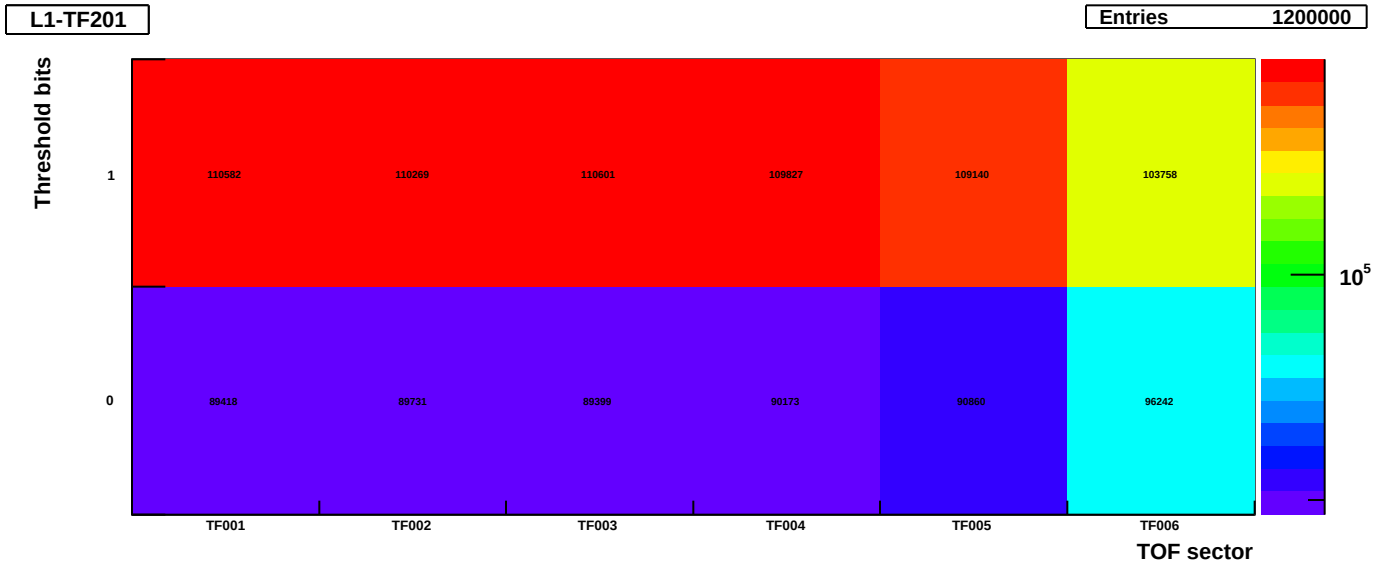

MIX-TF004
Entries 8400000
TOF MULT
TOF tray

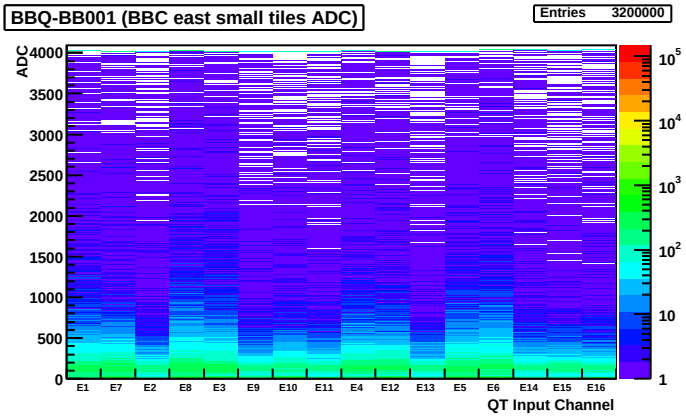

MIX-TF005
Entries 8400000
TOF MULT
TOF tray

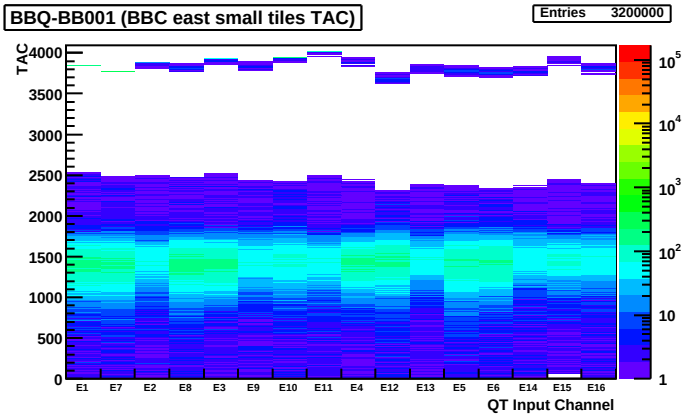

MIX-TF006
Entries 8400000
TOF MULT
TOF tray

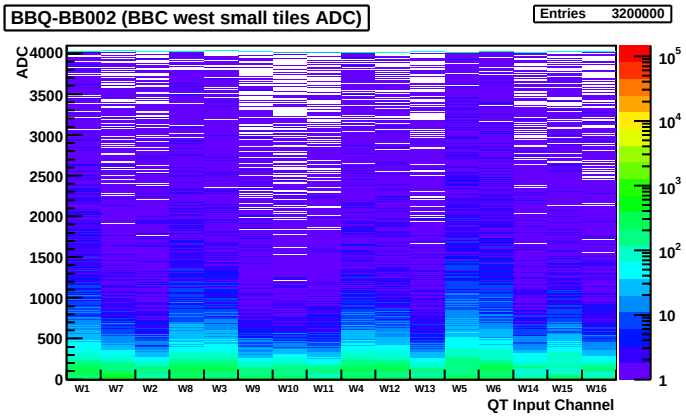

MIX-TF101
Entries 1200000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

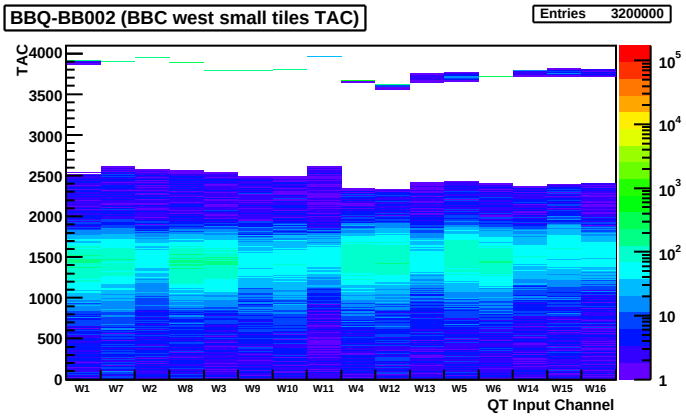

L1-TF201
Entries 200000
TOF total mult

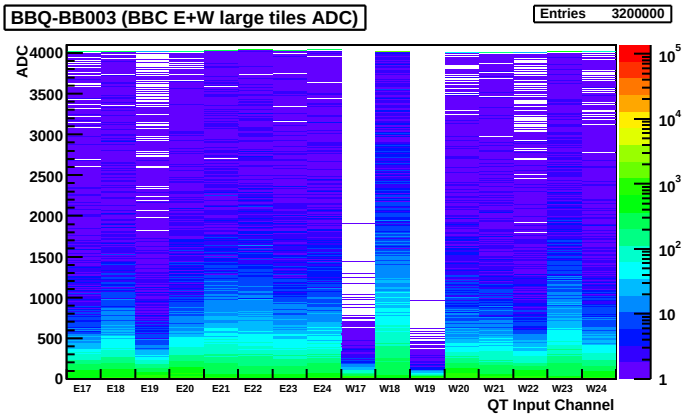

L1-TF201
Entries 1200000
Threshold bits
110582
110269
110601
109827
109140
103758
89418
89731
89399
90173
90860
96242
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

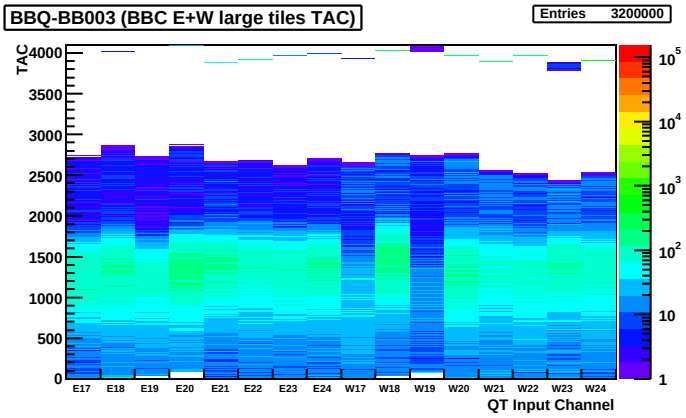

BBQ-BB001 (BBC east small tiles ADC)
Entries 3200000
ADC
QT Input Channel

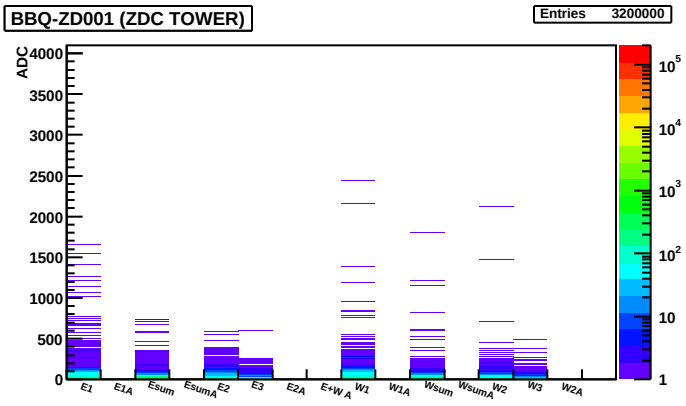

BBQ-BB001 (BBC east small tiles TAC)
Entries 3200000
TAC
QT Input Channel

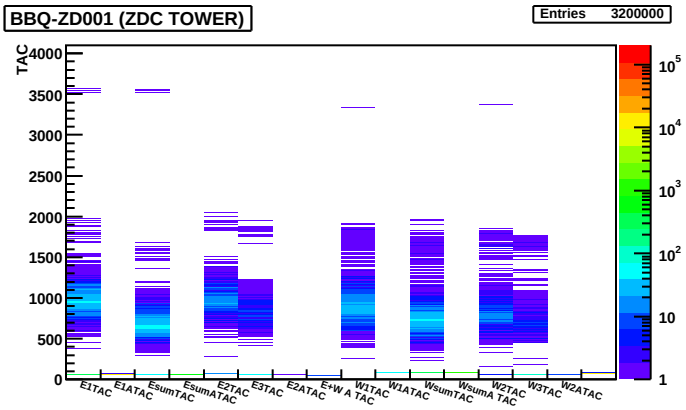

BBQ-BB002 (BBC west small tiles ADC)
Entries 3200000
ADC
QT Input Channel

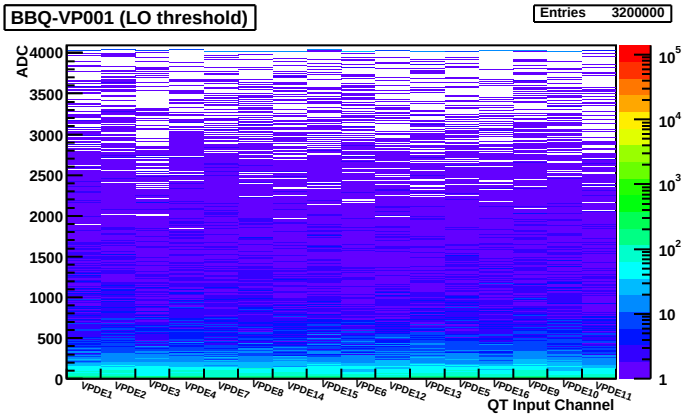

BBQ-BB002 (BBC west small tiles TAC)
Entries 3200000
TAC
QT Input Channel

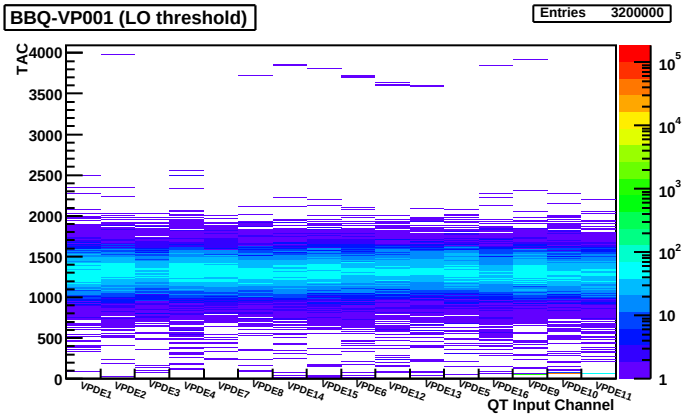

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3200000
ADC
QT Input Channel

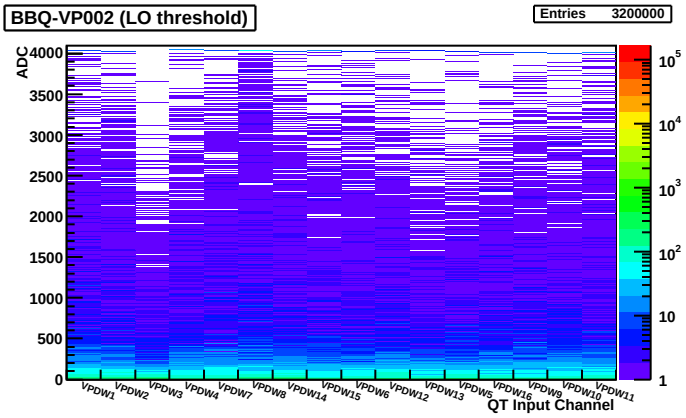

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3200000
TAC
QT Input Channel

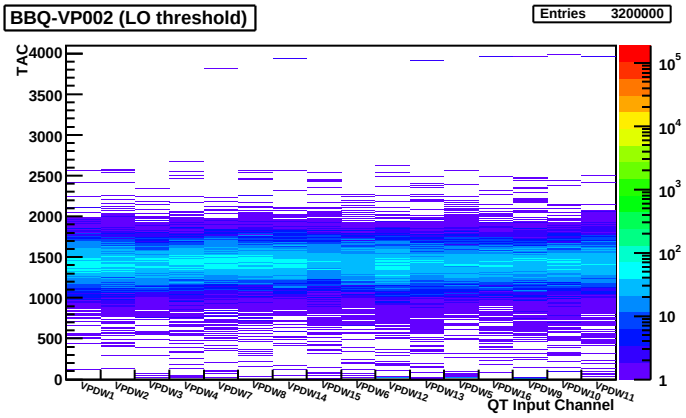

BBQ-ZD001 (ZDC TOWER)
Entries 3200000
ADC

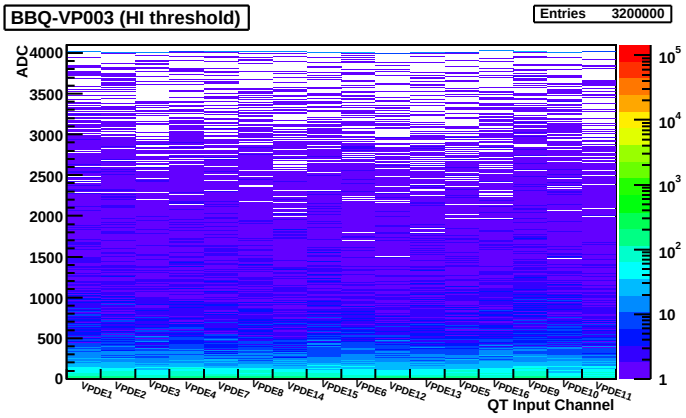

BBQ-ZD001 (ZDC TOWER)
Entries 3200000
TAC

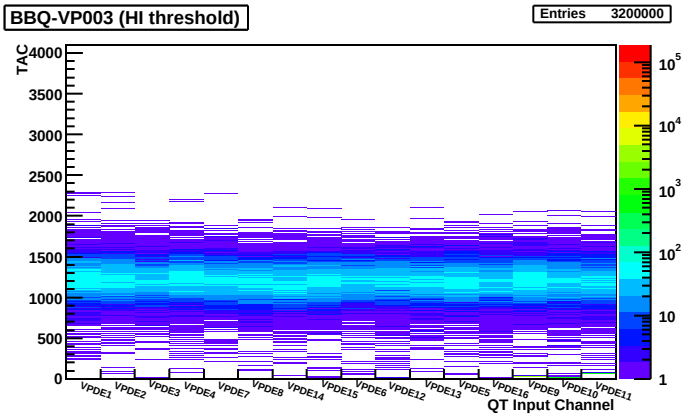

BBQ-VP001 (LO threshold)
Entries 3200000
ADC
QT Input Channel

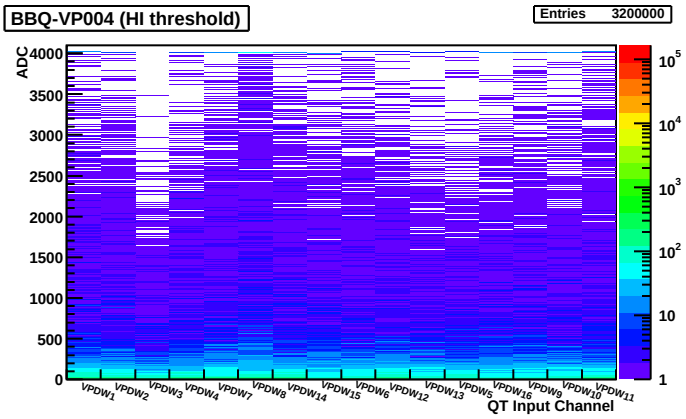

BBQ-VP001 (LO threshold)
Entries 3200000
TAC
QT Input Channel

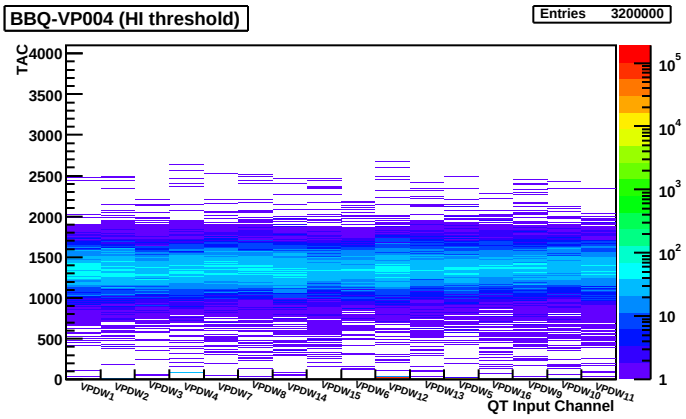

BBQ-VP002 (LO threshold)
Entries 3200000
ADC
QT Input Channel

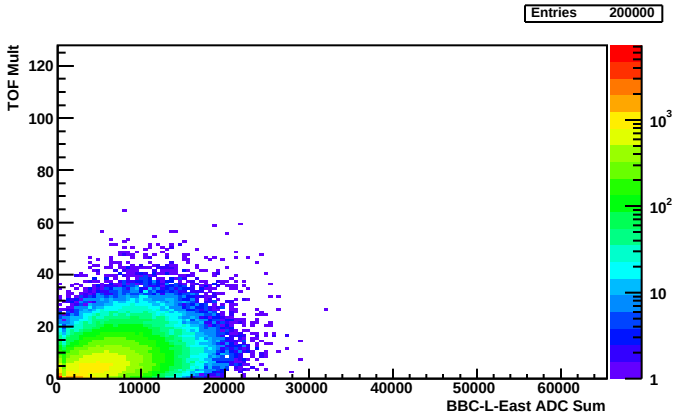

BBQ-VP002 (LO threshold)
Entries 3200000
TAC
QT Input Channel

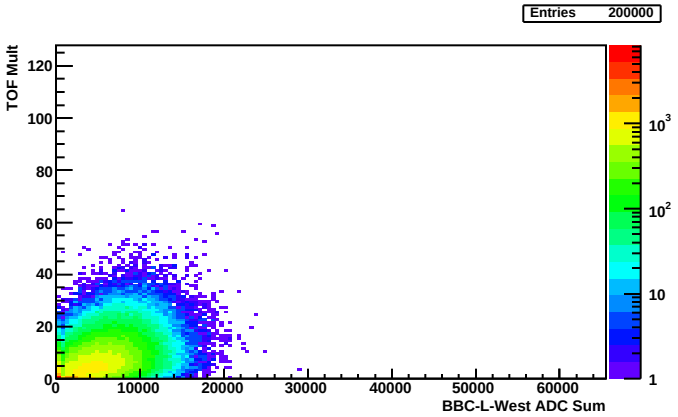

BBQ-VP003 (HI threshold)
Entries 3200000
ADC
QT Input Channel

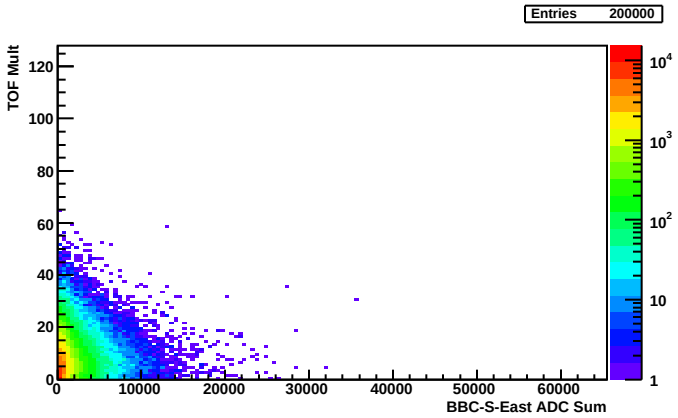

BBQ-VP003 (HI threshold)
Entries 3200000
TAC
QT Input Channel

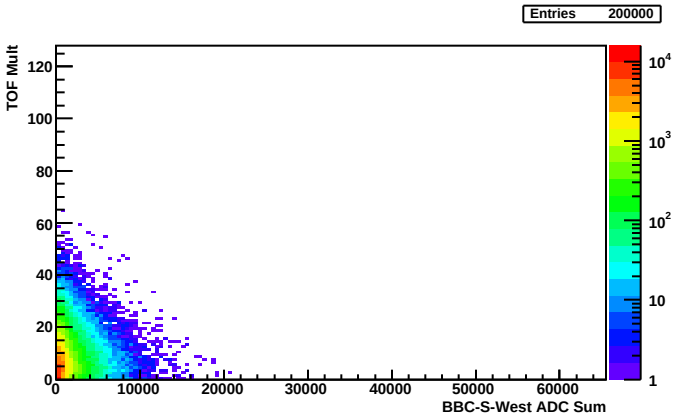

BBQ-VP004 (HI threshold)
Entries 3200000
ADC
QT Input Channel

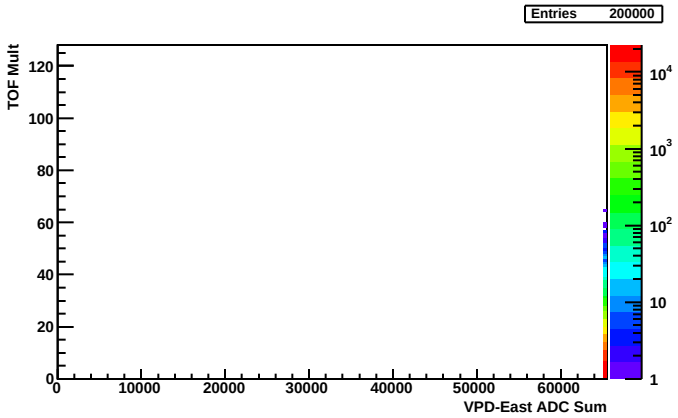

BBQ-VP004 (HI threshold)
Entries 3200000
TAC
QT Input Channel

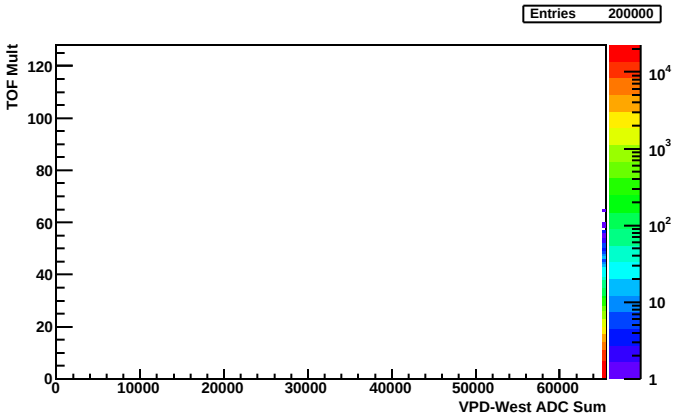

Entries 200000
TOF Mult
BBC-L-East ADC Sum

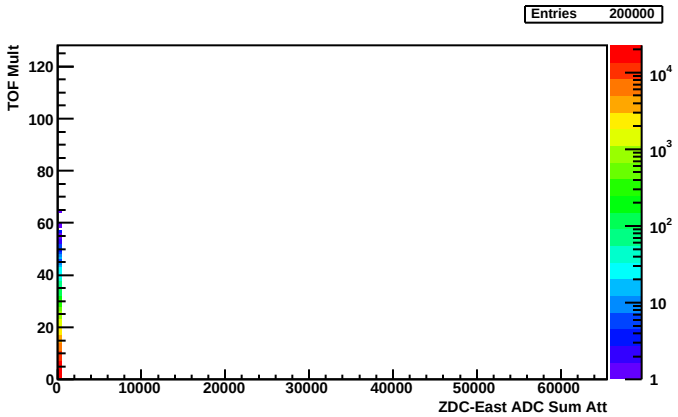

Entries 200000
TOF Mult
BBC-L-West ADC Sum

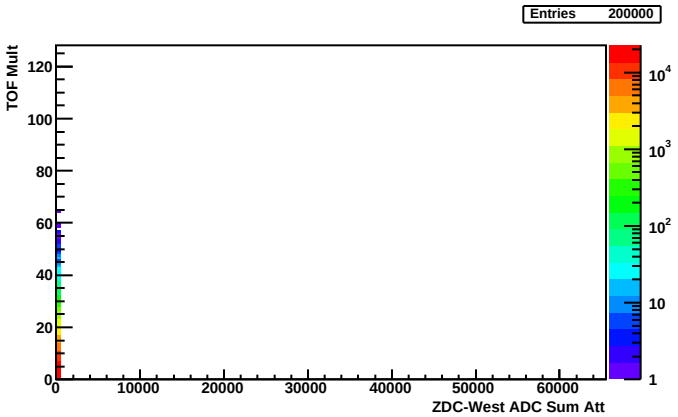

Entries 200000
TOF Mult
BBC-S-East ADC Sum

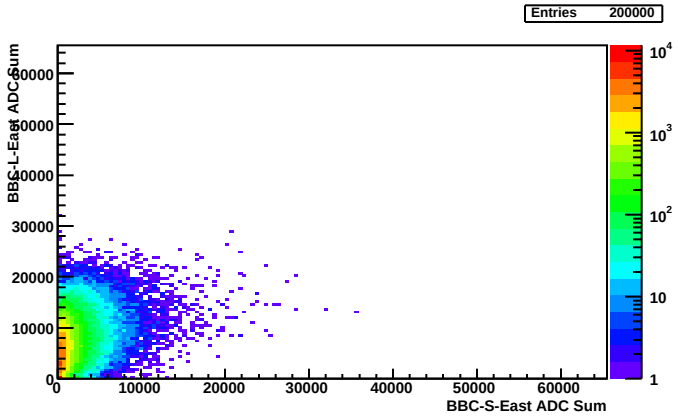

Entries 200000
TOF Mult
BBC-S-West ADC Sum

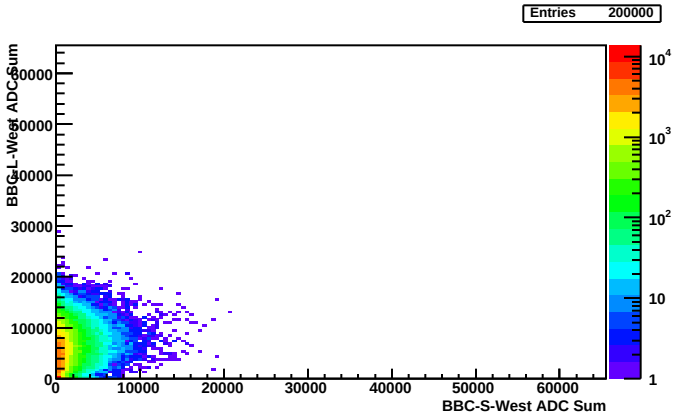

Entries 200000
TOF Mult
VPD-East ADC Sum

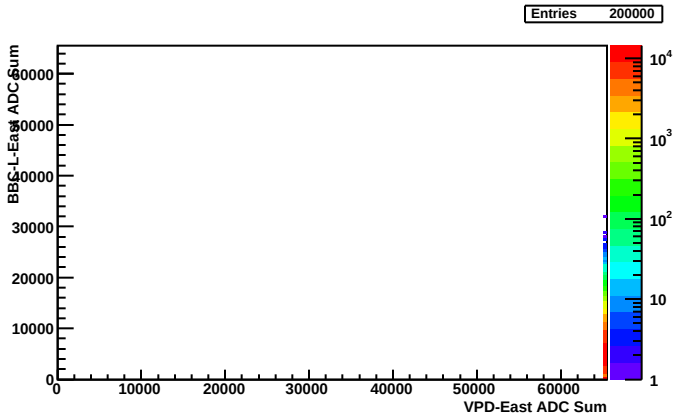

Entries 200000
TOF Mult
VPD-West ADC Sum

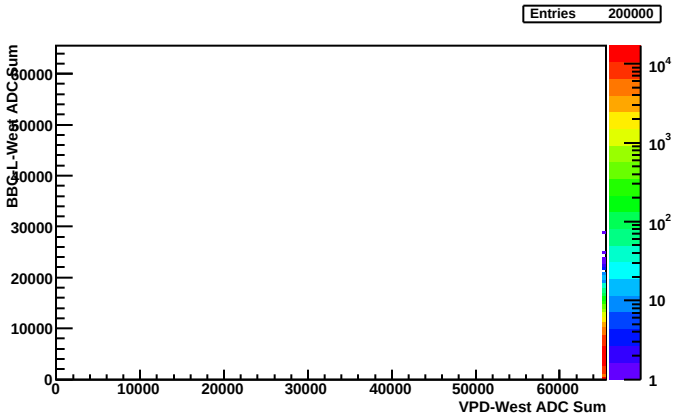

Entries 200000
TOF Mult
ZDC-East ADC Sum Att

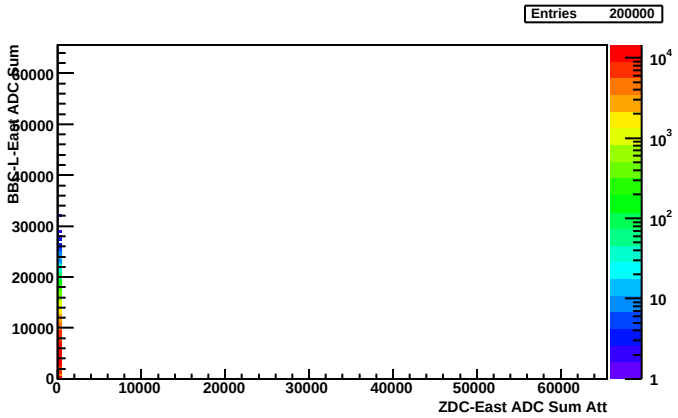

Entries 200000
TOF Mult
ZDC-West ADC Sum Att

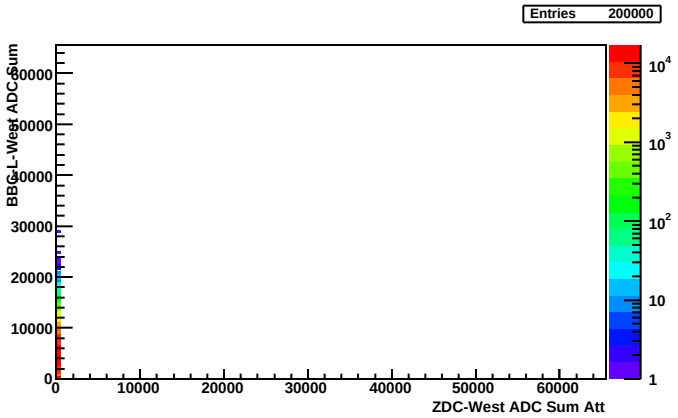

Entries 200000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

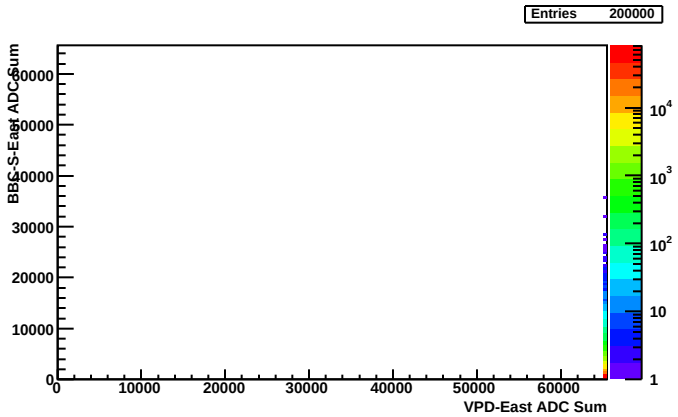

Entries 200000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

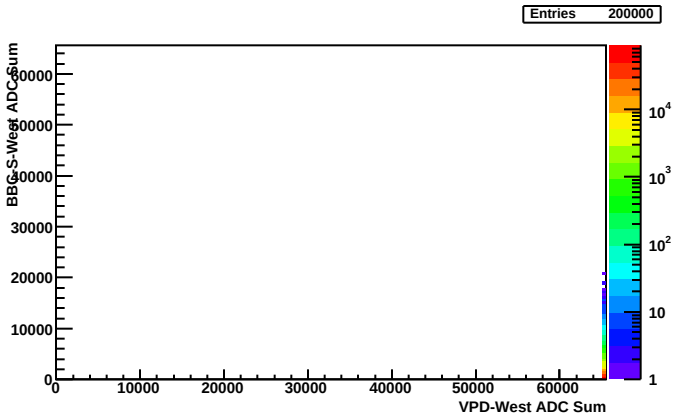

Entries 200000
BBC-L-East ADC Sum
VPD-East ADC Sum

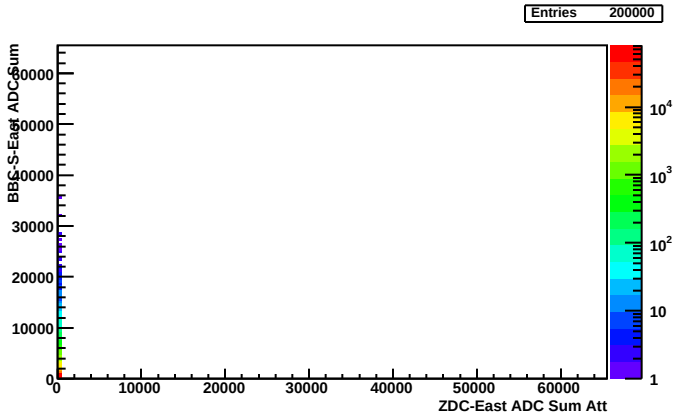

Entries 200000
BBC-L-West ADC Sum
VPD-West ADC Sum

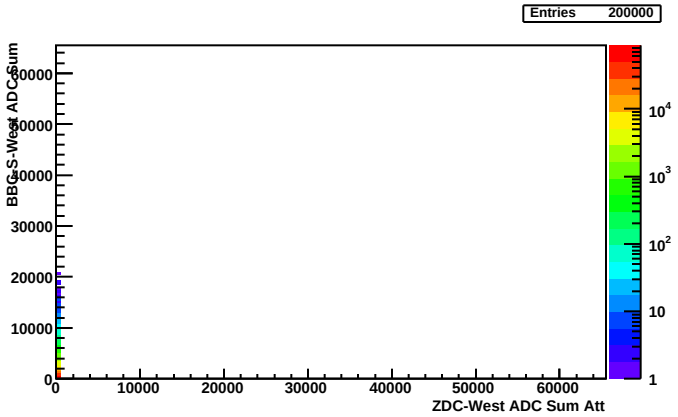

Entries 200000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

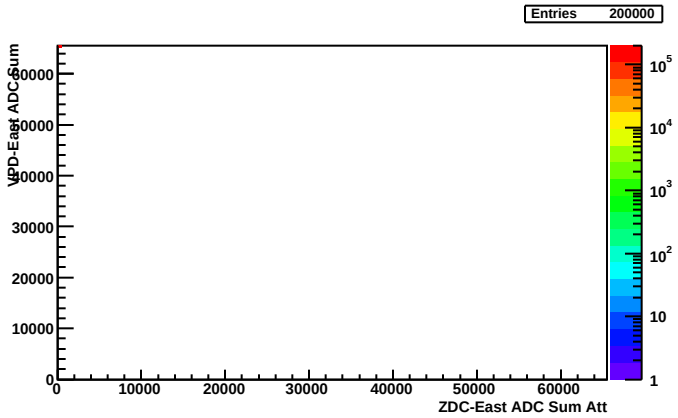

Entries 200000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

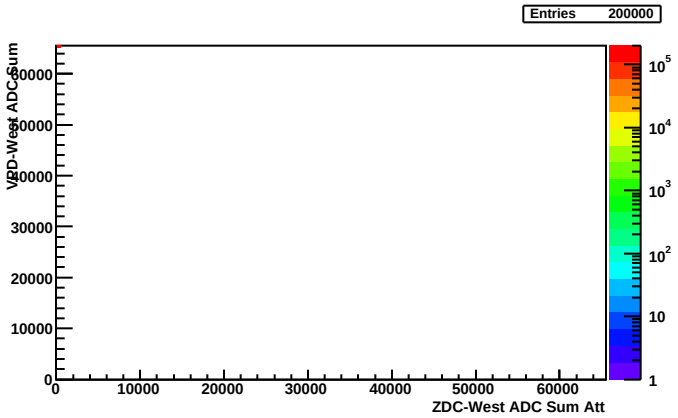

Entries 200000
BBC-S-East ADC Sum
VPD-East ADC Sum

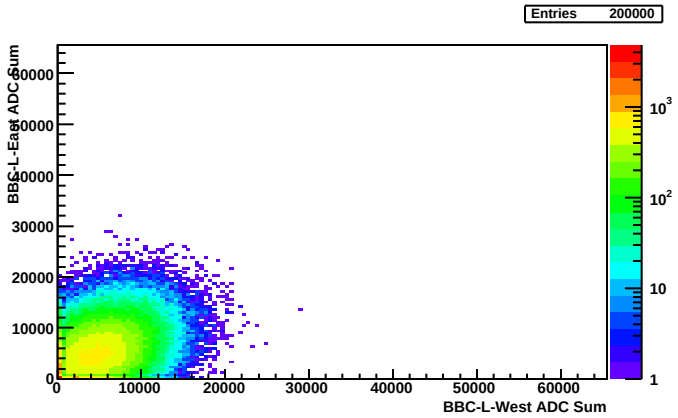

Entries 200000
BBC-S-West ADC Sum
VPD-West ADC Sum

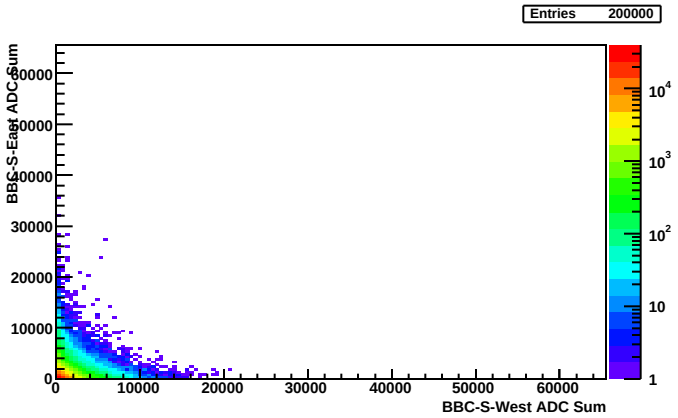

Entries 200000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

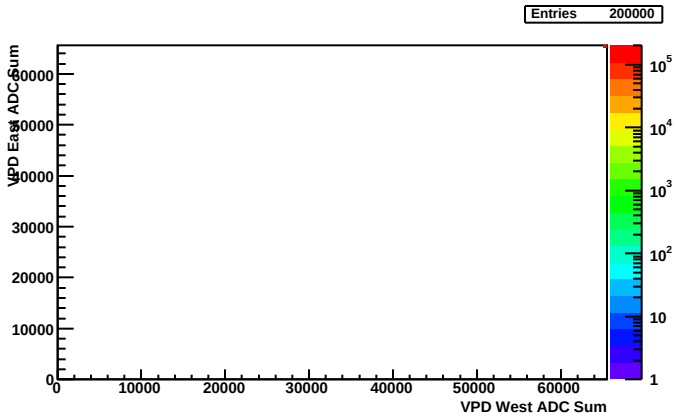

Entries 200000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

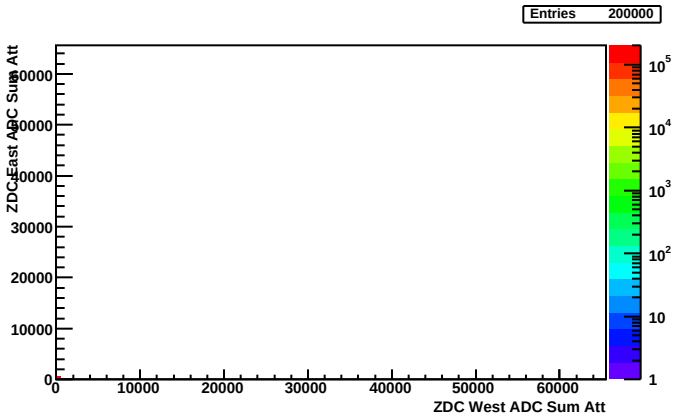

Entries 200000
VPD-East ADC Sum
ZDC-East ADC Sum Att

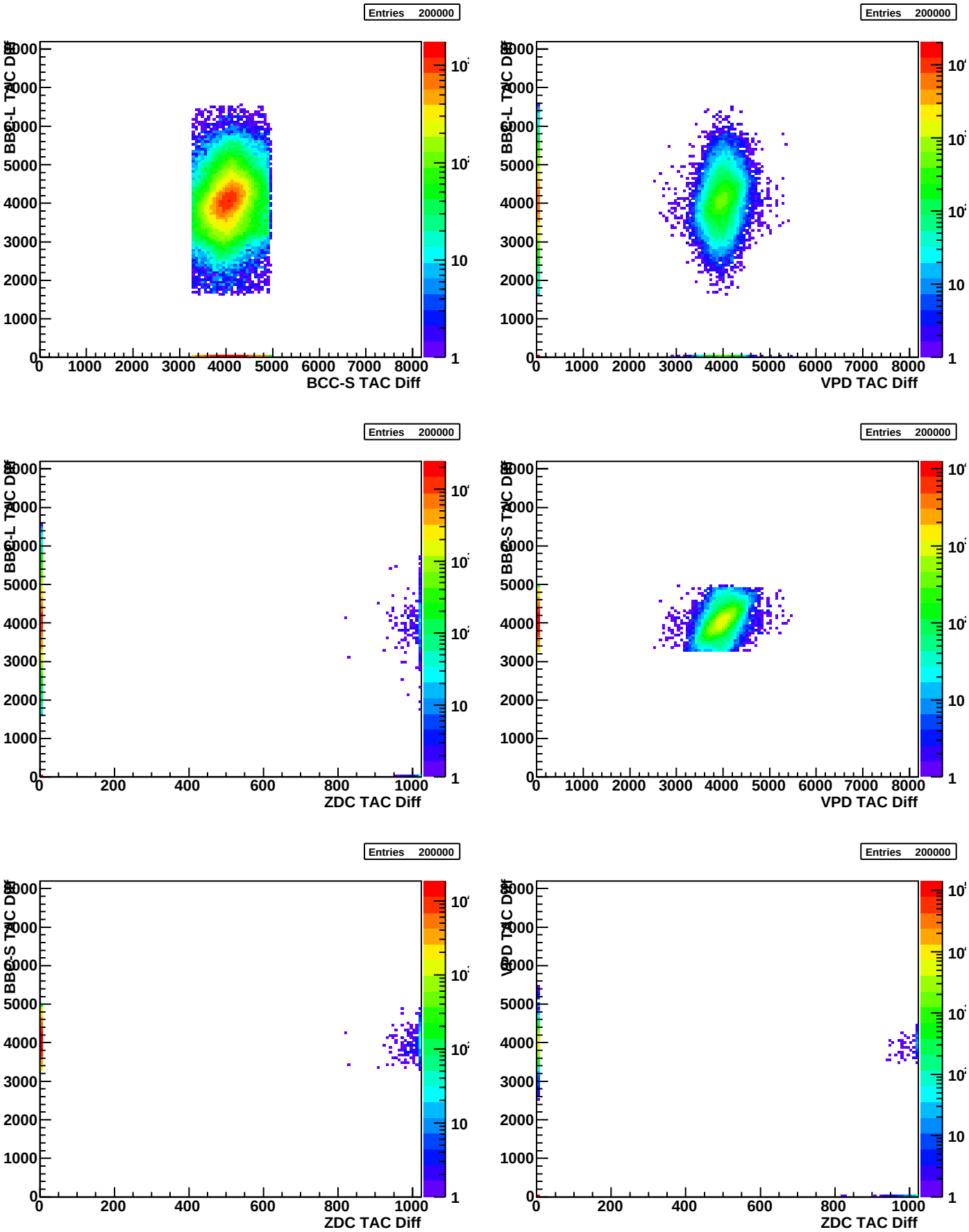

Entries 200000
VPD-West ADC Sum
ZDC-West ADC Sum Att

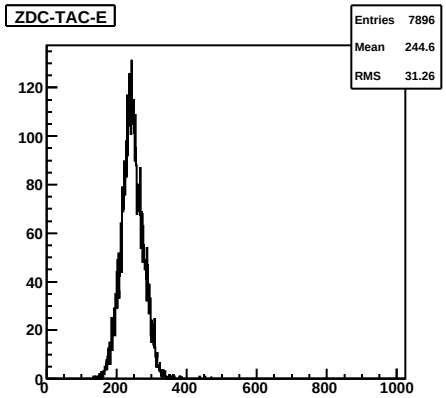

Entries 200000
BBC-L-East ADC Sum
BBC-L-West ADC Sum

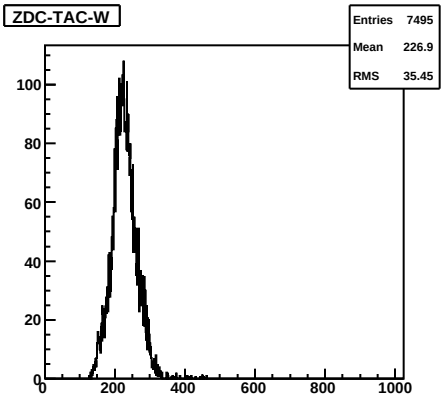

Entries 200000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

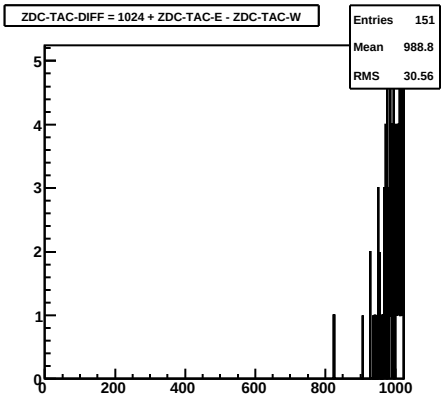

Entries 200000
VPD East ADC Sum
VPD West ADC Sum

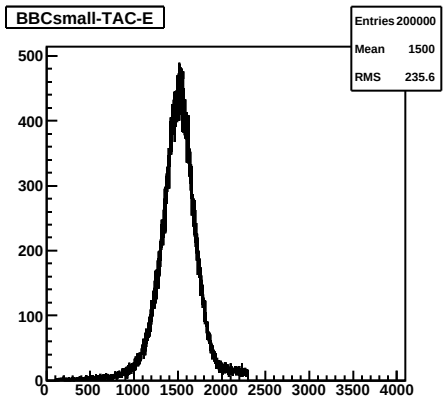

Entries 200000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

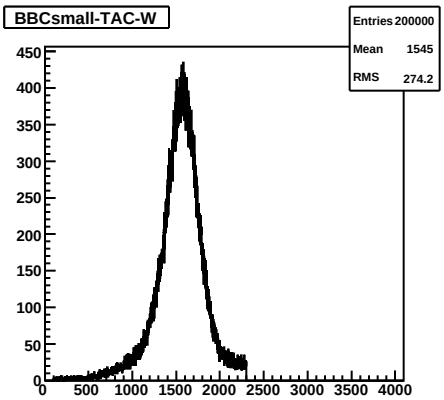

Entries 200000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 200000
BBC-L TAC Diff
VPD TAC Diff
Entries 200000
BBC-L TAC Diff
ZDC TAC Diff
Entries 200000
BBC-S TAC Diff
VPD TAC Diff
Entries 200000
BBC-S TAC Diff
ZDC TAC Diff
Entries 200000
VPD TAC Diff
ZDC TAC Diff

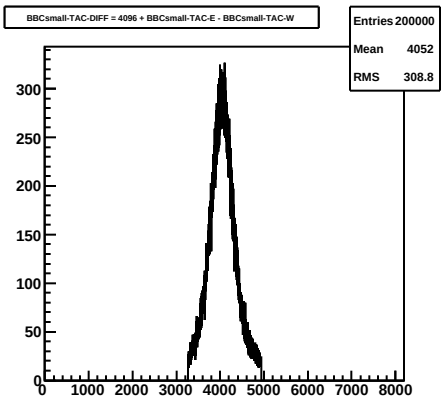

ZDC-TAC-E
Entries 7896
Mean 244.6
RMS 31.26

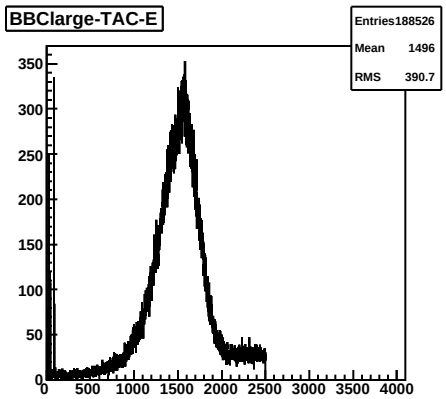

ZDC-TAC-W
Entries 7495
Mean 226.9
RMS 35.45

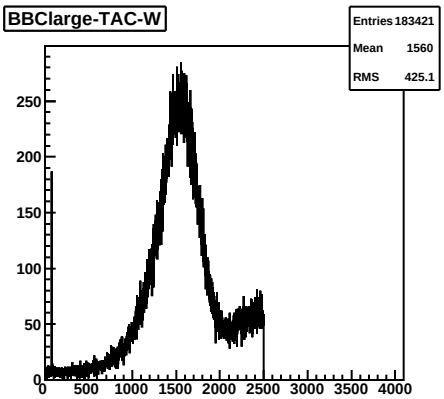

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 151
Mean 988.8
RMS 30.56

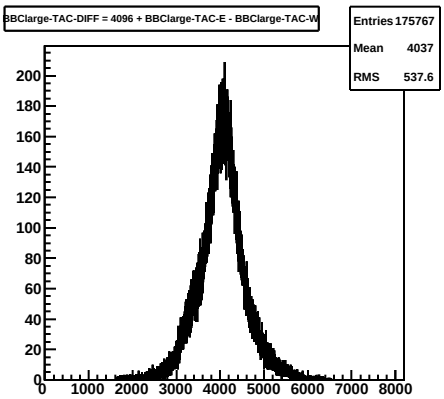

BBCsmall-TAC-E
Entries 200000
Mean 1500
RMS 235.6

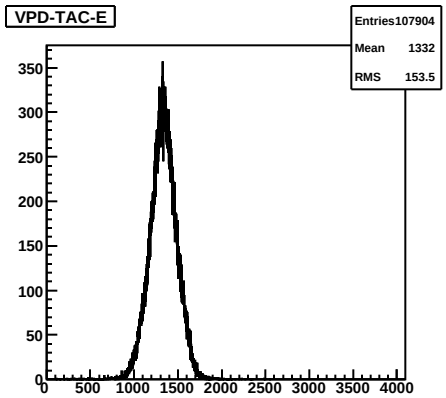

BBCsmall-TAC-W
Entries 200000
Mean 1545
RMS 274.2

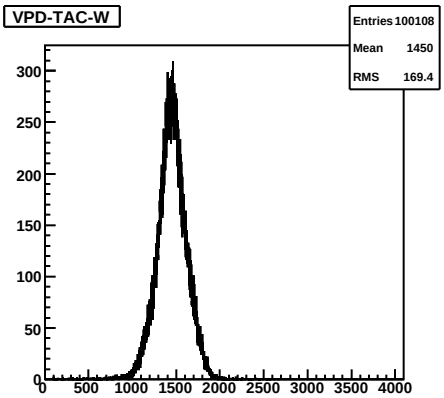

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 200000
Mean 4052
RMS 308.8

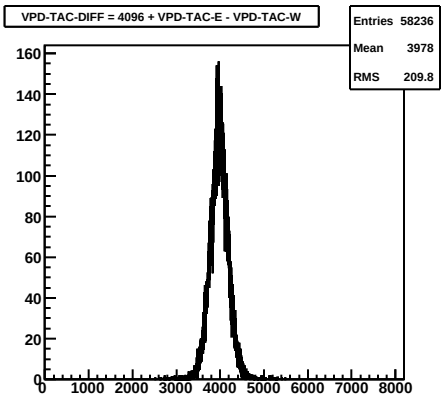

BBClarge-TAC-E
Entries 188526
Mean 1496
RMS 390.7

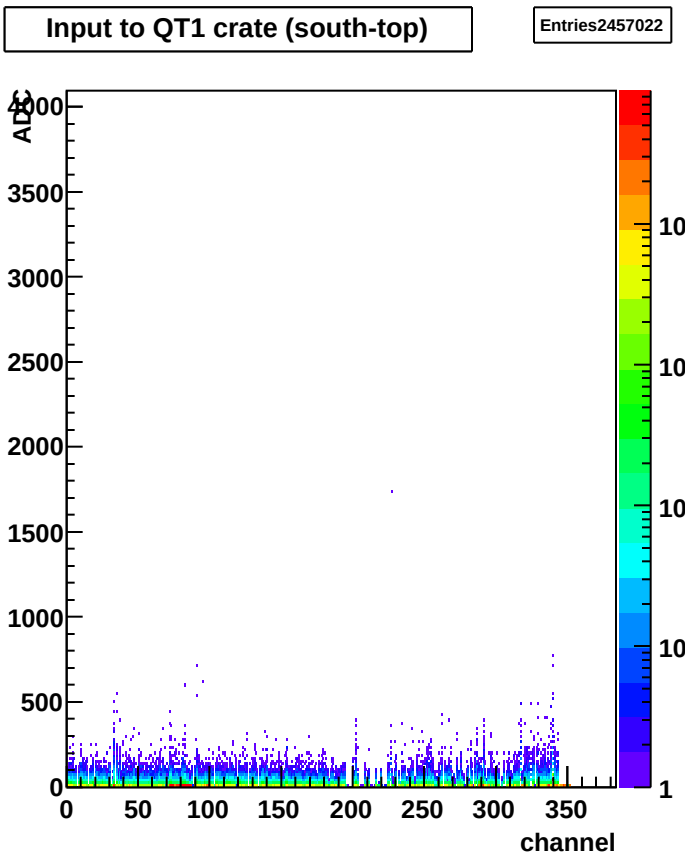

BBClarge-TAC-W
Entries 183421
Mean 1560
RMS 425.1

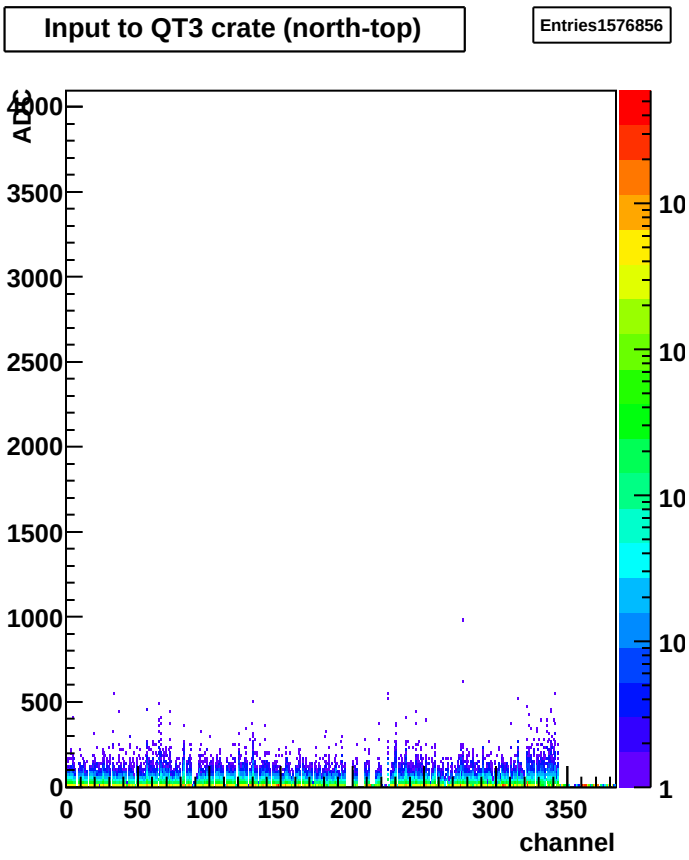

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 175767
Mean 4037
RMS 537.6

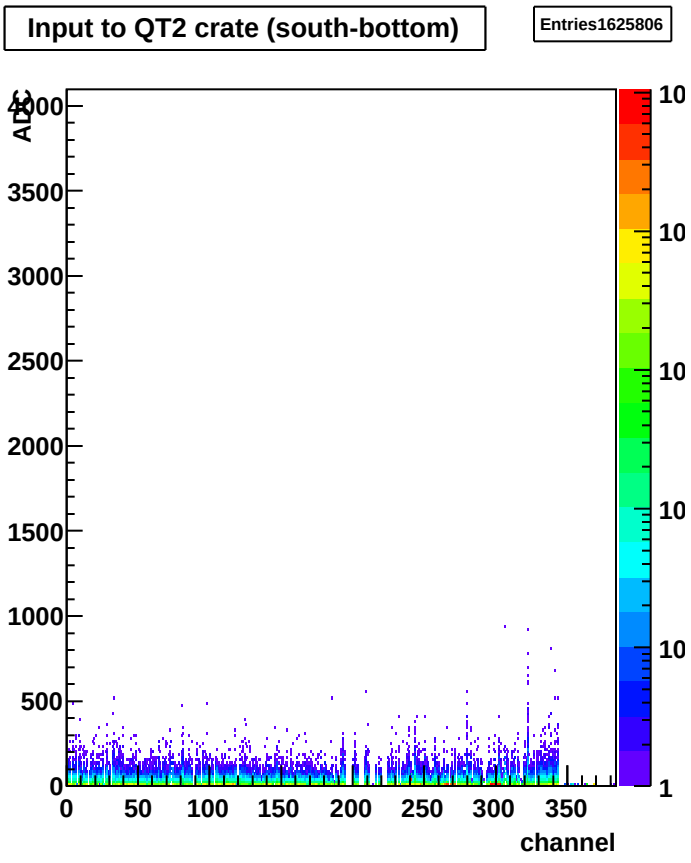

VPD-TAC-E
Entries 107904
Mean 1332
RMS 153.5

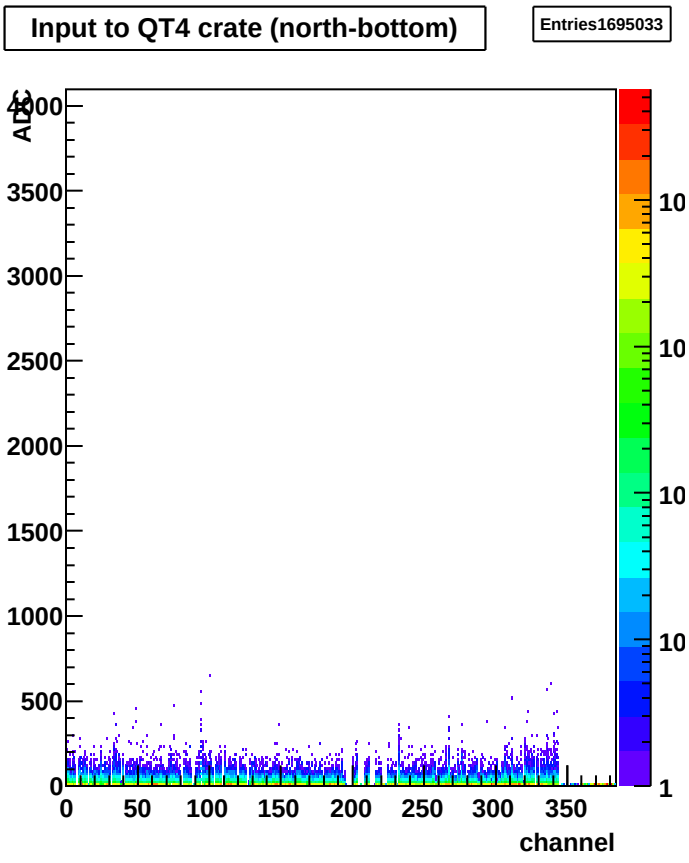

VPD-TAC-W
Entries 100108
Mean 1450
RMS 169.4

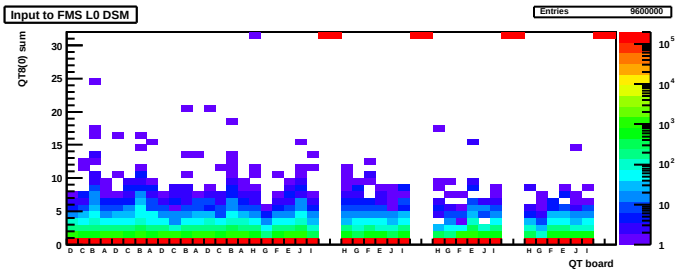

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 58236
Mean 3978
RMS 209.8

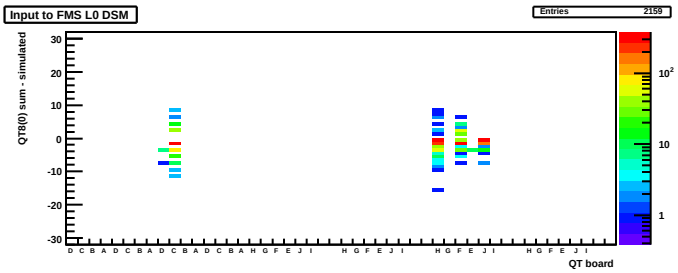

Input to QT1 crate (south-top)
Entries2457022
ADC
channel

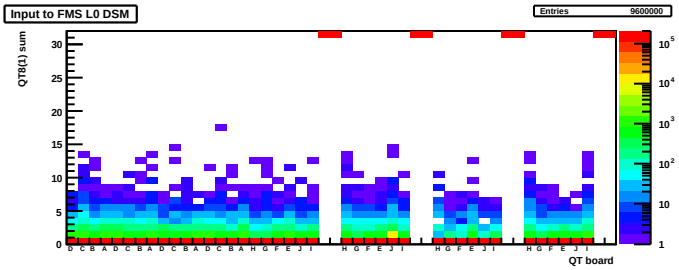

Input to QT3 crate (north-top)
Entries1576856
ADC
channel

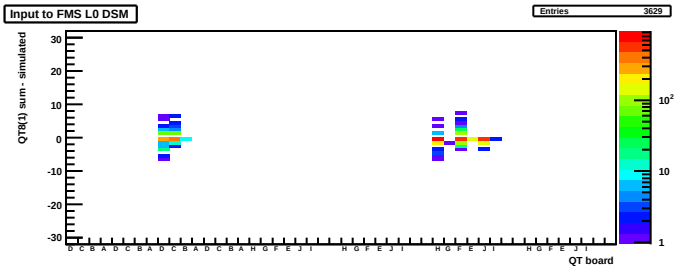

Input to QT2 crate (south-bottom)
Entries1625806
ADC
channel

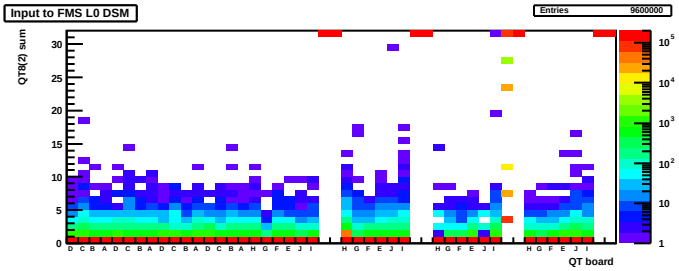

Input to QT4 crate (north-bottom)
Entries1695033
ADC
channel

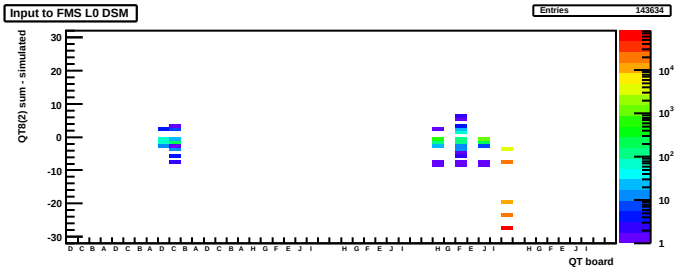

Input to FMS L0 DSM
Entries 9600000
QT8(0) sum
QT board

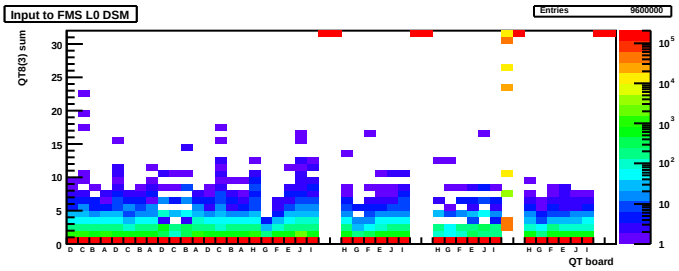

Input to FMS L0 DSM
Entries 2159
QT8(0) sum - simulated
QT board

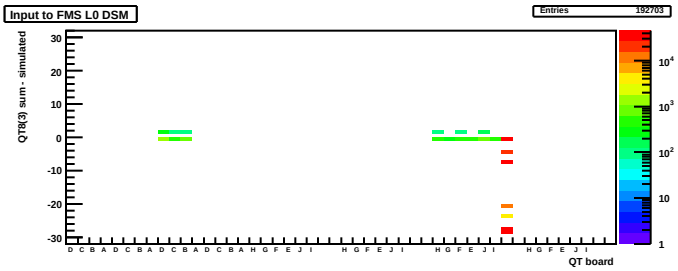

Input to FMS L0 DSM
Entries 9600000
QT8(1) sum
QT board

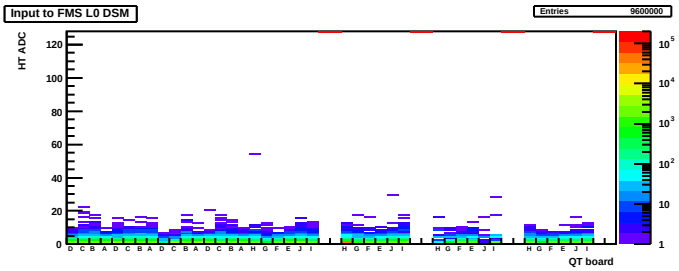

Input to FMS L0 DSM
Entries 3629
QT8(1) sum - simulated
QT board

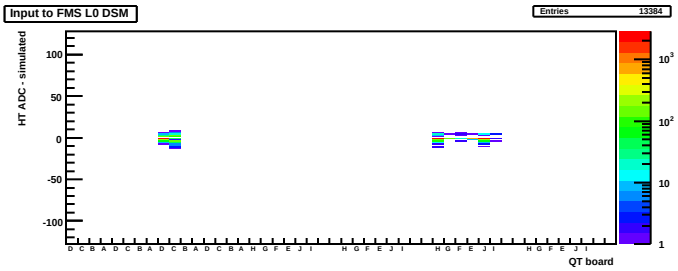

Input to FMS L0 DSM
Entries 9600000
QT8(2) sum
QT board

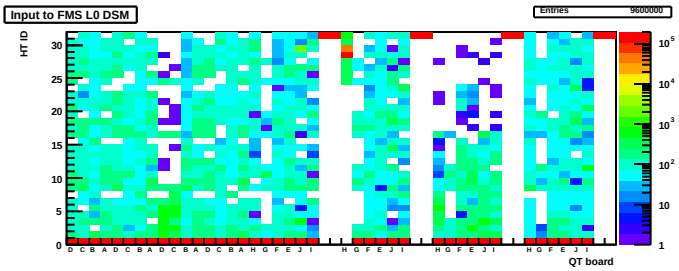

Input to FMS L0 DSM
Entries 143634
QT8(2) sum - simulated
QT board

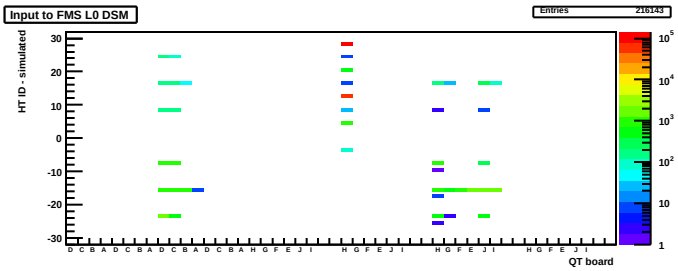

Input to FMS L0 DSM
Entries 9600000
QT8(3) sum
QT board

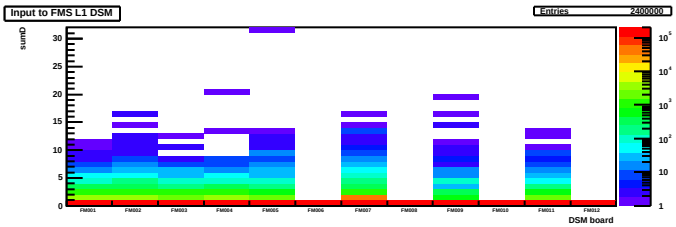

Input to FMS L0 DSM
Entries 192703
QT8(3) sum - simulated
QT board

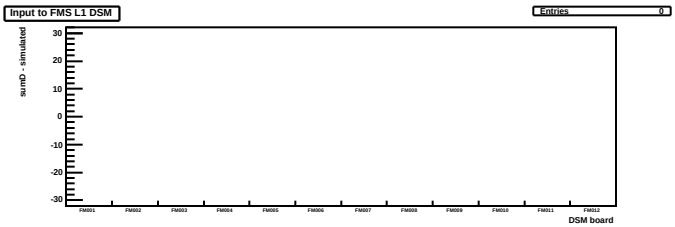

Input to FMS L0 DSM
Entries 9600000
HT ADC
QT board

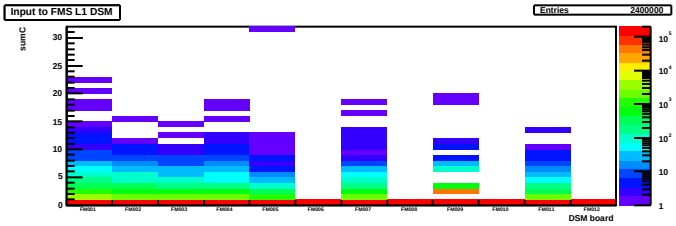

Input to FMS L0 DSM
Entries 13384
HT ADC - simulated
QT board

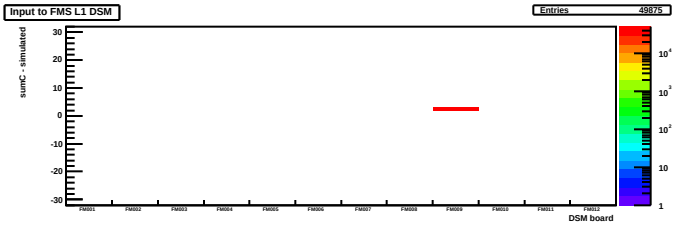

Input to FMS L0 DSM
Entries 9600000
HT ID
QT board

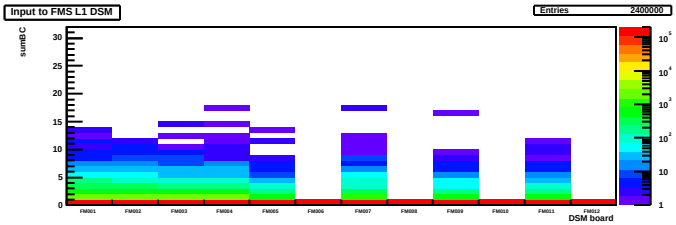

Input to FMS L0 DSM
Entries 216143
HT ID - simulated
QT board

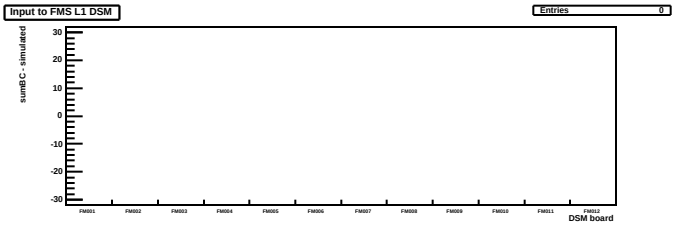

Input to FMS L1 DSM
Entries 2400000
sumD
DSM board

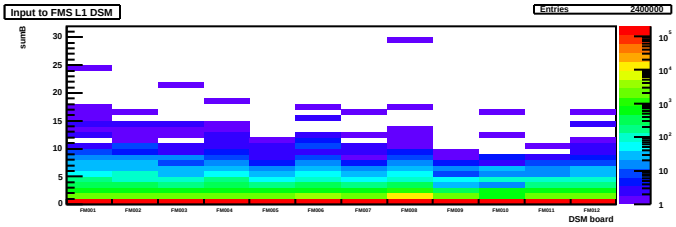

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

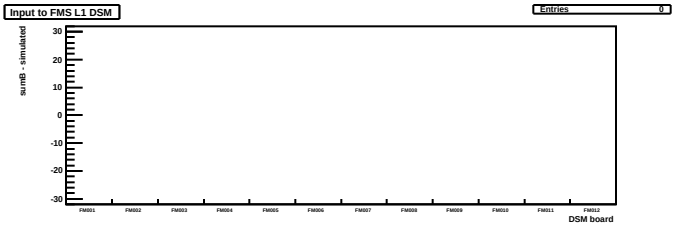

Input to FMS L1 DSM
Entries 2400000
sumC
DSM board

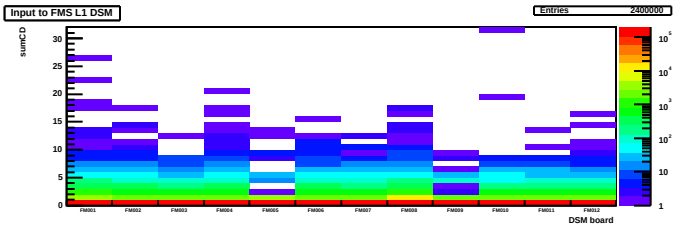

Input to FMS L1 DSM
Entries 49875
sumC - simulated
DSM board

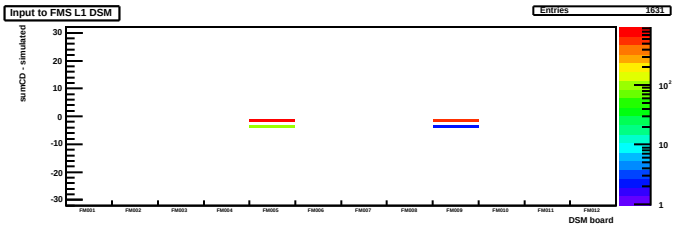

Input to FMS L1 DSM
Entries 2400000
sumBC
DSM board

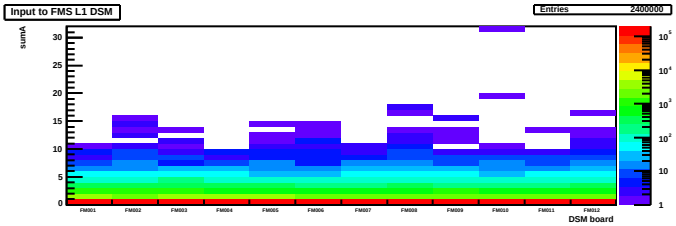

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

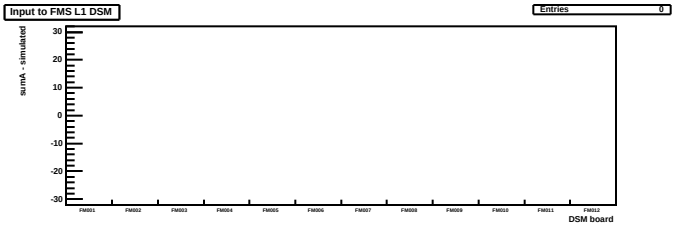

Input to FMS L1 DSM
Entries 2400000
sumB
DSM board

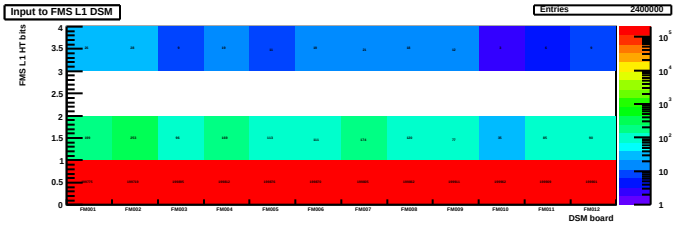

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

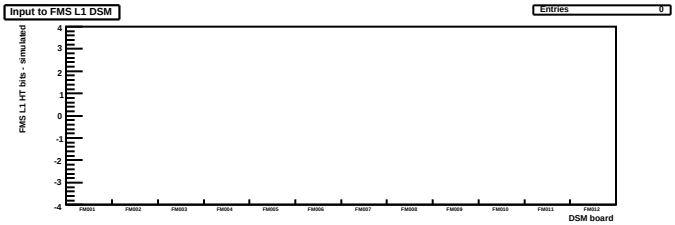

Input to FMS L1 DSM
Entries 2400000
sumCD
DSM board

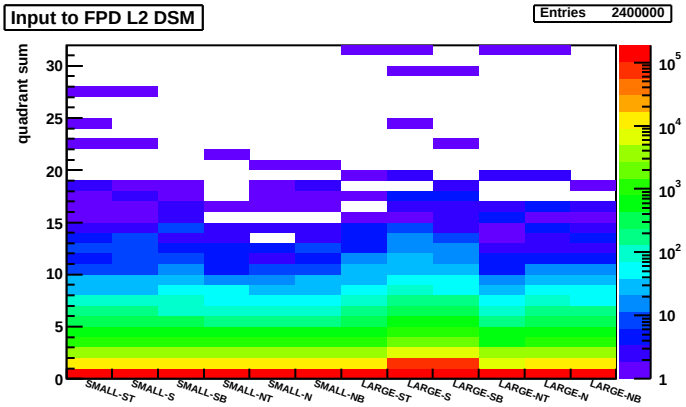

Input to FMS L1 DSM
Entries 1631
sumCD - simulated
DSM board

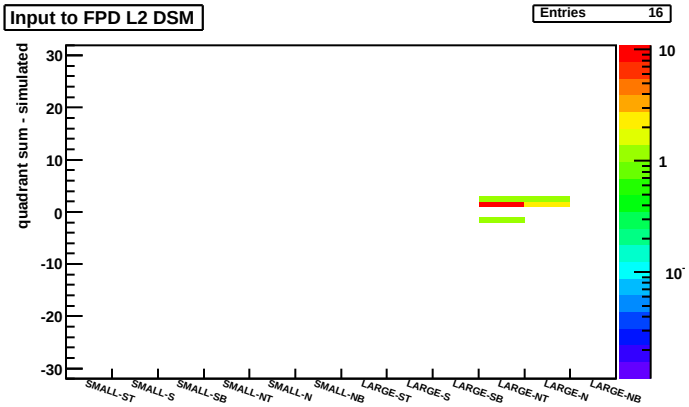

Input to FMS L1 DSM
Entries 2400000
sumA
DSM board

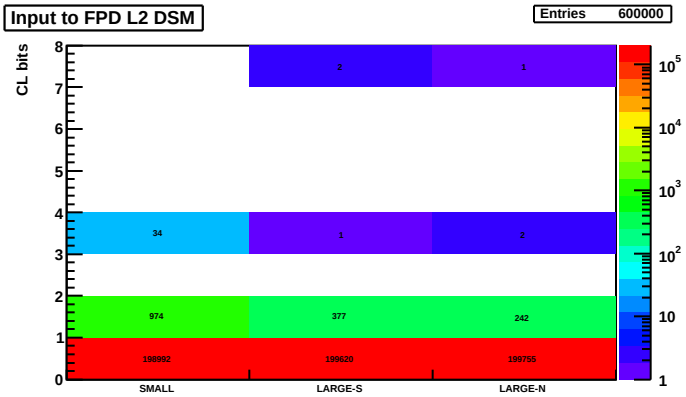

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

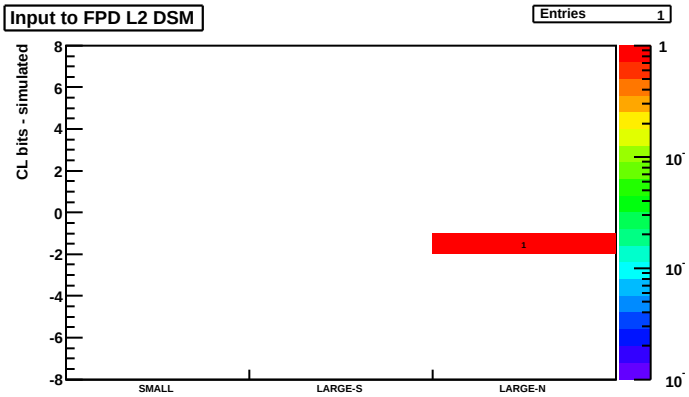

Input to FMS L1 DSM
Entries 2400000
FMS L1 HT bits
DSM board

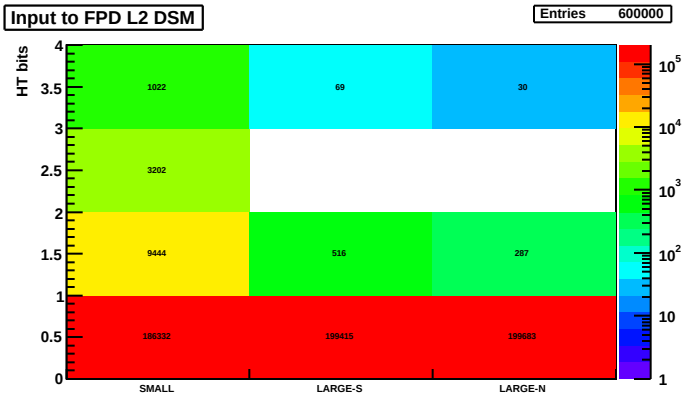

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

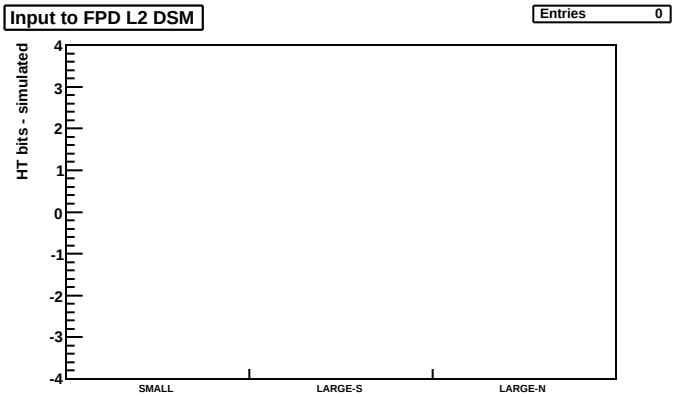

Input to FPD L2 DSM
Entries 2400000
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

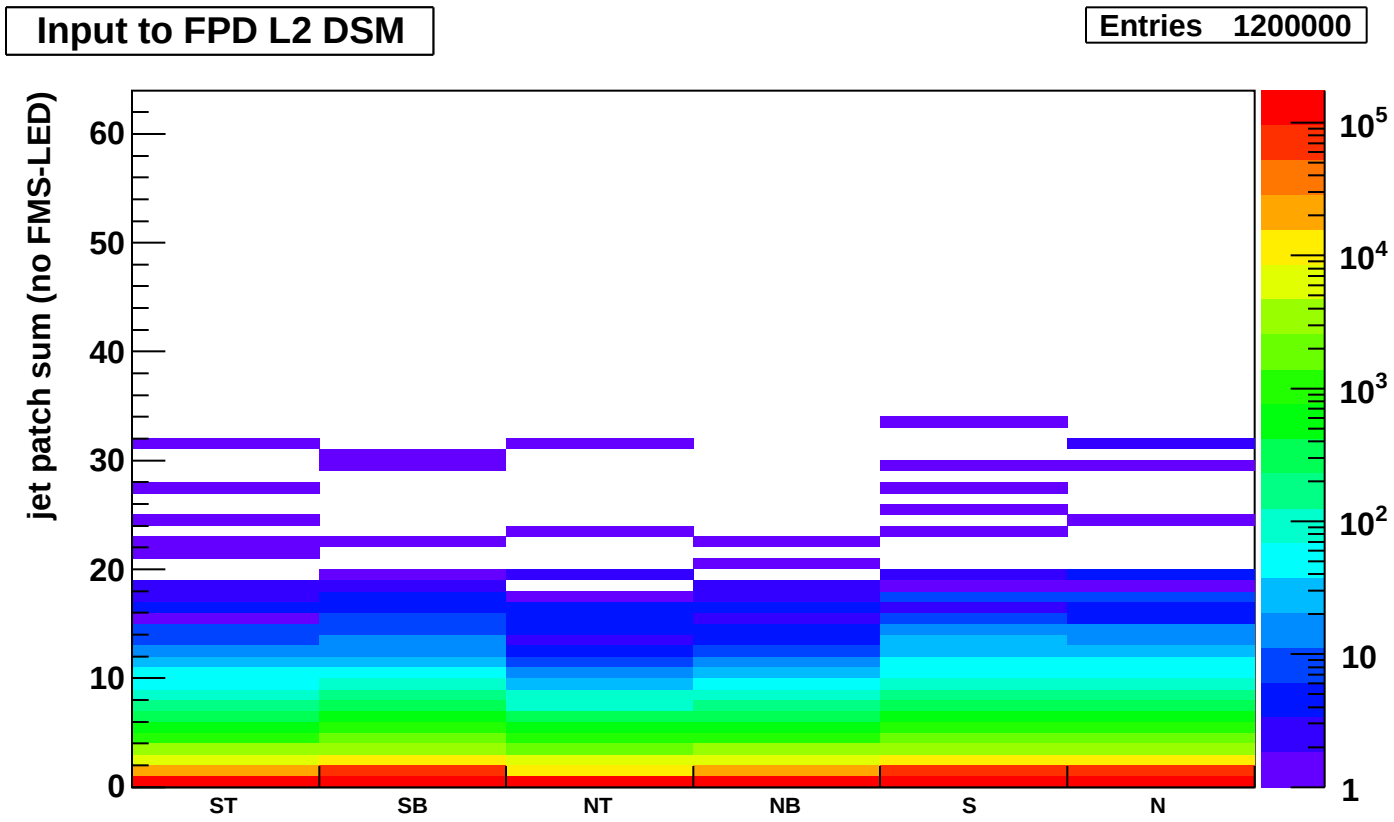

Input to FPD L2 DSM
Entries 16
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

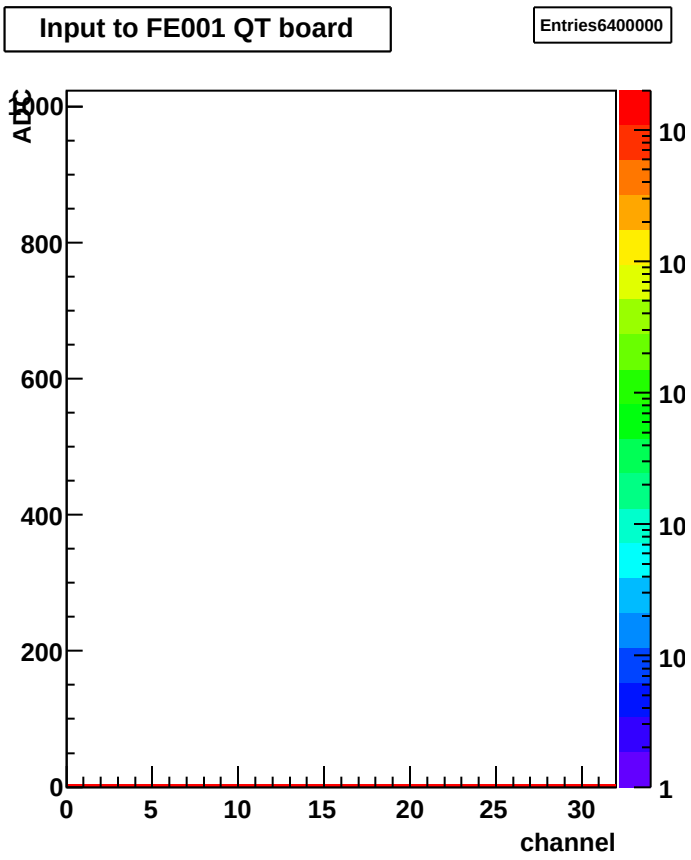

Input to FPD L2 DSM
Entries 600000
CL bits
2
1
34
1
2
974
377
242
198992
199620
199755
SMALL
LARGE-S
LARGE-N

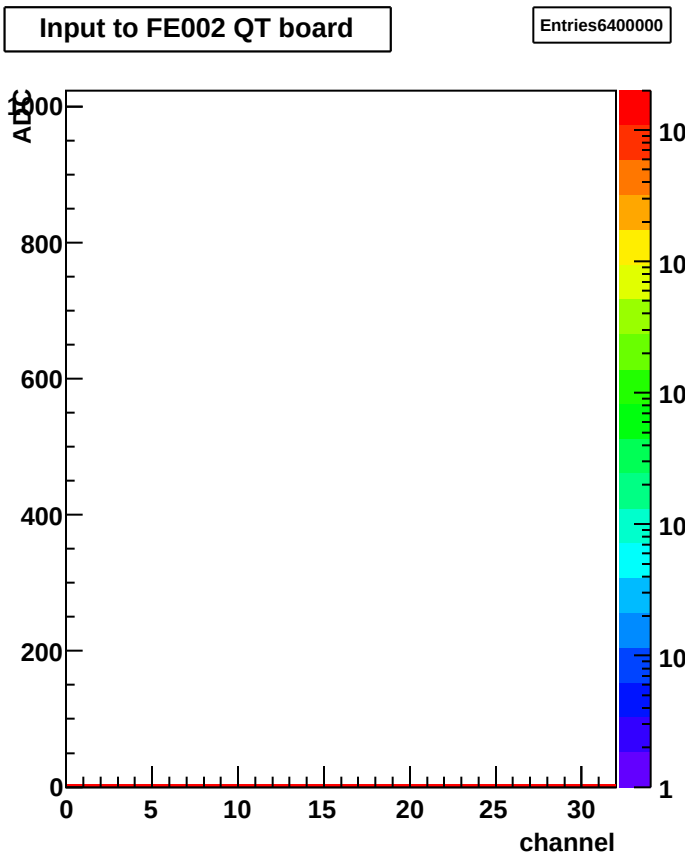

Input to FPD L2 DSM
Entries 1
CL bits - simulated
1
SMALL
LARGE-S
LARGE-N

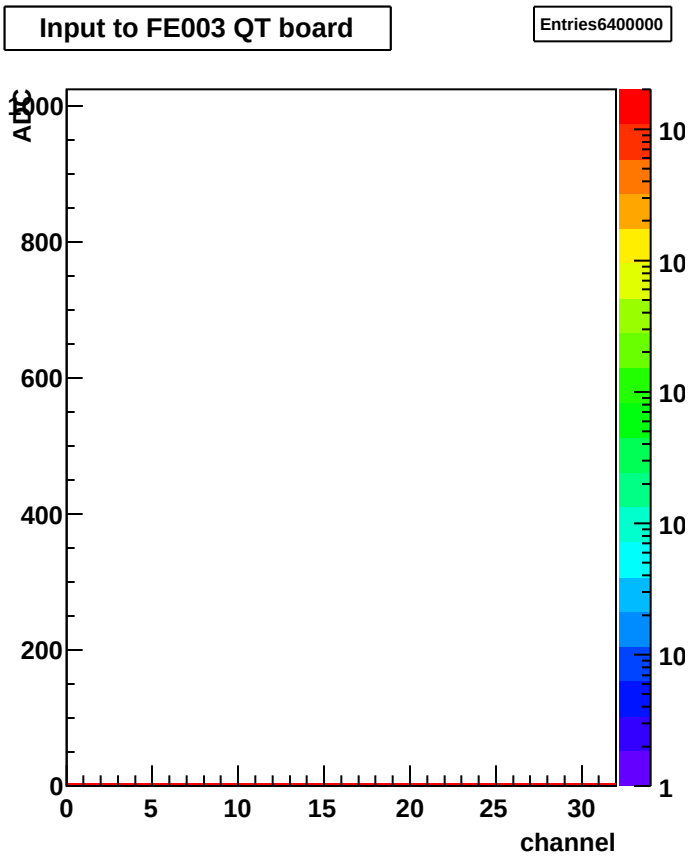

Input to FPD L2 DSM
Entries 600000
HT bits
1022
69
30
3202
9444
516
287
186332
199415
199683
SMALL
LARGE-S
LARGE-N

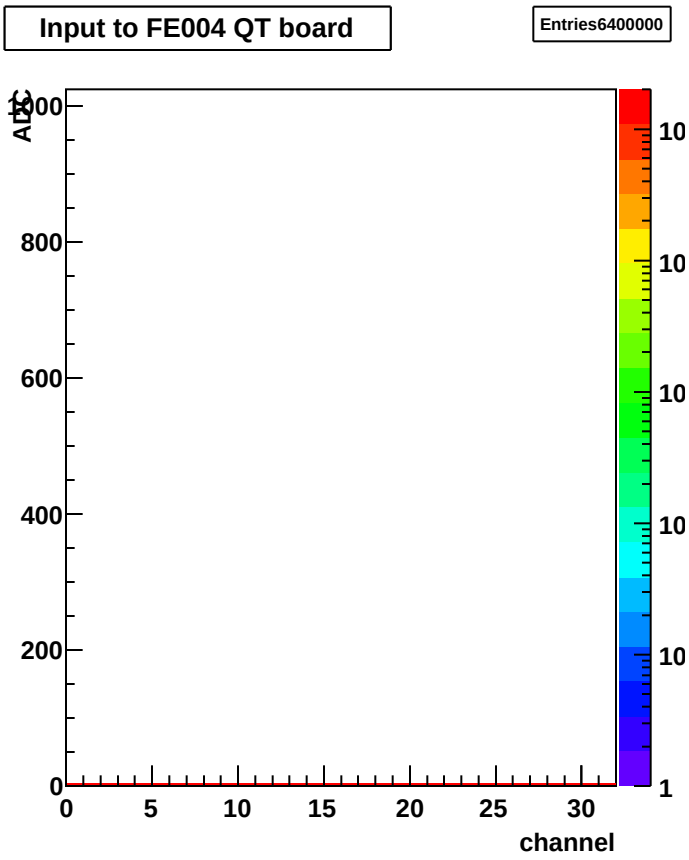

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

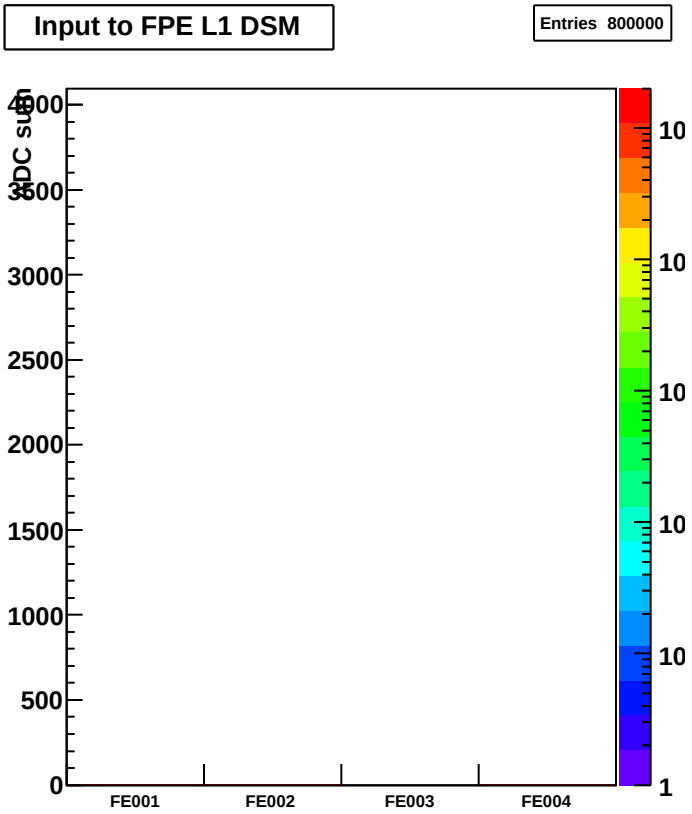

Input to FPD L2 DSM
Entries 1200000
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

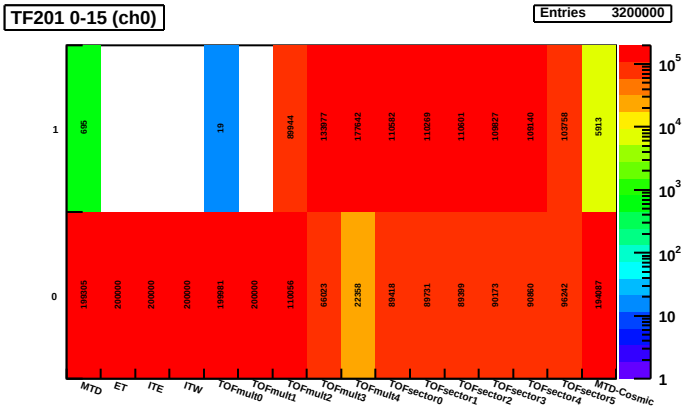

Input to FE001 QT board
Entries6400000
ADC
channel

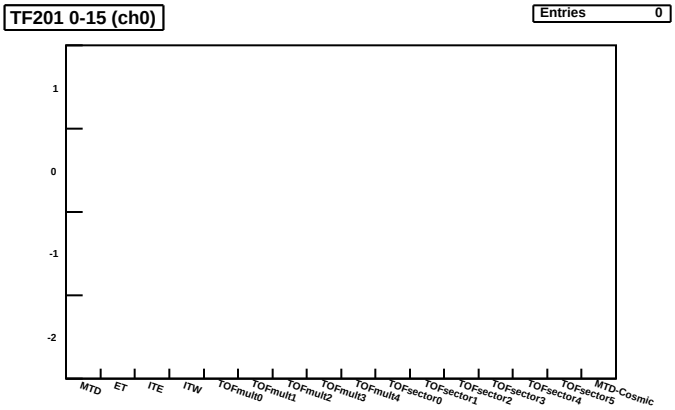

Input to FE002 QT board
Entries6400000
ADC
channel

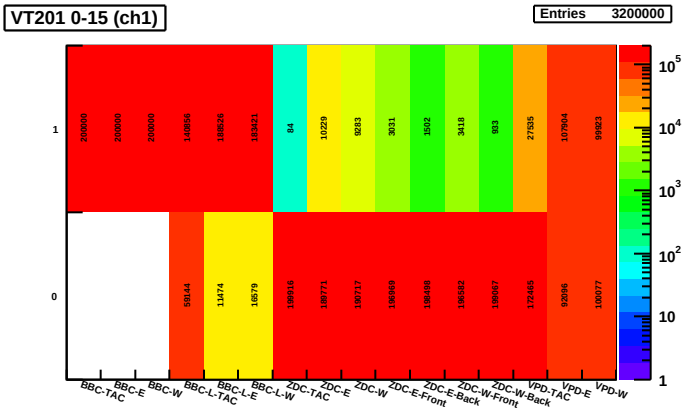

Input to FE003 QT board
Entries6400000
ADC
channel

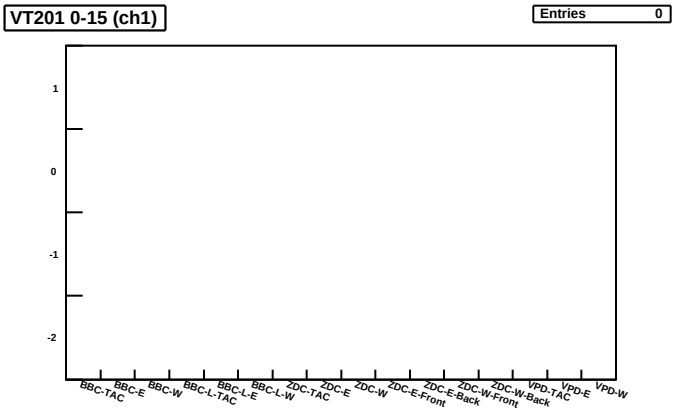

Input to FE004 QT board
Entries6400000
ADC
channel

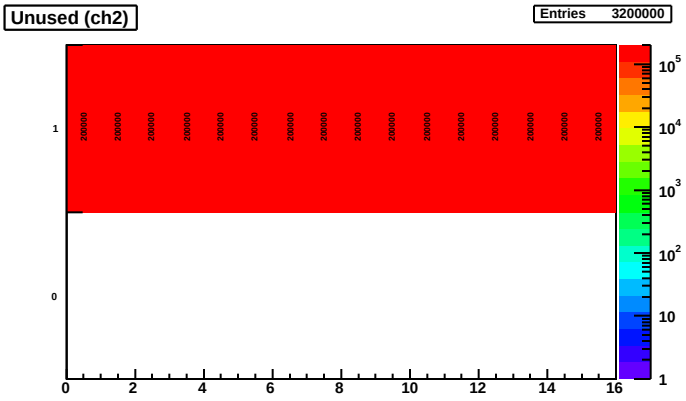

Input to FPE L1 DSM
Entries 800000
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

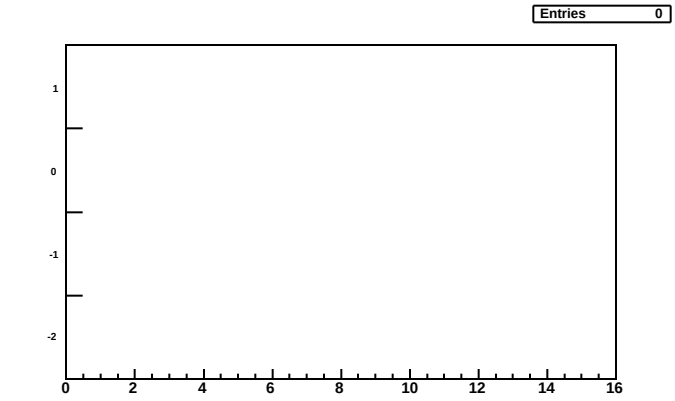

TF201 0-15 (ch0)
Entries 3200000
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

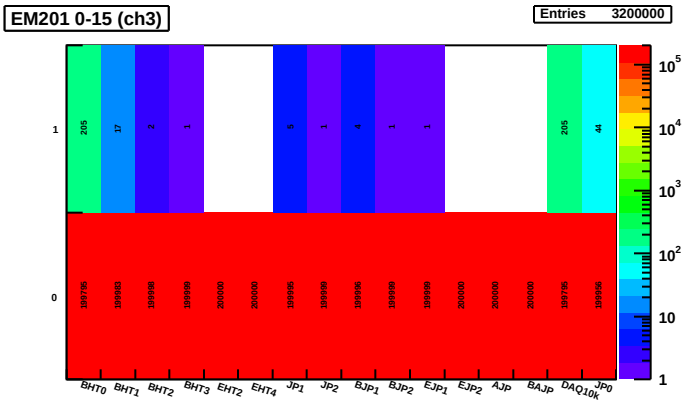

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

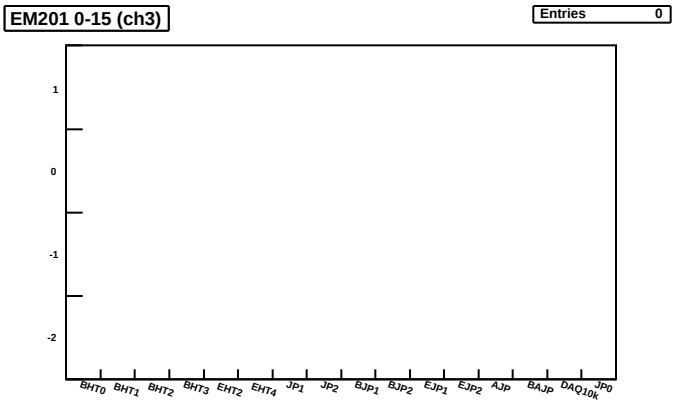

VT201 0-15 (ch1)
Entries 3200000
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

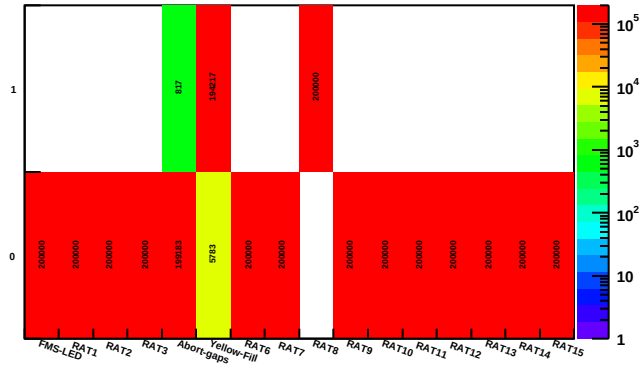

Unused (ch2)
Entries 3200000

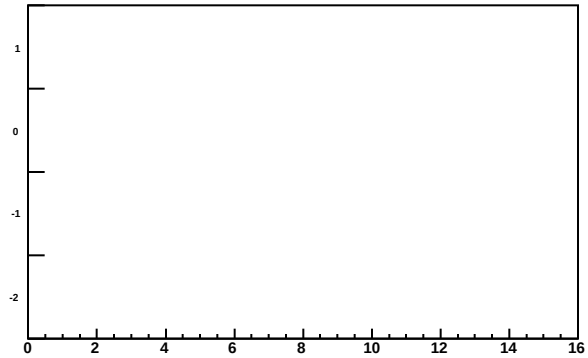

Entries 0


EM201 0-15 (ch3)
Entries 3200000
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

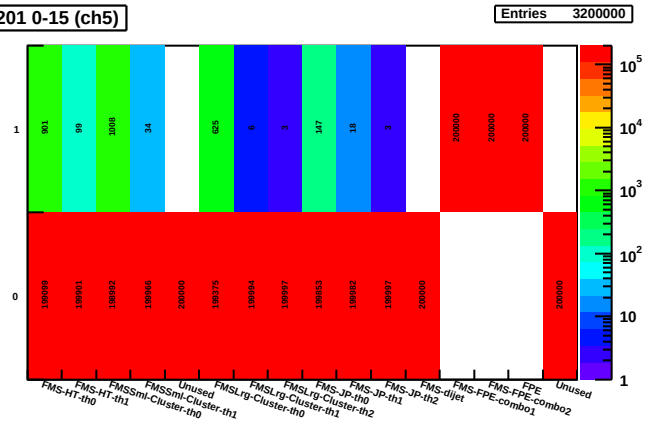

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

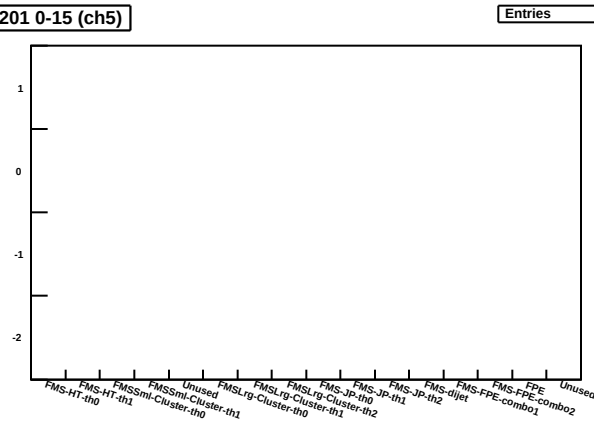




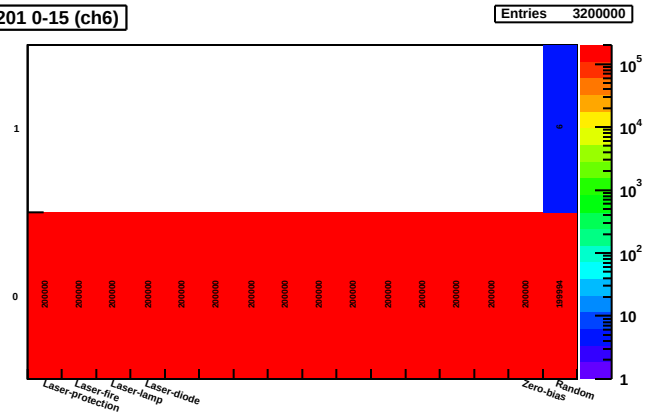
FP201 0-15 (ch5)

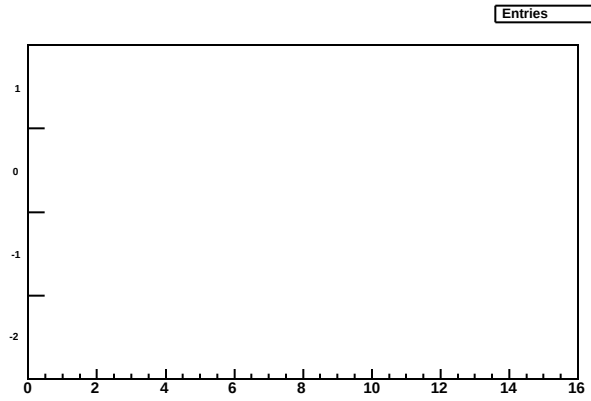
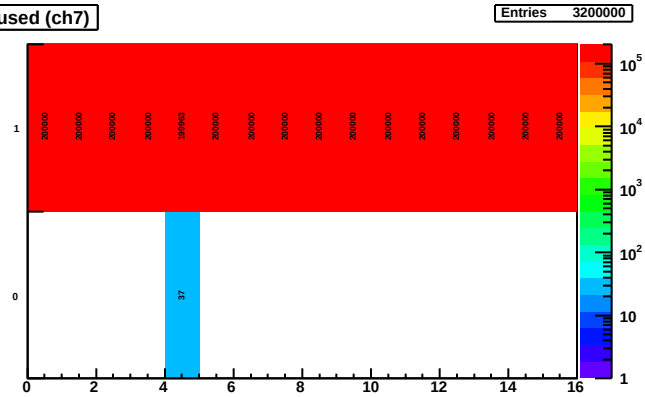
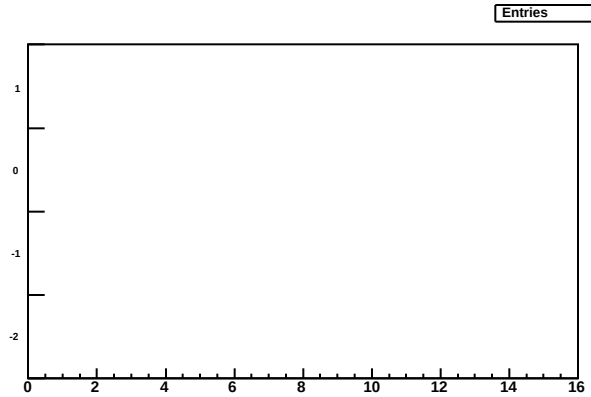


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)