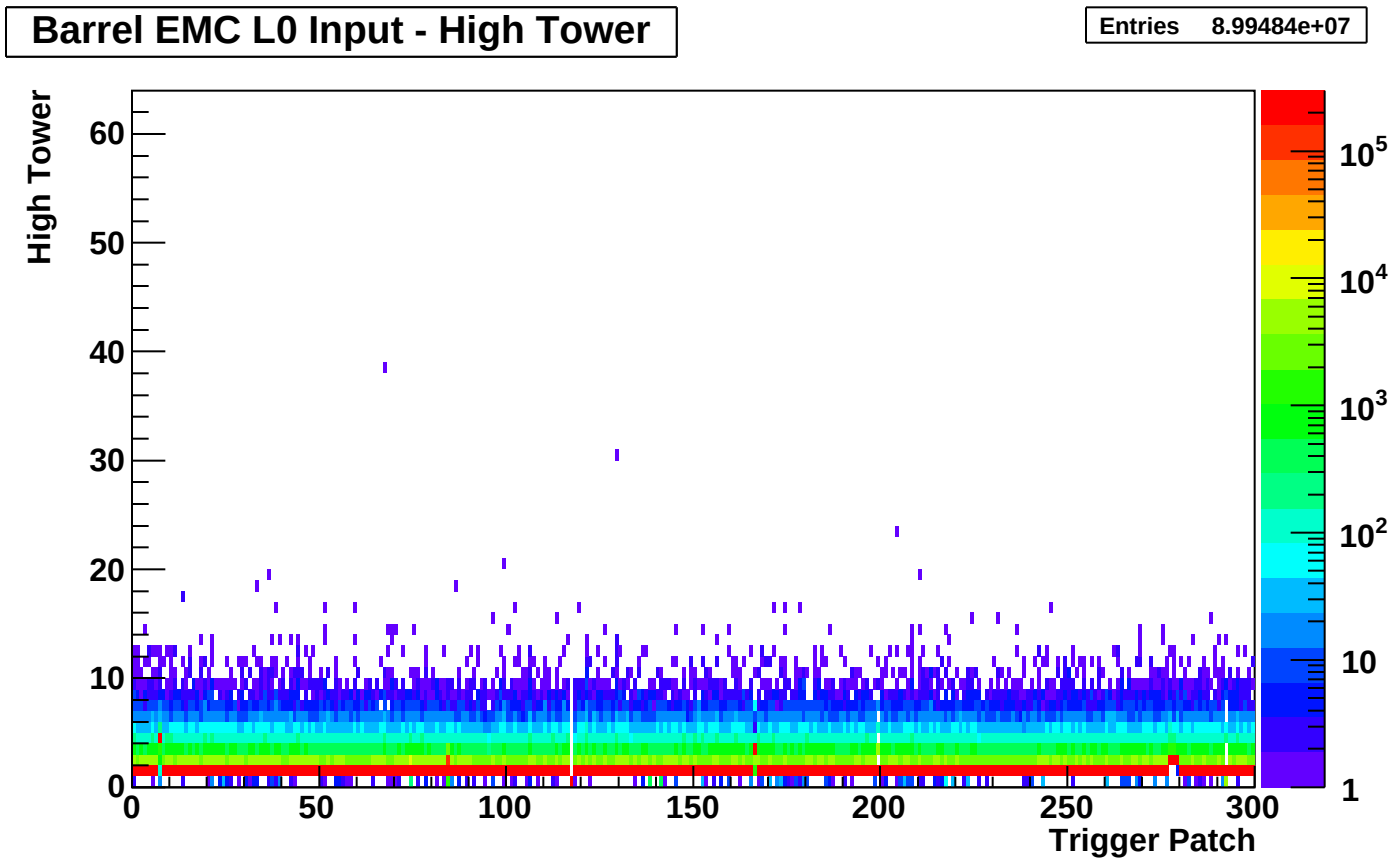
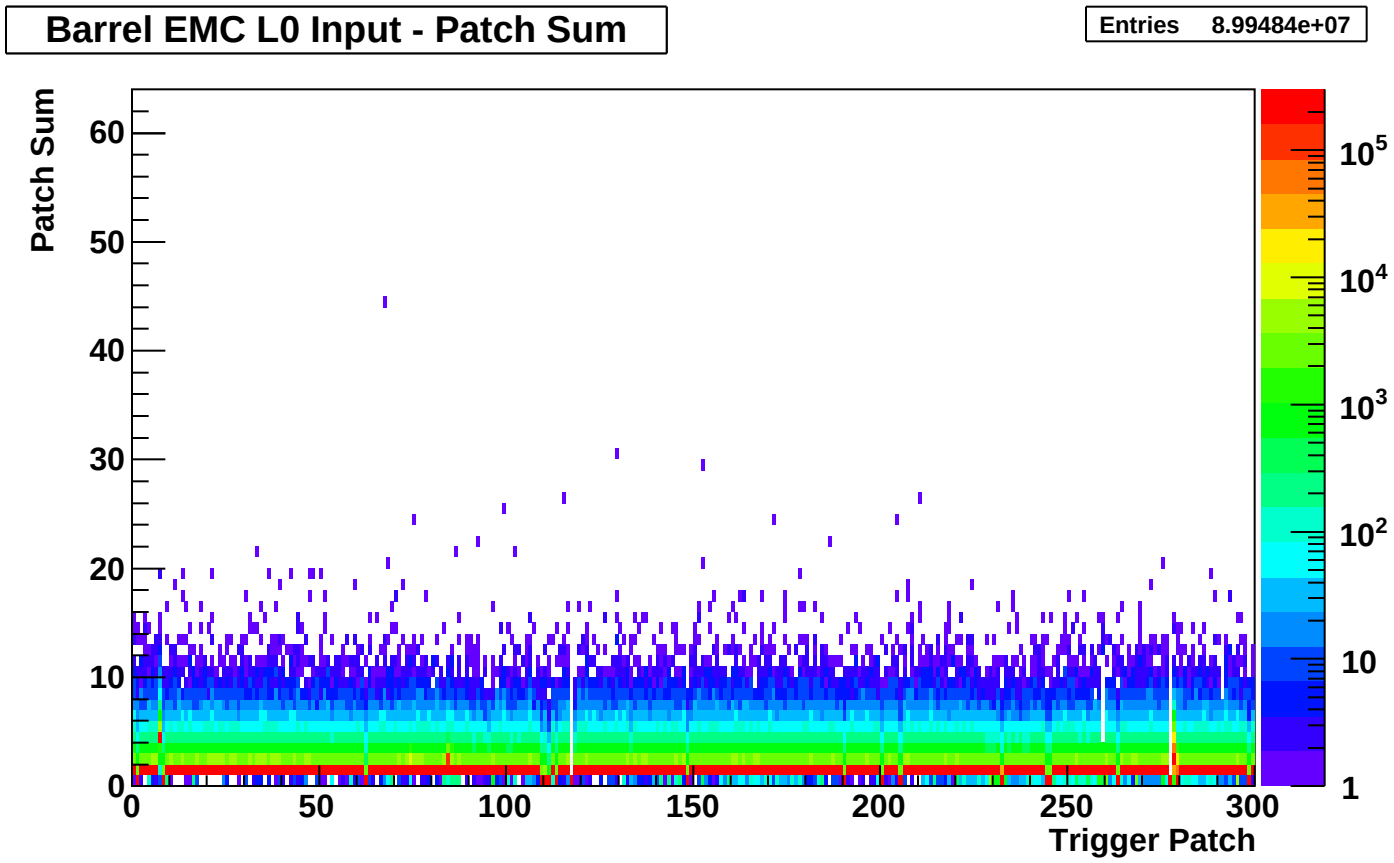
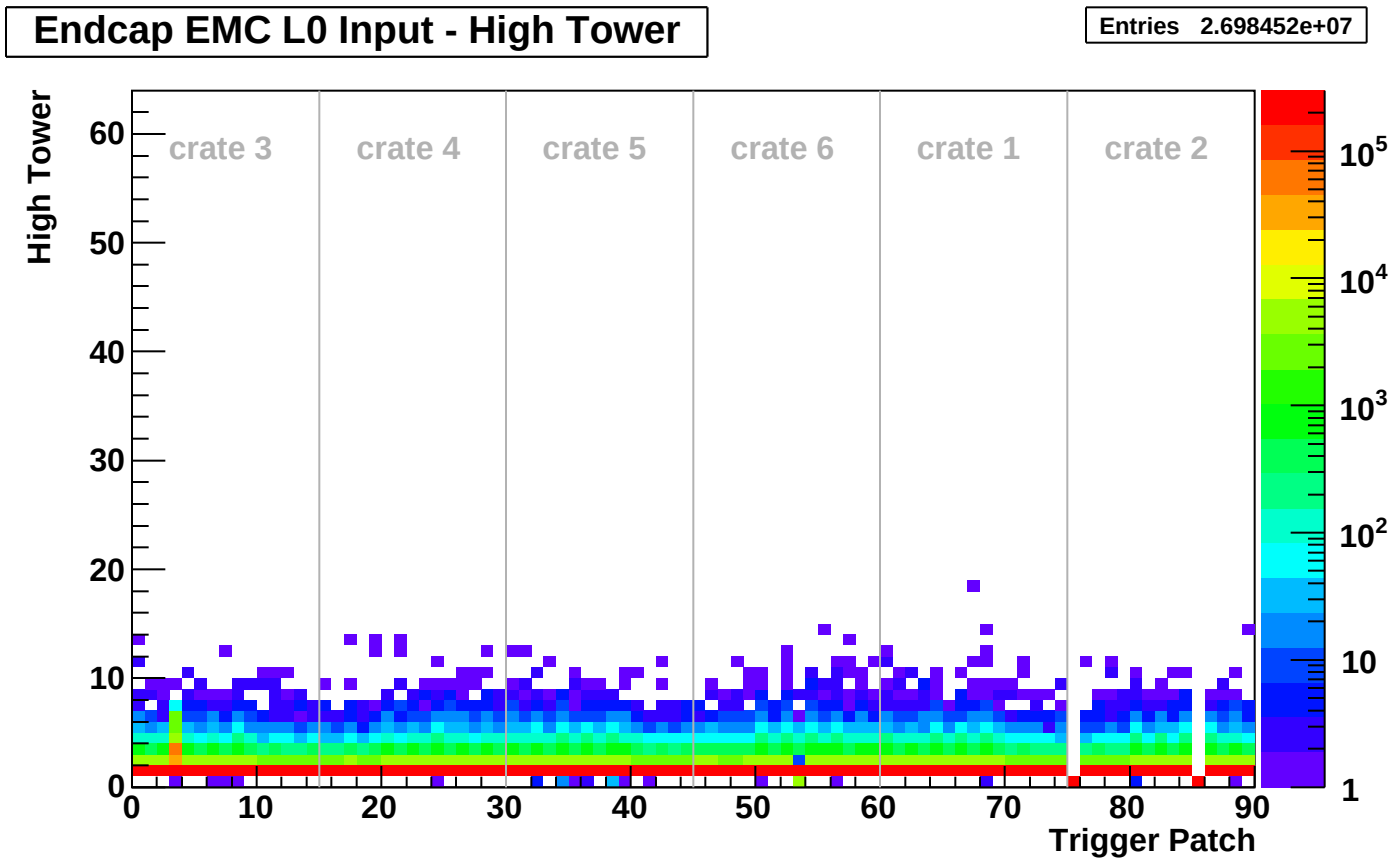
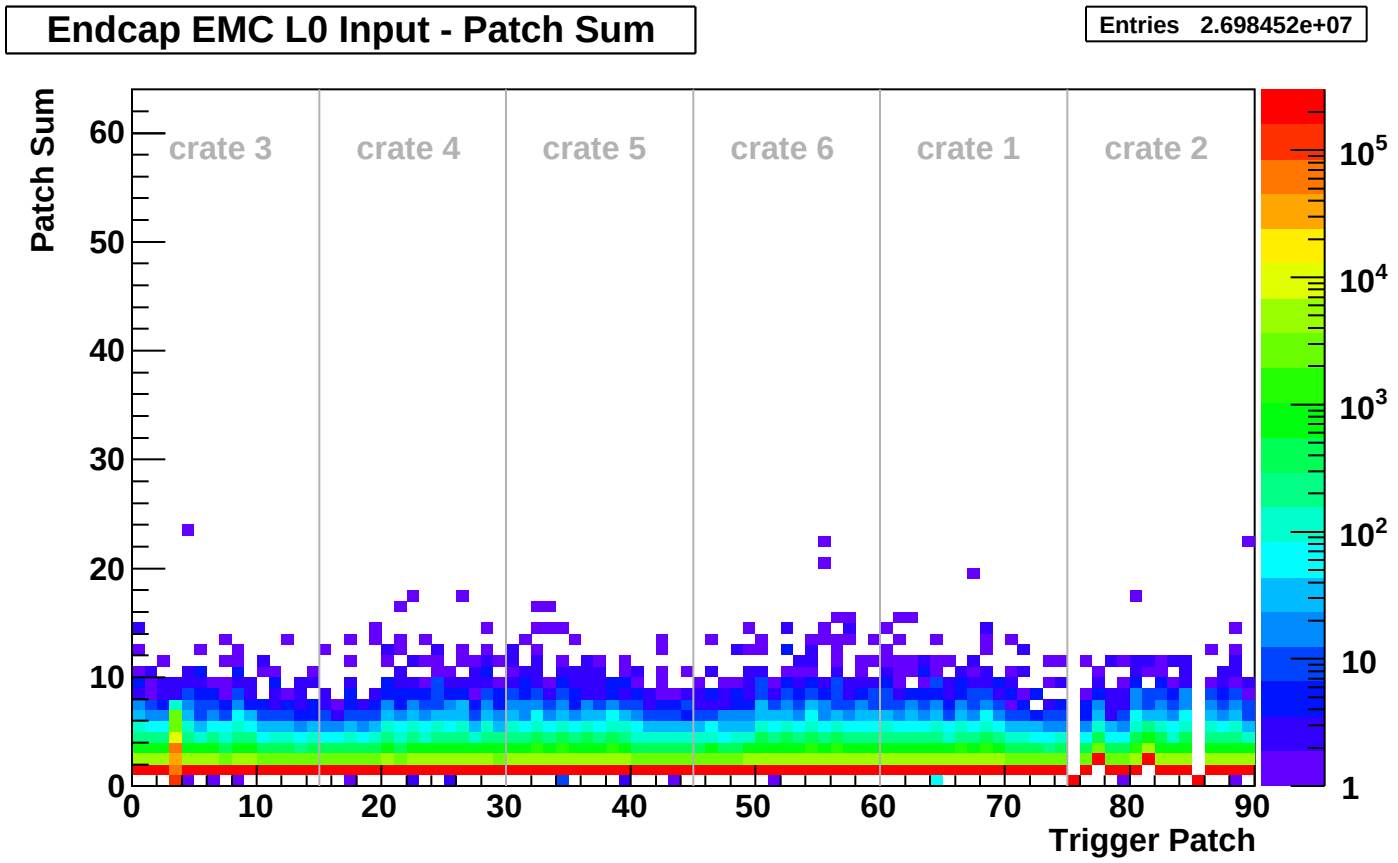
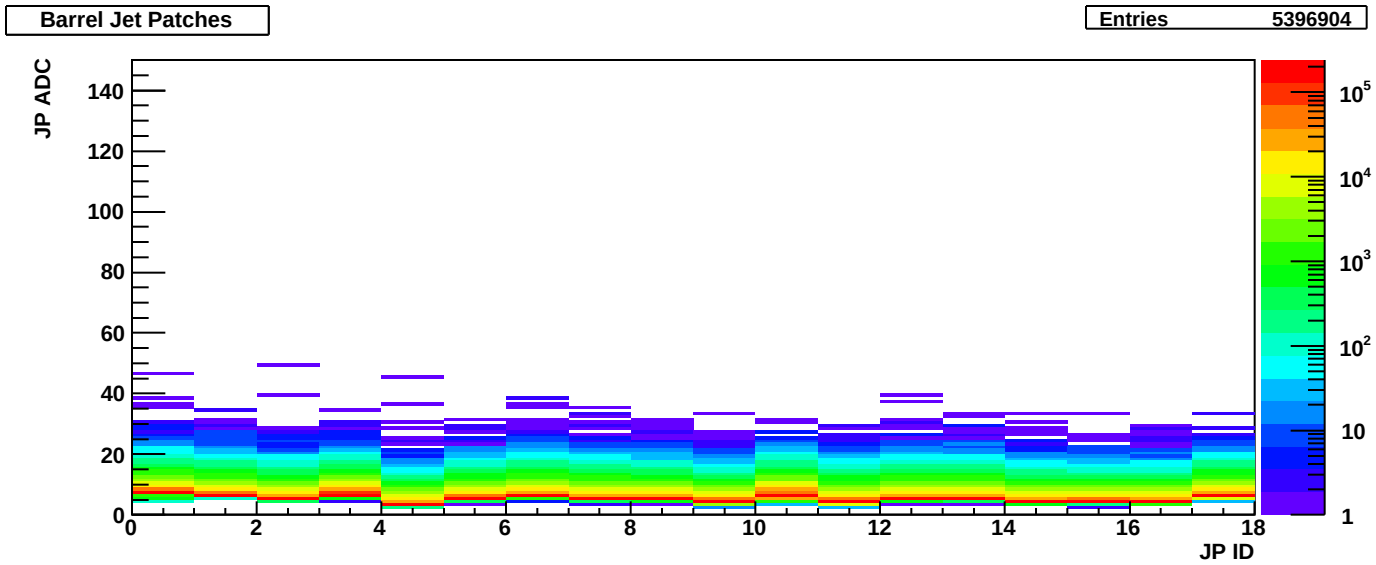
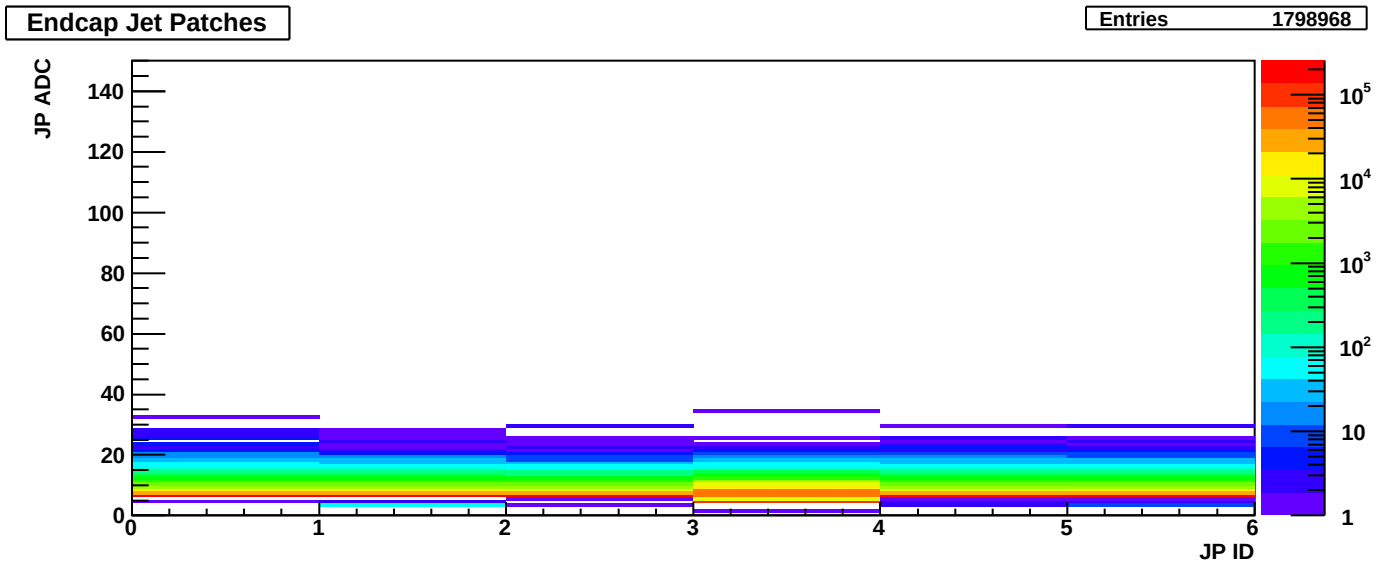

Barrel EMC L0 Input - High Tower
Entries 8.99484e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

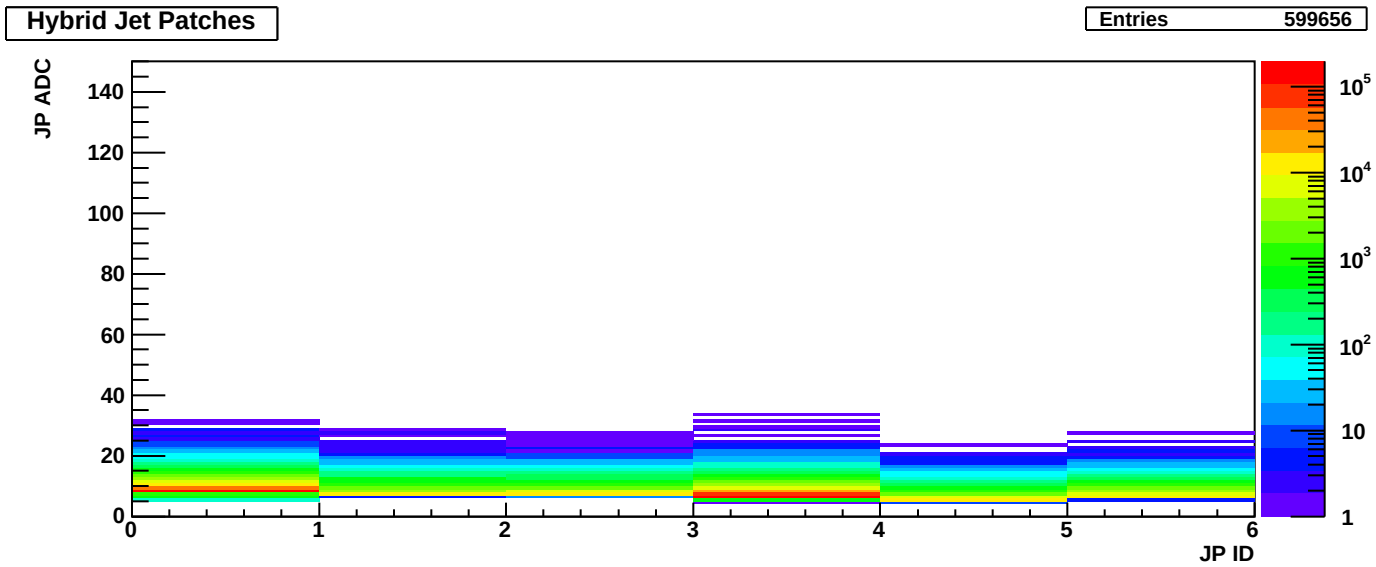

Barrel EMC L0 Input - Patch Sum
Entries 8.99484e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

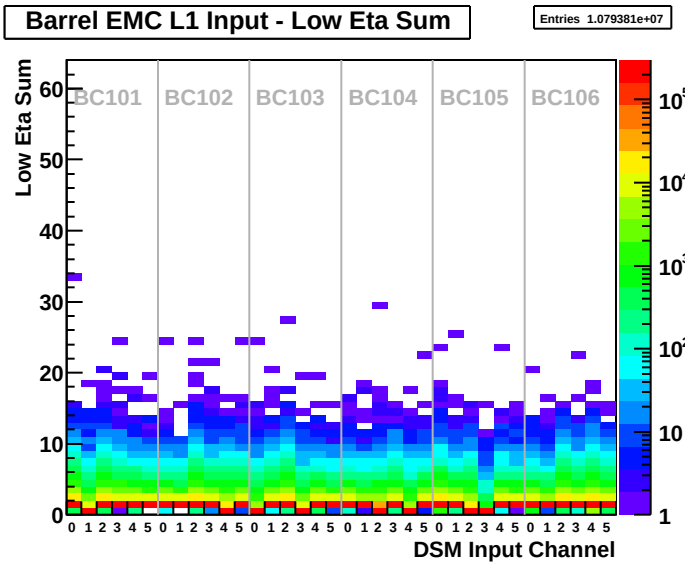

Endcap EMC L0 Input - High Tower
Entries 2.698452e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

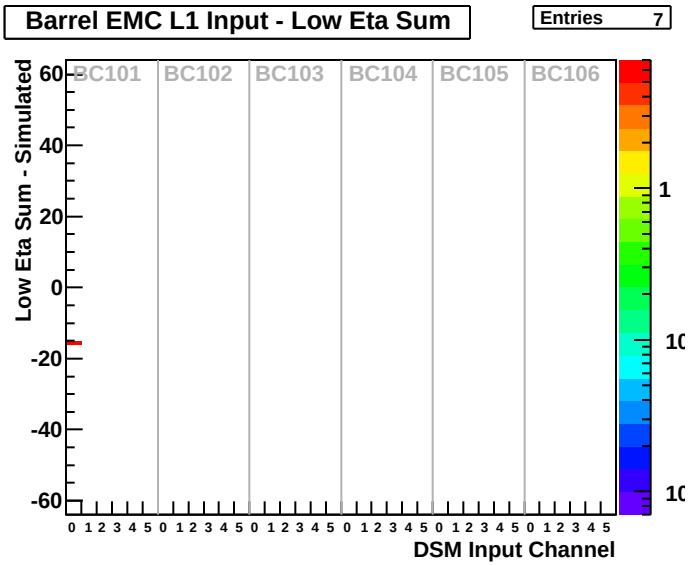

Endcap EMC L0 Input - Patch Sum
Entries 2.698452e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

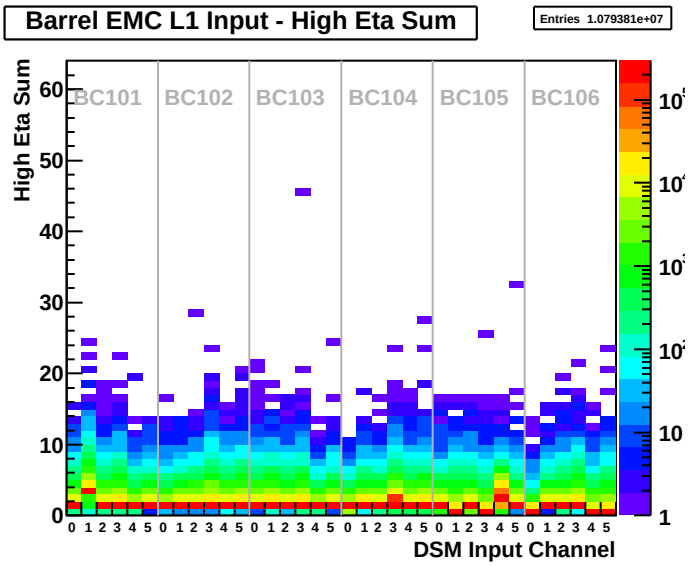

Barrel Jet Patches
Entries 5396904
JP ADC
JP ID

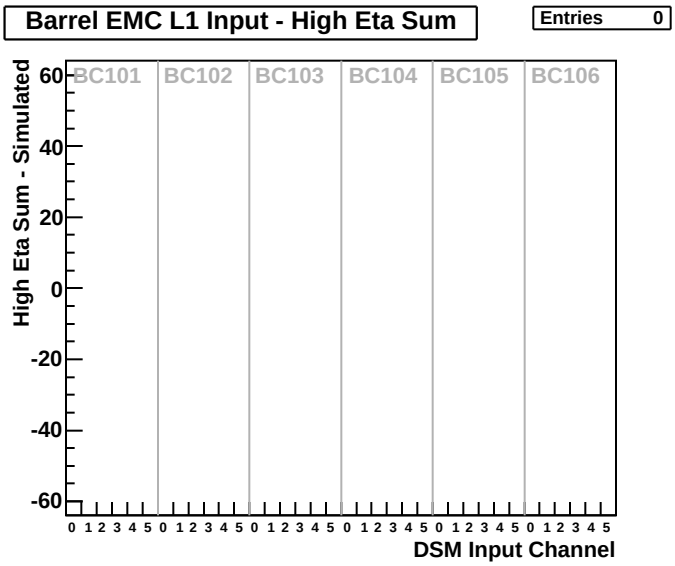

Endcap Jet Patches
Entries 1798968
JP ADC
JP ID

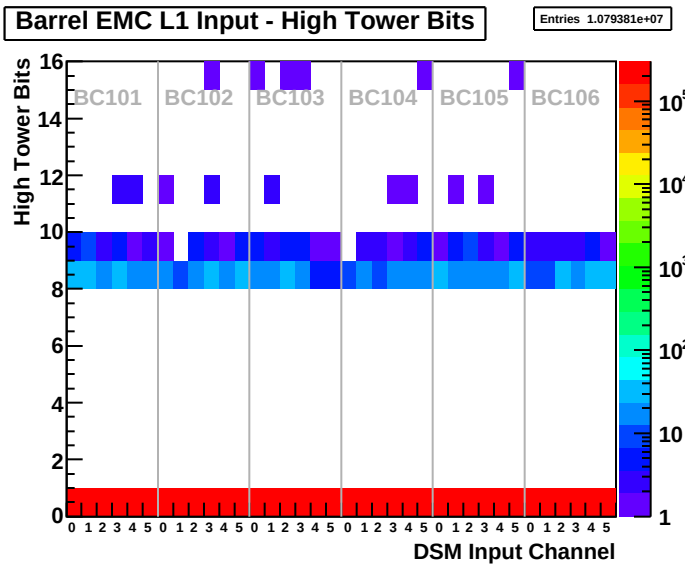

Hybrid Jet Patches
Entries 599656
JP ADC
JP ID

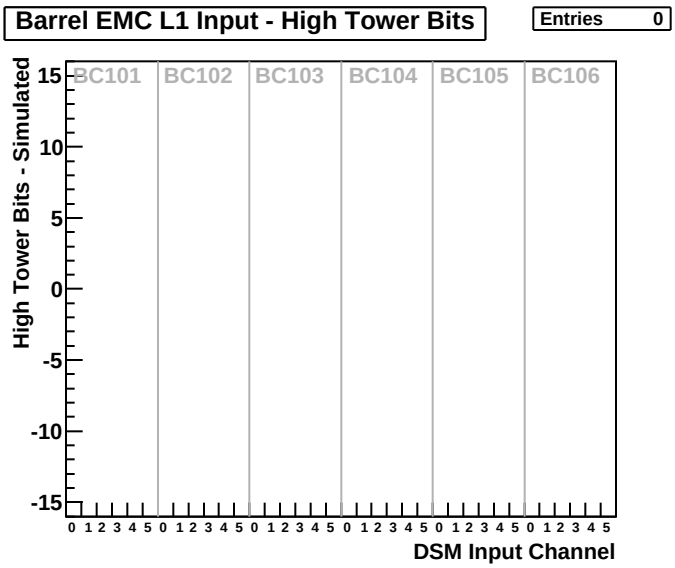

Barrel EMC L1 Input - Low Eta Sum
Entries 1.079381e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

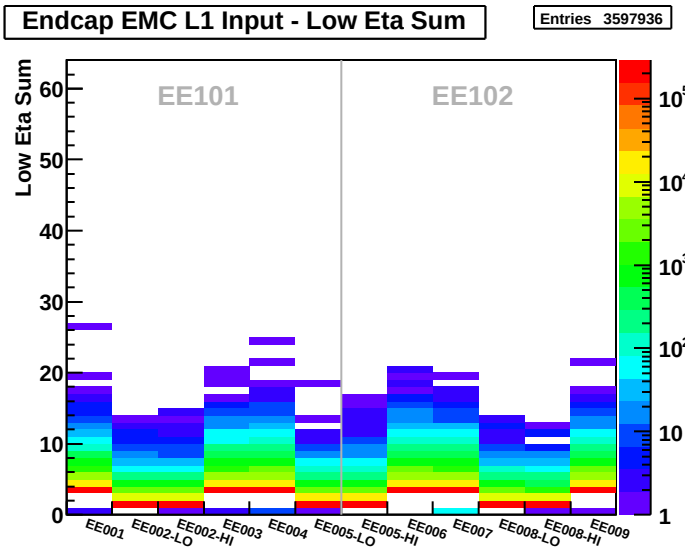

Barrel EMC L1 Input - Low Eta Sum
Entries 7
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

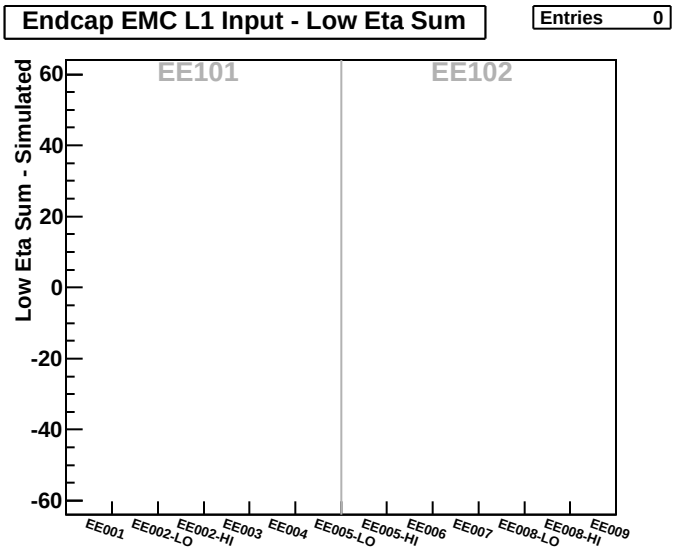

Barrel EMC L1 Input - High Eta Sum
Entries 1.079381e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

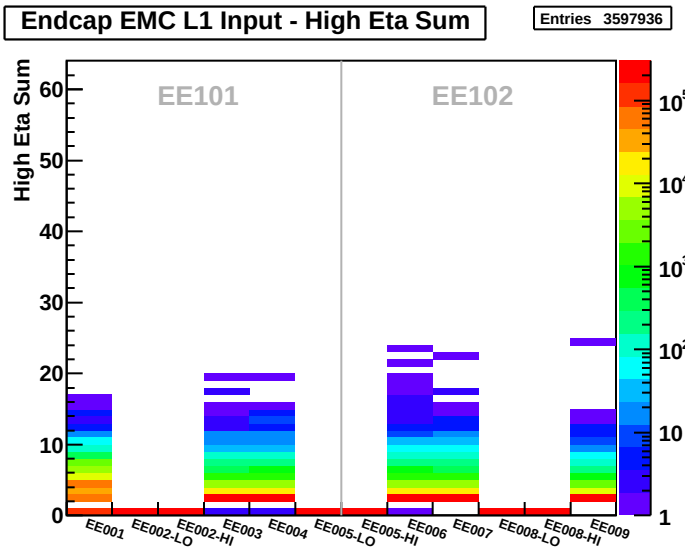

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

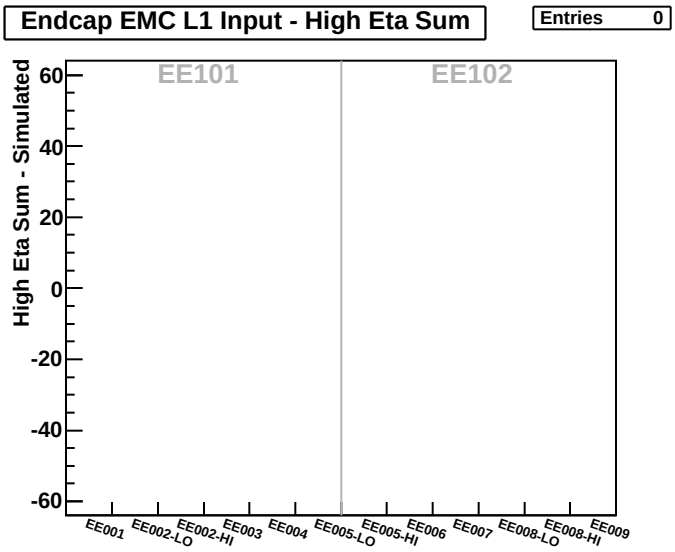

Barrel EMC L1 Input - High Tower Bits
Entries 1.079381e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

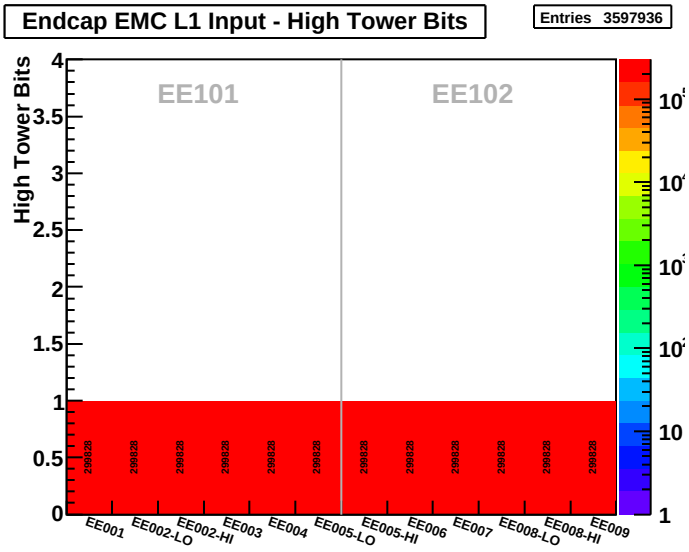

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

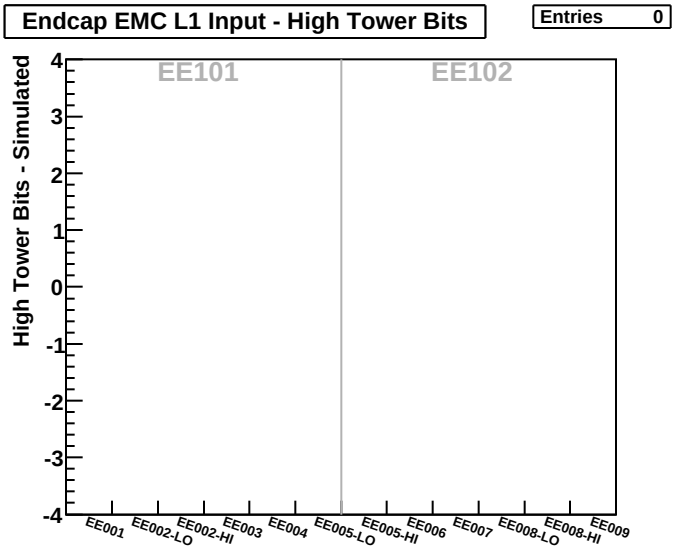

Endcap EMC L1 Input - Low Eta Sum
Entries 3597936
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

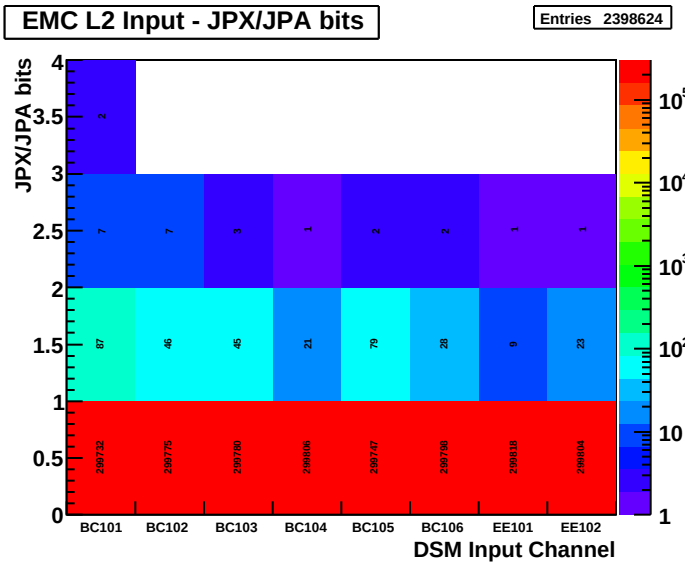

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

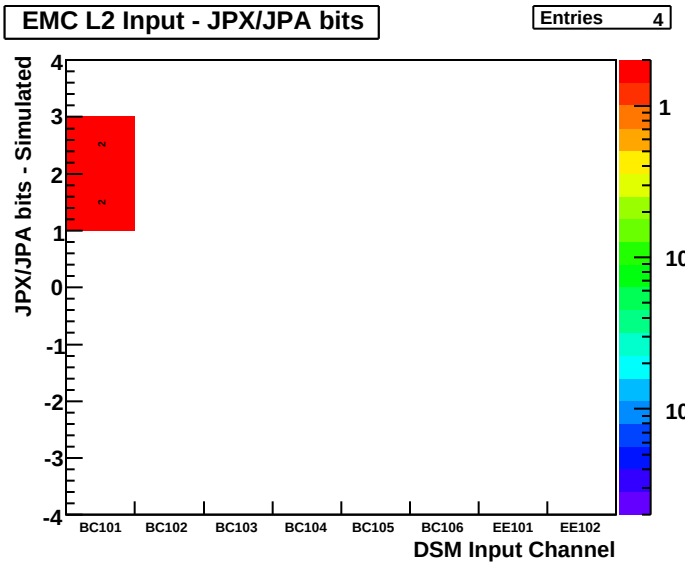

Endcap EMC L1 Input - High Eta Sum
Entries 3597936
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

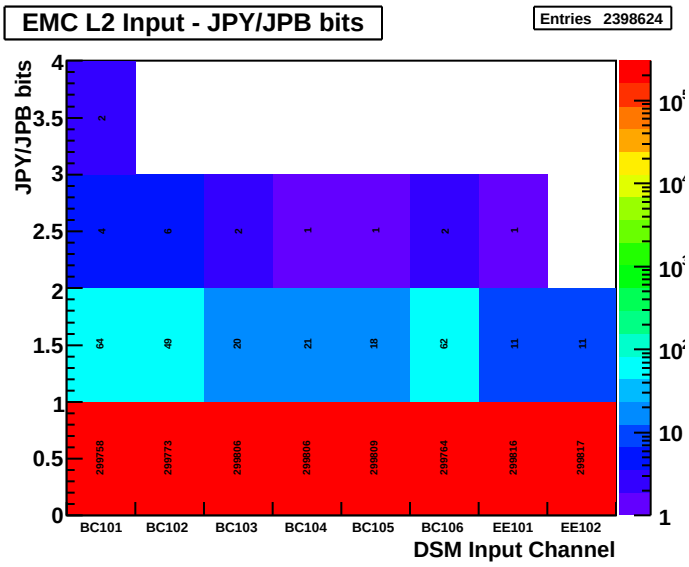

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

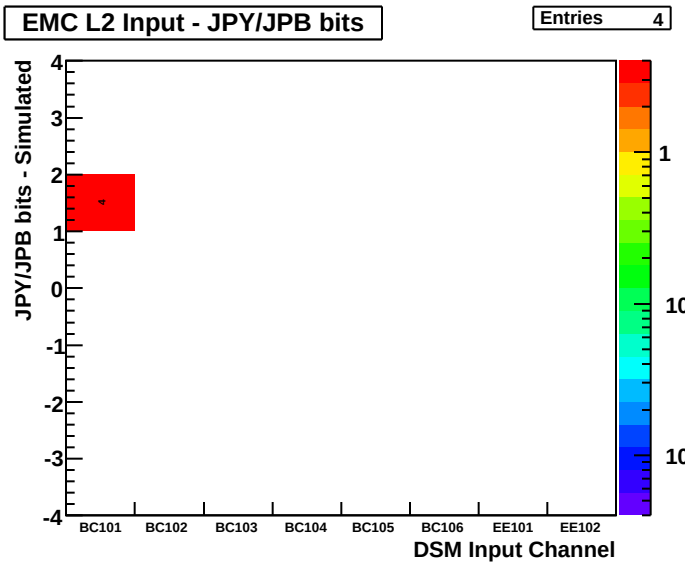

Endcap EMC L1 Input - High Tower Bits
Entries 3597936
High Tower Bits
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

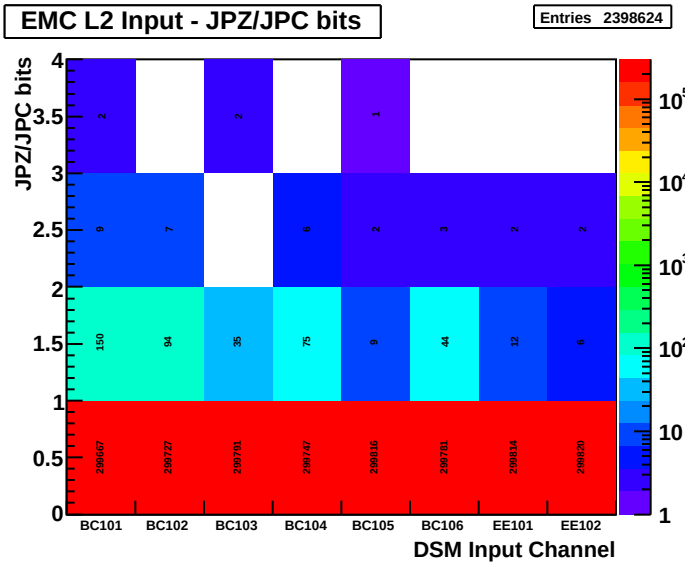

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

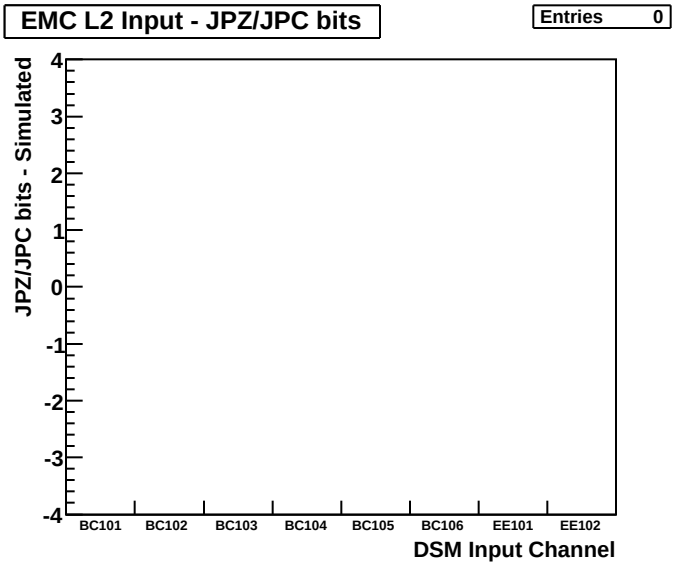

EMC L2 Input - JPX/JPA bits
Entries 2398624
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

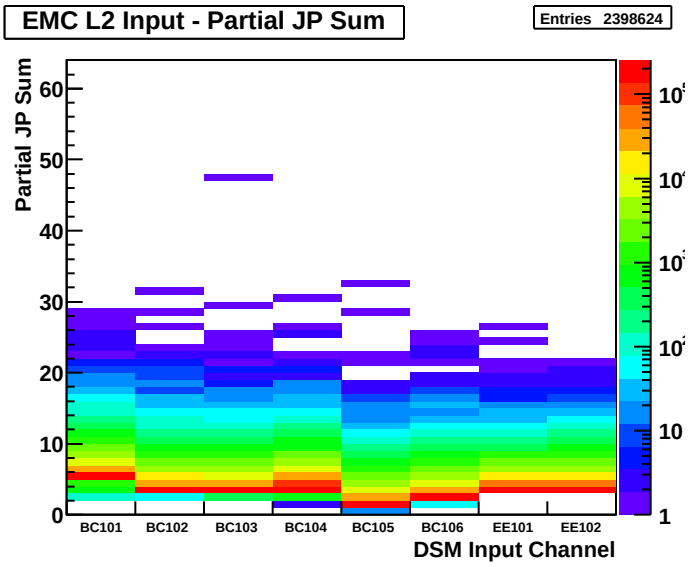

EMC L2 Input - JPX/JPA bits
Entries 4
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

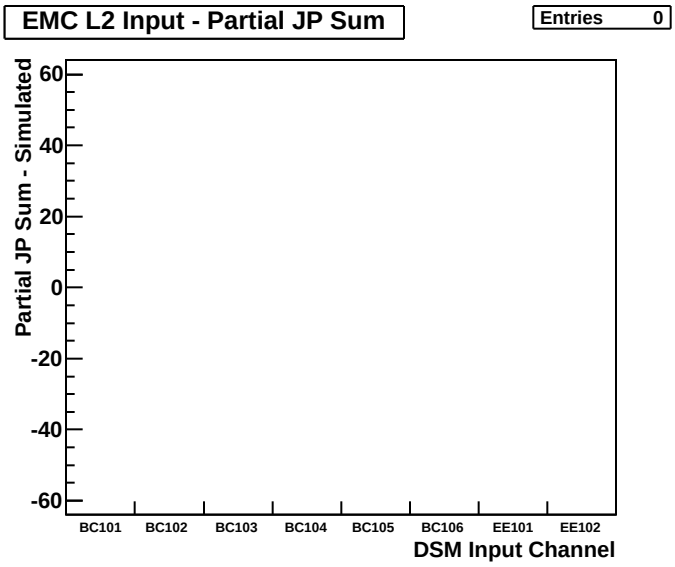

EMC L2 Input - JPY/JPB bits
Entries 2398624
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

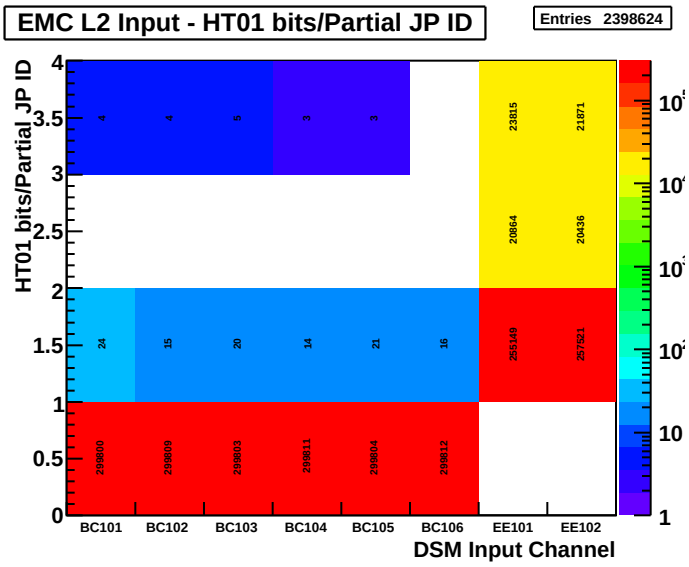

EMC L2 Input - JPY/JPB bits
Entries 4
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

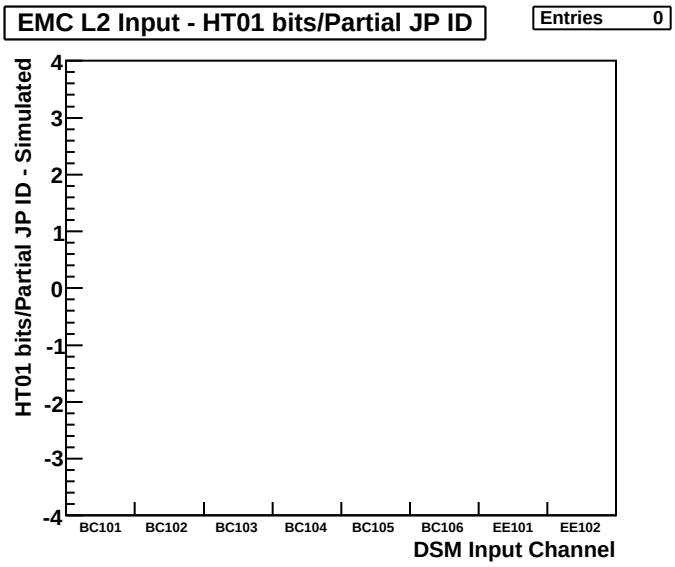

EMC L2 Input - JPZ/JPC bits
Entries 2398624
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

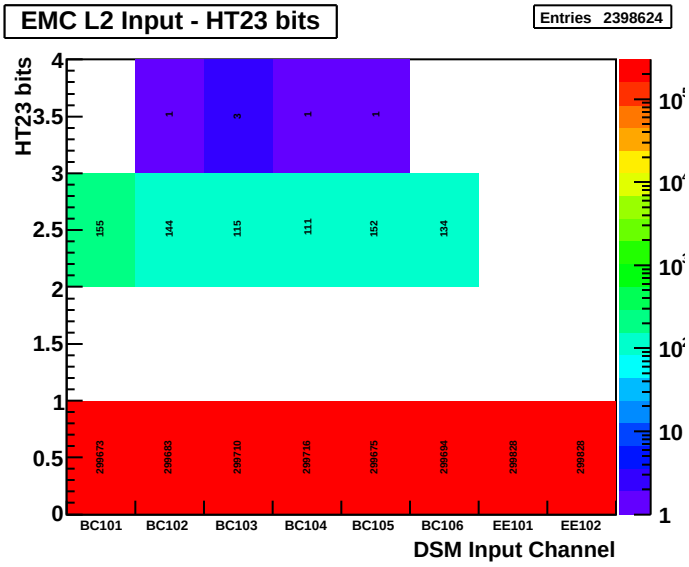

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

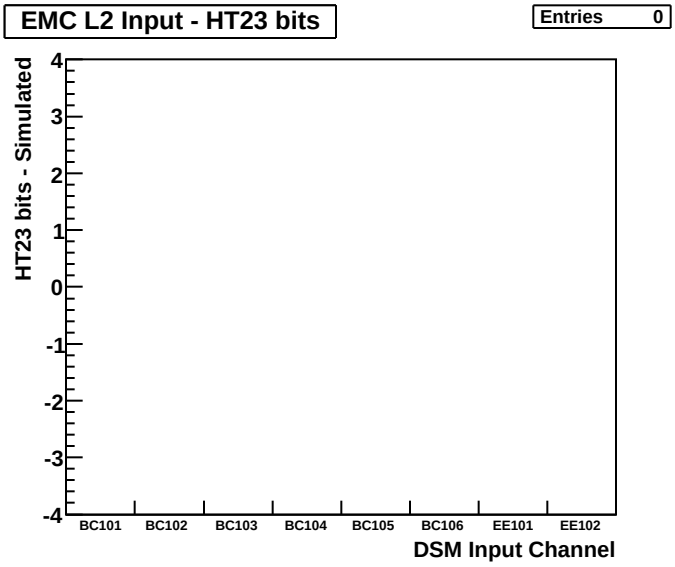

EMC L2 Input - Partial JP Sum
Entries 2398624
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

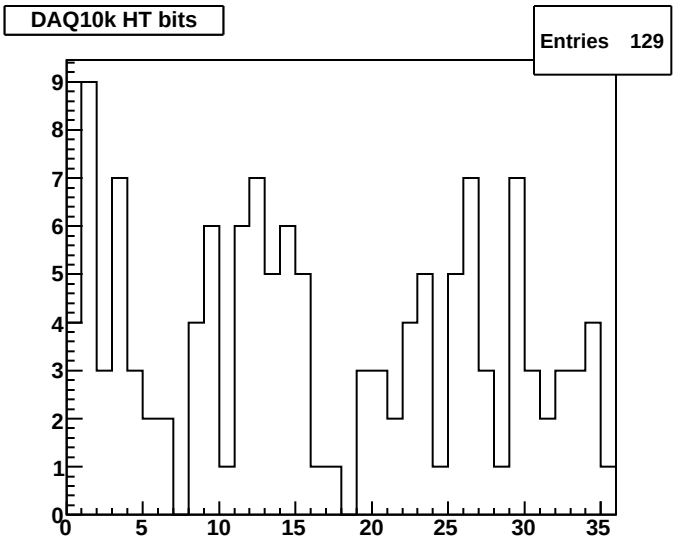

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

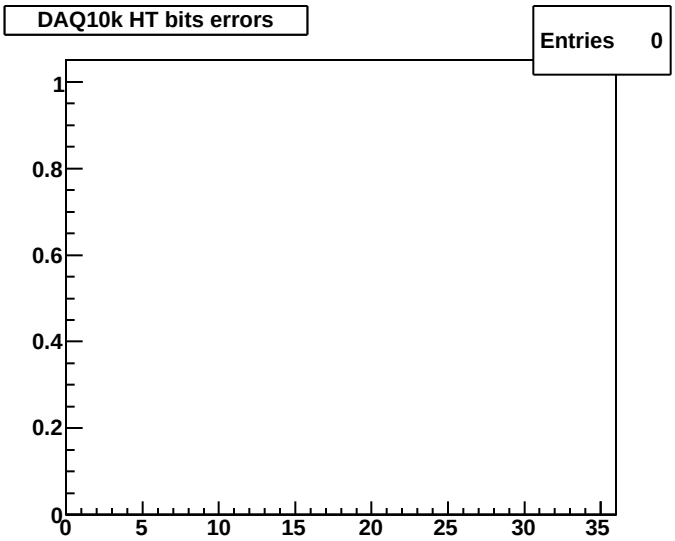

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2398624
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

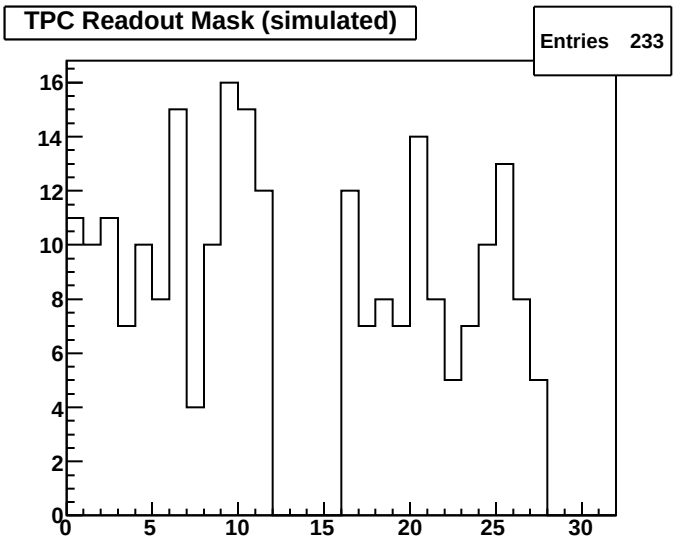

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

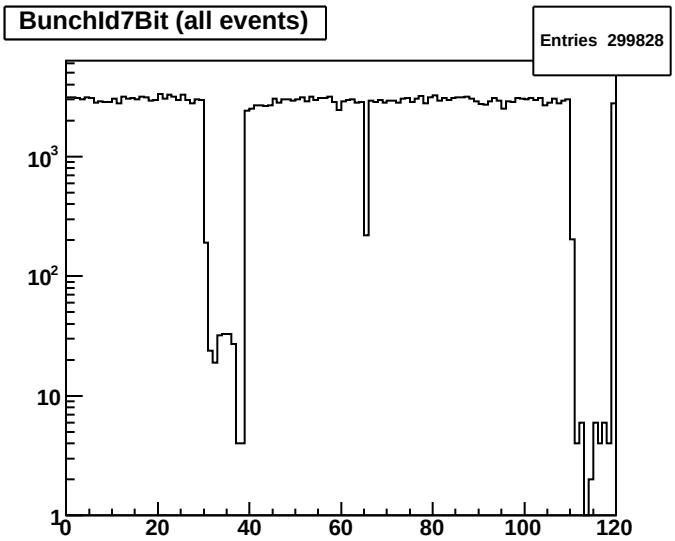

EMC L2 Input - HT23 bits
Entries 2398624
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

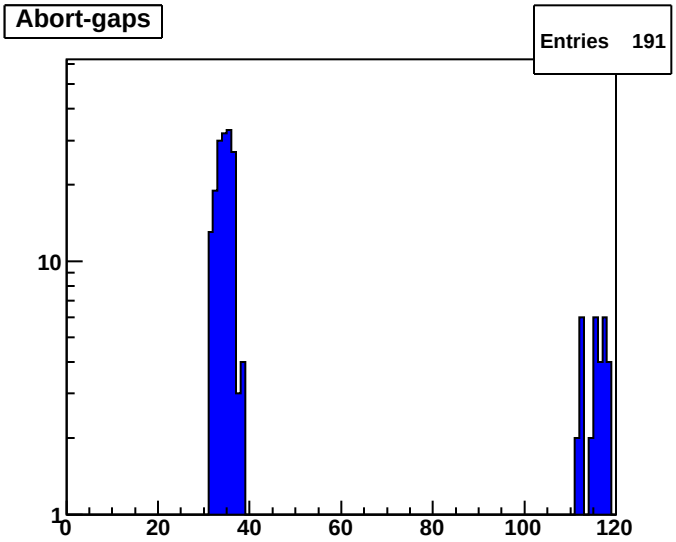

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

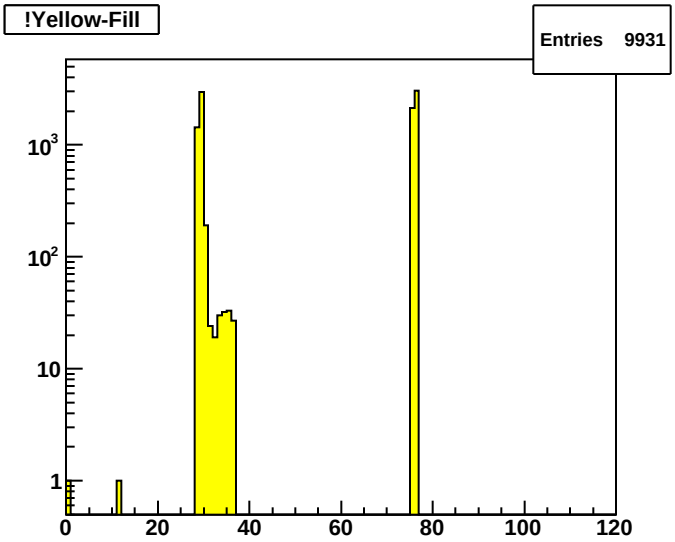

DAQ10k HT bits
Entries 129

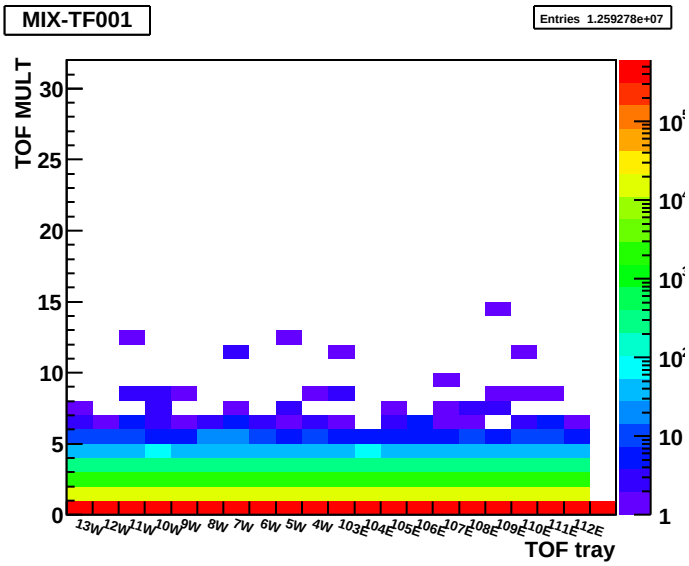

DAQ10k HT bits errors
Entries 0

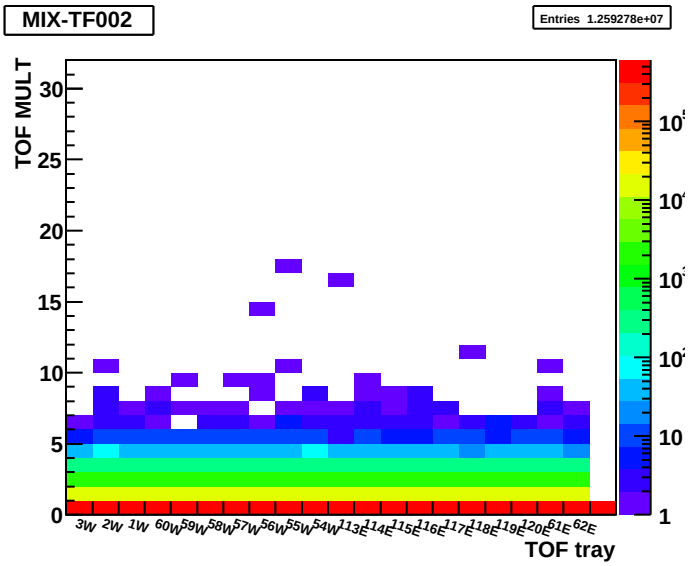

TPC Readout Mask (simulated)
Entries 233

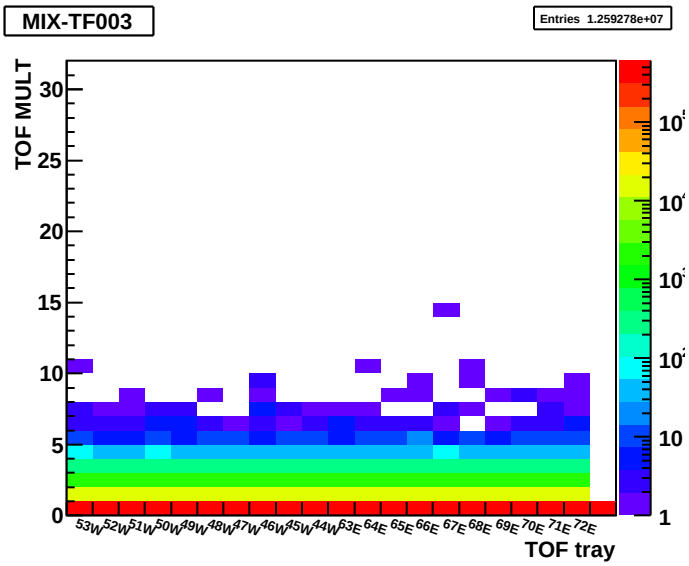

BunchId7Bit (all events)
Entries 299828

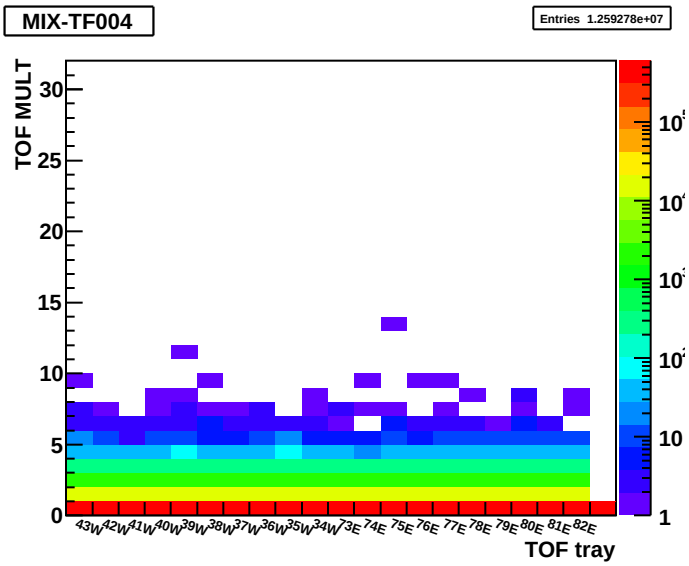

Abort-gaps
Entries 191

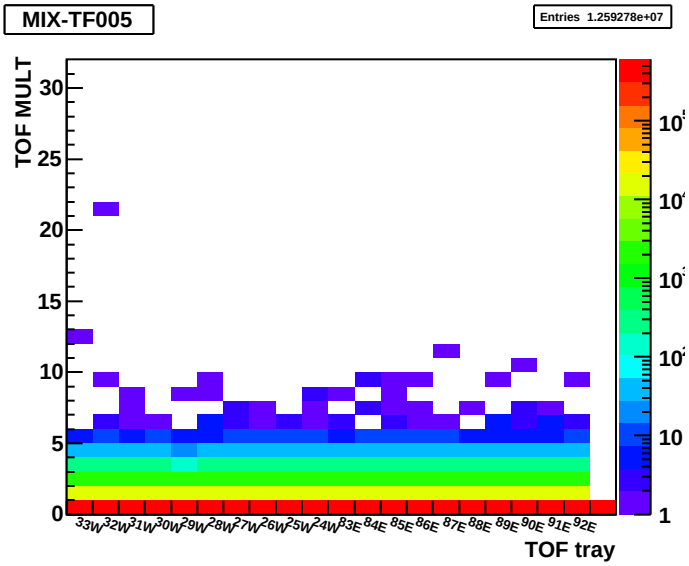

!Yellow-Fill
Entries 9931

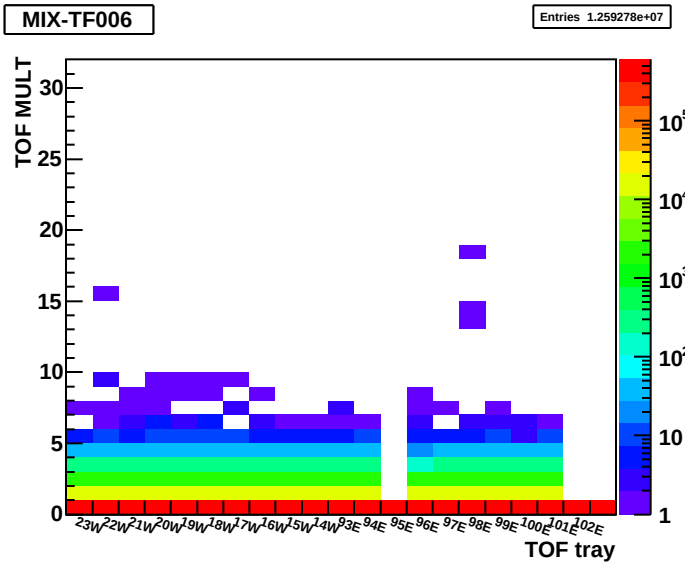

MIX-TF001
Entries 1.259278e+07
TOF MULT
TOF tray

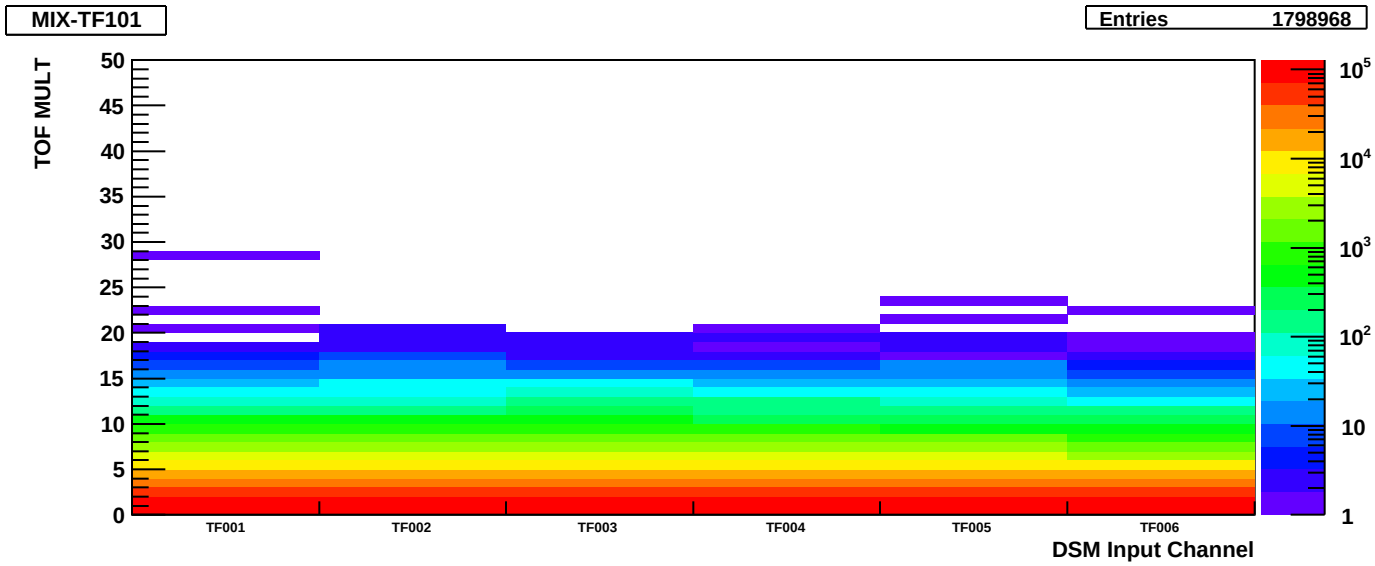

MIX-TF002
Entries 1.259278e+07
TOF MULT
TOF tray

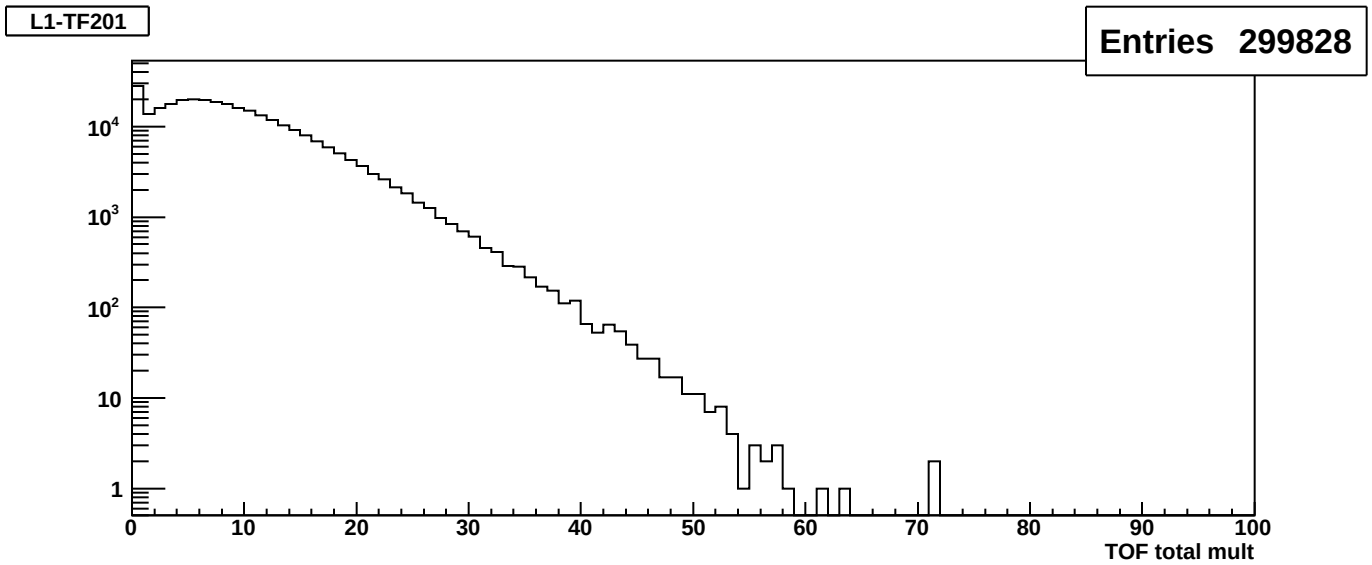

MIX-TF003
Entries 1.259278e+07
TOF MULT
TOF tray

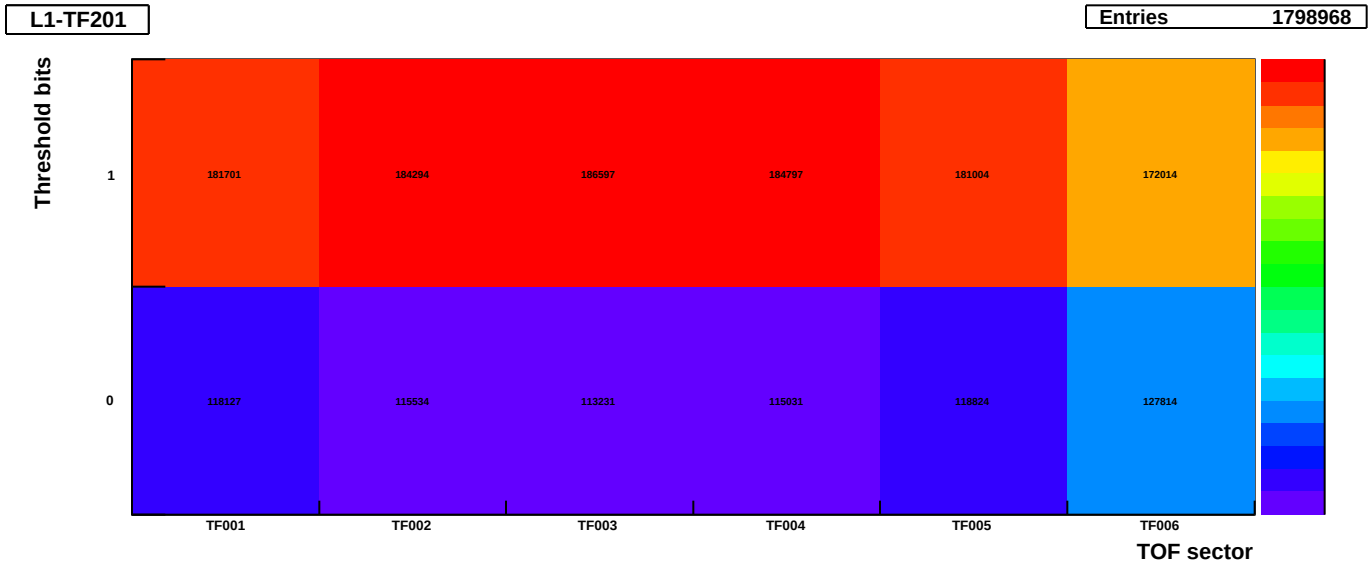

MIX-TF004
Entries 1.259278e+07
TOF MULT
TOF tray

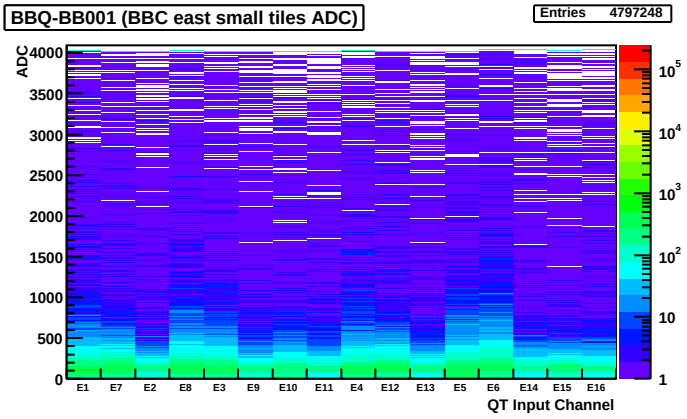

MIX-TF005
Entries 1.259278e+07
TOF MULT
TOF tray

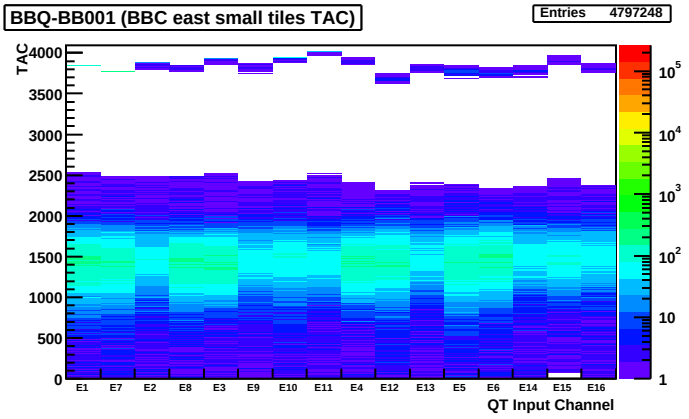

MIX-TF006
Entries 1.259278e+07
TOF MULT
TOF tray

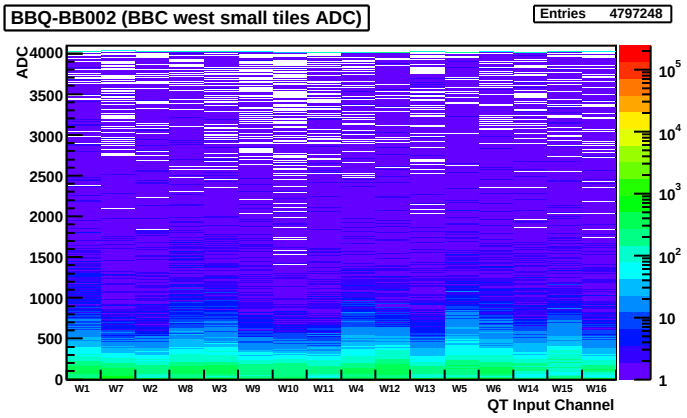

MIX-TF101
Entries 1798968
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

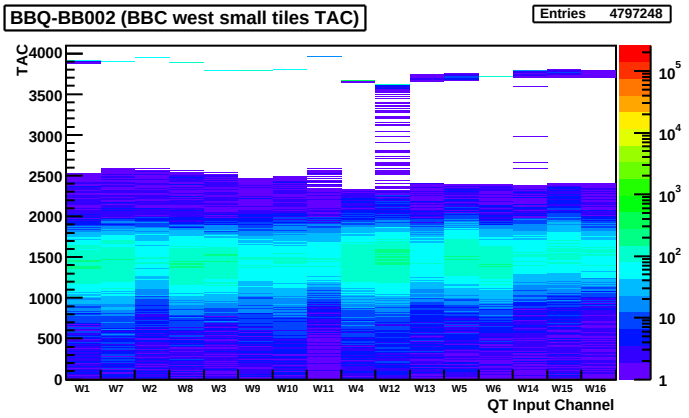

L1-TF201
Entries 299828
TOF total mult

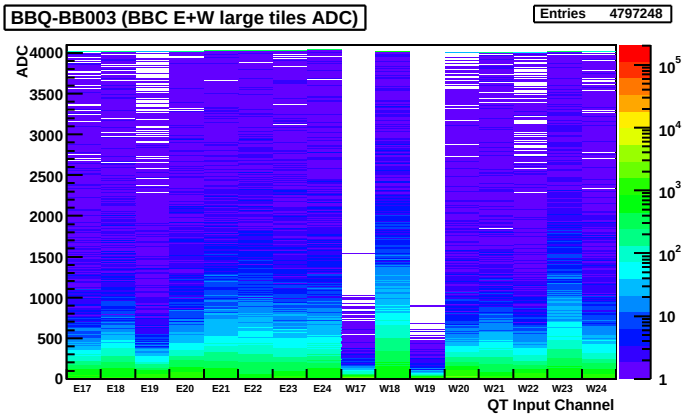

L1-TF201
Entries 1798968
Threshold bits
181701
184294
186597
184797
181004
172014
118127
115534
113231
115031
118824
127814
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

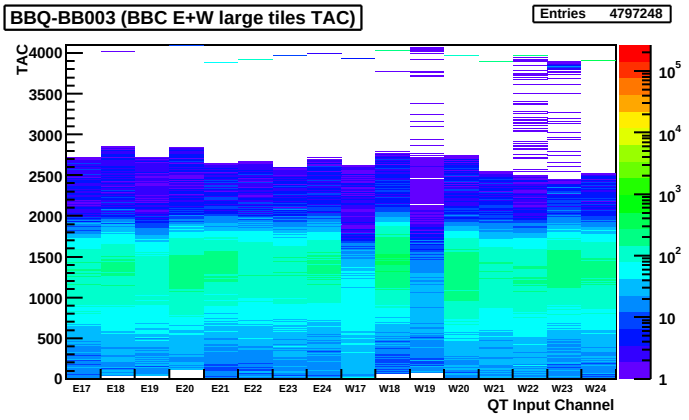

BBQ-BB001 (BBC east small tiles ADC)
Entries 4797248
ADC
QT Input Channel

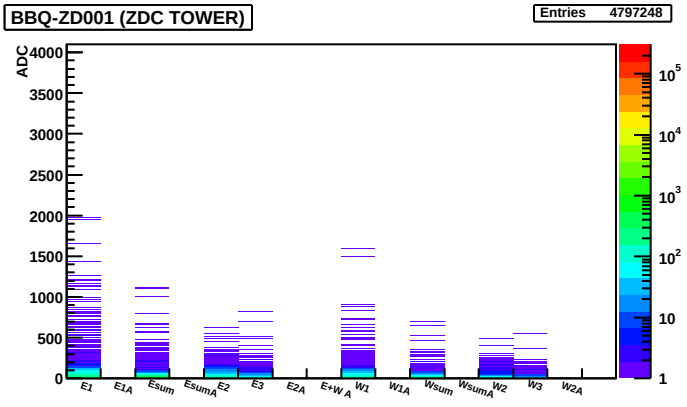

BBQ-BB001 (BBC east small tiles TAC)
Entries 4797248
TAC
QT Input Channel

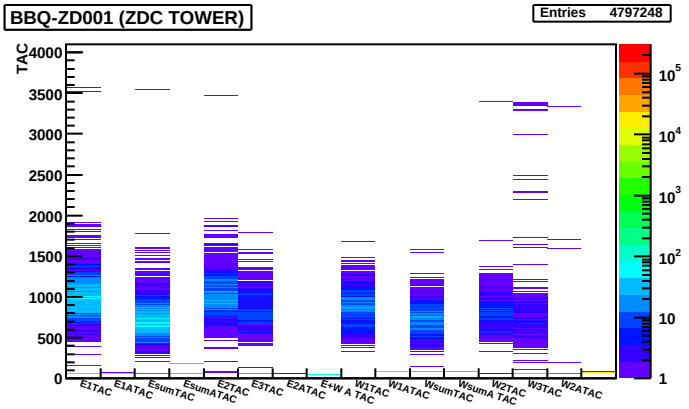

BBQ-BB002 (BBC west small tiles ADC)
Entries 4797248
ADC
QT Input Channel

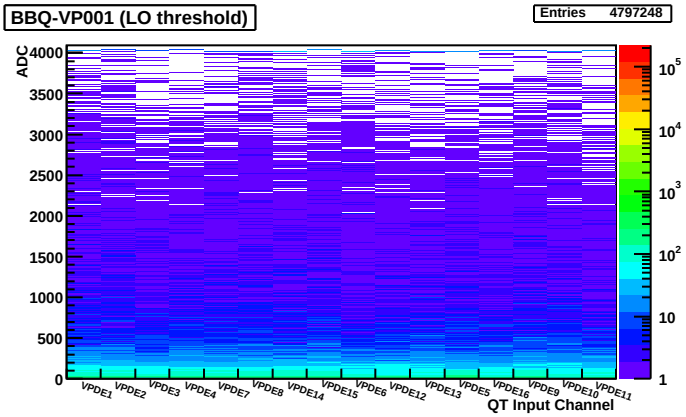

BBQ-BB002 (BBC west small tiles TAC)
Entries 4797248
TAC
QT Input Channel

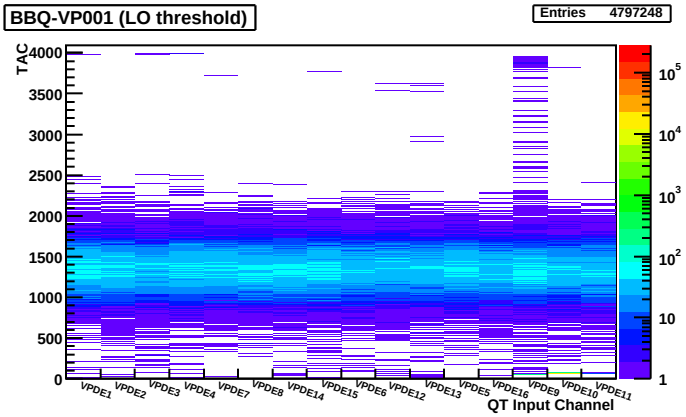

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4797248
ADC
QT Input Channel

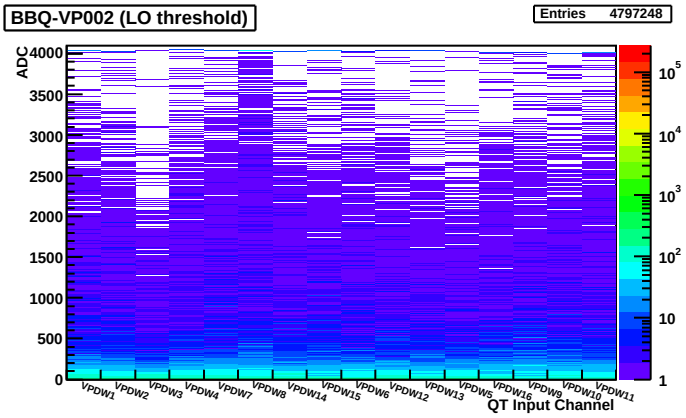

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4797248
TAC
QT Input Channel

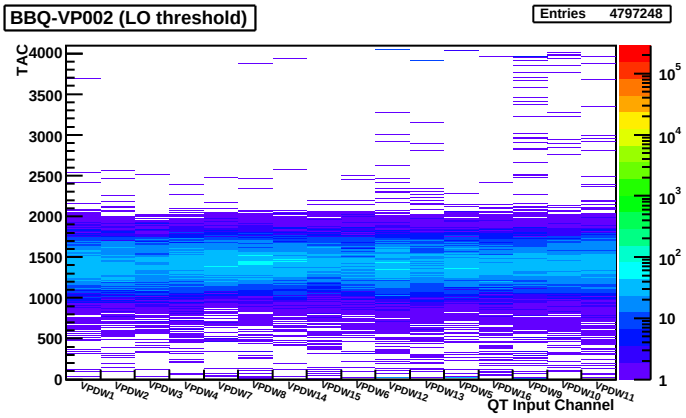

BBQ-ZD001 (ZDC TOWER)
Entries 4797248
ADC

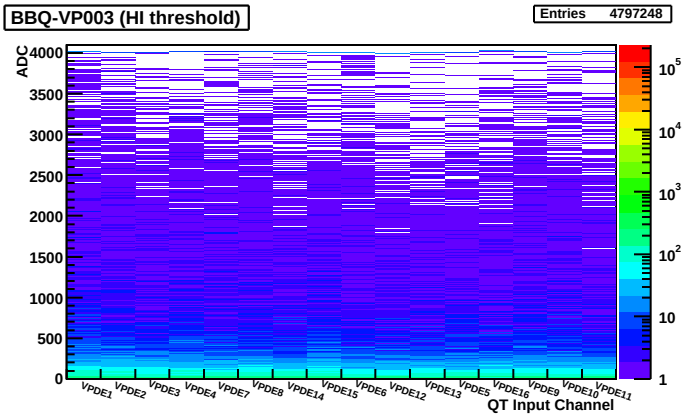

BBQ-ZD001 (ZDC TOWER)
Entries 4797248
TAC

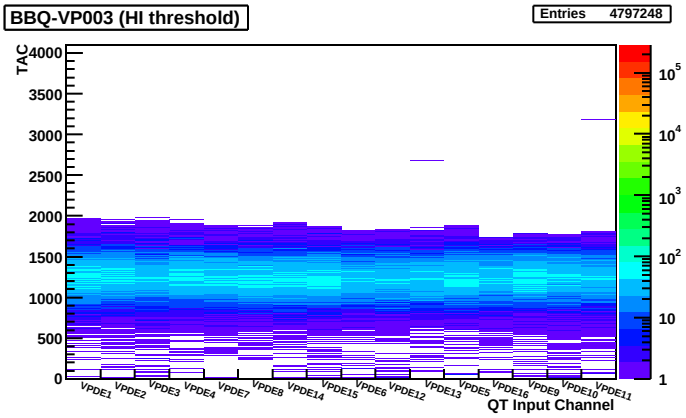

BBQ-VP001 (LO threshold)
Entries 4797248
ADC
QT Input Channel

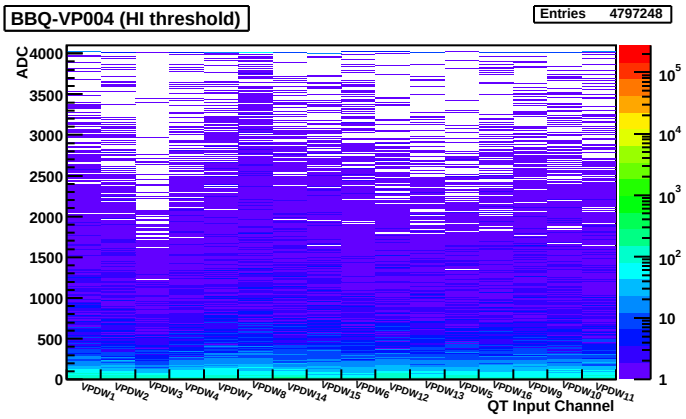

BBQ-VP001 (LO threshold)
Entries 4797248
TAC
QT Input Channel

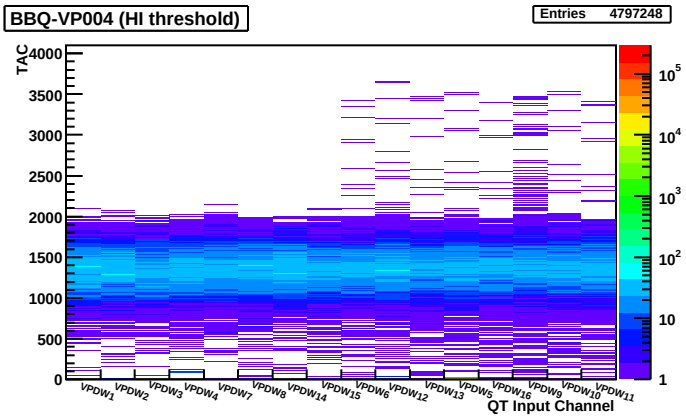

BBQ-VP002 (LO threshold)
Entries 4797248
ADC
QT Input Channel

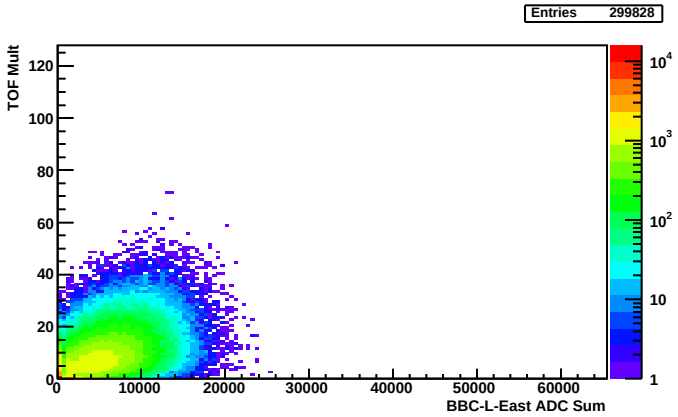

BBQ-VP002 (LO threshold)
Entries 4797248
TAC
QT Input Channel

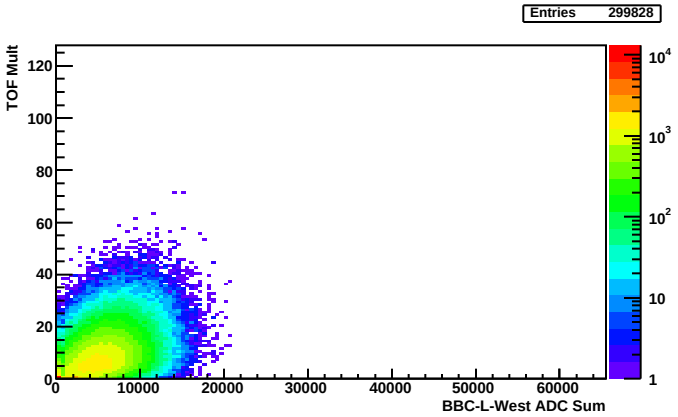

BBQ-VP003 (HI threshold)
Entries 4797248
ADC
QT Input Channel

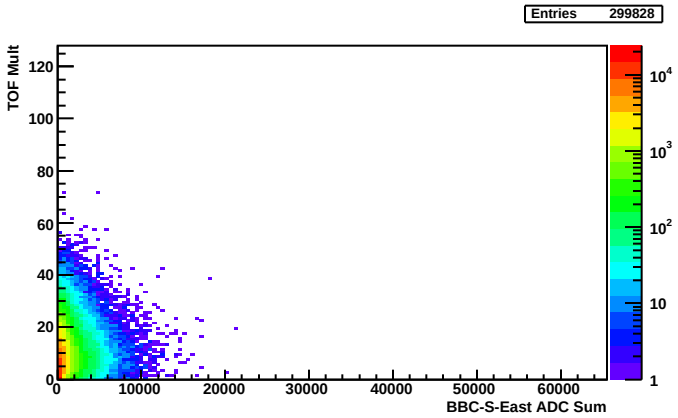

BBQ-VP003 (HI threshold)
Entries 4797248
TAC
QT Input Channel

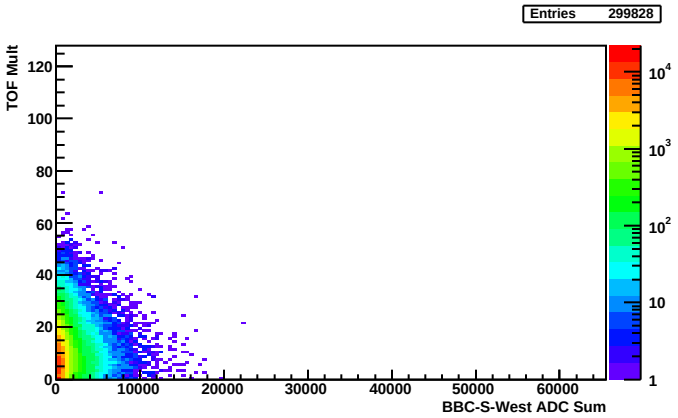

BBQ-VP004 (HI threshold)
Entries 4797248
ADC
QT Input Channel

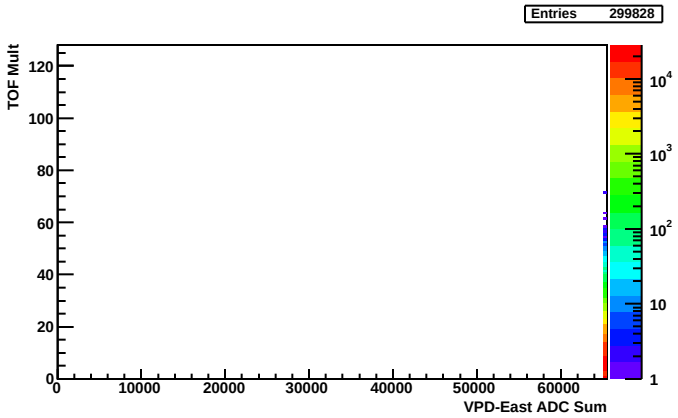

BBQ-VP004 (HI threshold)
Entries 4797248
TAC
QT Input Channel

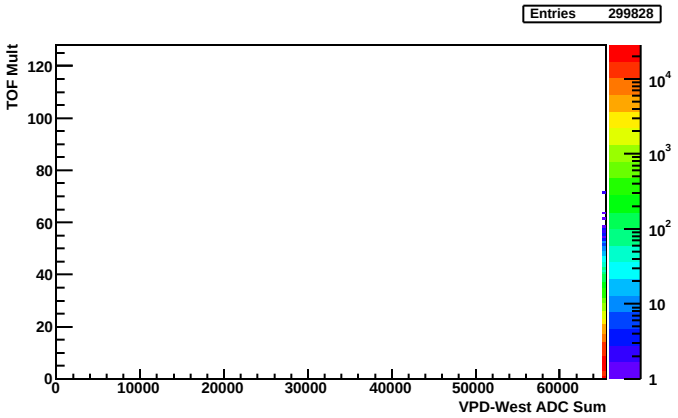

Entries 299828
TOF Mult
BBC-L-East ADC Sum

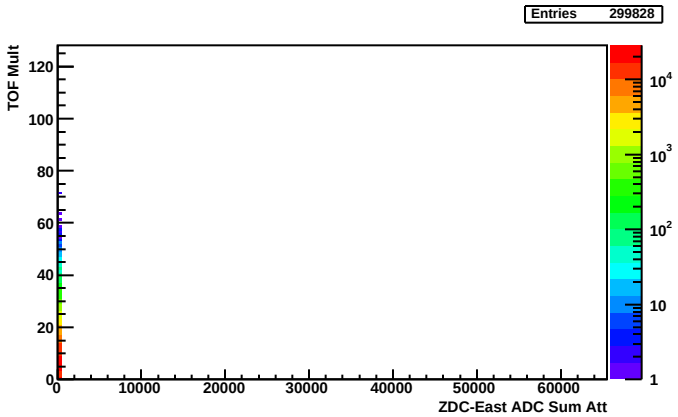

Entries 299828
TOF Mult
BBC-L-West ADC Sum

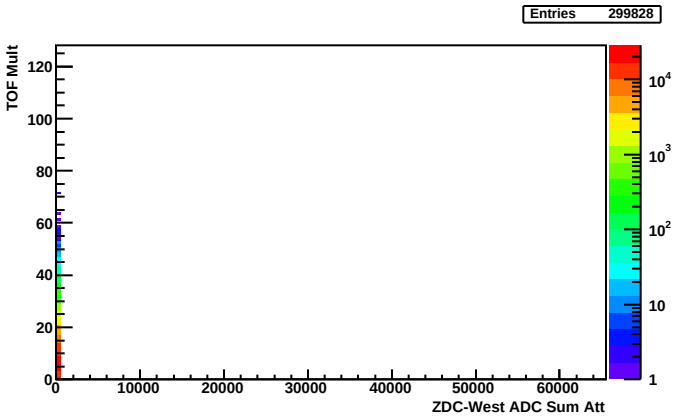

Entries 299828
TOF Mult
BBC-S-East ADC Sum

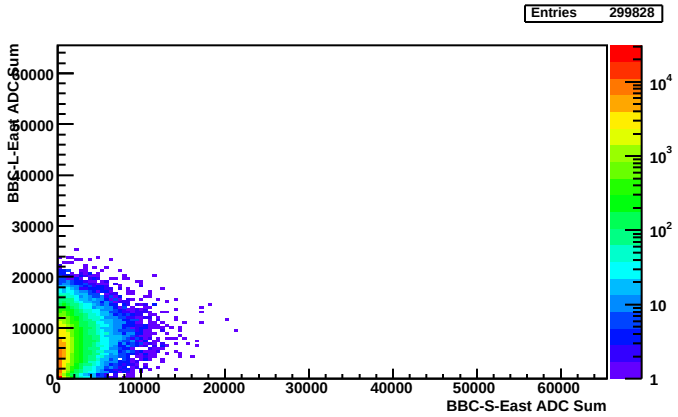

Entries 299828
TOF Mult
BBC-S-West ADC Sum

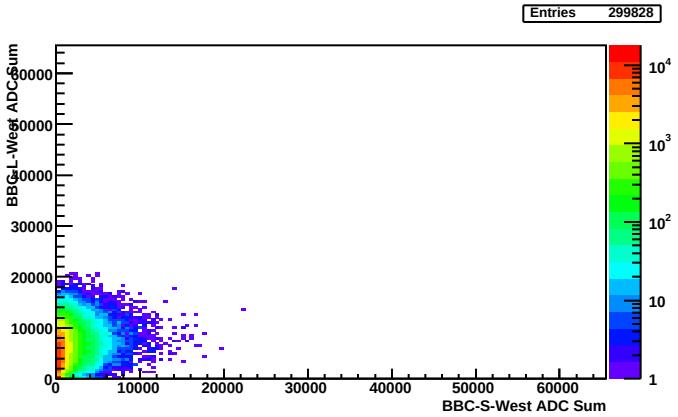

Entries 299828
TOF Mult
VPD-East ADC Sum

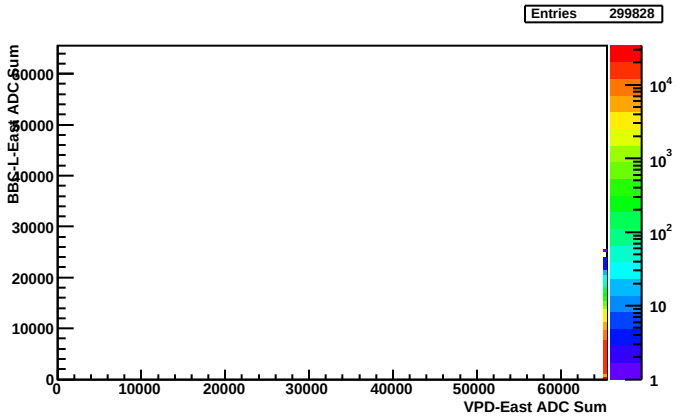

Entries 299828
TOF Mult
VPD-West ADC Sum

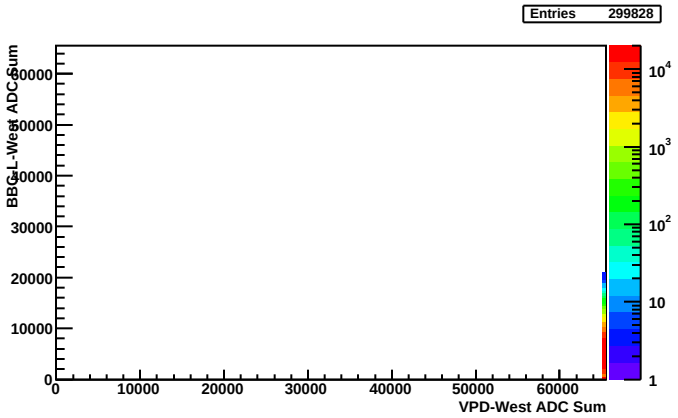

Entries 299828
TOF Mult
ZDC-East ADC Sum Att

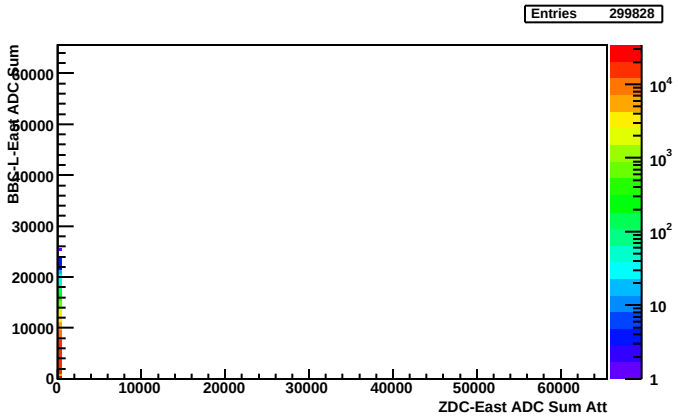

Entries 299828
TOF Mult
ZDC-West ADC Sum Att

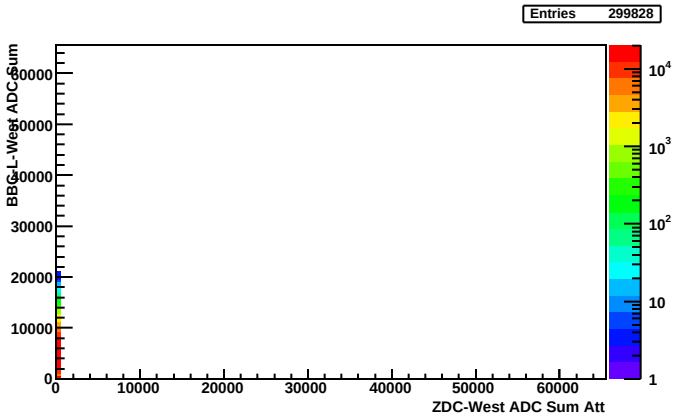

Entries 299828
BBC-L-East ADC Sum
BBC-S-East ADC Sum

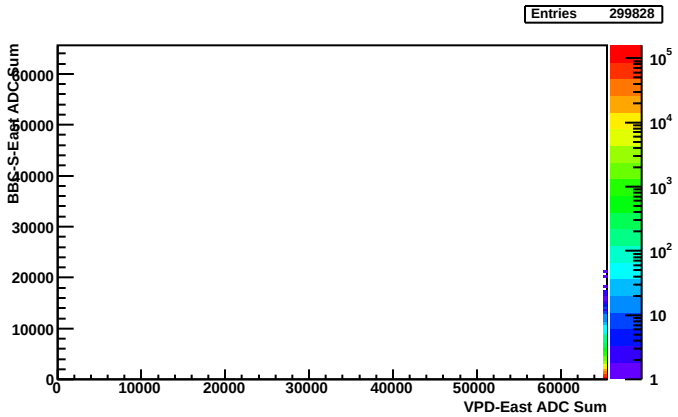

Entries 299828
BBC-L-West ADC Sum
BBC-S-West ADC Sum

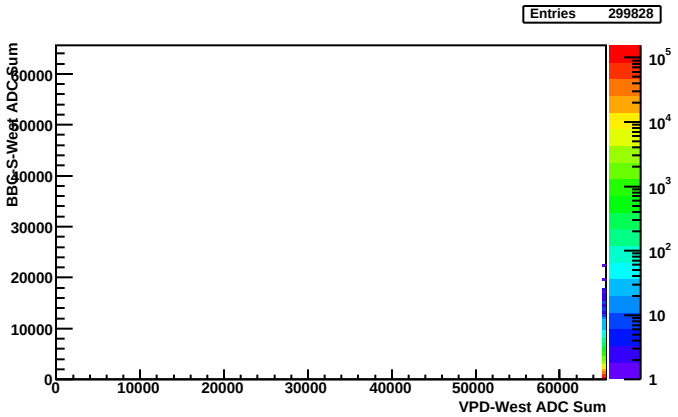

Entries 299828
BBC-L-East ADC Sum
VPD-East ADC Sum

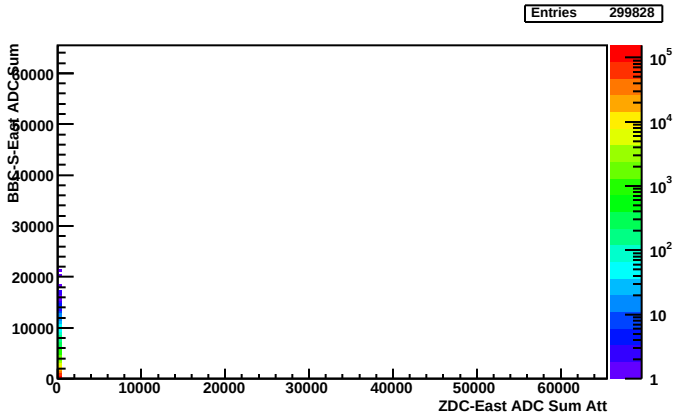

Entries 299828
BBC-L-West ADC Sum
VPD-West ADC Sum

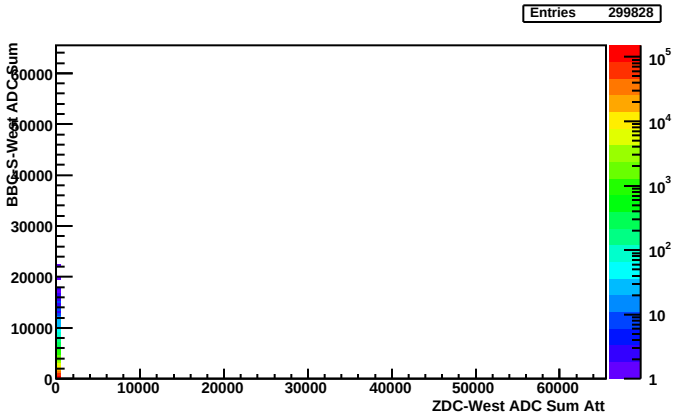

Entries 299828
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

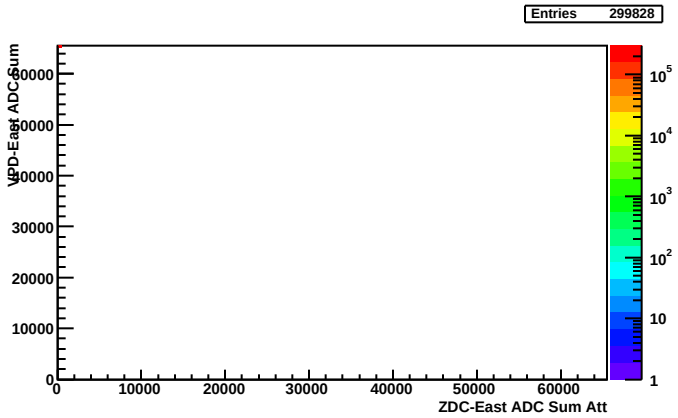

Entries 299828
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

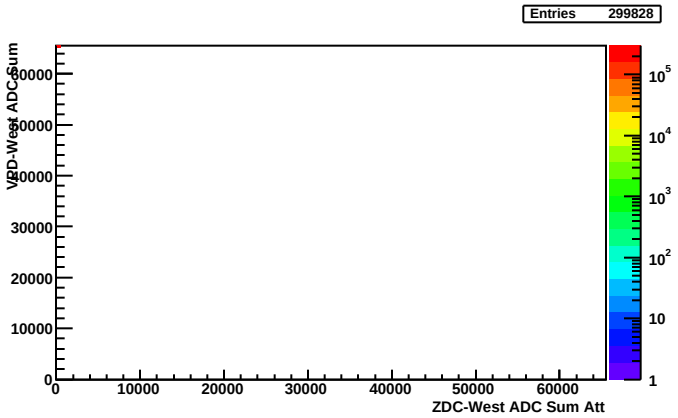

Entries 299828
BBC-S-East ADC Sum
VPD-East ADC Sum

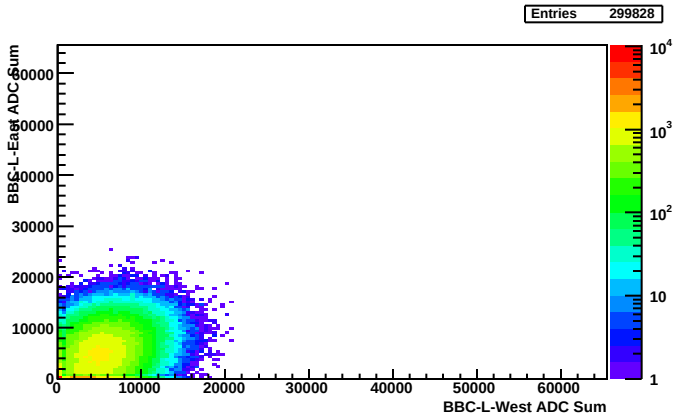

Entries 299828
BBC-S-West ADC Sum
VPD-West ADC Sum

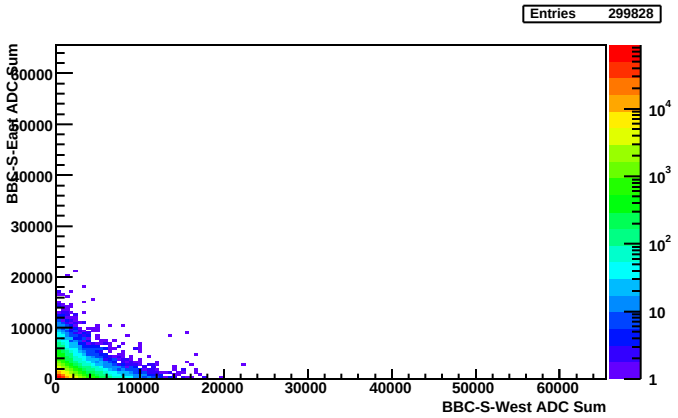

Entries 299828
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

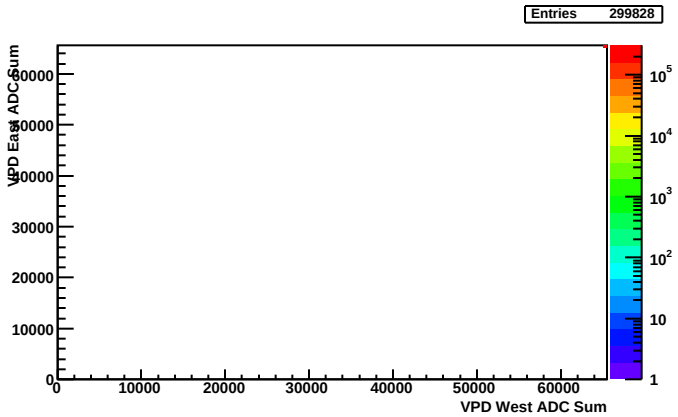

Entries 299828
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

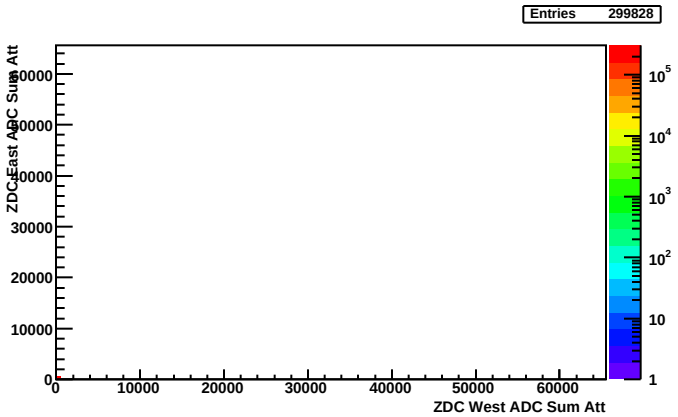

Entries 299828
VPD-East ADC Sum
ZDC-East ADC Sum Att

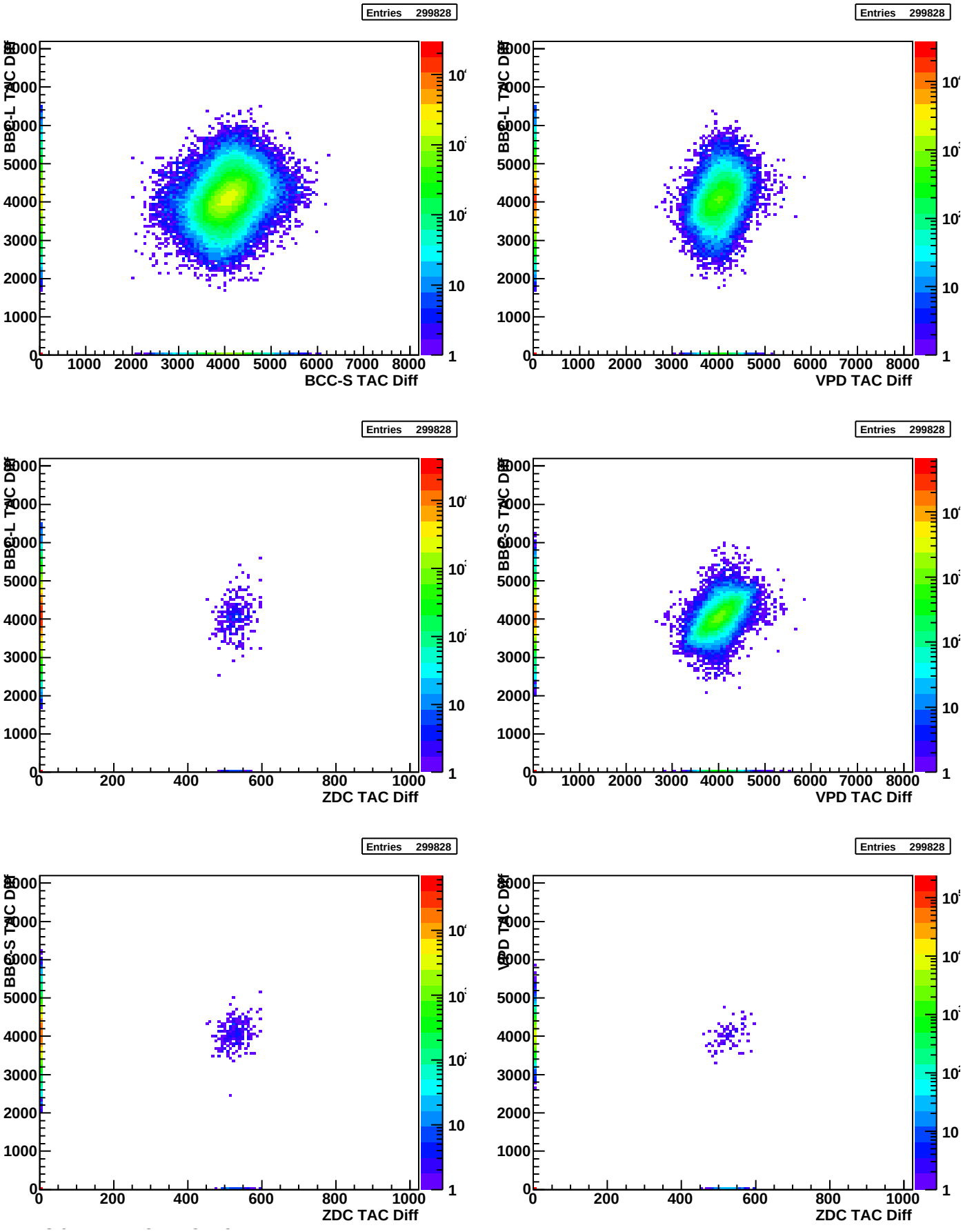

Entries 299828
VPD-West ADC Sum
ZDC-West ADC Sum Att

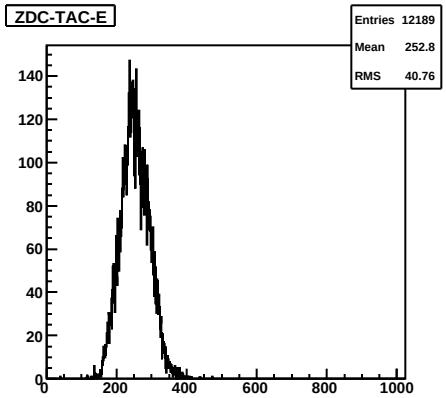

Entries 299828
BBC-L-East ADC Sum
BBC-L-West ADC Sum

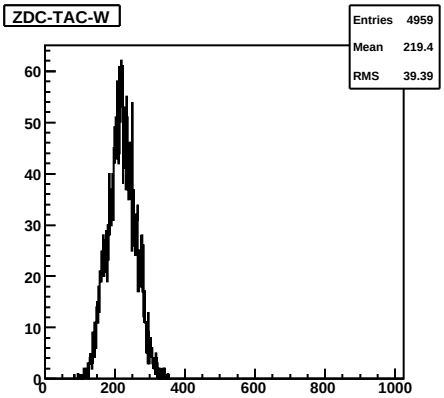

Entries 299828
BBC-S-East ADC Sum
BBC-S-West ADC Sum

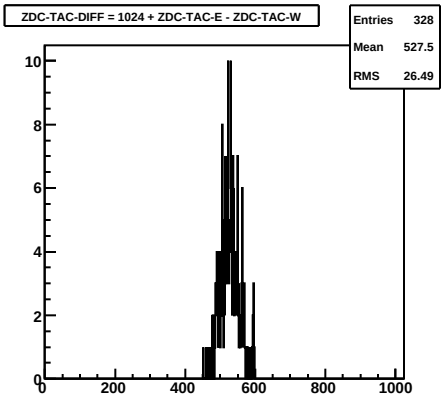

Entries 299828
VPD East ADC Sum
VPD West ADC Sum

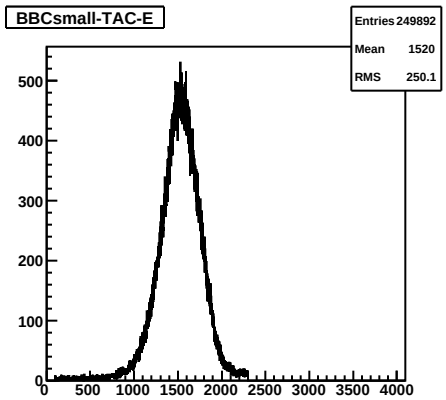

Entries 299828
ZDC East ADC Sum Att
ZDC West ADC Sum Att

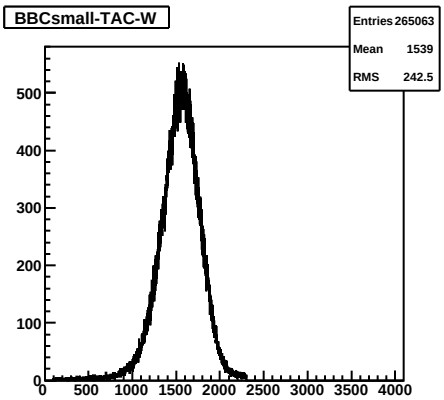

Entries 299828
BBC-L TAC Diff
BCC-S TAC Diff
Entries 299828
BBC-L TAC Diff
VPD TAC Diff
Entries 299828
BBC-L TAC Diff
ZDC TAC Diff
Entries 299828
BBC-S TAC Diff
VPD TAC Diff
Entries 299828
BBC-S TAC Diff
ZDC TAC Diff
Entries 299828
VPD TAC Diff
ZDC TAC Diff

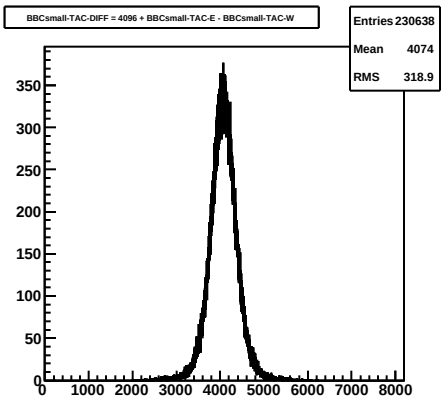

ZDC-TAC-E
Entries 12189
Mean 252.8
RMS 40.76

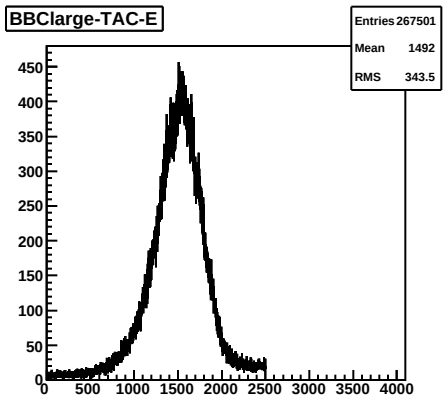

ZDC-TAC-W
Entries 4959
Mean 219.4
RMS 39.39

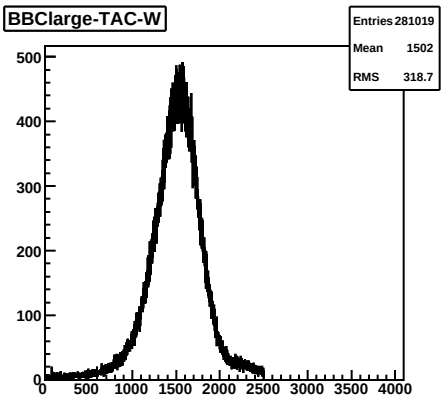

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 328
Mean 527.5
RMS 26.49

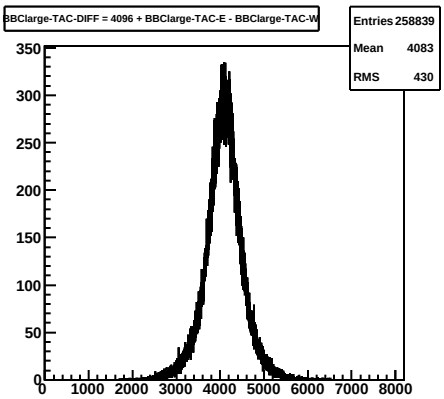

BBCsmall-TAC-E
Entries 249892
Mean 1520
RMS 250.1

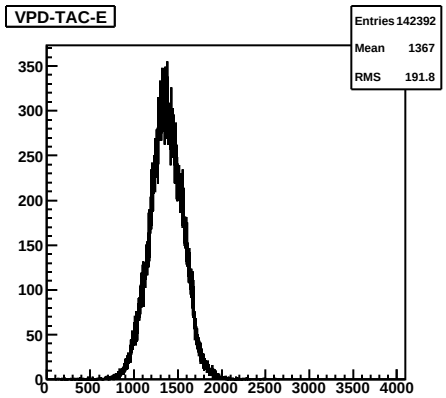

BBCsmall-TAC-W
Entries 265063
Mean 1539
RMS 242.5

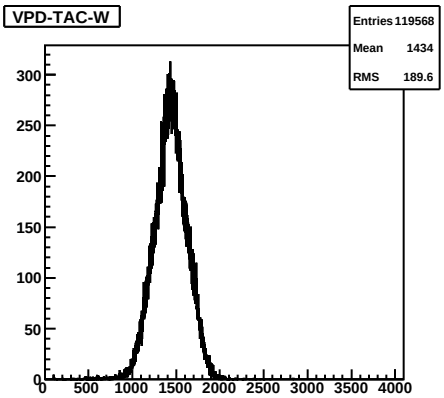

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 230638
Mean 4074
RMS 318.9

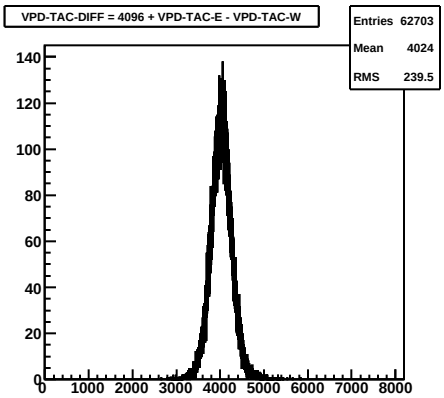

BBClarge-TAC-E
Entries 267501
Mean 1492
RMS 343.5

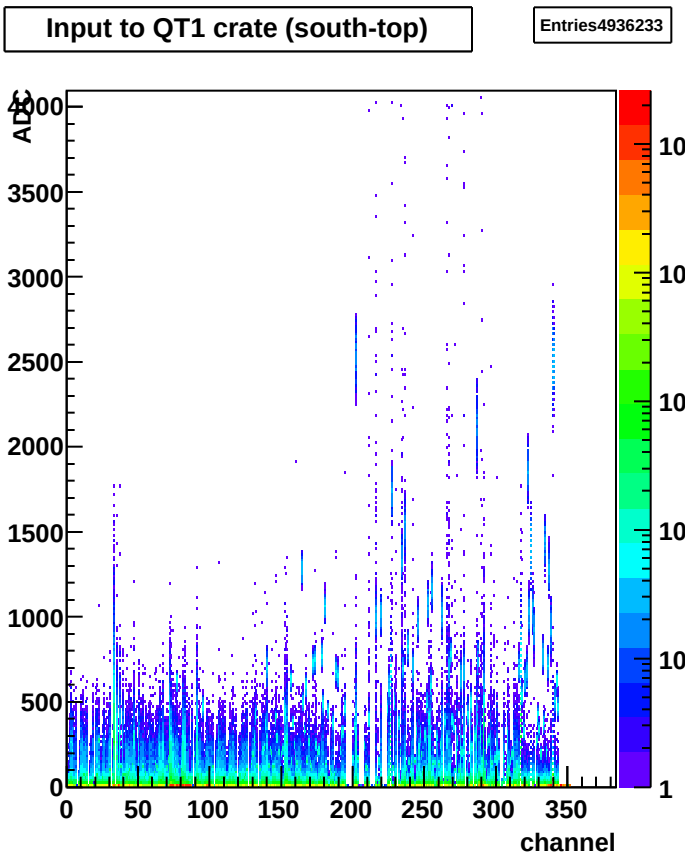

BBClarge-TAC-W
Entries 281019
Mean 1502
RMS 318.7

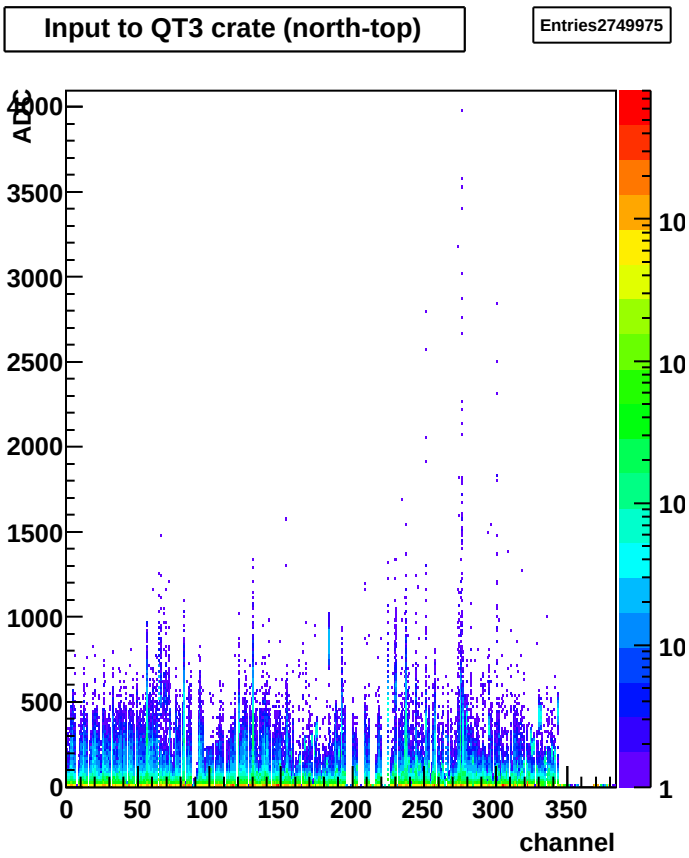

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 258839
Mean 4083
RMS 430

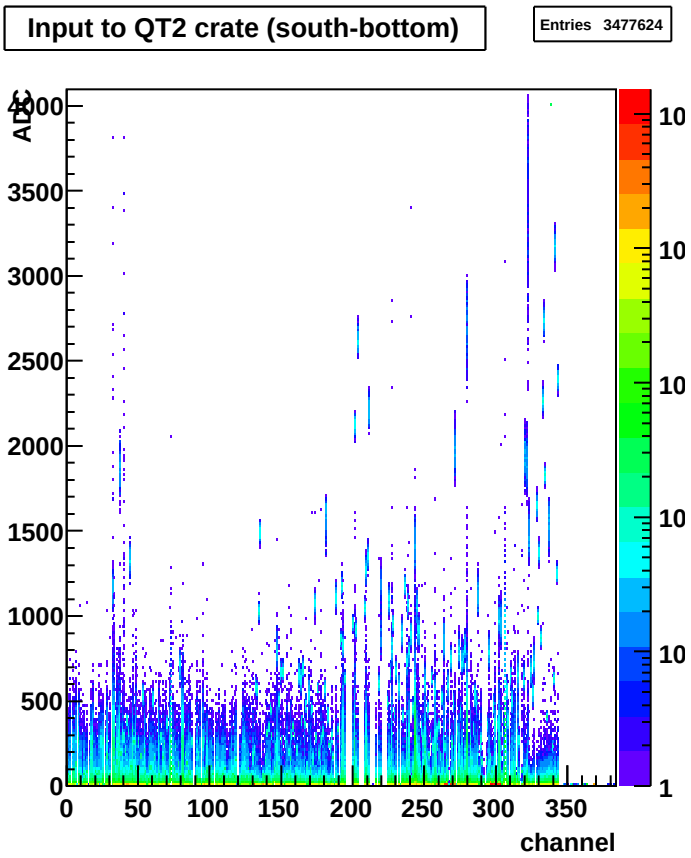

VPD-TAC-E
Entries 142392
Mean 1367
RMS 191.8

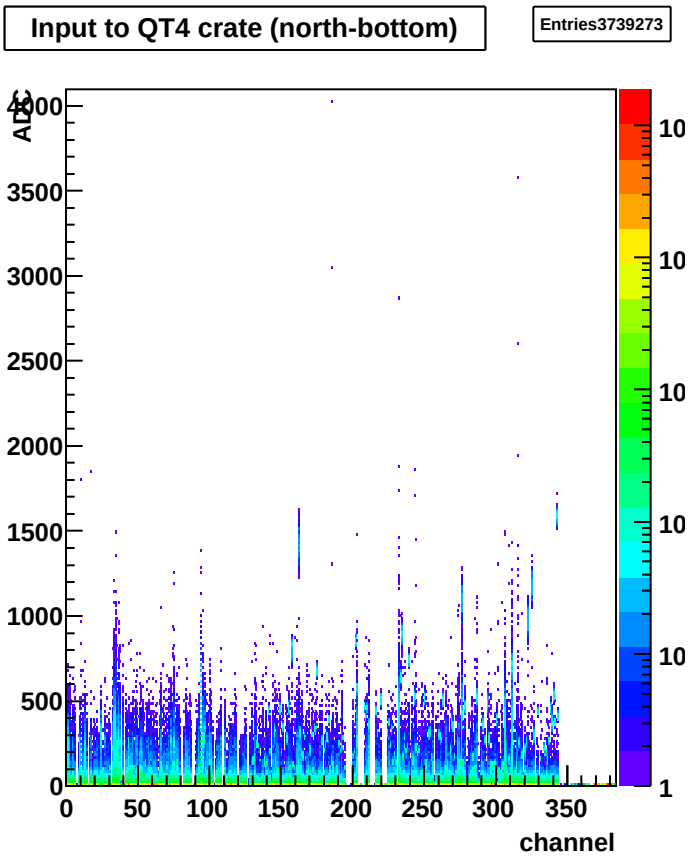

VPD-TAC-W
Entries 119568
Mean 1434
RMS 189.6

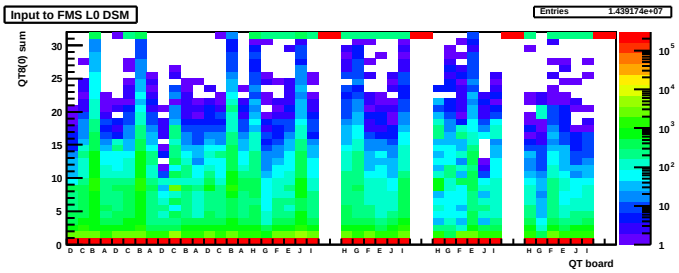

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 62703
Mean 4024
RMS 239.5

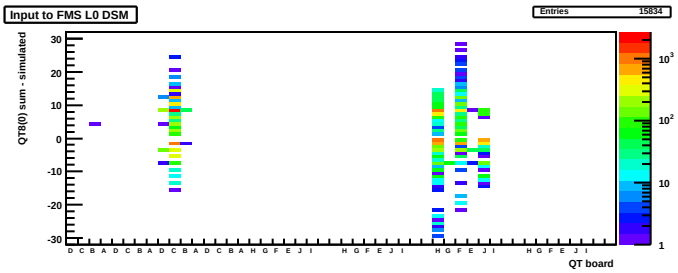

Input to QT1 crate (south-top)
Entries4936233
ADC
channel

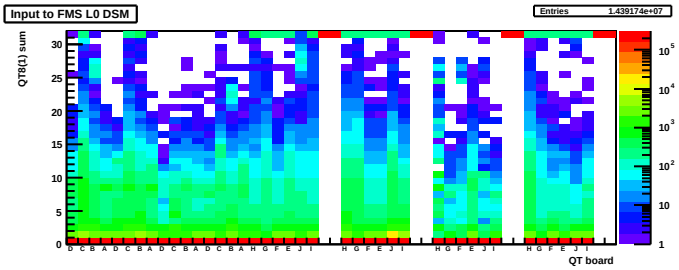

Input to QT3 crate (north-top)
Entries2749975
ADC
channel

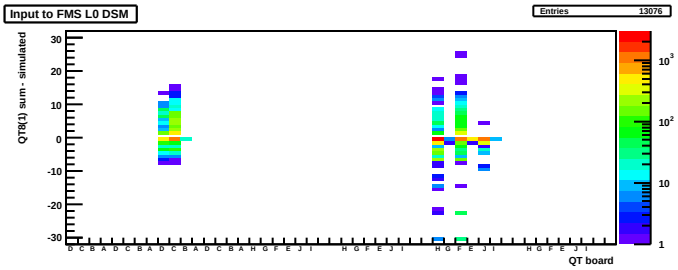

Input to QT2 crate (south-bottom)
Entries 3477624
ADC
channel

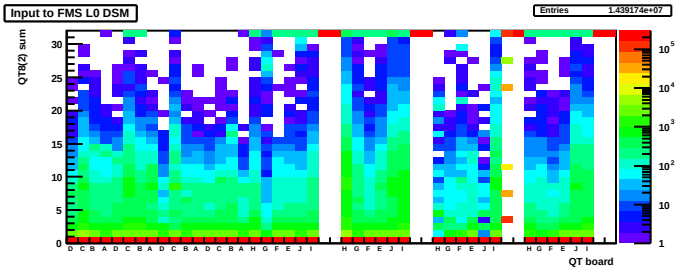

Input to QT4 crate (north-bottom)
Entries3739273
ADC
channel

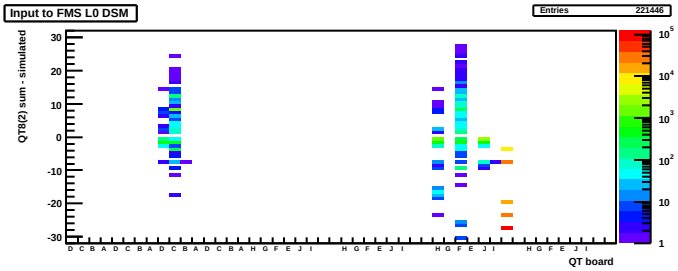

Input to FMS L0 DSM
Entries 1.439174e+07
QT8(0) sum
QT board

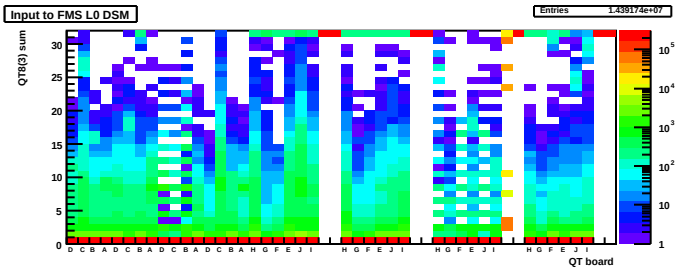

Input to FMS L0 DSM
Entries 15834
QT8(0) sum - simulated
QT board

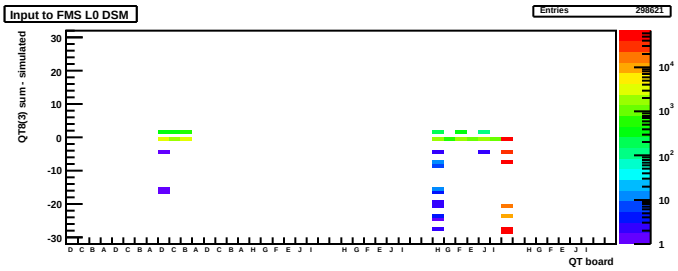

Input to FMS L0 DSM
Entries 1.439174e+07
QT8(1) sum
QT board

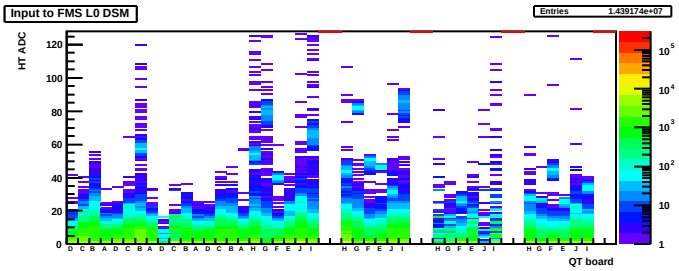

Input to FMS L0 DSM
Entries 13076
QT8(1) sum - simulated
QT board

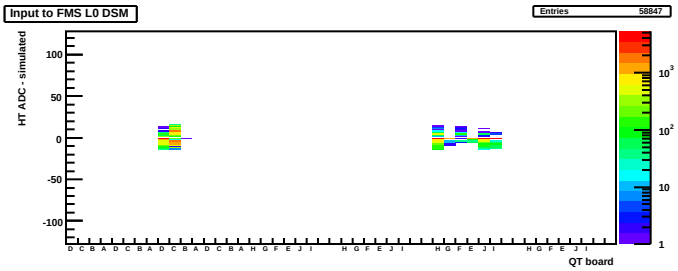

Input to FMS L0 DSM
Entries 1.439174e+07
QT8(2) sum
QT board

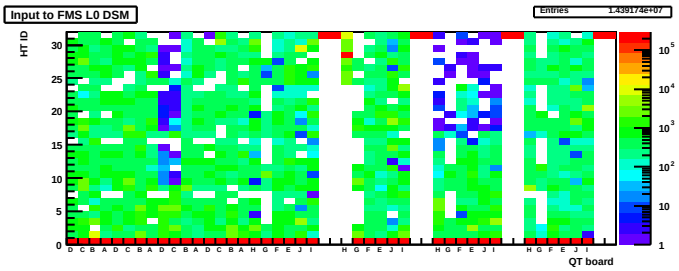

Input to FMS L0 DSM
Entries 221446
QT8(2) sum - simulated
QT board

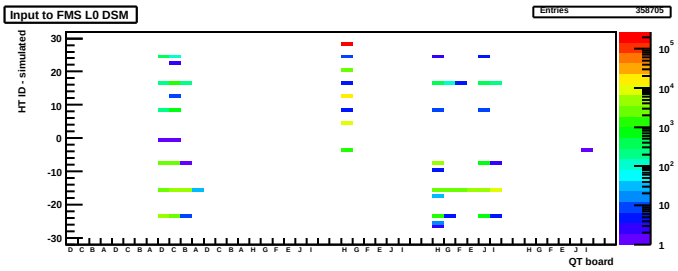

Input to FMS L0 DSM
Entries 1.439174e+07
QT8(3) sum
QT board

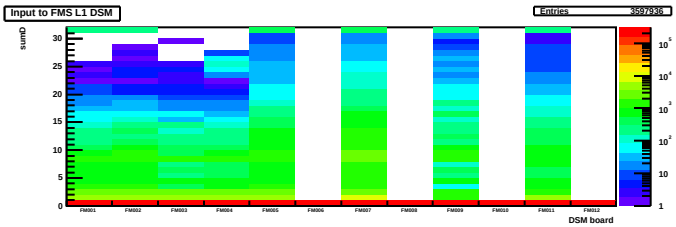

Input to FMS L0 DSM
Entries 298621
QT8(3) sum - simulated
QT board

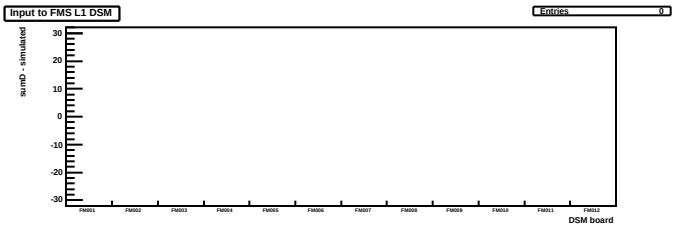

Input to FMS L0 DSM
Entries 1.439174e+07
HT ADC
QT board

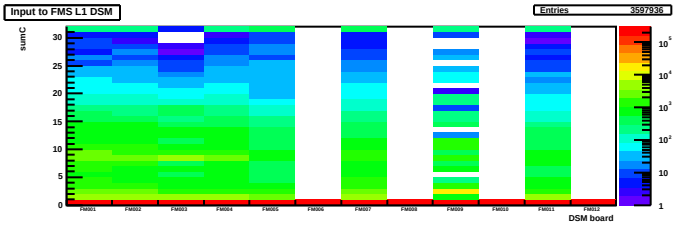

Input to FMS L0 DSM
Entries 58847
HT ADC - simulated
QT board

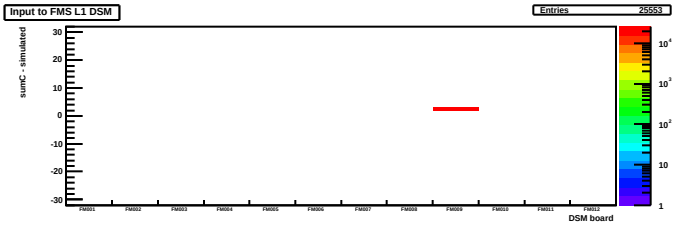

Input to FMS L0 DSM
Entries 1.439174e+07
HT ID
QT board

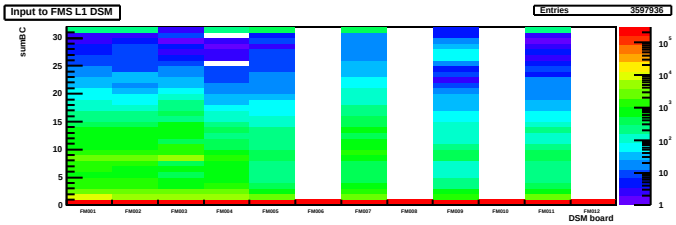

Input to FMS L0 DSM
Entries 358705
HT ID - simulated
QT board

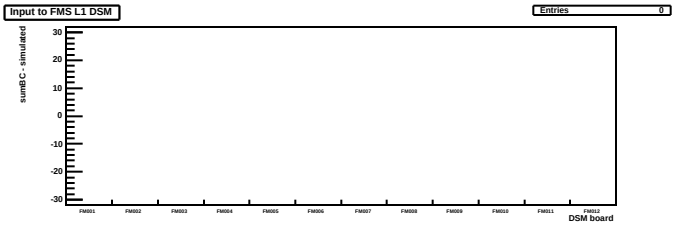

Input to FMS L1 DSM
Entries 3597936
sumD
DSM board

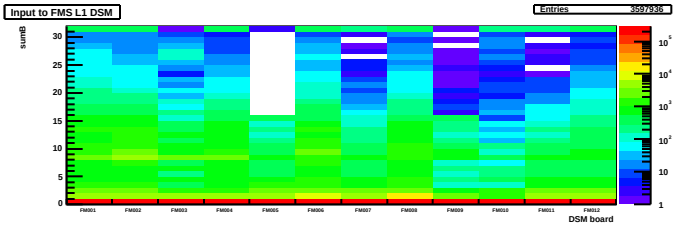

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

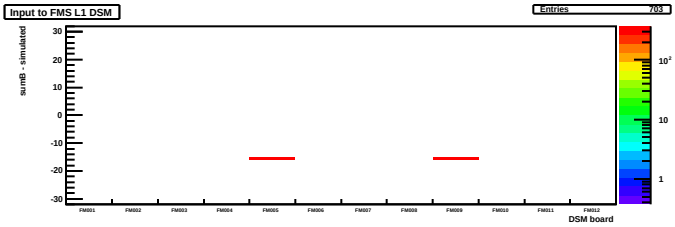

Input to FMS L1 DSM
Entries 3597936
sumC
DSM board

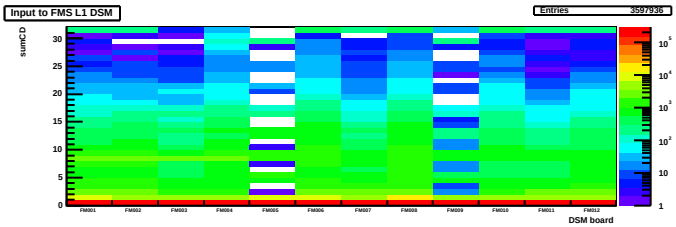

Input to FMS L1 DSM
Entries 25553
sumC - simulated
DSM board

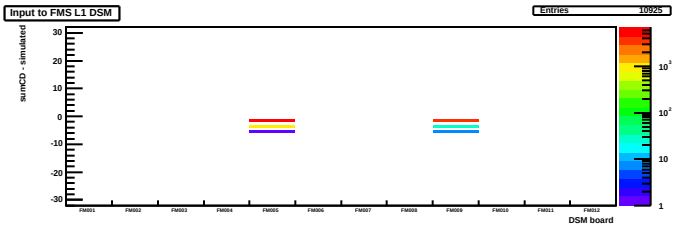

Input to FMS L1 DSM
Entries 3597936
sumBC
DSM board

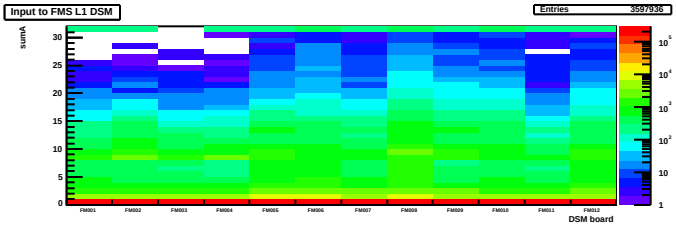

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

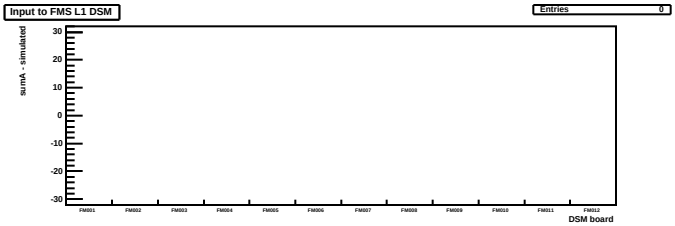

Input to FMS L1 DSM
Entries 3597936
sumB
DSM board

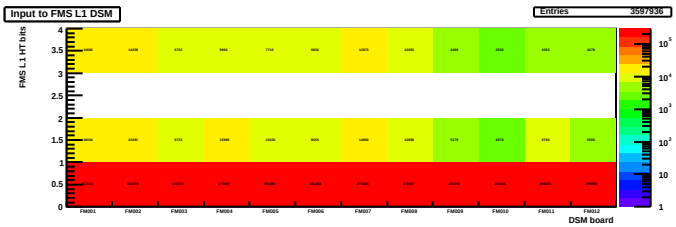

Input to FMS L1 DSM
Entries 703
sumB - simulated
DSM board

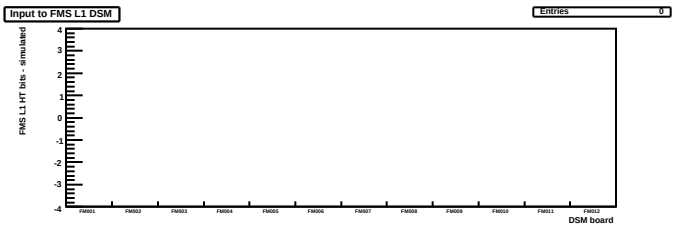

Input to FMS L1 DSM
Entries 3597936
sumCD
DSM board

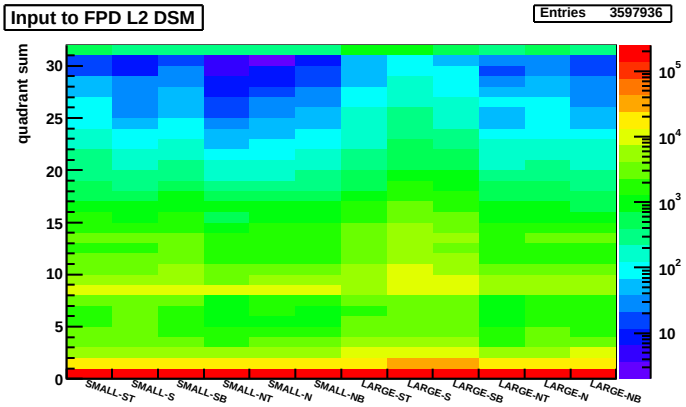

Input to FMS L1 DSM
Entries 10925
sumCD - simulated
DSM board

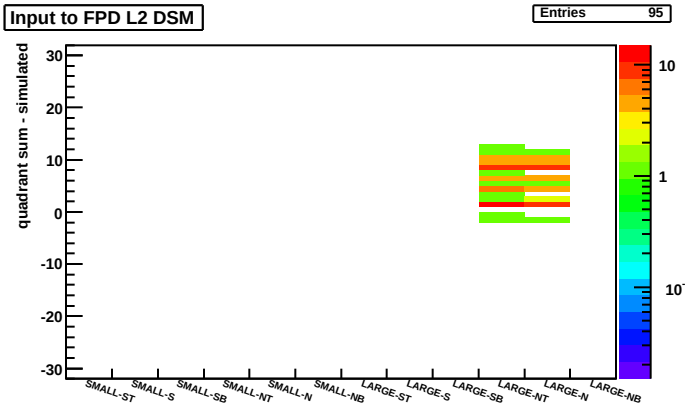

Input to FMS L1 DSM
Entries 3597936
sumA
DSM board

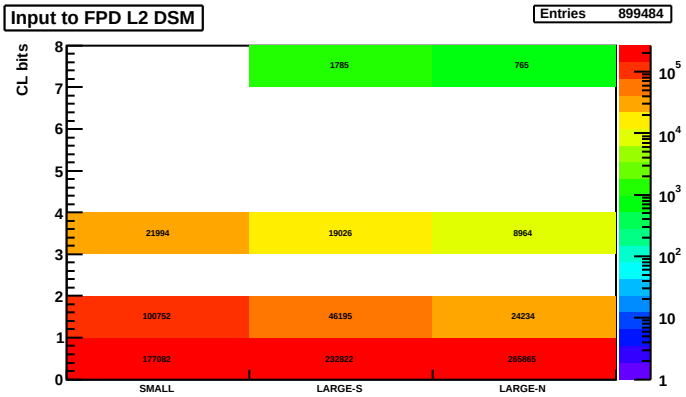

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

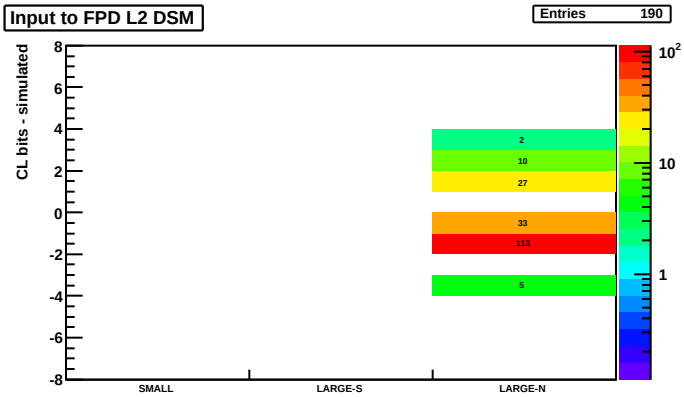

Input to FMS L1 DSM
Entries 3597936
FMS L1 HT bits
DSM board

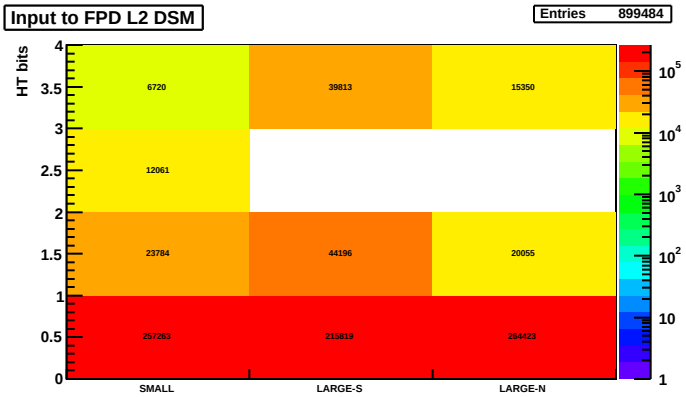

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

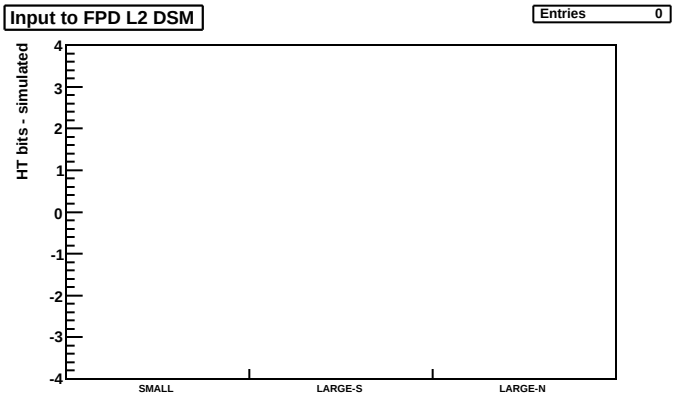

Input to FPD L2 DSM
Entries 3597936
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

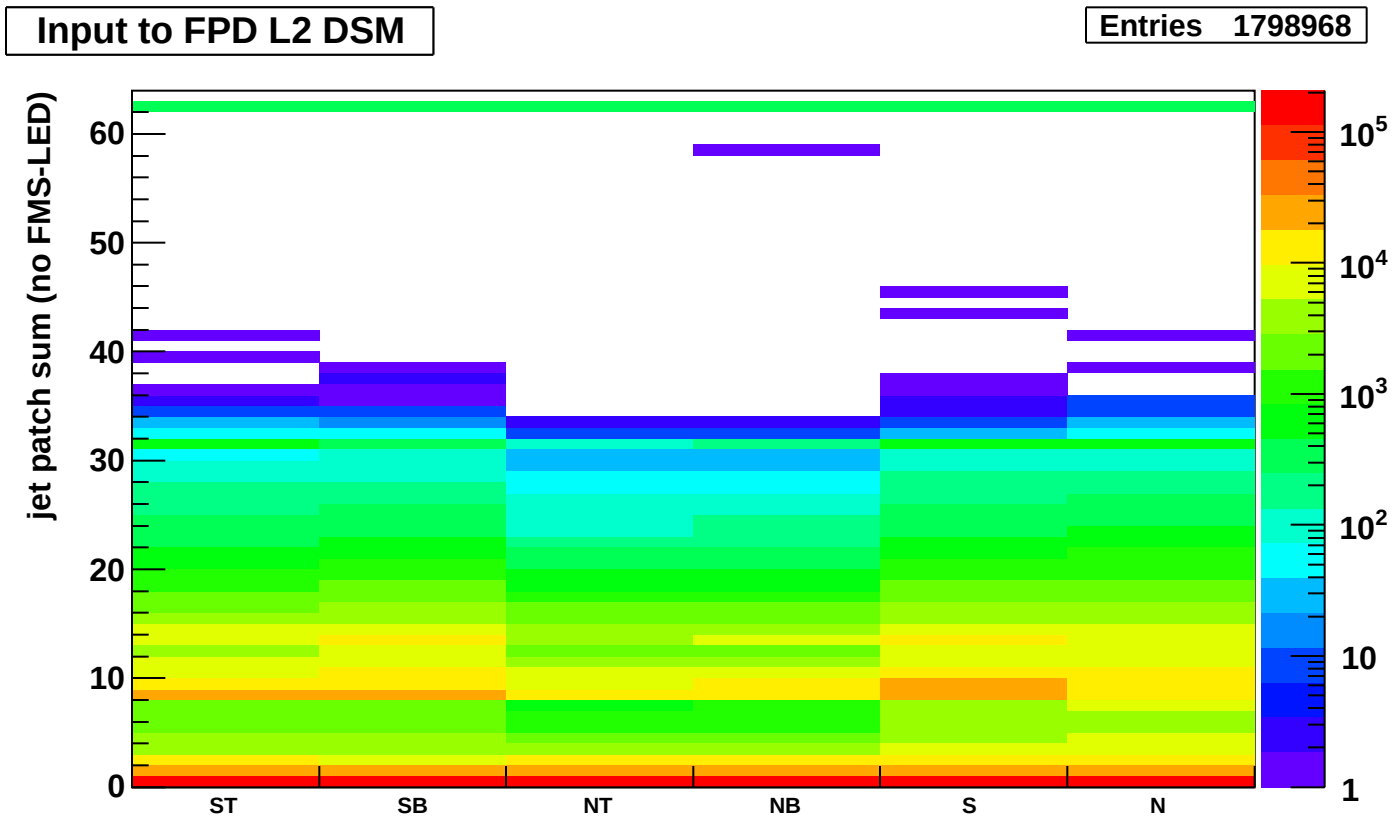

Input to FPD L2 DSM
Entries 95
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

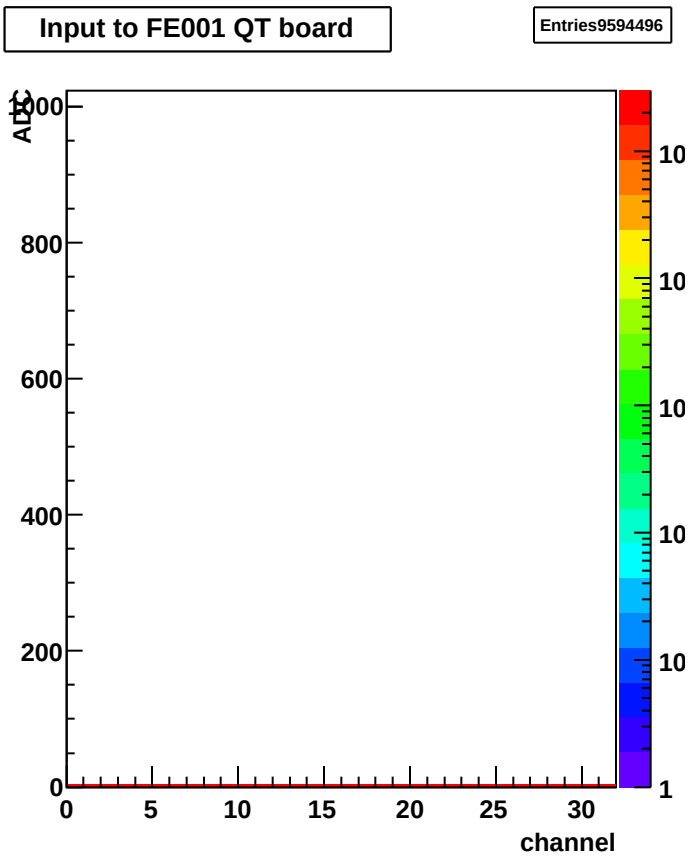

Input to FPD L2 DSM
Entries 899484
CL bits
1785
765
21994
19026
8964
100752
46195
24234
177082
232822
265865
SMALL
LARGE-S
LARGE-N

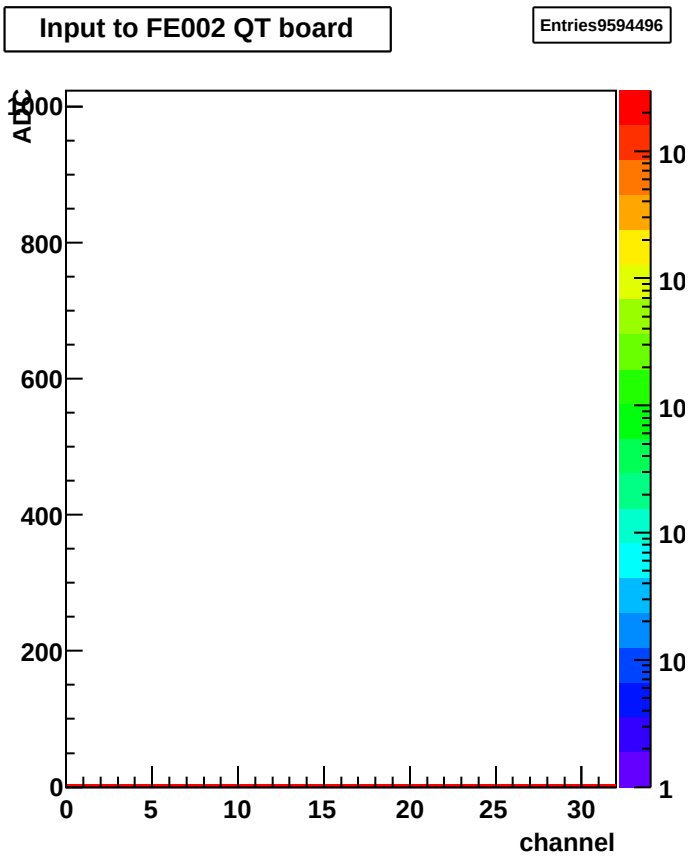

Input to FPD L2 DSM
Entries 190
CL bits - simulated
2
10
27
33
113
5
SMALL
LARGE-S
LARGE-N

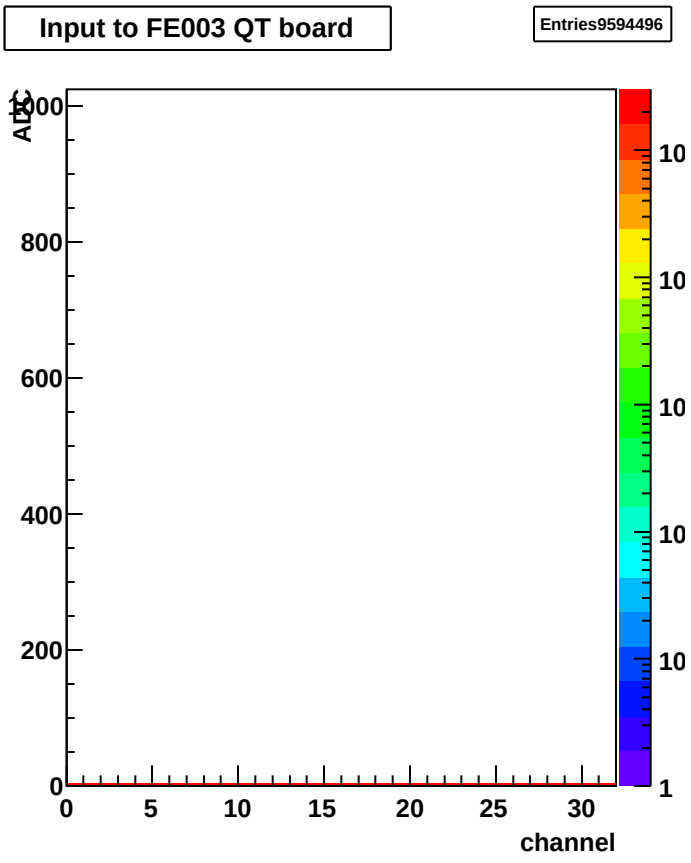

Input to FPD L2 DSM
Entries 899484
HT bits
6720
39813
15350
12061
23784
44196
20055
257263
215819
264423
SMALL
LARGE-S
LARGE-N

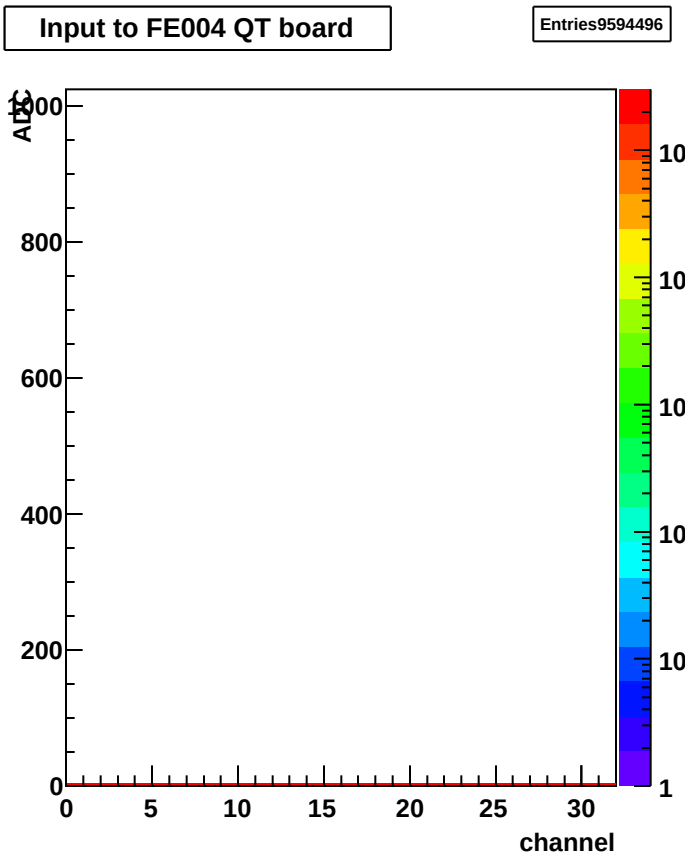

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

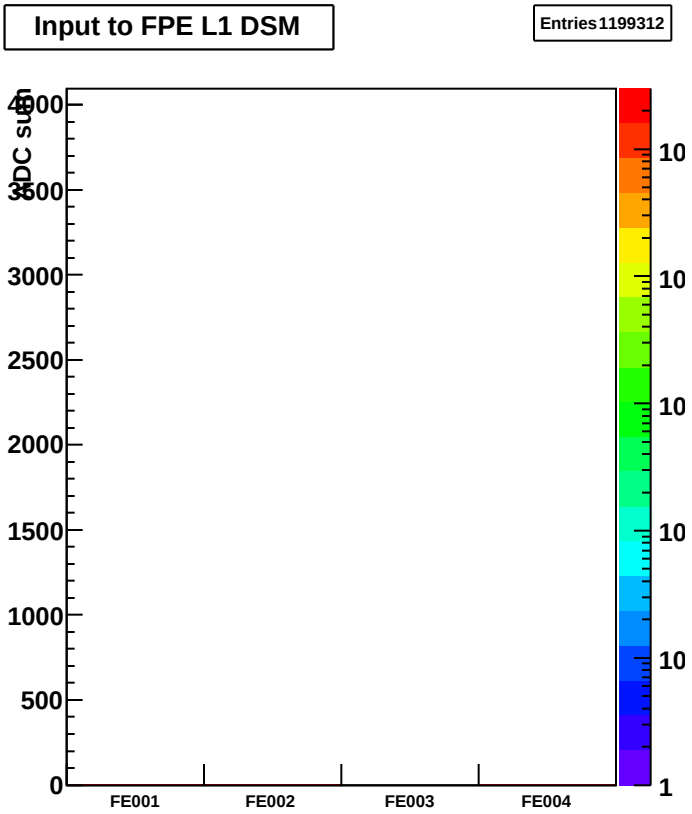

Input to FPD L2 DSM
Entries 1798968
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

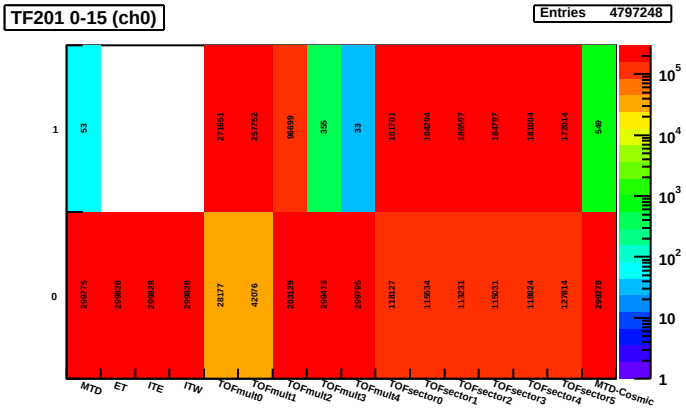

Input to FE001 QT board
Entries9594496
ADC
channel

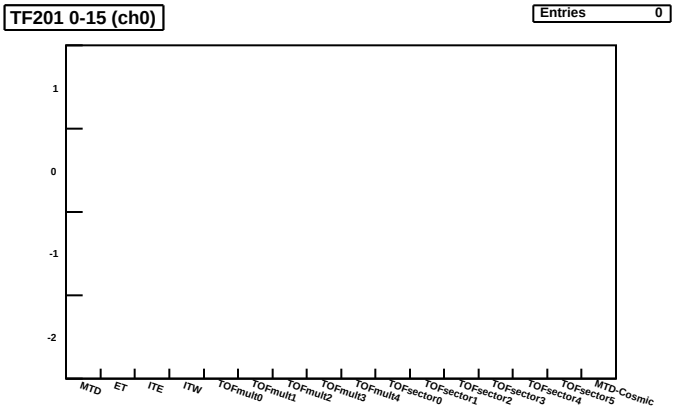

Input to FE002 QT board
Entries9594496
ADC
channel

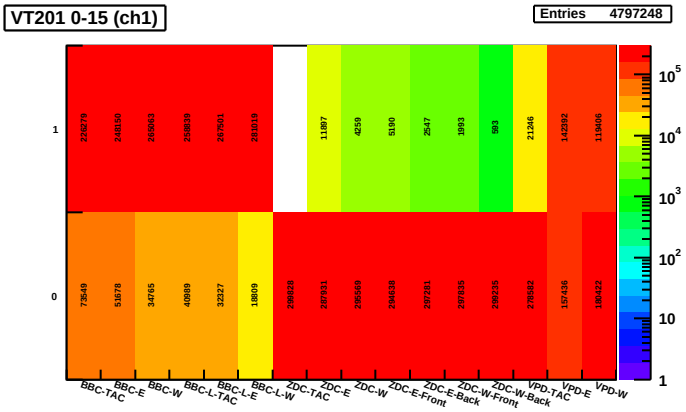

Input to FE003 QT board
Entries9594496
ADC
channel

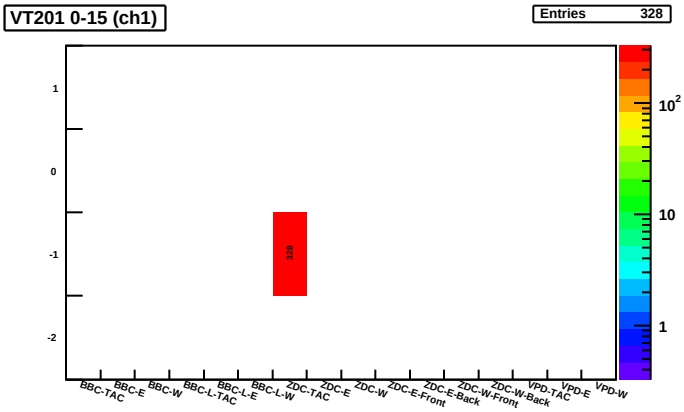

Input to FE004 QT board
Entries9594496
ADC
channel

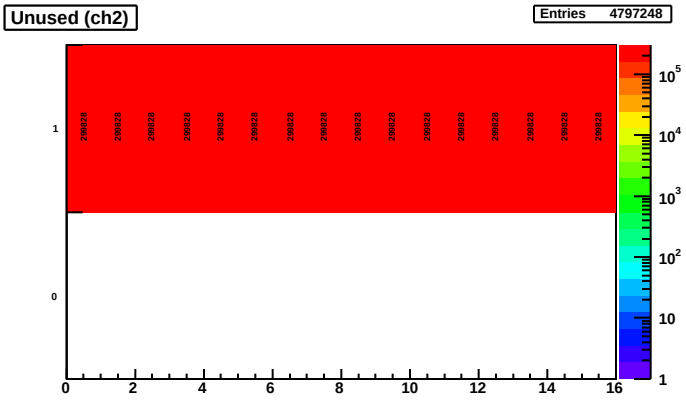

Input to FPE L1 DSM
Entries 1199312
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

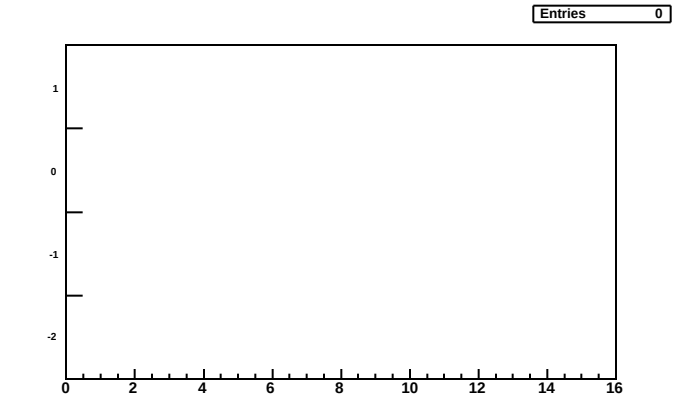

TF201 0-15 (ch0)
Entries 4797248
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

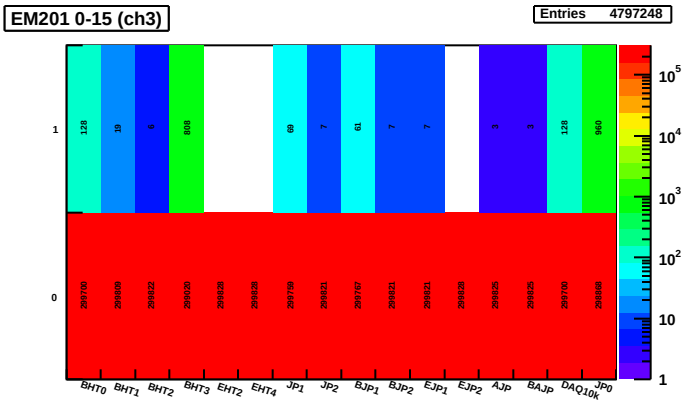

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

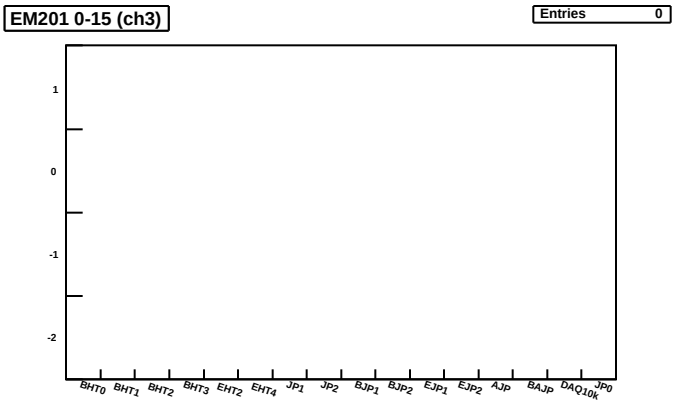

VT201 0-15 (ch1)
Entries 4797248
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 328
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

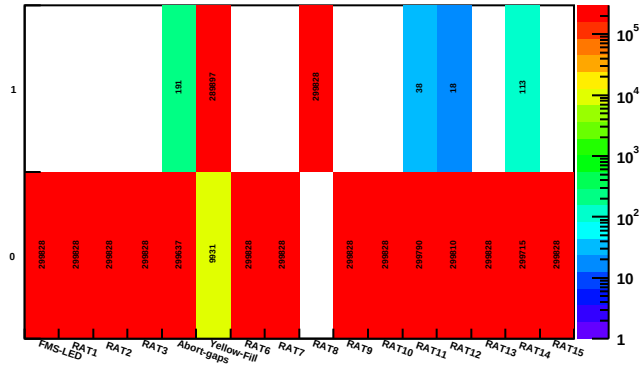

Unused (ch2)
Entries 4797248

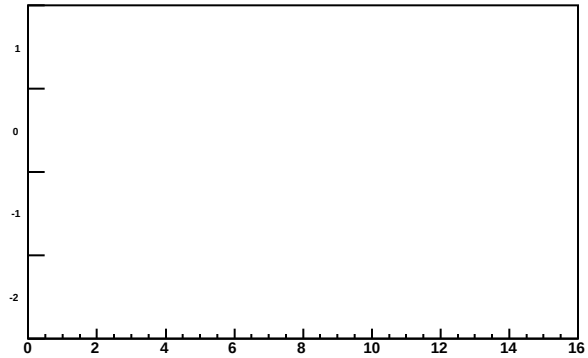

Entries 0


EM201 0-15 (ch3)
Entries 4797248
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

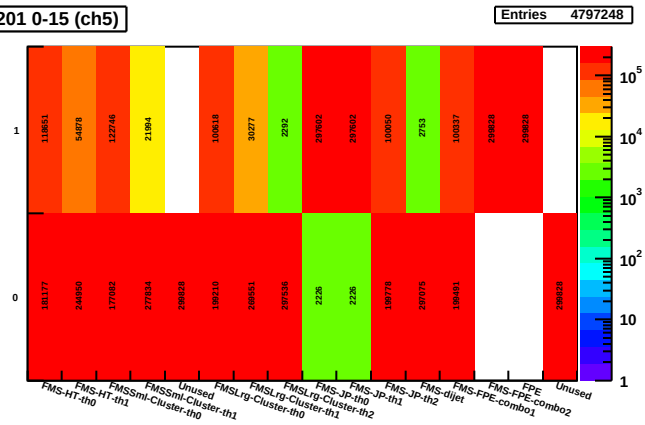

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

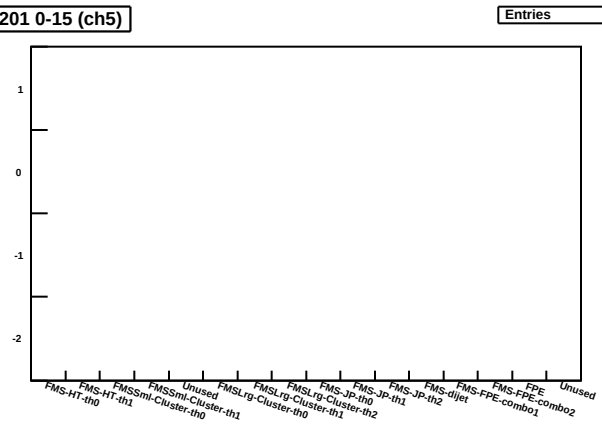




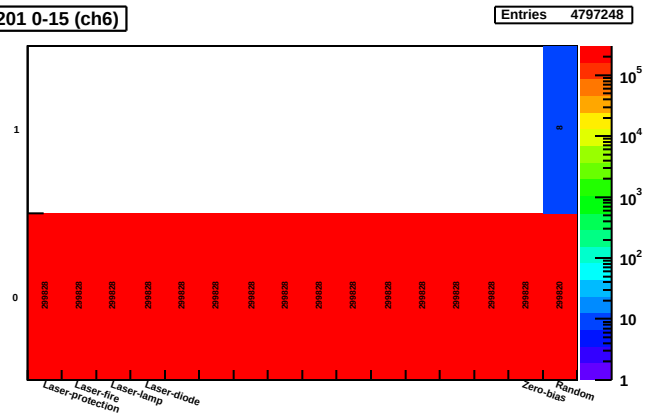
FP201 0-15 (ch5)

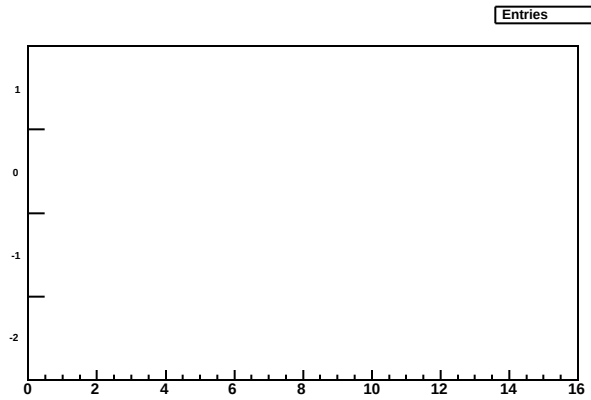
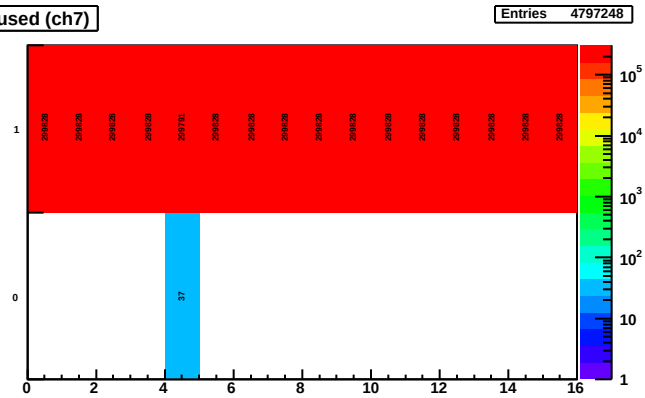
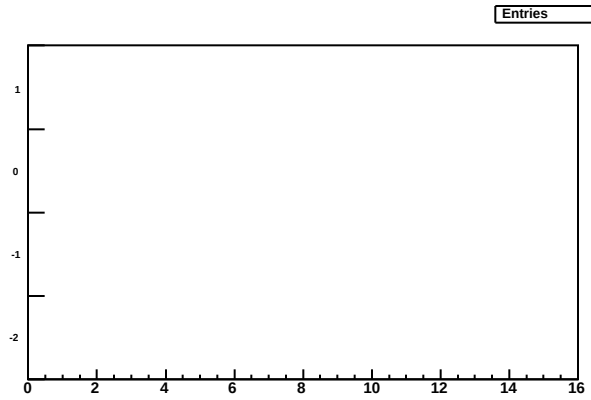


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)