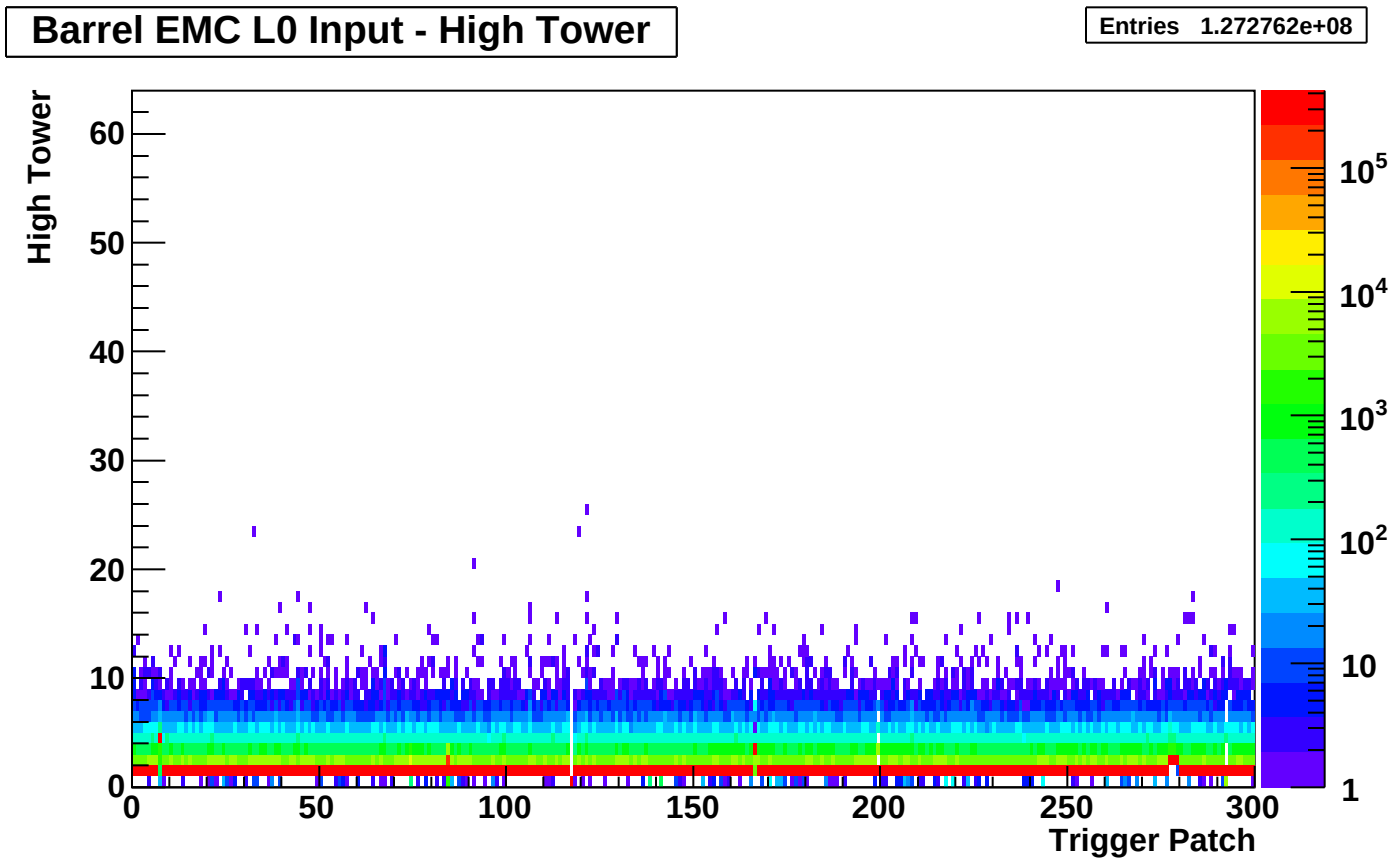
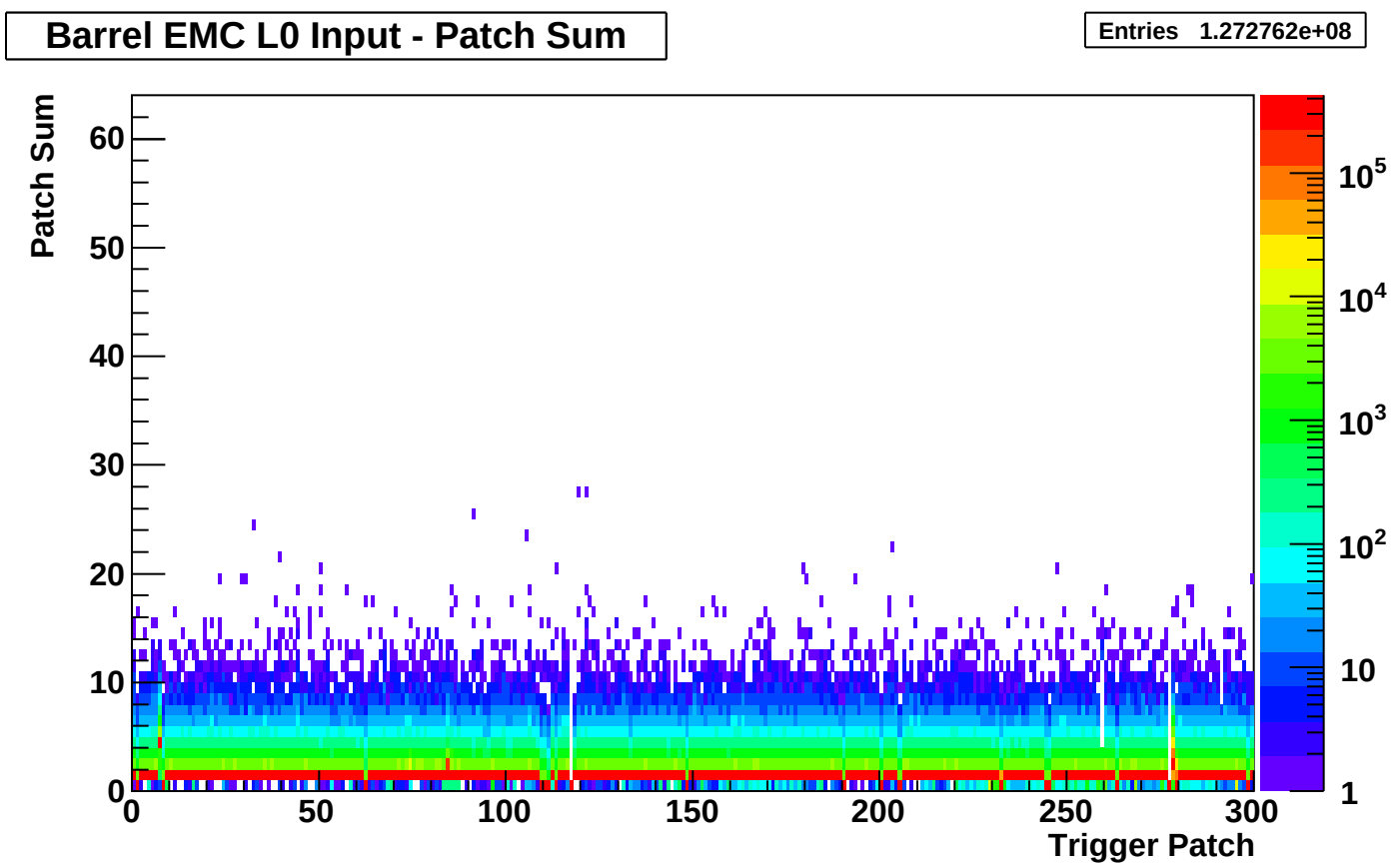
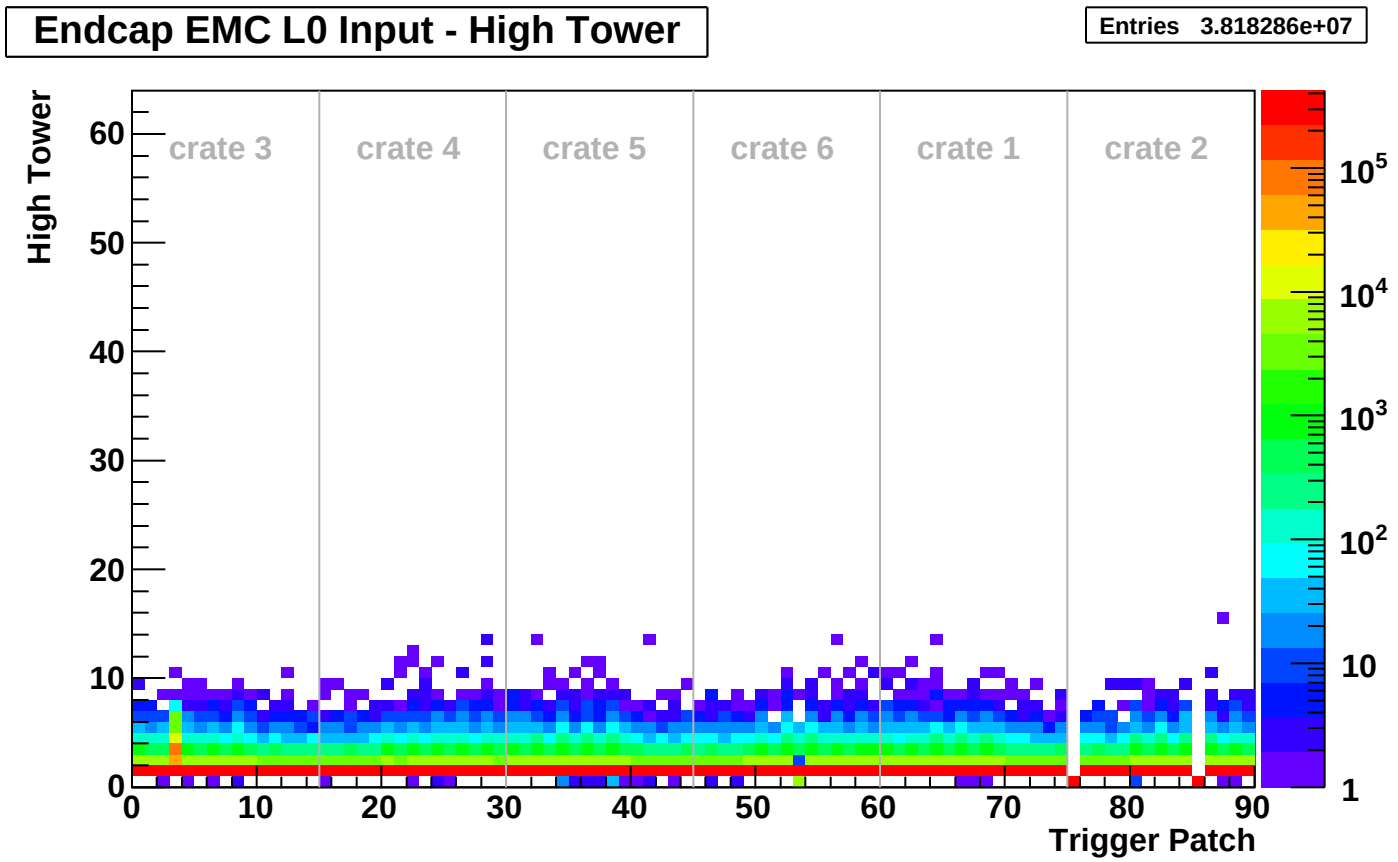
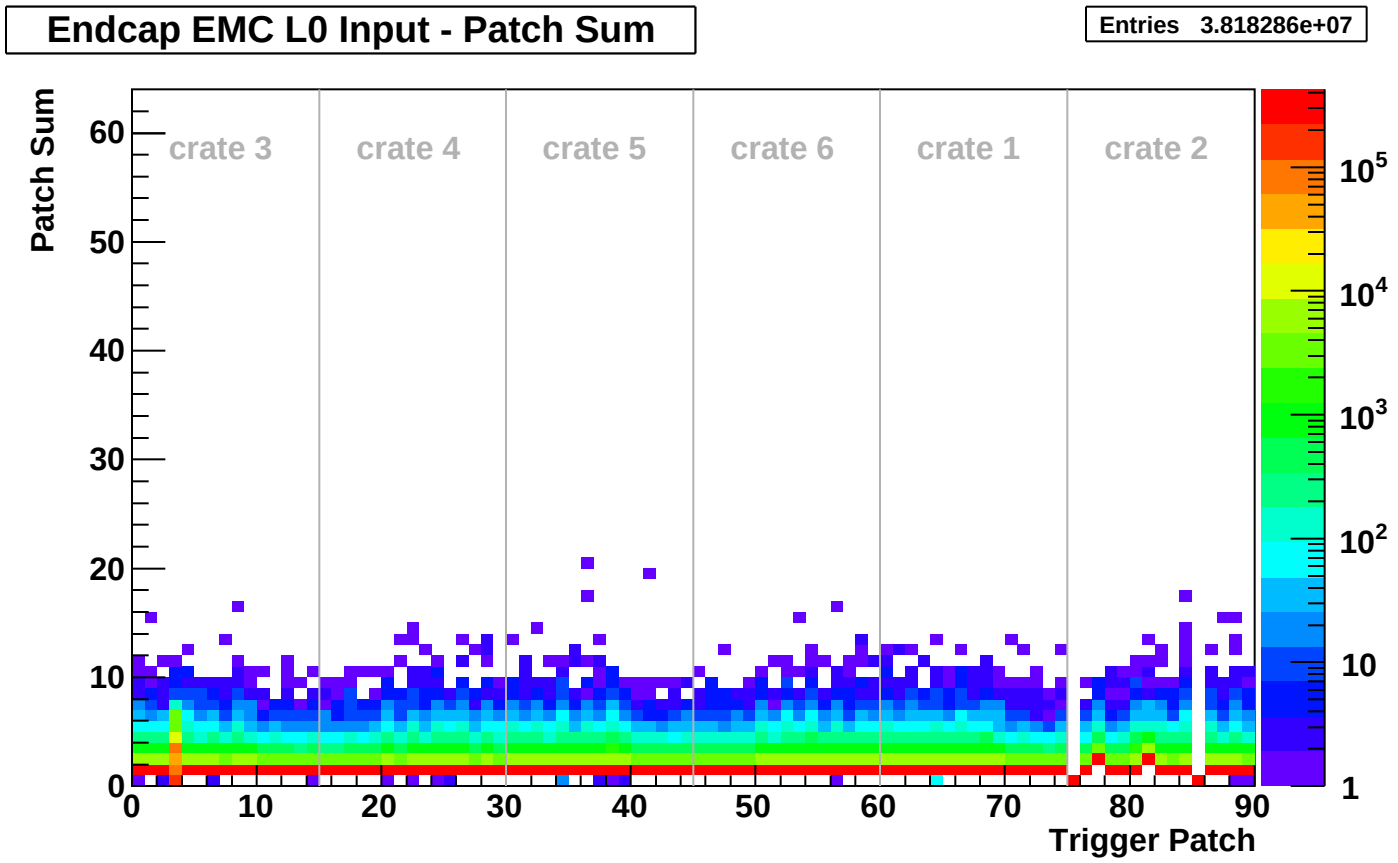
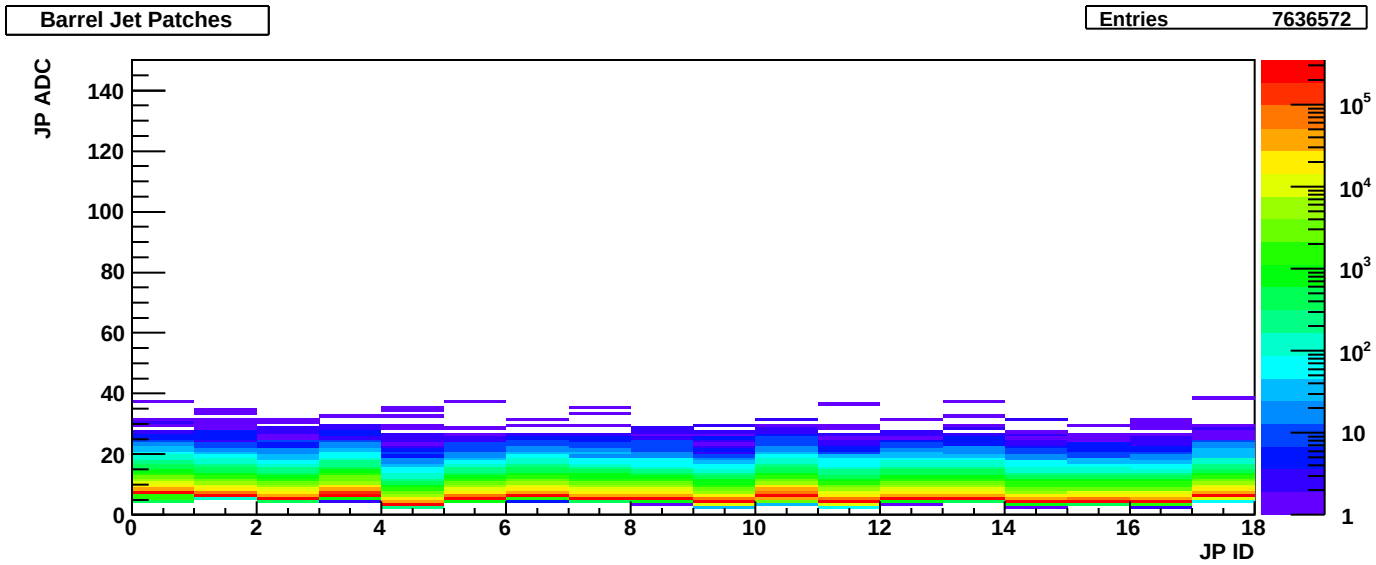
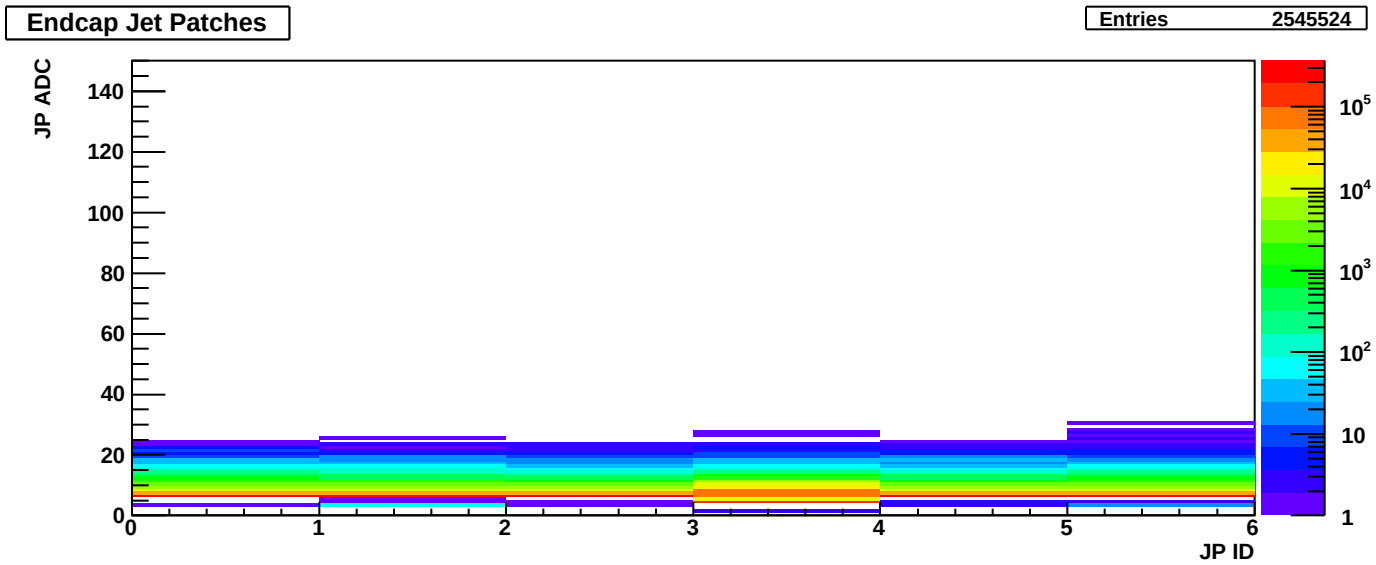

Barrel EMC L0 Input - High Tower
Entries 1.272762e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

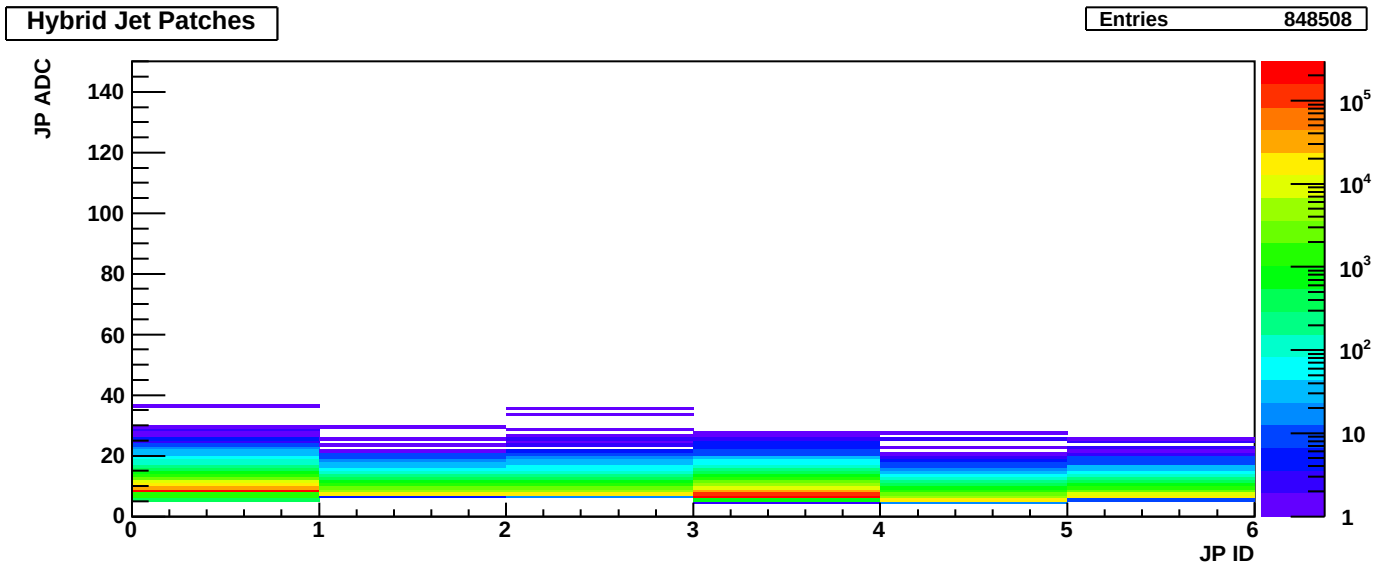

Barrel EMC L0 Input - Patch Sum
Entries 1.272762e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

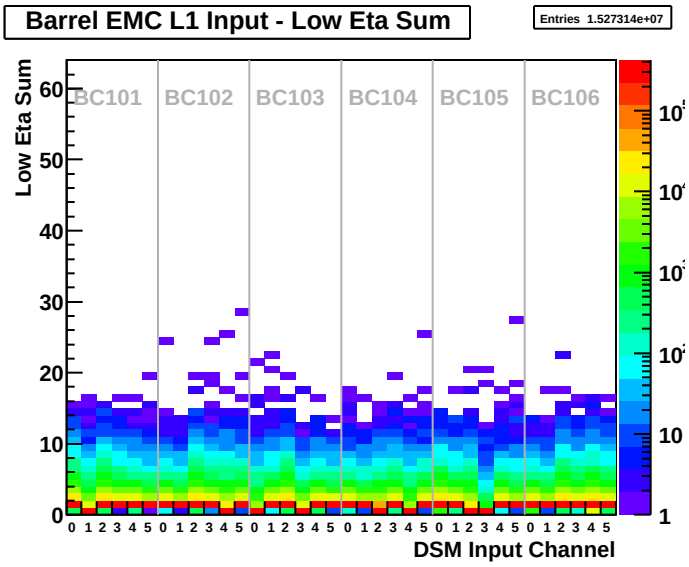

Endcap EMC L0 Input - High Tower
Entries 3.818286e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

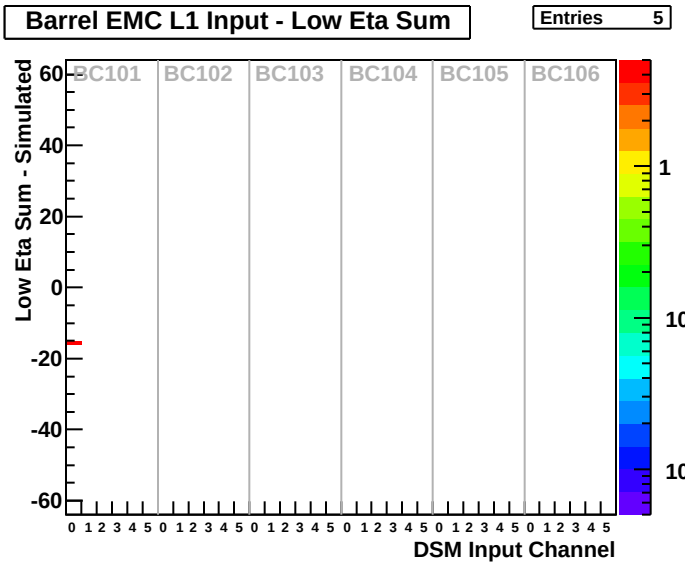

Endcap EMC L0 Input - Patch Sum
Entries 3.818286e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

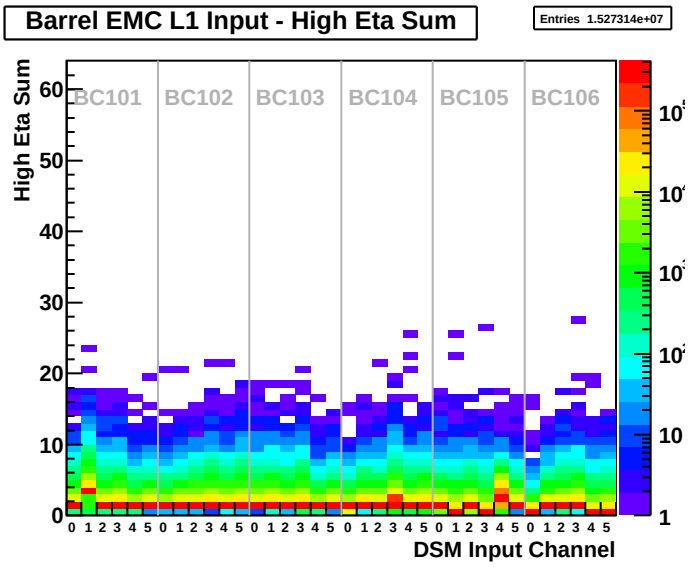

Barrel Jet Patches
Entries 7636572
JP ADC
JP ID

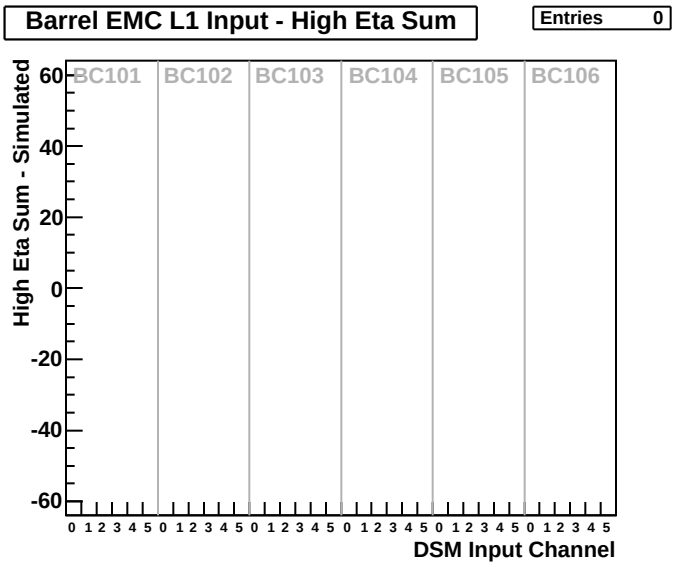

Endcap Jet Patches
Entries 2545524
JP ADC
JP ID

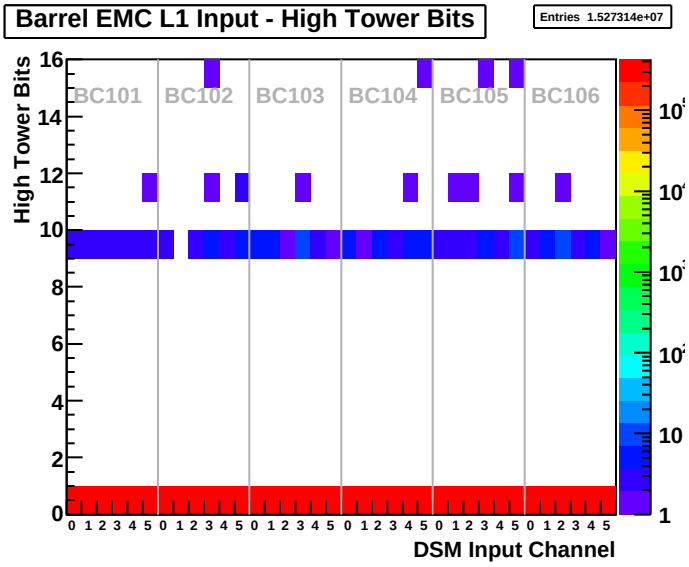

Hybrid Jet Patches
Entries 848508
JP ADC
JP ID

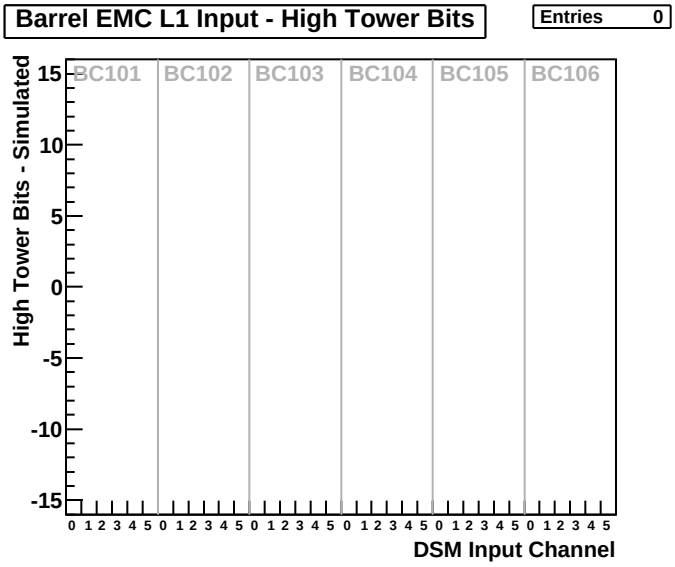

Barrel EMC L1 Input - Low Eta Sum
Entries 1.527314e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

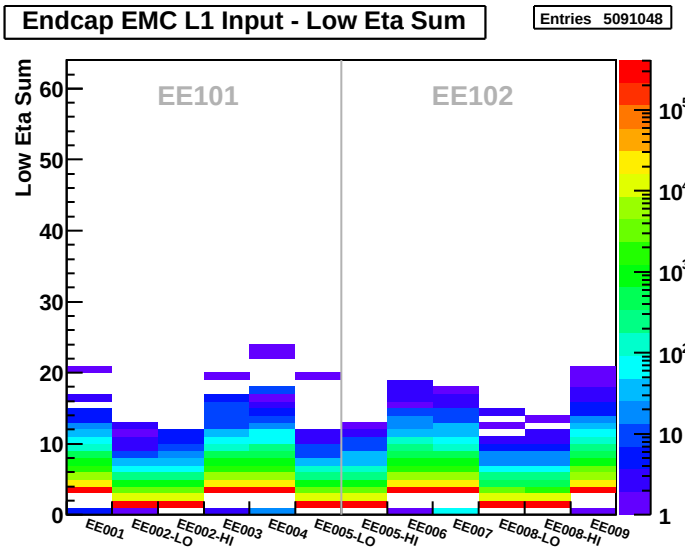

Barrel EMC L1 Input - Low Eta Sum
Entries 5
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

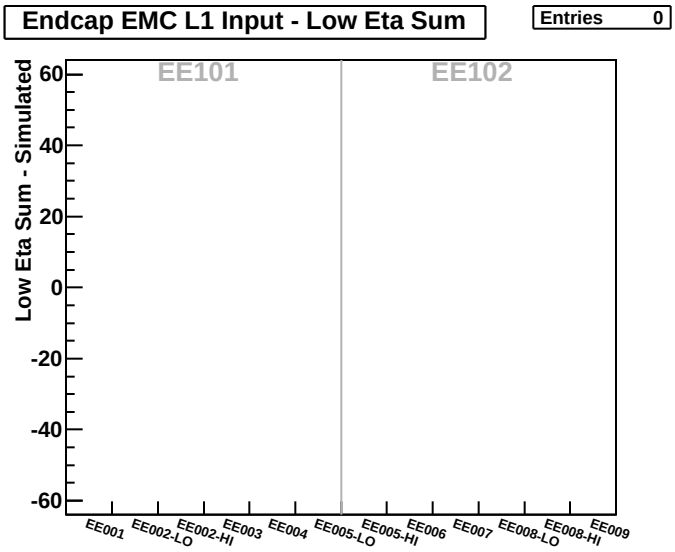

Barrel EMC L1 Input - High Eta Sum
Entries 1.527314e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

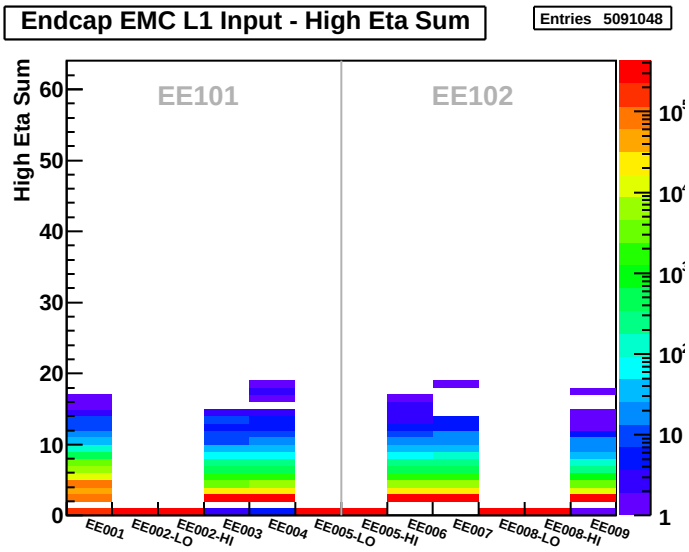

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

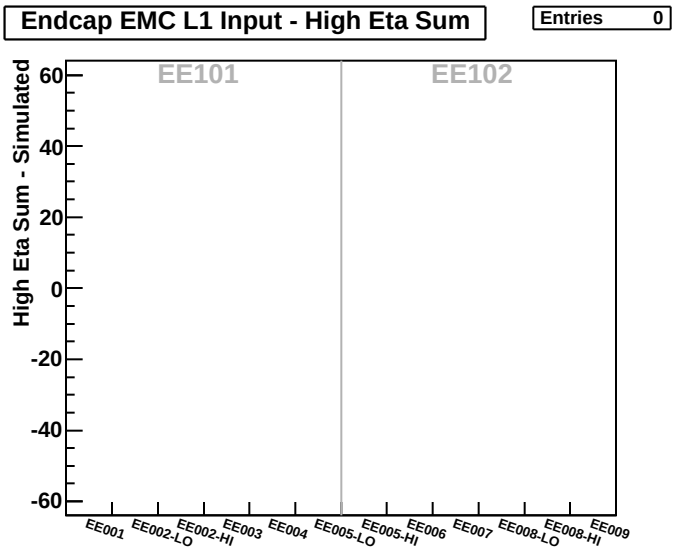

Barrel EMC L1 Input - High Tower Bits
Entries 1.527314e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

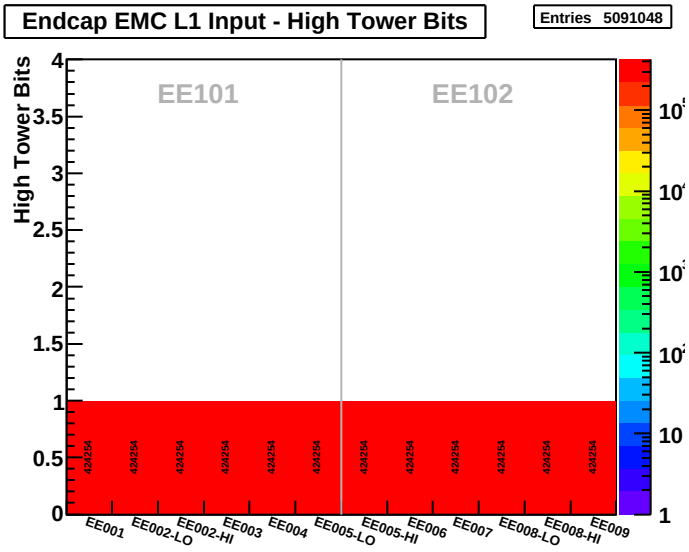

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

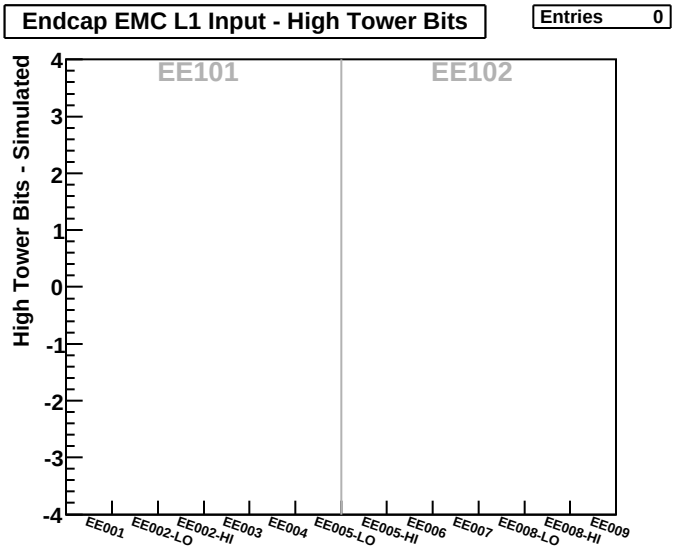

Endcap EMC L1 Input - Low Eta Sum
Entries 5091048
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

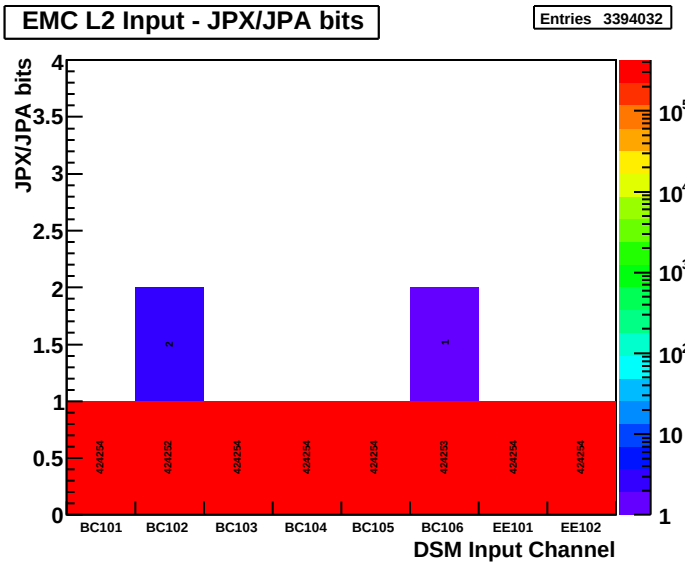

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

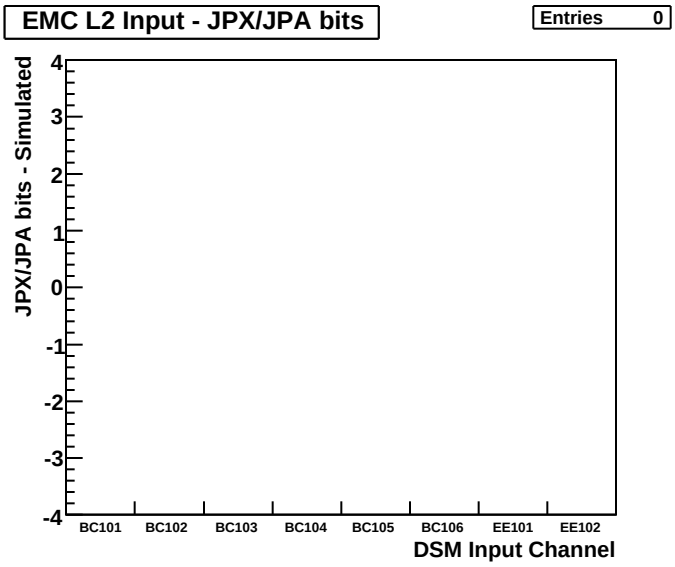

Endcap EMC L1 Input - High Eta Sum
Entries 5091048
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

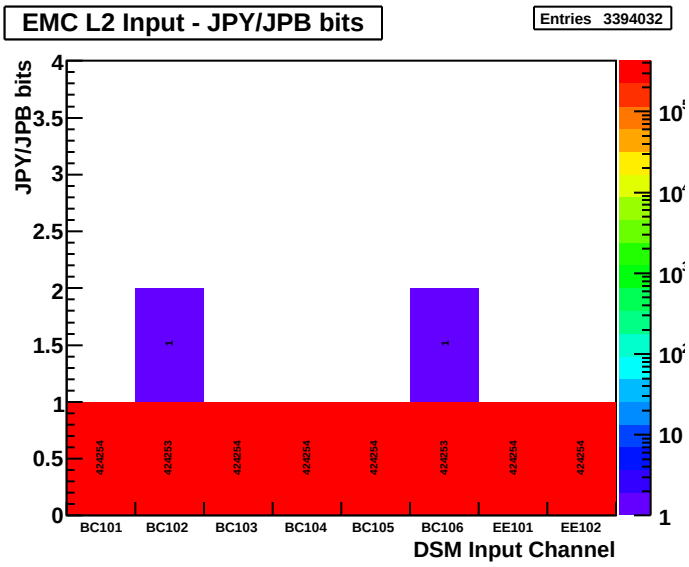

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

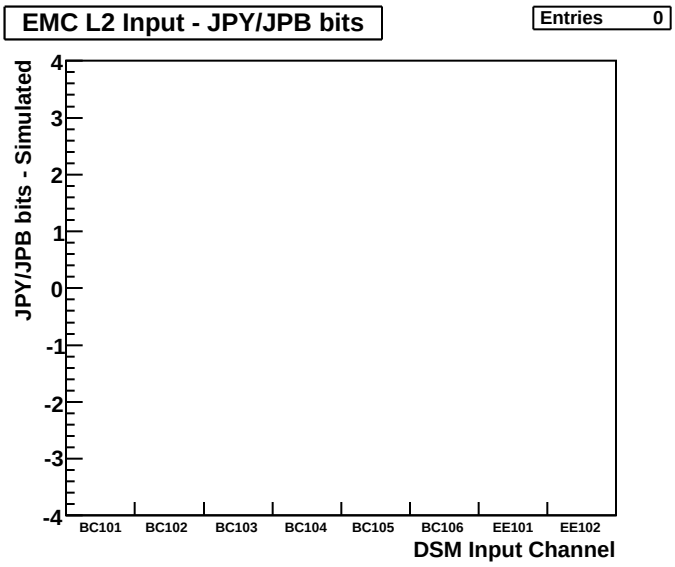

Endcap EMC L1 Input - High Tower Bits
Entries 5091048
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

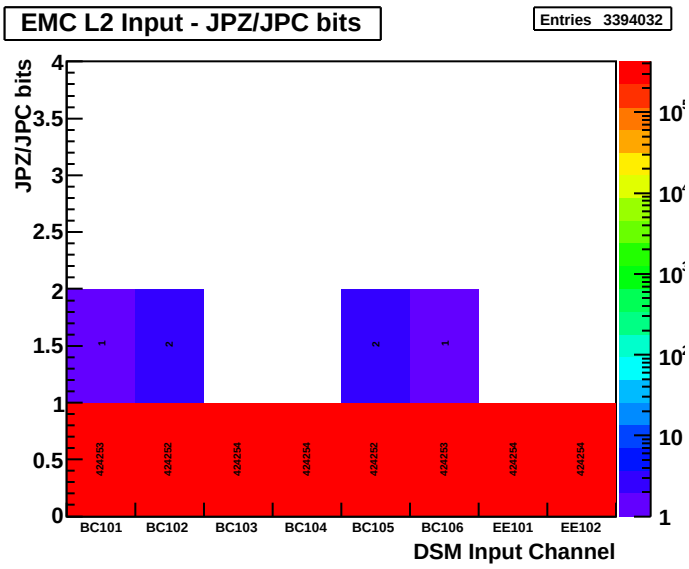

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

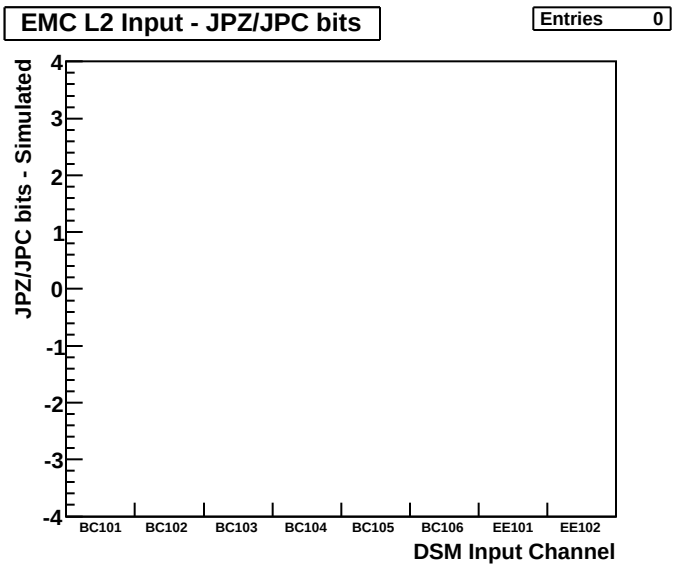

EMC L2 Input - JPX/JPA bits
Entries 3394032
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

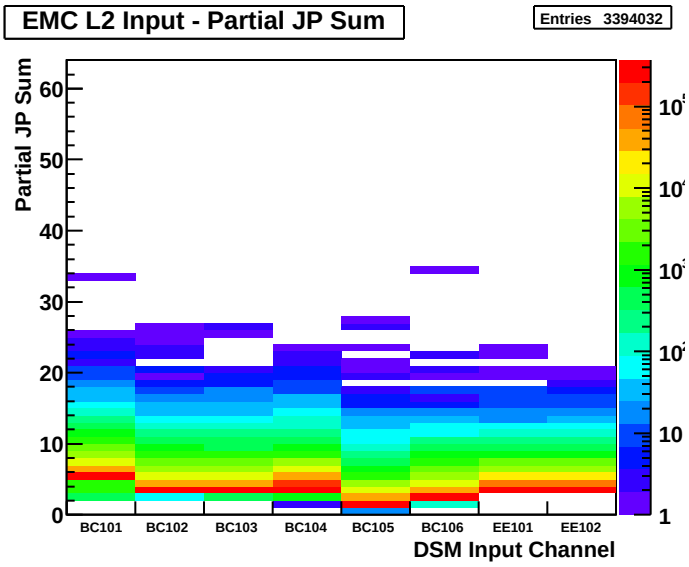

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

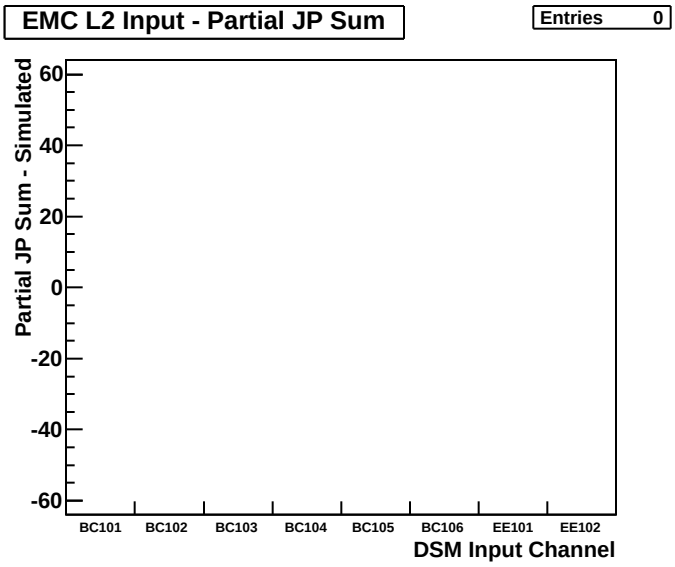

EMC L2 Input - JPY/JPB bits
Entries 3394032
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

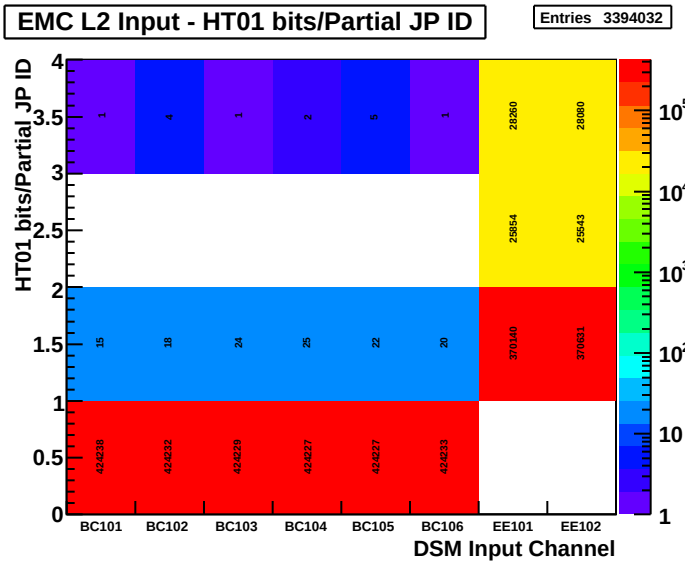

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

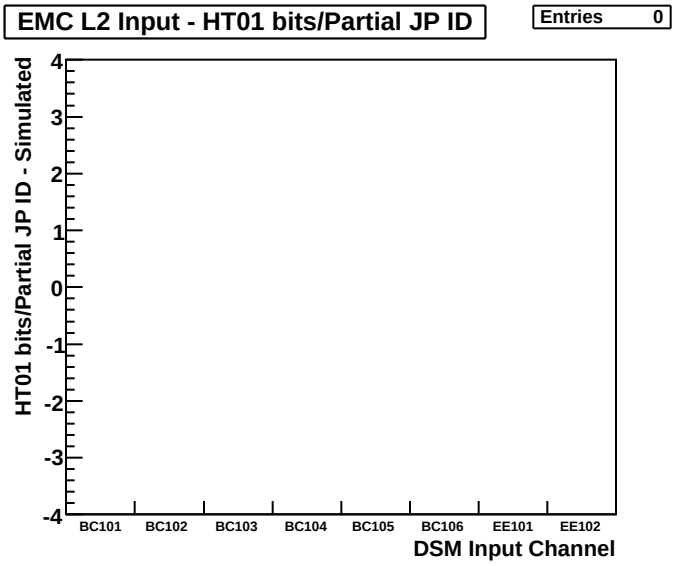

EMC L2 Input - JPZ/JPC bits
Entries 3394032
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

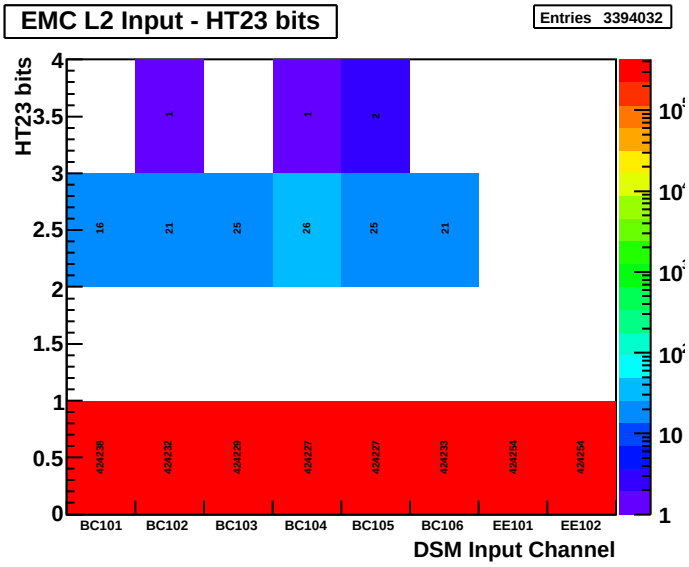

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

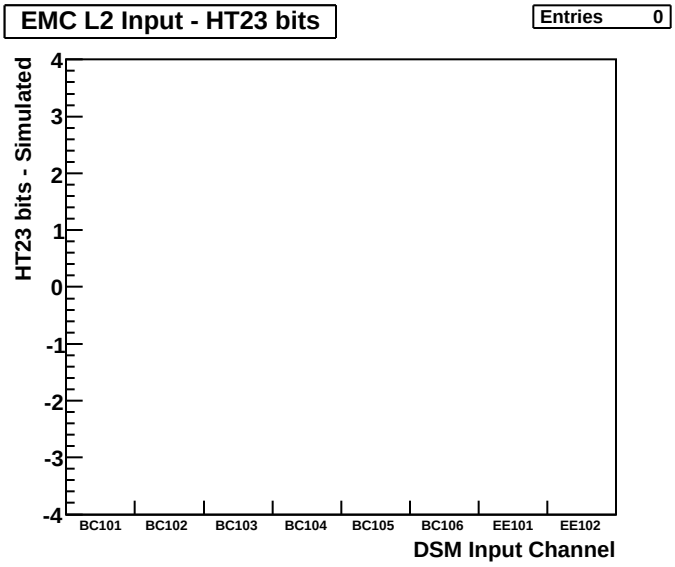

EMC L2 Input - Partial JP Sum
Entries 3394032
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

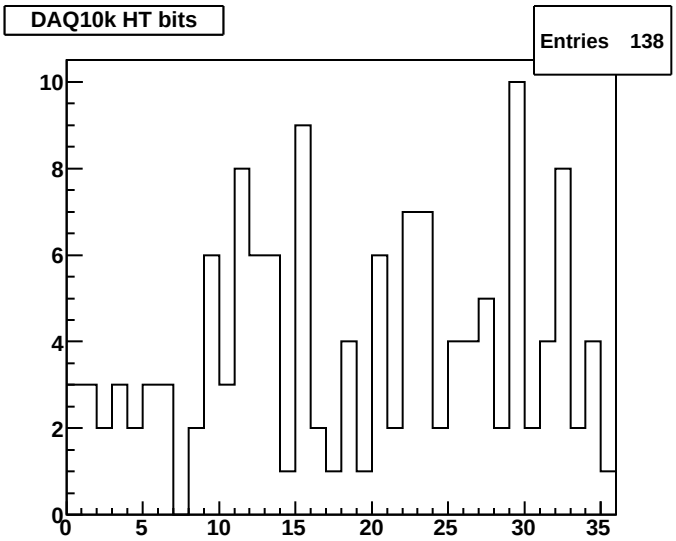

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

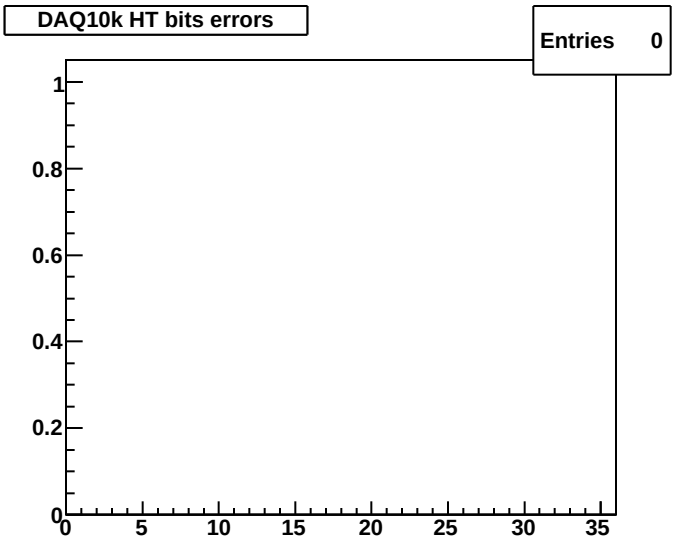

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3394032
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

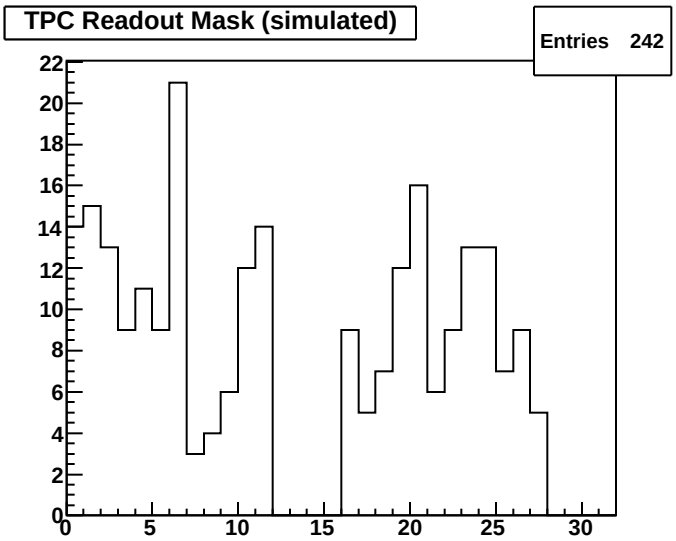

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

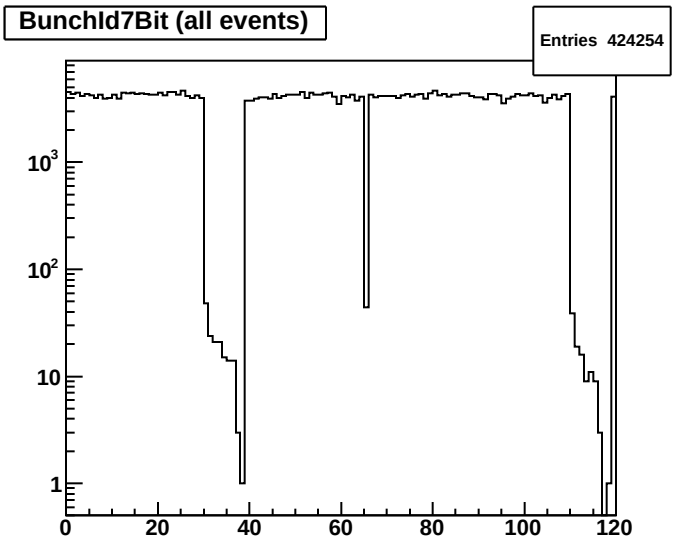

EMC L2 Input - HT23 bits
Entries 3394032
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

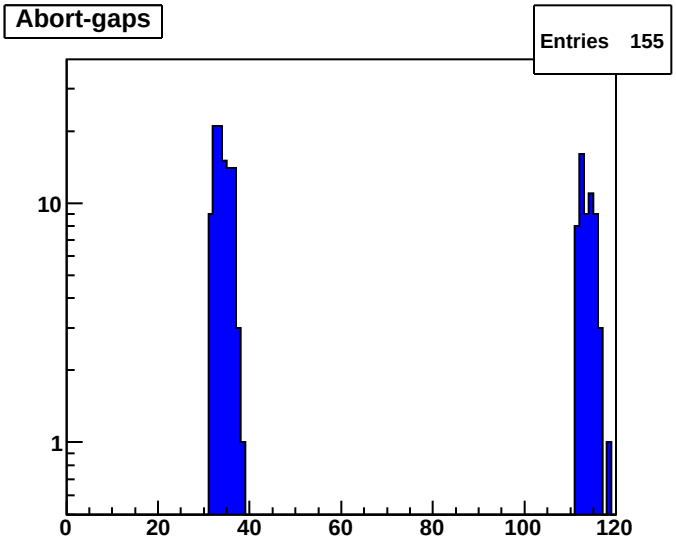

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

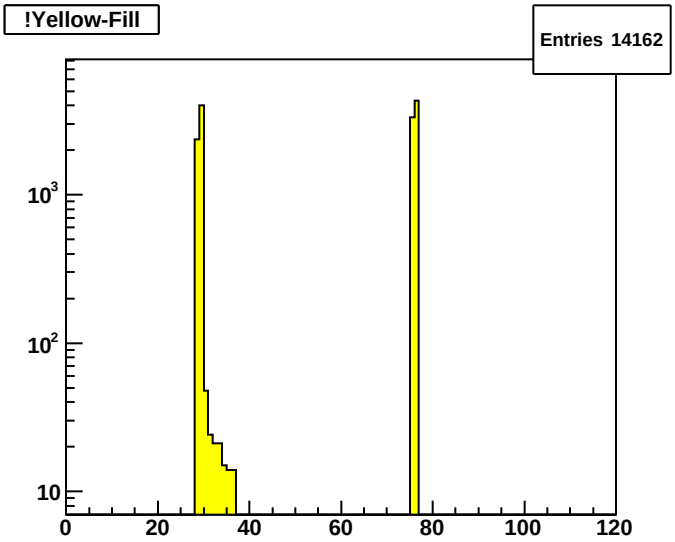

DAQ10k HT bits
Entries 138

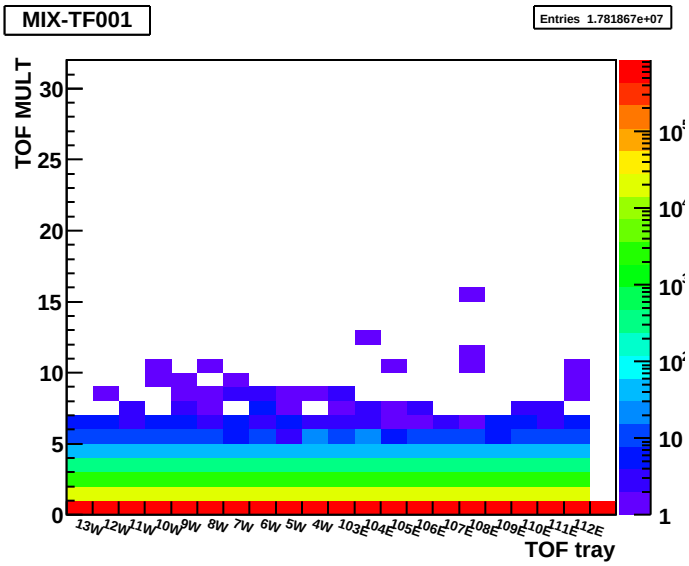

DAQ10k HT bits errors
Entries 0

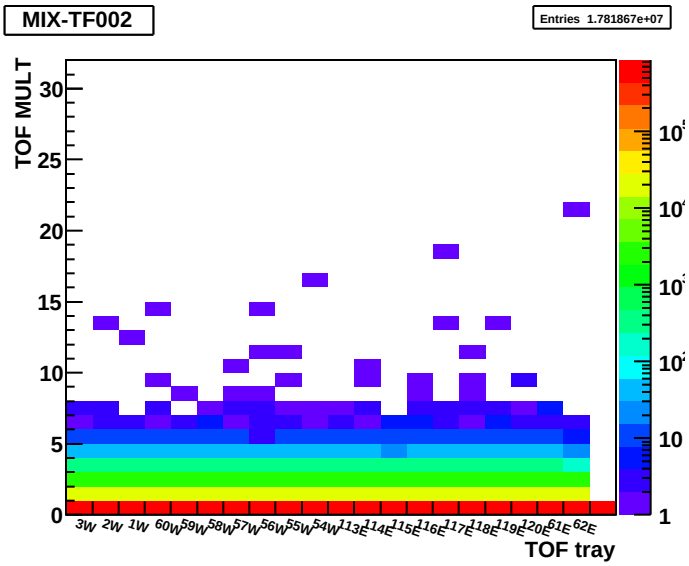

TPC Readout Mask (simulated)
Entries 242

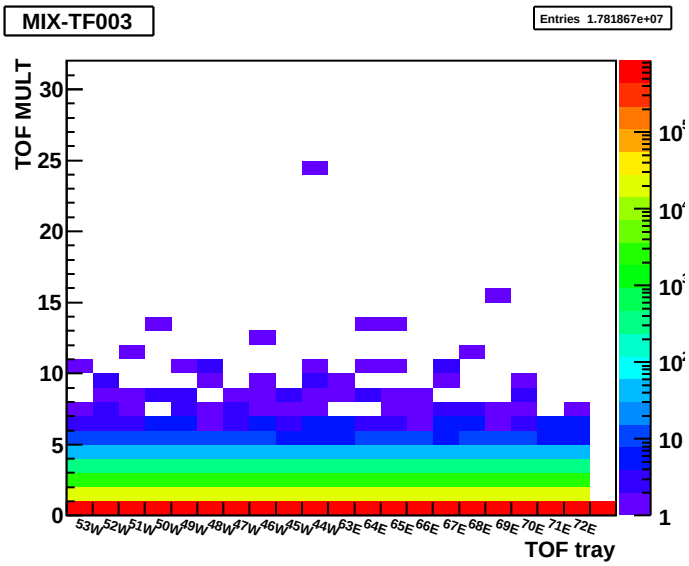

BunchId7Bit (all events)
Entries 424254

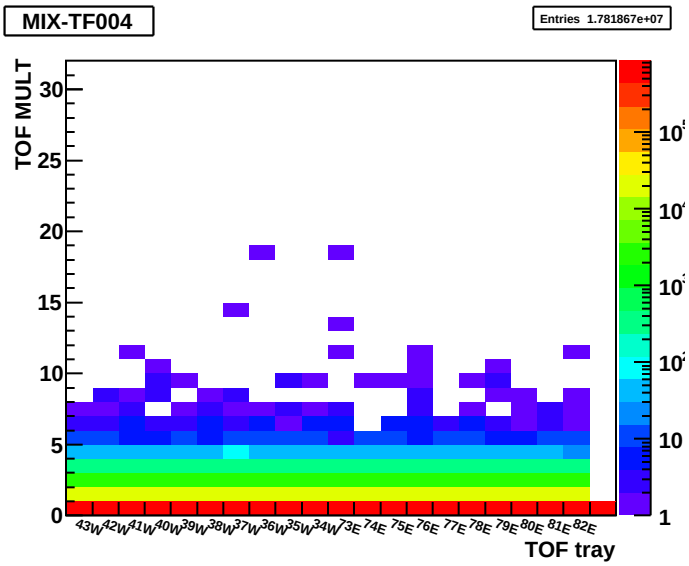

Abort-gaps
Entries 155

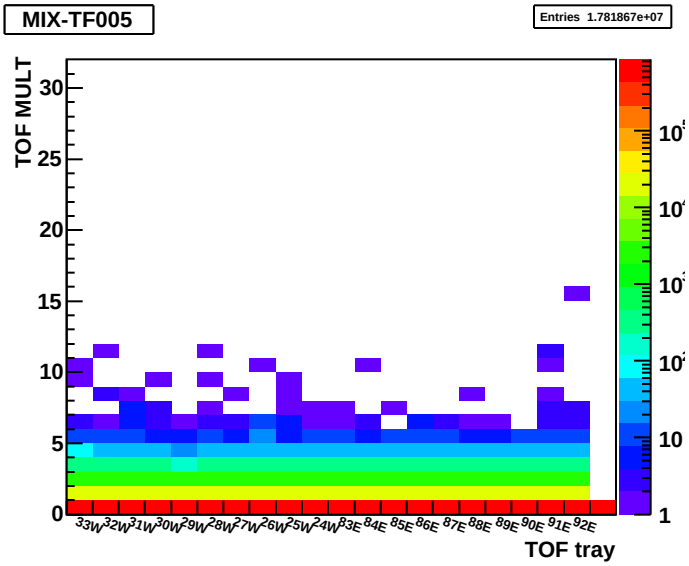

!Yellow-Fill
Entries 14162

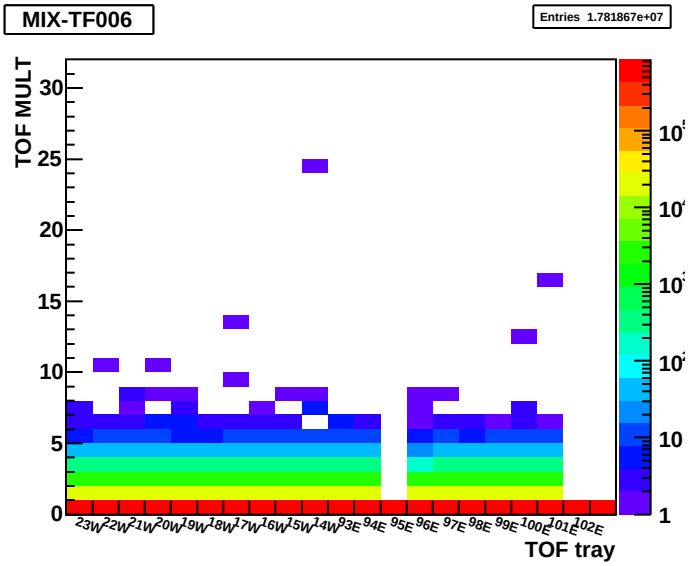

MIX-TF001
Entries 1.781867e+07
TOF MULT
TOF tray

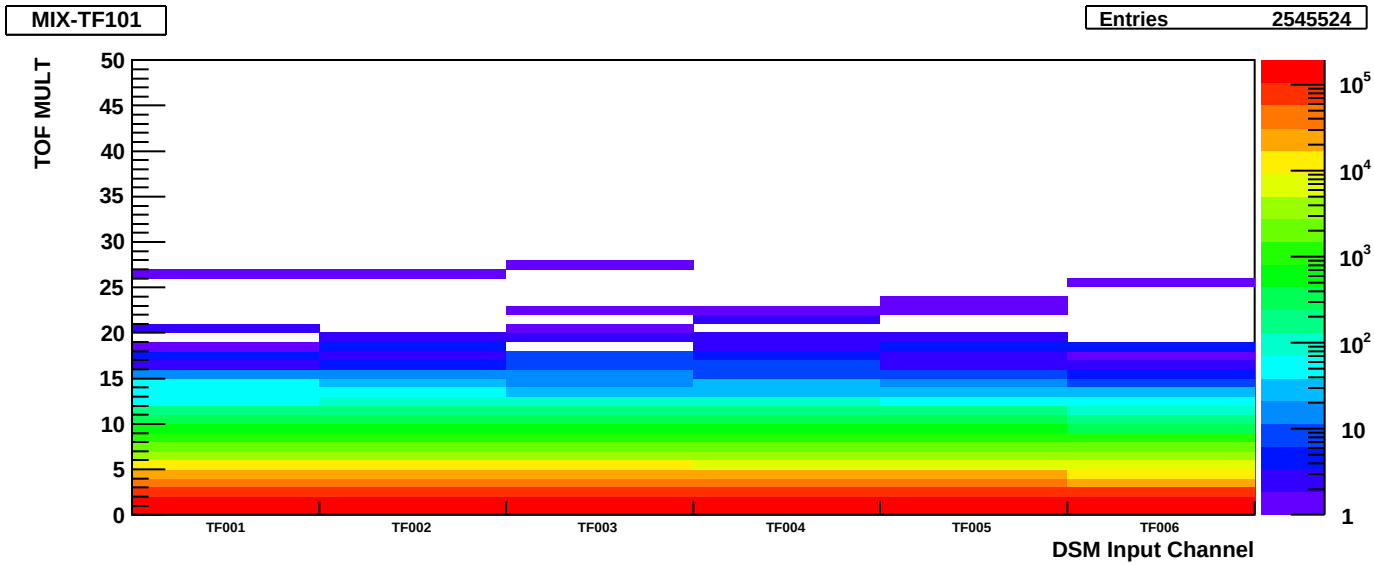

MIX-TF002
Entries 1.781867e+07
TOF MULT
TOF tray

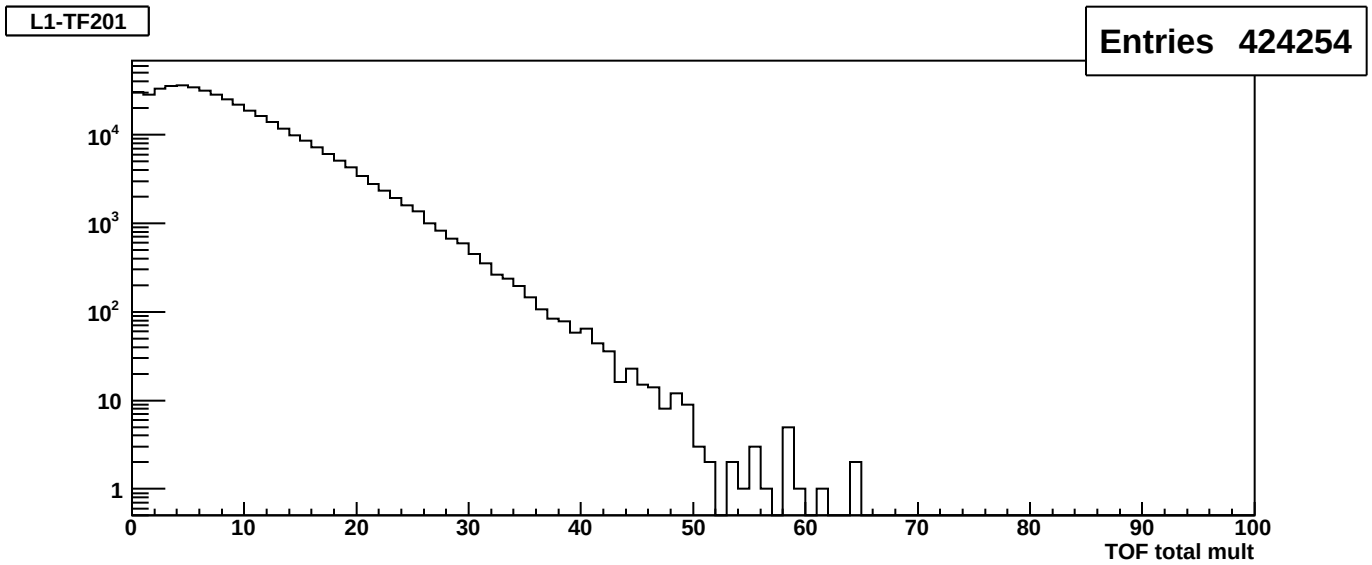

MIX-TF003
Entries 1.781867e+07
TOF MULT
TOF tray

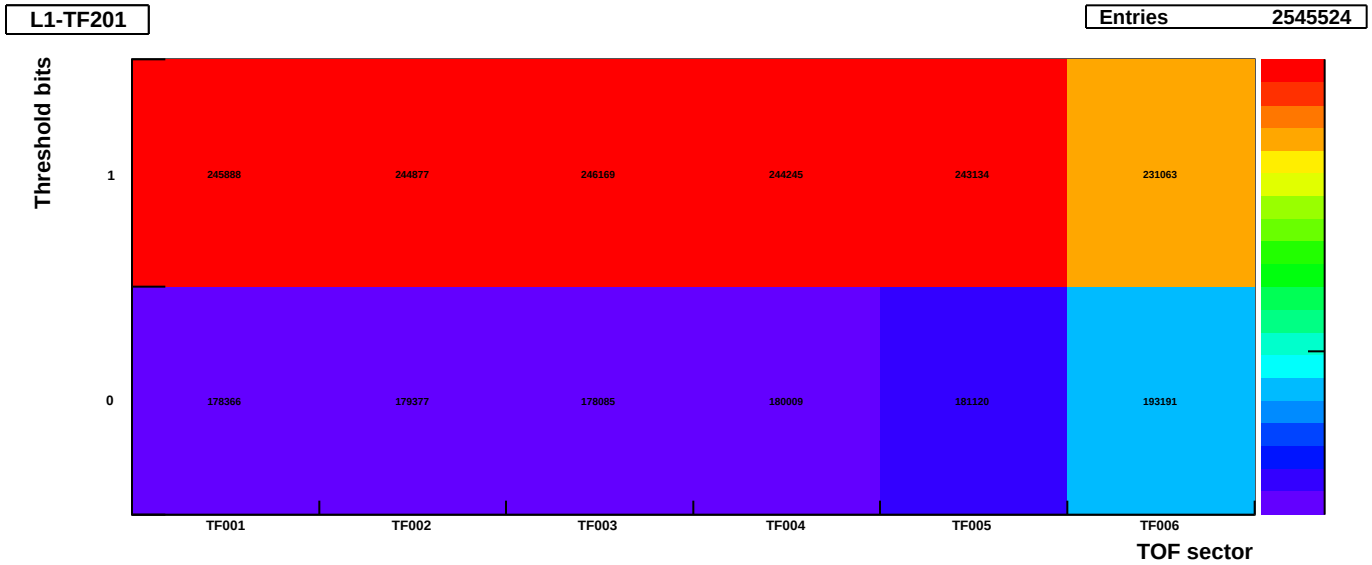

MIX-TF004
Entries 1.781867e+07
TOF MULT
TOF tray

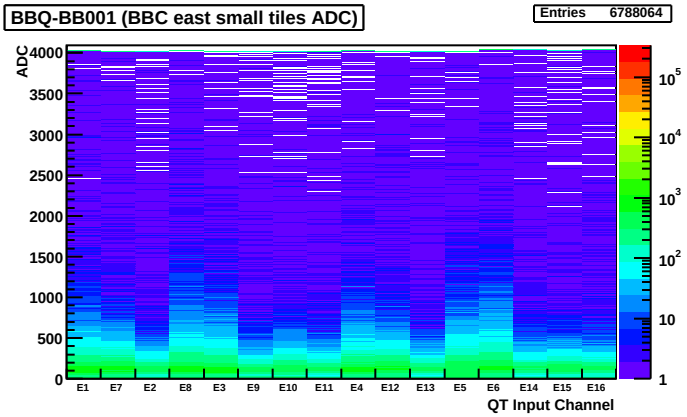

MIX-TF005
Entries 1.781867e+07
TOF MULT
TOF tray

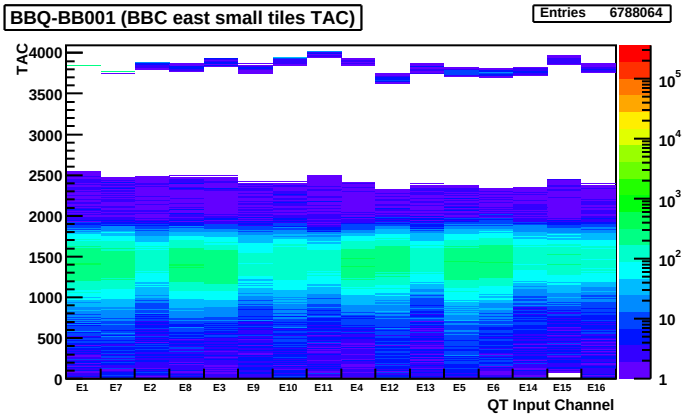

MIX-TF006
Entries 1.781867e+07
TOF MULT
TOF tray

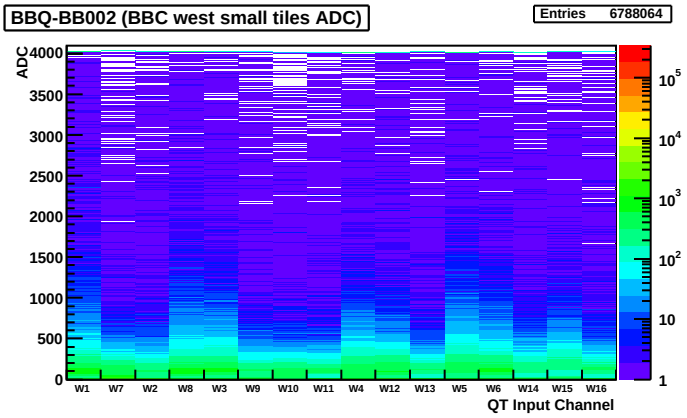

MIX-TF101
Entries 2545524
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

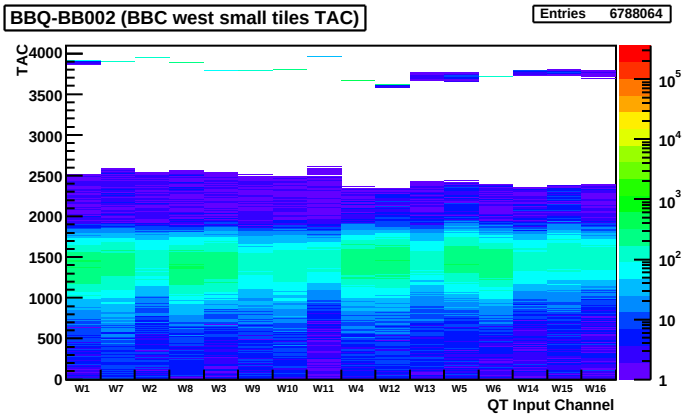

L1-TF201
Entries 424254
TOF total mult

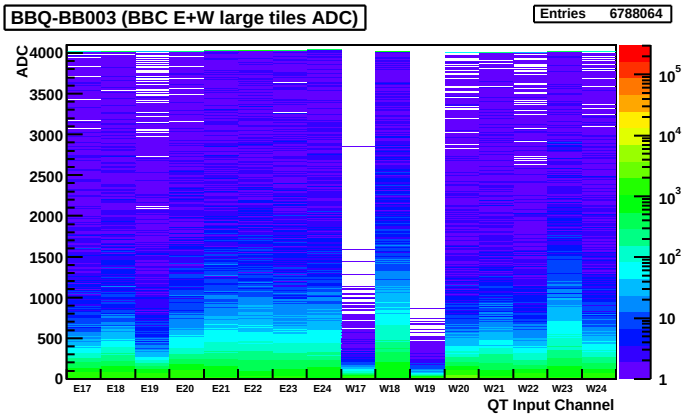

L1-TF201
Entries 2545524
Threshold bits
245888
244877
246169
244245
243134
231063
178366
179377
178085
180009
181120
193191
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

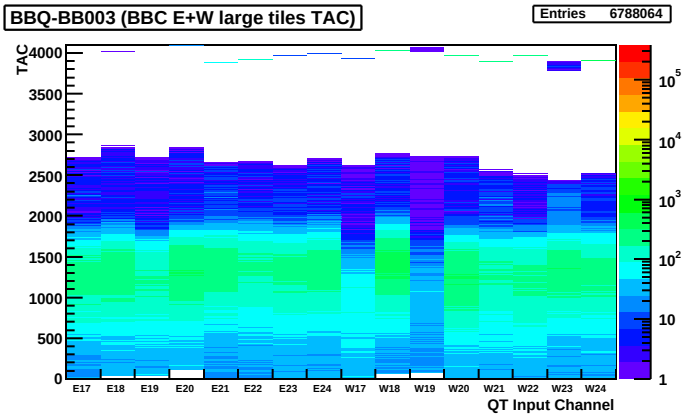

BBQ-BB001 (BBC east small tiles ADC)
Entries 6788064
ADC
QT Input Channel

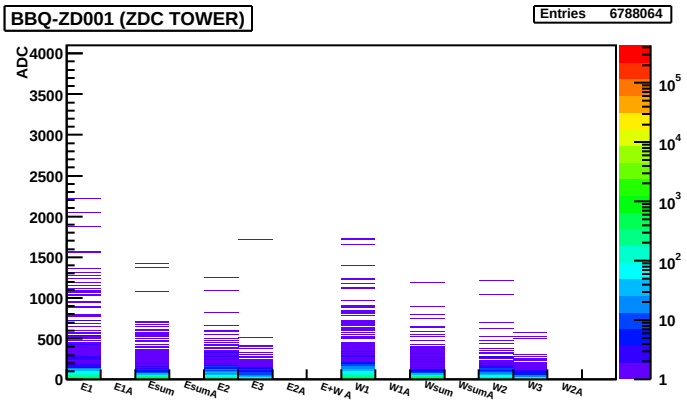

BBQ-BB001 (BBC east small tiles TAC)
Entries 6788064
TAC
QT Input Channel

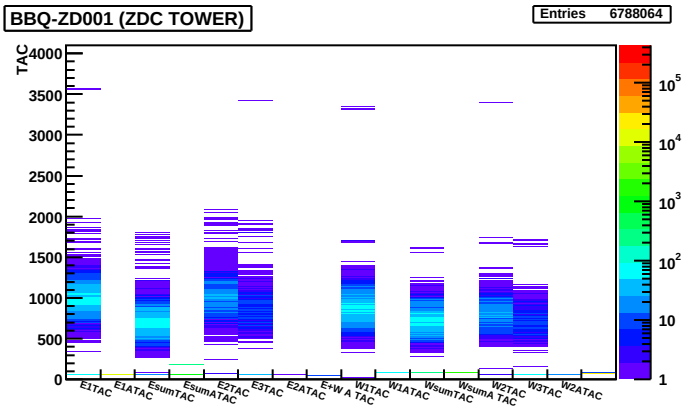

BBQ-BB002 (BBC west small tiles ADC)
Entries 6788064
ADC
QT Input Channel

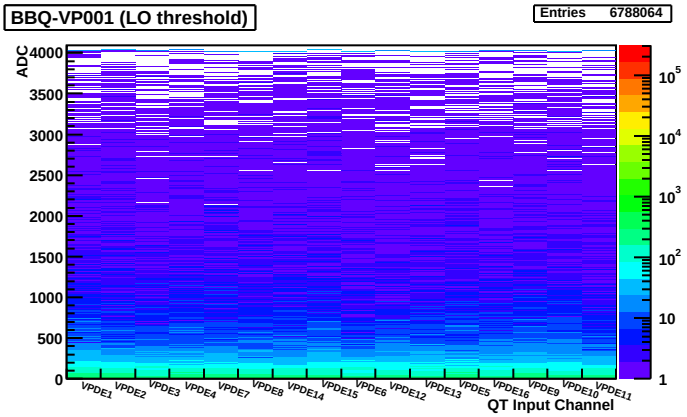

BBQ-BB002 (BBC west small tiles TAC)
Entries 6788064
TAC
QT Input Channel

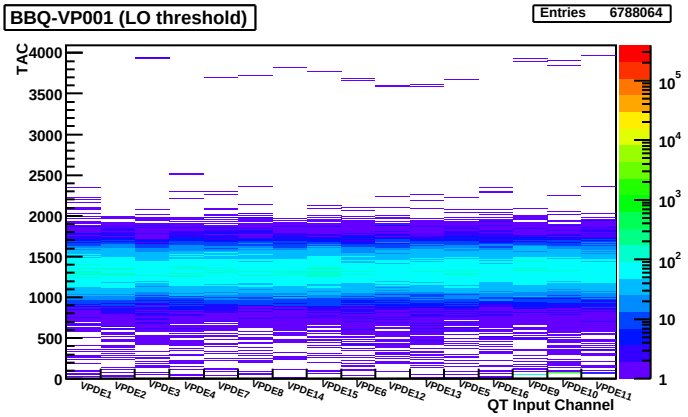

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6788064
ADC
QT Input Channel

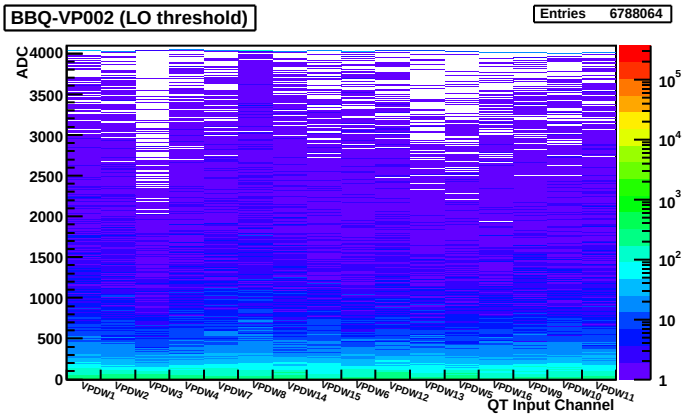

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6788064
TAC
QT Input Channel

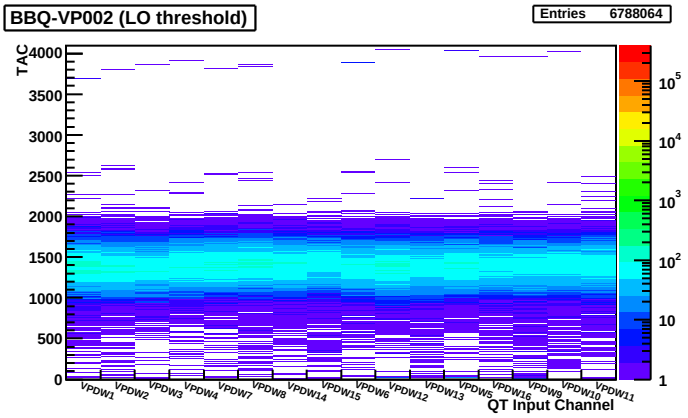

BBQ-ZD001 (ZDC TOWER)
Entries 6788064
ADC

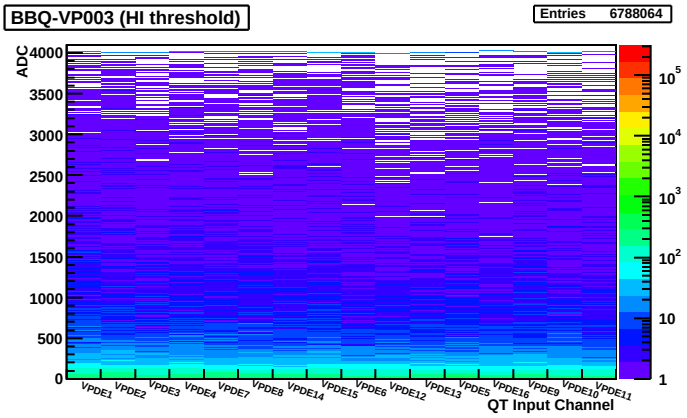

BBQ-ZD001 (ZDC TOWER)
Entries 6788064
TAC

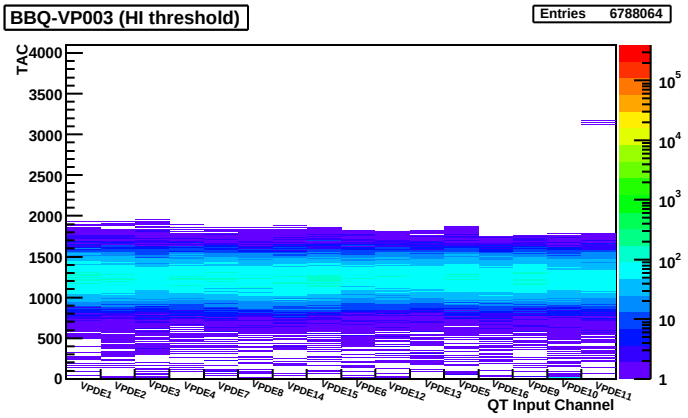

BBQ-VP001 (LO threshold)
Entries 6788064
ADC
QT Input Channel

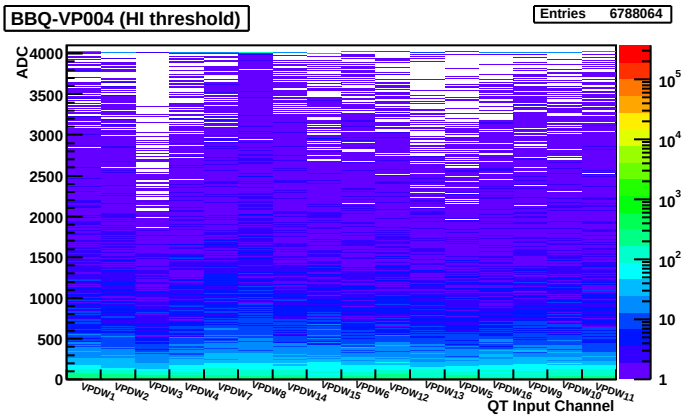

BBQ-VP001 (LO threshold)
Entries 6788064
TAC
QT Input Channel

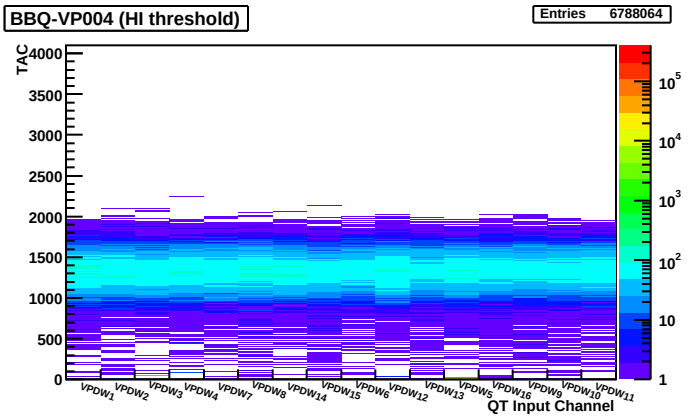

BBQ-VP002 (LO threshold)
Entries 6788064
ADC
QT Input Channel

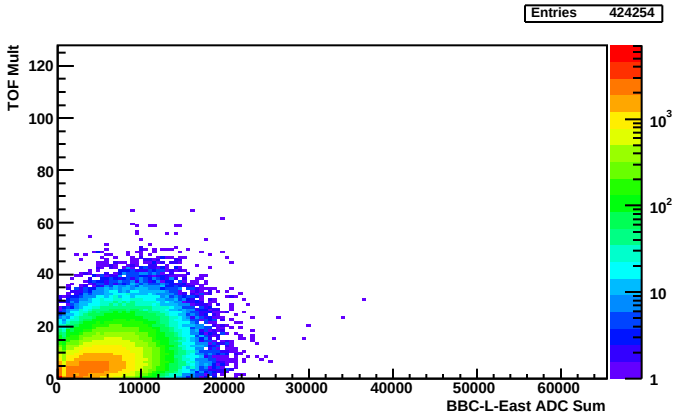

BBQ-VP002 (LO threshold)
Entries 6788064
TAC
QT Input Channel

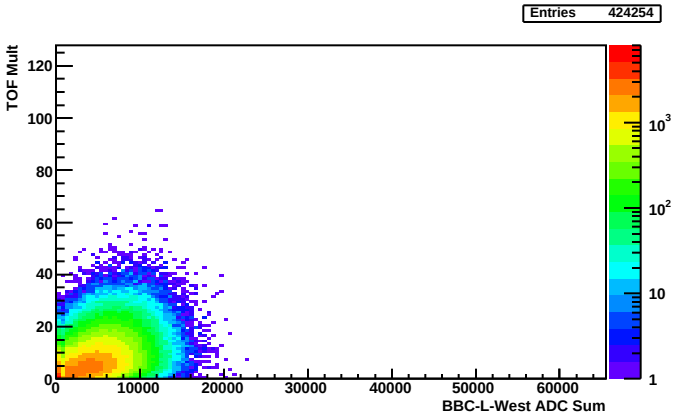

BBQ-VP003 (HI threshold)
Entries 6788064
ADC
QT Input Channel

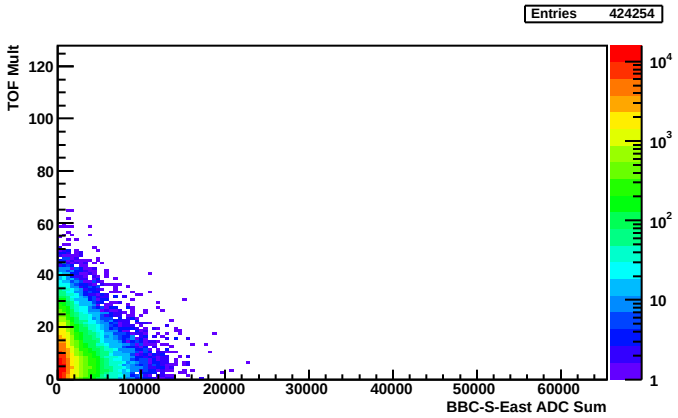

BBQ-VP003 (HI threshold)
Entries 6788064
TAC
QT Input Channel

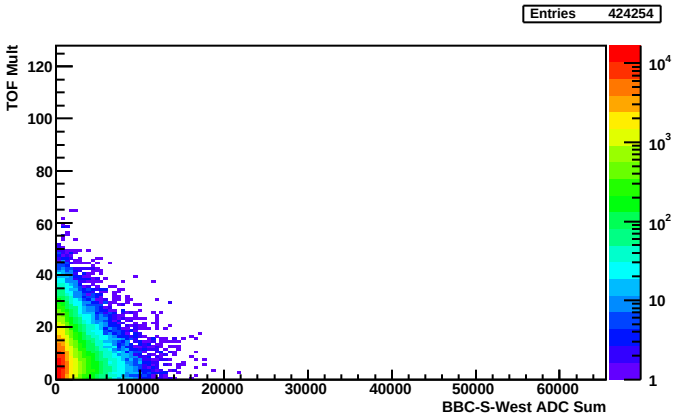

BBQ-VP004 (HI threshold)
Entries 6788064
ADC
QT Input Channel

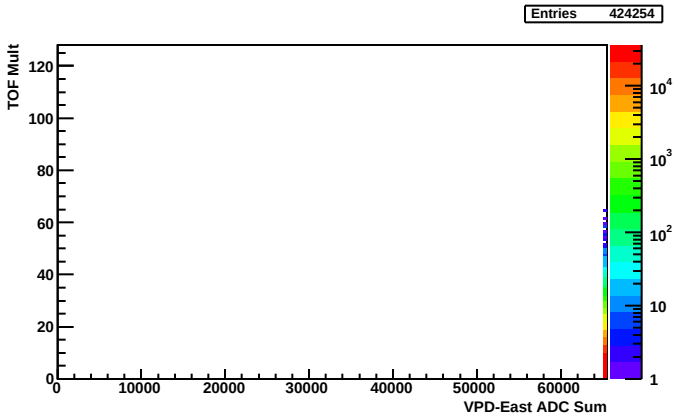

BBQ-VP004 (HI threshold)
Entries 6788064
TAC
QT Input Channel

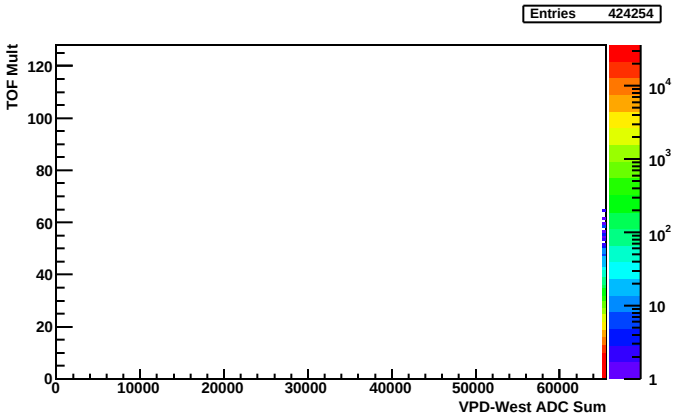

Entries 424254
TOF Mult
BBC-L-East ADC Sum

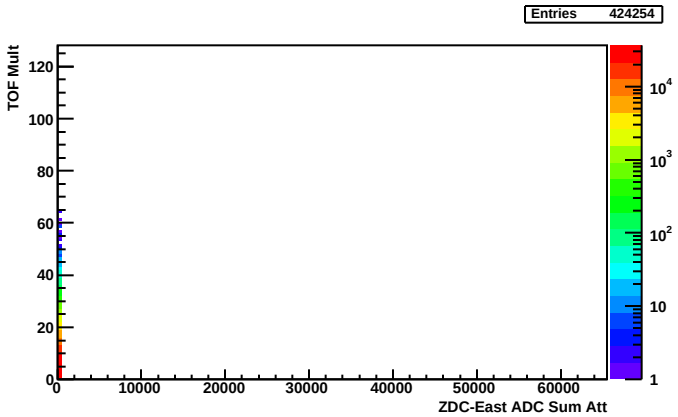

Entries 424254
TOF Mult
BBC-L-West ADC Sum

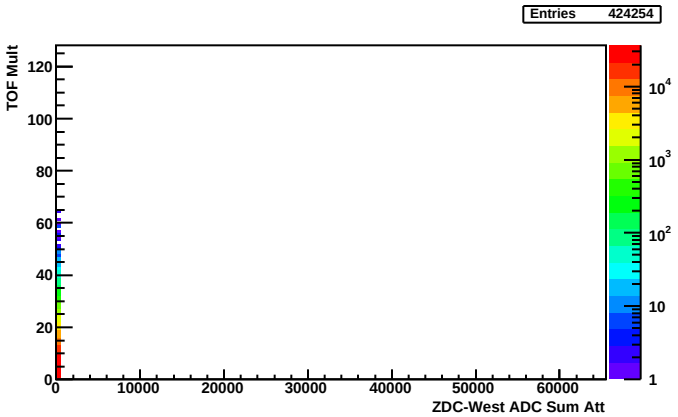

Entries 424254
TOF Mult
BBC-S-East ADC Sum

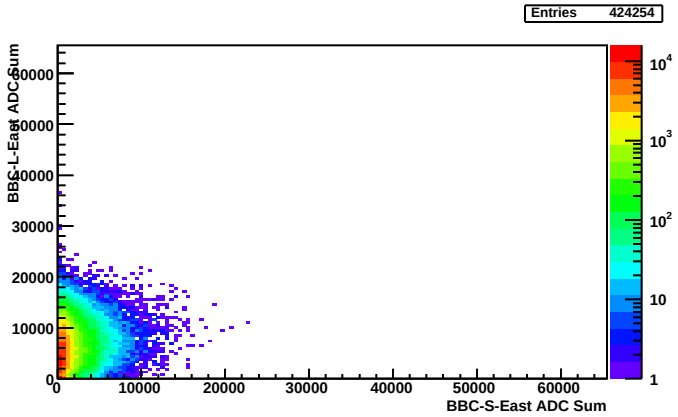

Entries 424254
TOF Mult
BBC-S-West ADC Sum

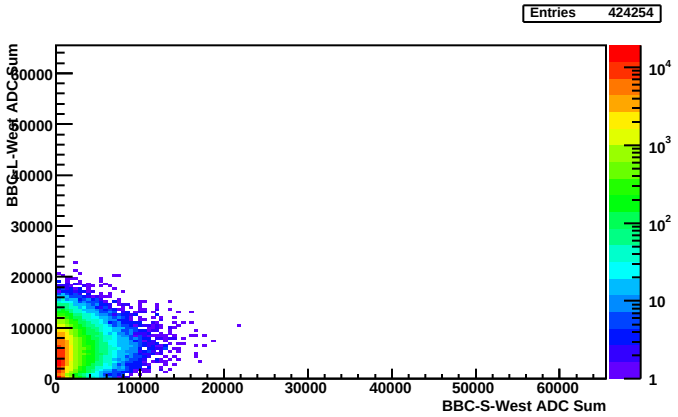

Entries 424254
TOF Mult
VPD-East ADC Sum

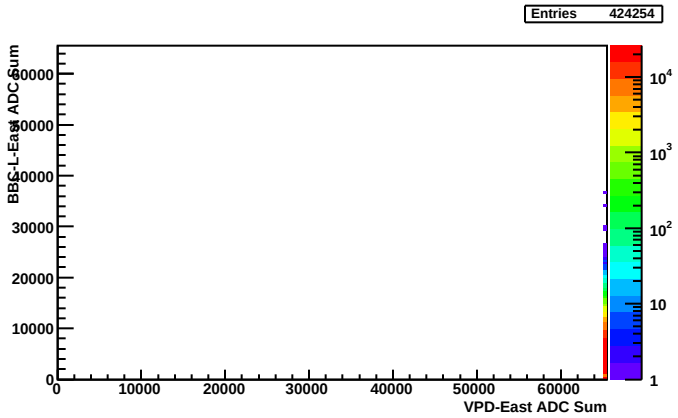

Entries 424254
TOF Mult
VPD-West ADC Sum

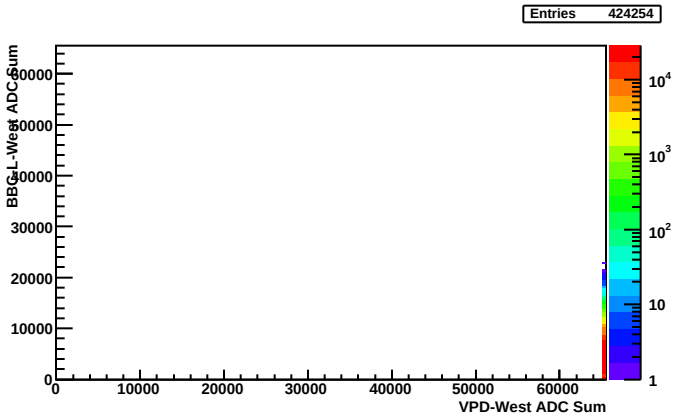

Entries 424254
TOF Mult
ZDC-East ADC Sum Att

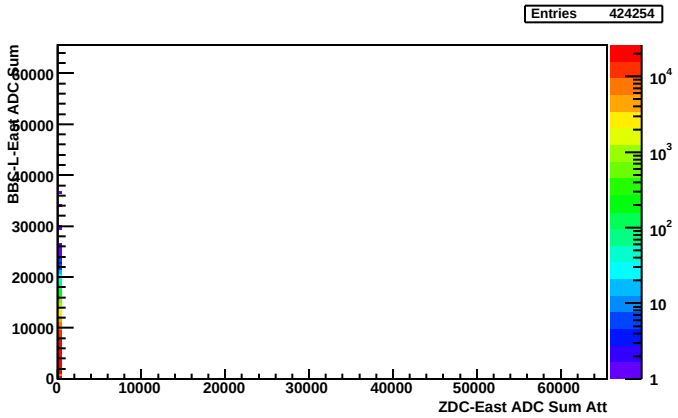

Entries 424254
TOF Mult
ZDC-West ADC Sum Att

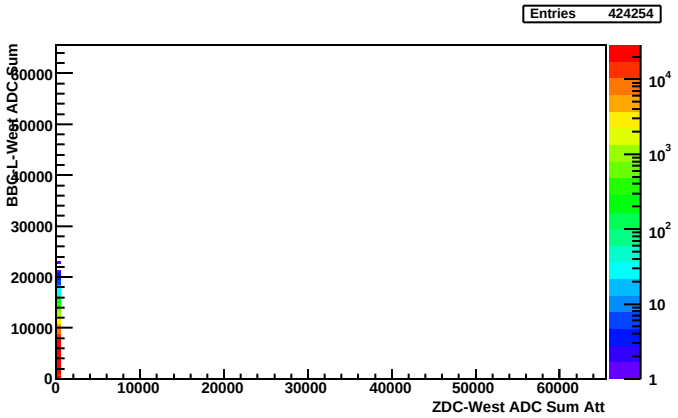

Entries 424254
BBC-L-East ADC Sum
BBC-S-East ADC Sum

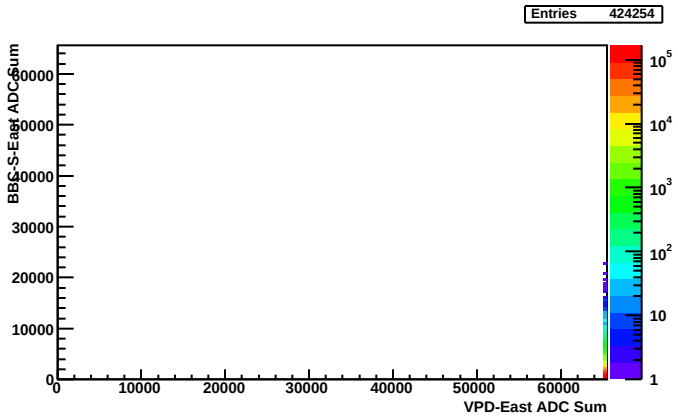

Entries 424254
BBC-L-West ADC Sum
BBC-S-West ADC Sum

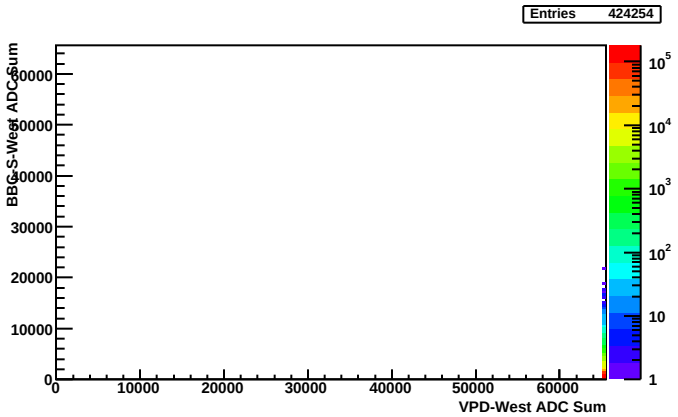

Entries 424254
BBC-L-East ADC Sum
VPD-East ADC Sum

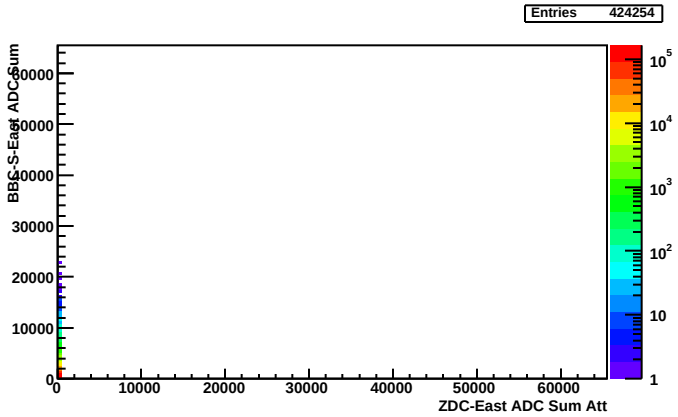

Entries 424254
BBC-L-West ADC Sum
VPD-West ADC Sum

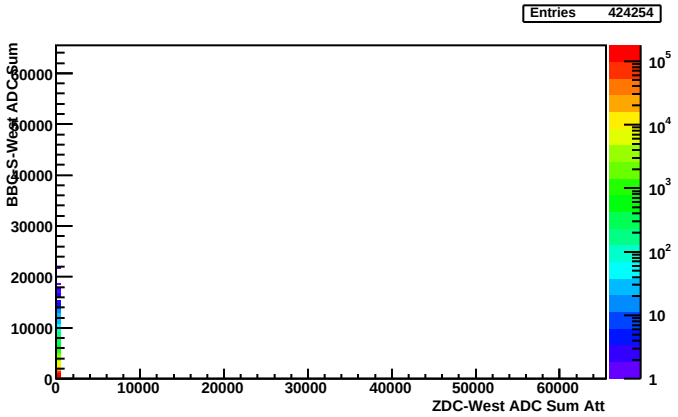

Entries 424254
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

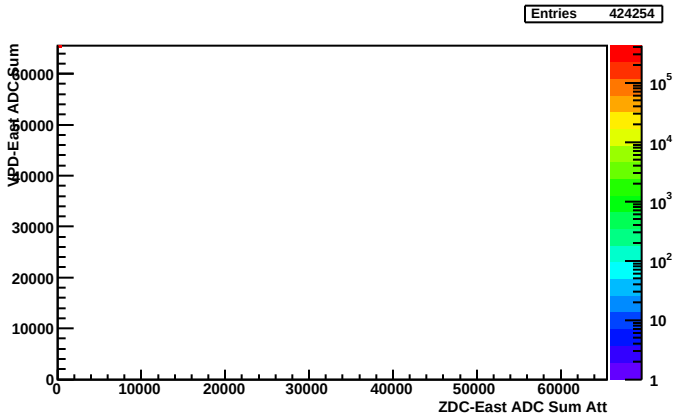

Entries 424254
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

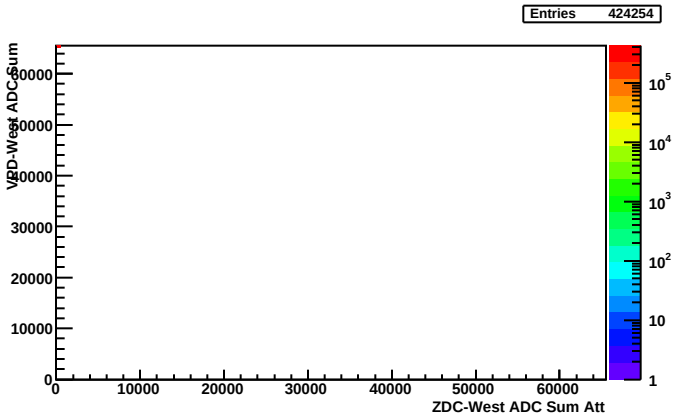

Entries 424254
BBC-S-East ADC Sum
VPD-East ADC Sum

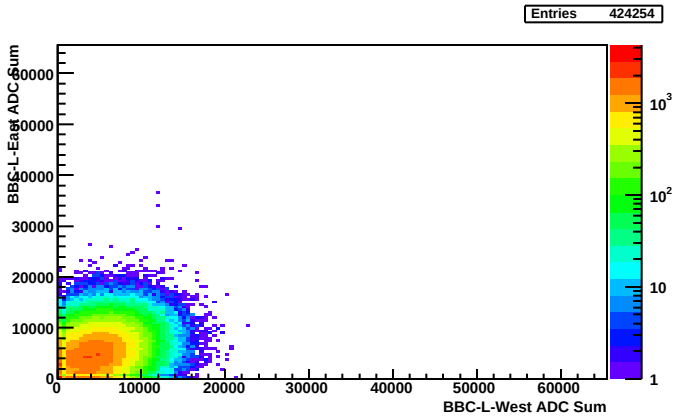

Entries 424254
BBC-S-West ADC Sum
VPD-West ADC Sum

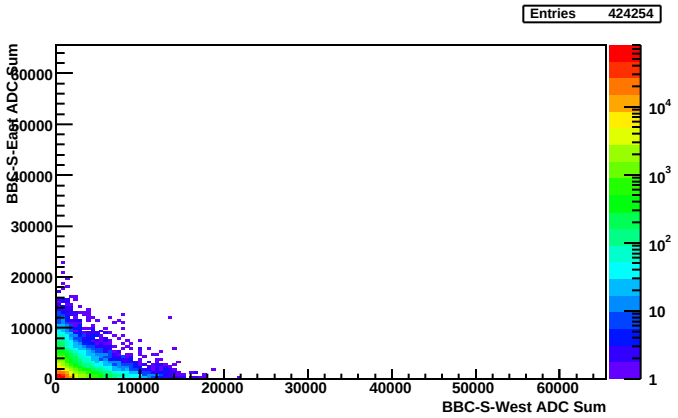

Entries 424254
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

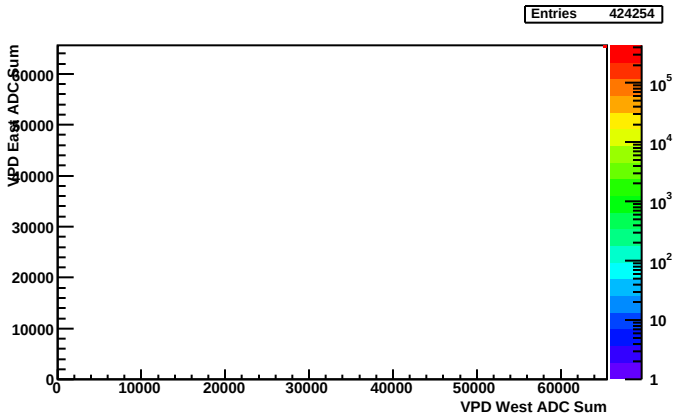

Entries 424254
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

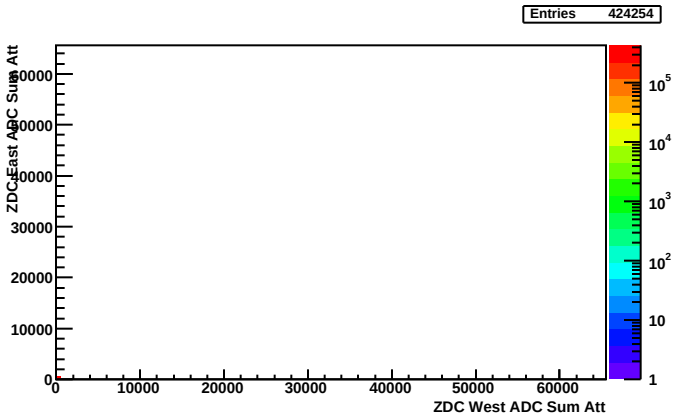

Entries 424254
VPD-East ADC Sum
ZDC-East ADC Sum Att

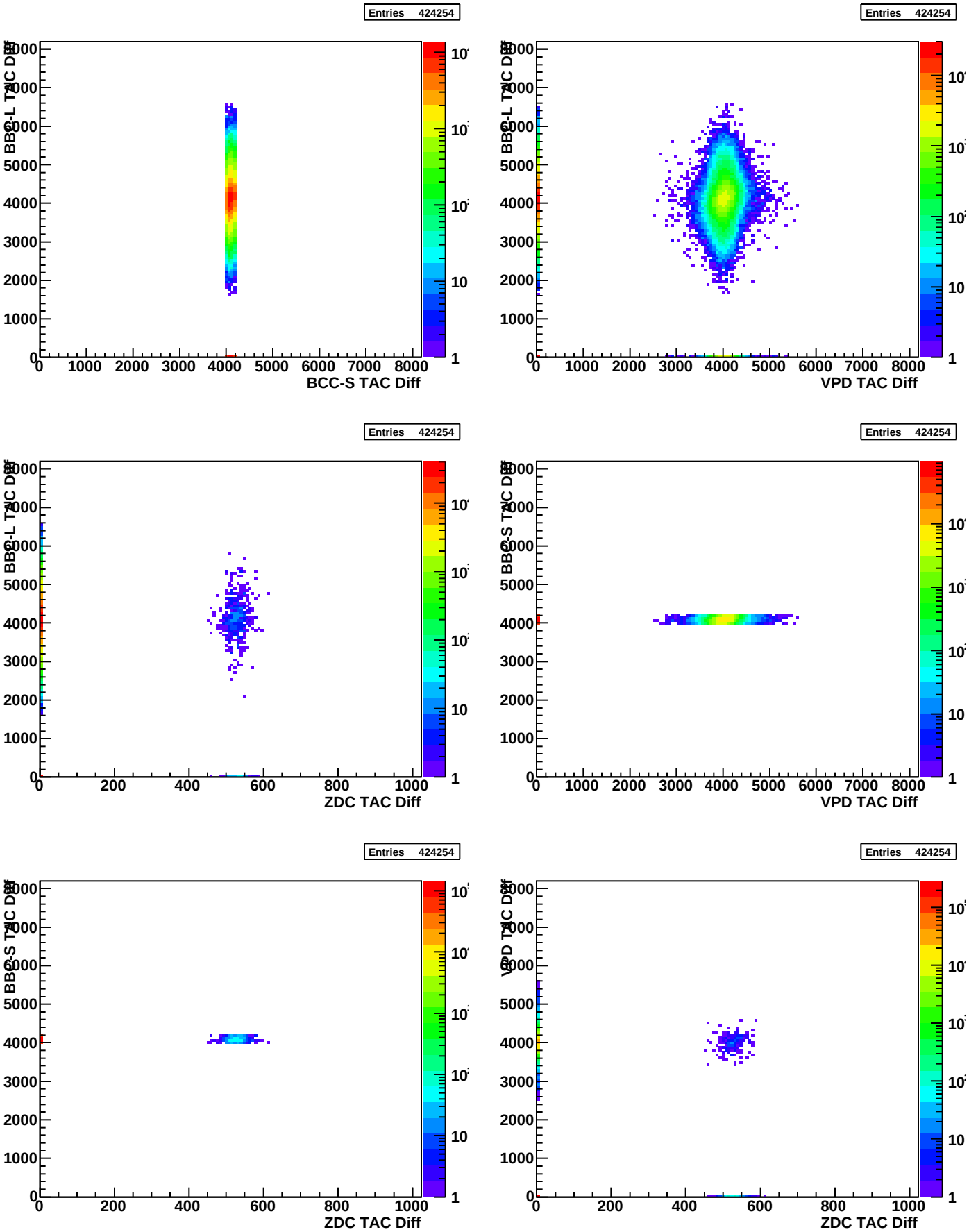

Entries 424254
VPD-West ADC Sum
ZDC-West ADC Sum Att

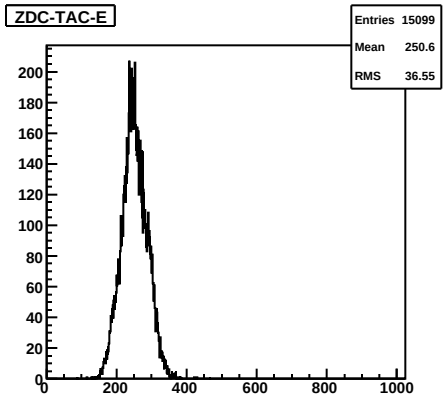

Entries 424254
BBC-L-East ADC Sum
BBC-L-West ADC Sum

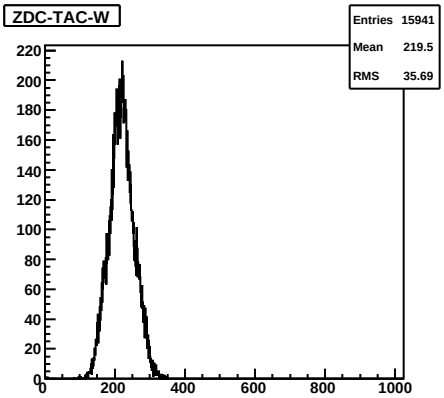

Entries 424254
BBC-S-East ADC Sum
BBC-S-West ADC Sum

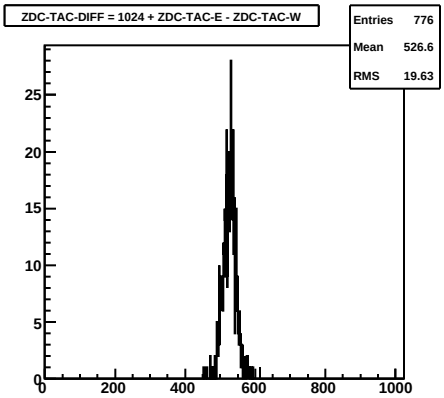

Entries 424254
VPD East ADC Sum
VPD West ADC Sum

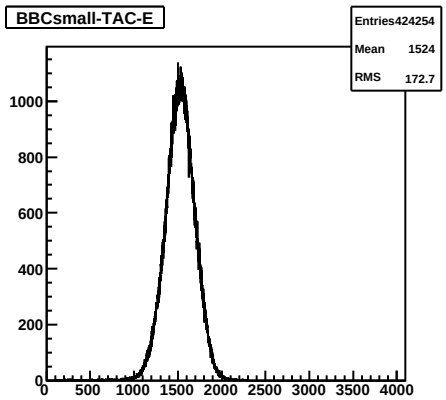

Entries 424254
ZDC East ADC Sum Att
ZDC West ADC Sum Att

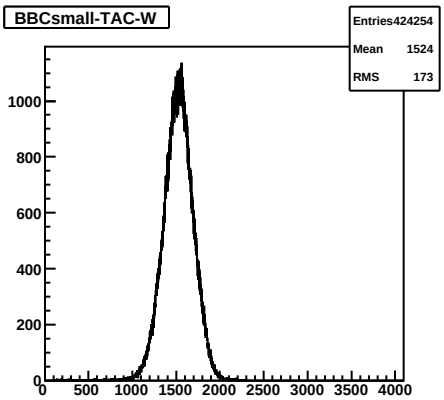

Entries 424254
BBC-L TAC Diff
BCC-S TAC Diff
Entries 424254
BBC-L TAC Diff
VPD TAC Diff
Entries 424254
BBC-L TAC Diff
ZDC TAC Diff
Entries 424254
BBC-S TAC Diff
VPD TAC Diff
Entries 424254
BBC-S TAC Diff
ZDC TAC Diff
Entries 424254
VPD TAC Diff
ZDC TAC Diff

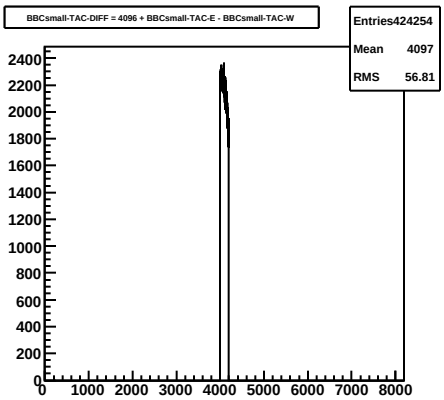

ZDC-TAC-E
Entries 15099
Mean 250.6
RMS 36.55

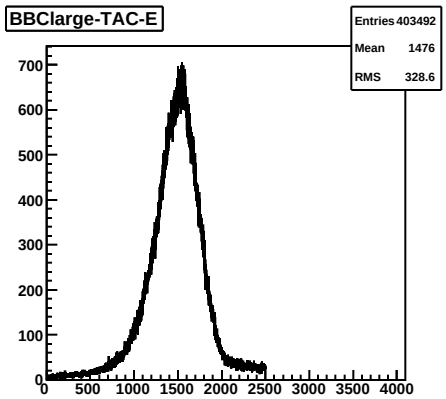

ZDC-TAC-W
Entries 15941
Mean 219.5
RMS 35.69

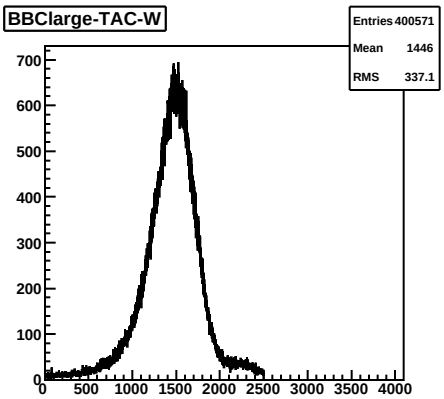

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 776
Mean 526.6
RMS 19.63

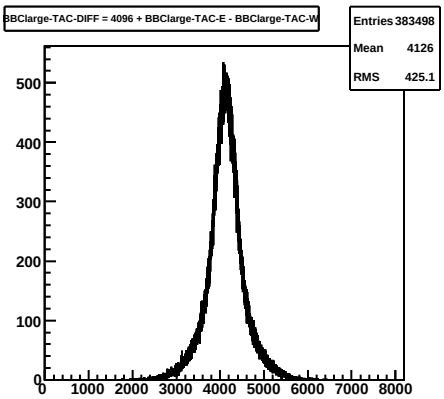

BBCsmall-TAC-E
Entries 424254
Mean 1524
RMS 172.7

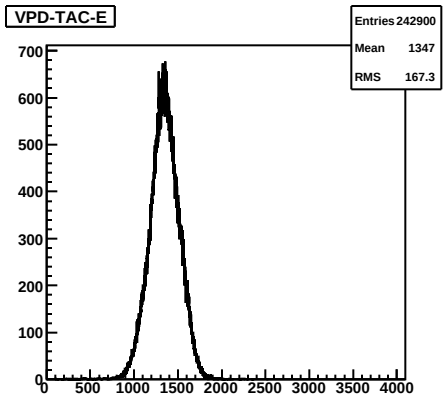

BBCsmall-TAC-W
Entries 424254
Mean 1524
RMS 173

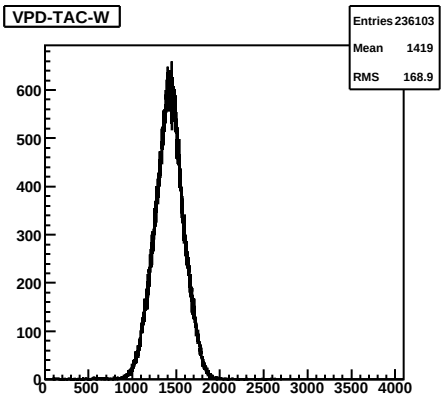

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 424254
Mean 4097
RMS 56.81

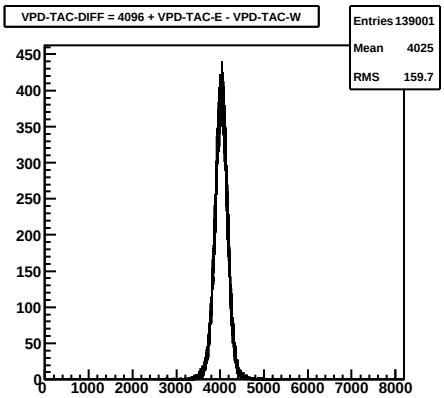

BBClarge-TAC-E
Entries 403492
Mean 1476
RMS 328.6

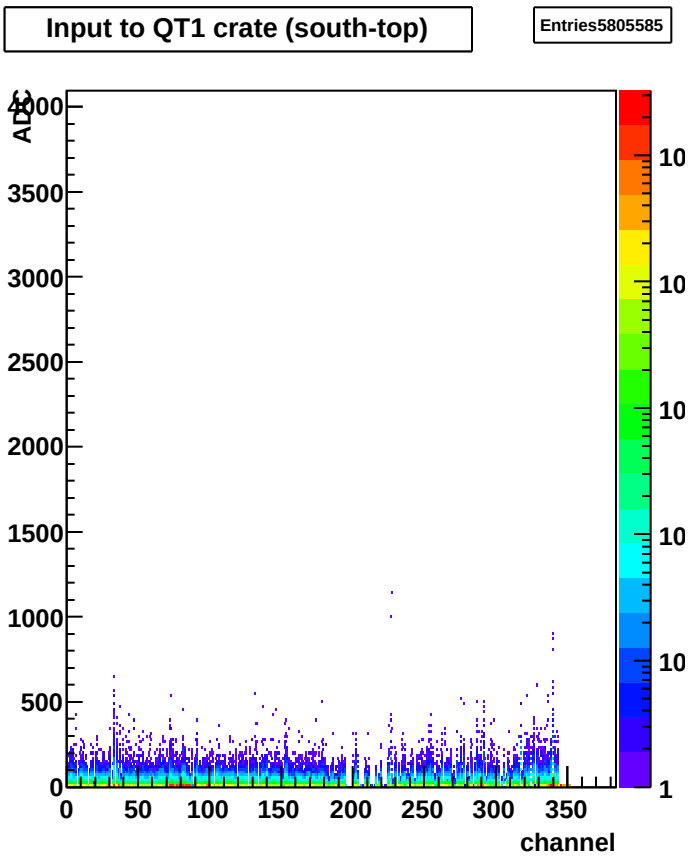

BBClarge-TAC-W
Entries 400571
Mean 1446
RMS 337.1

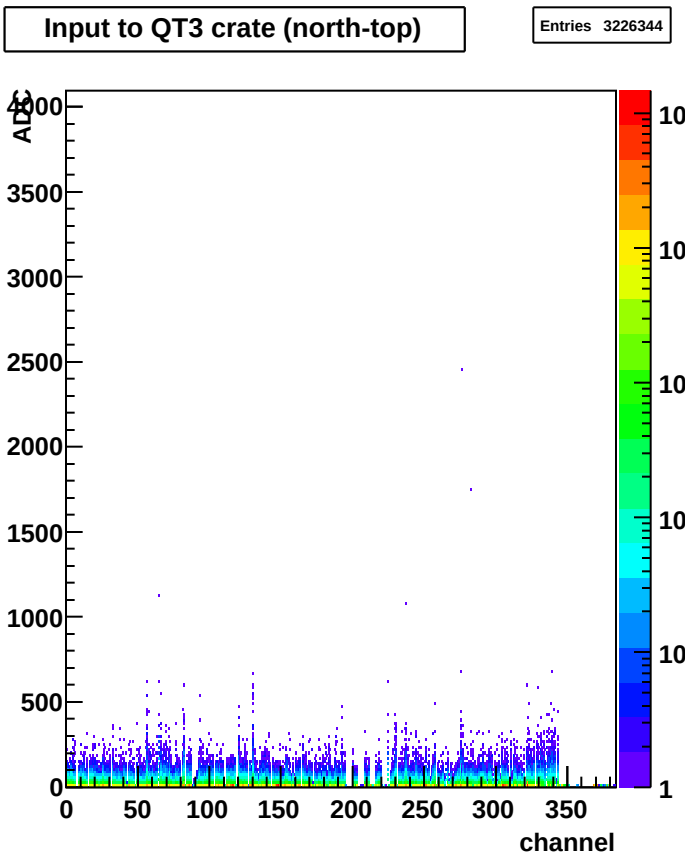

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 383498
Mean 4126
RMS 425.1

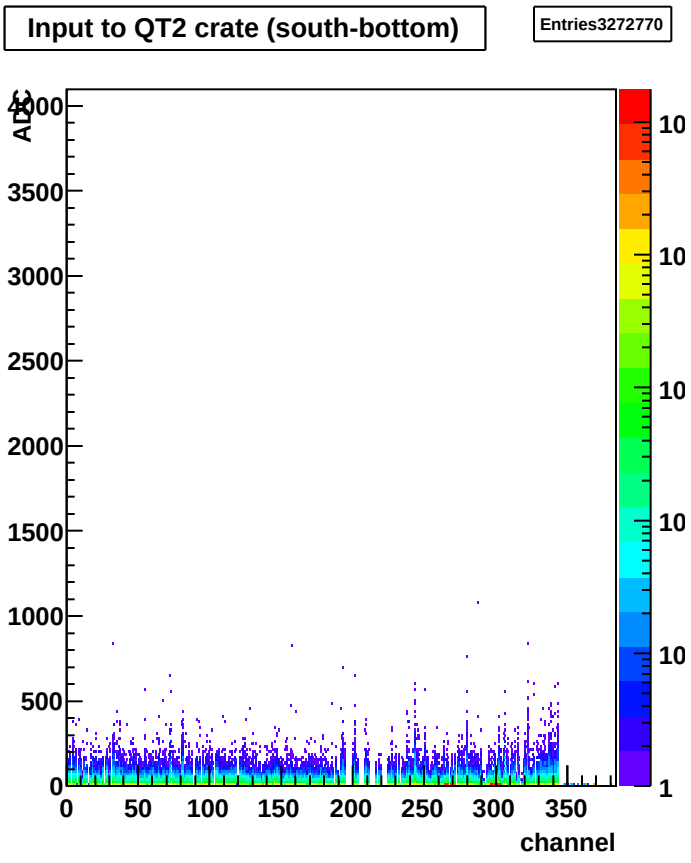

VPD-TAC-E
Entries 242900
Mean 1347
RMS 167.3

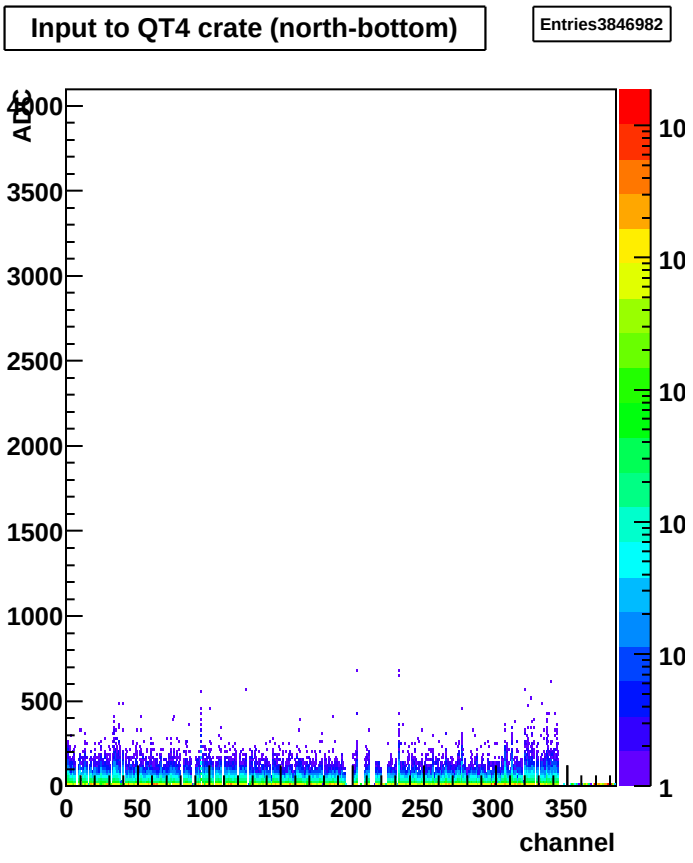

VPD-TAC-W
Entries 236103
Mean 1419
RMS 168.9

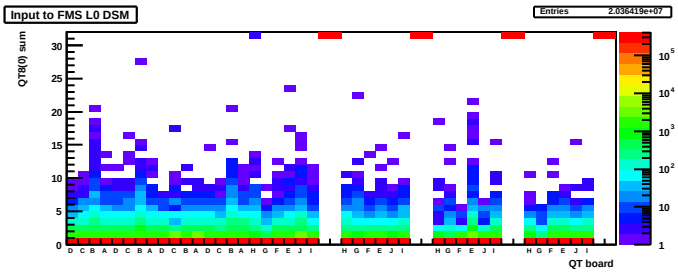

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 139001
Mean 4025
RMS 159.7

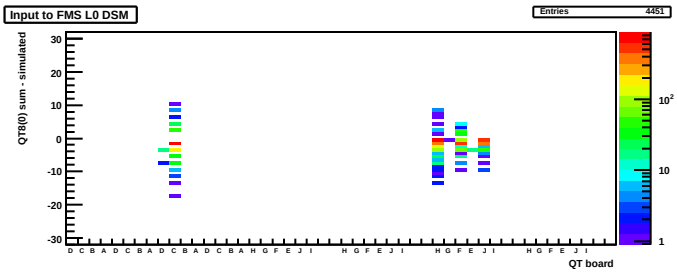

Input to QT1 crate (south-top)
Entries5805585
ADC
channel

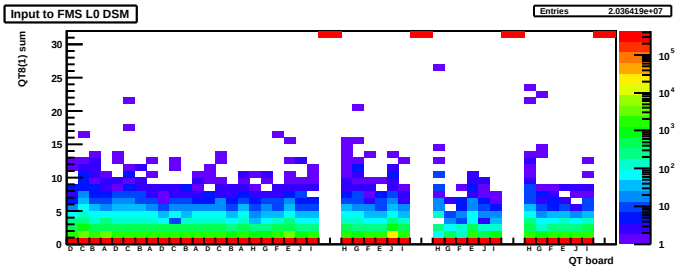

Input to QT3 crate (north-top)
Entries 3226344
ADC
channel

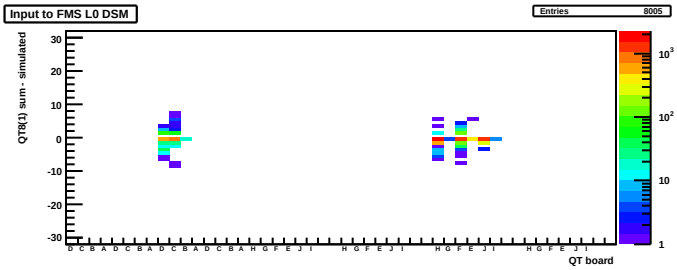

Input to QT2 crate (south-bottom)
Entries3272770
ADC
channel

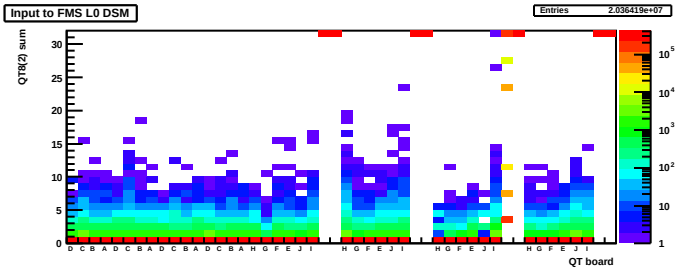

Input to QT4 crate (north-bottom)
Entries3846982
ADC
channel

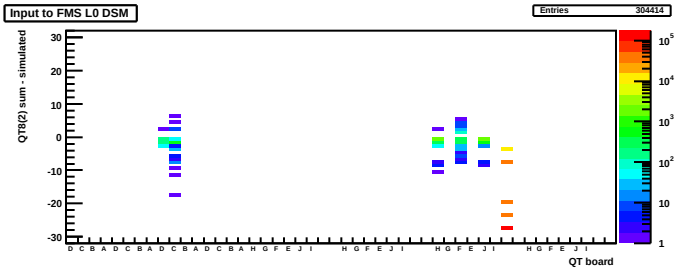

Input to FMS L0 DSM
Entries 2.036419e+07
QT8(0) sum
QT board

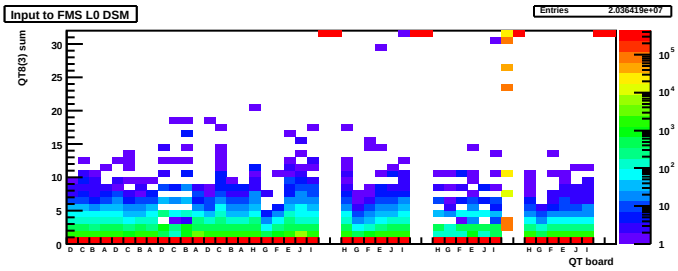

Input to FMS L0 DSM
Entries 4451
QT8(0) sum - simulated
QT board

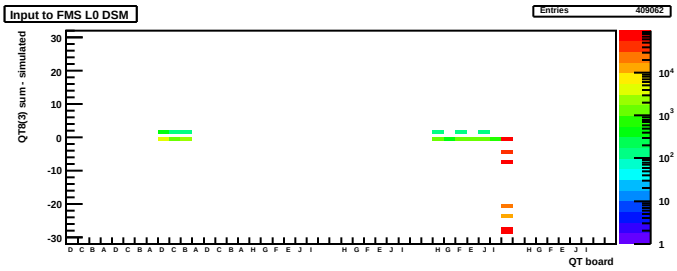

Input to FMS L0 DSM
Entries 2.036419e+07
QT8(1) sum
QT board

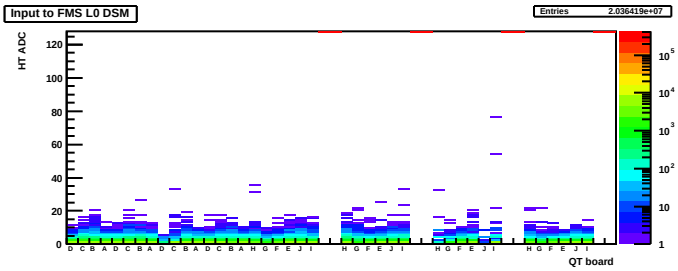

Input to FMS L0 DSM
Entries 8005
QT8(1) sum - simulated
QT board

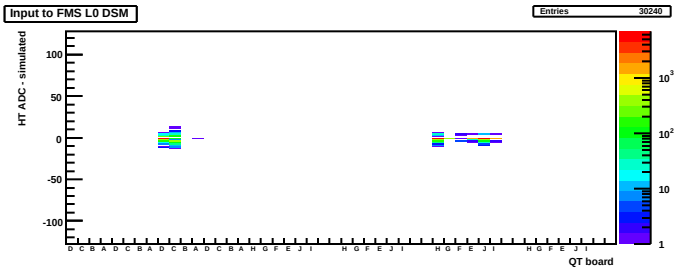

Input to FMS L0 DSM
Entries 2.036419e+07
QT8(2) sum
QT board

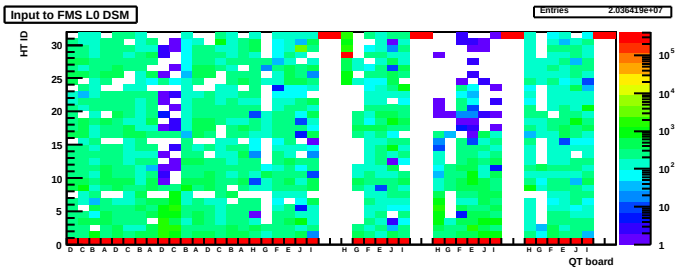

Input to FMS L0 DSM
Entries 304414
QT8(2) sum - simulated
QT board

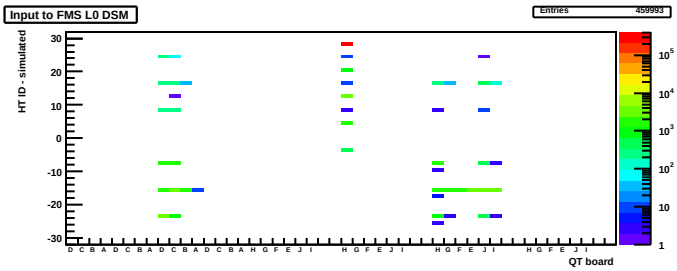

Input to FMS L0 DSM
Entries 2.036419e+07
QT8(3) sum
QT board

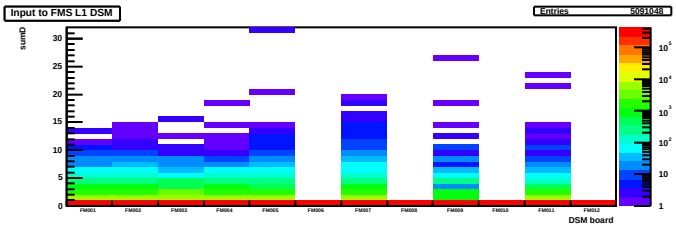

Input to FMS L0 DSM
Entries 409062
QT8(3) sum - simulated
QT board

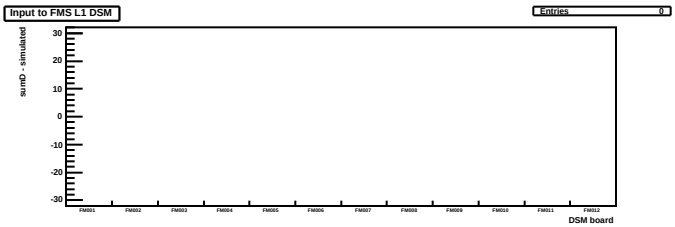

Input to FMS L0 DSM
Entries 2.036419e+07
HT ADC
QT board

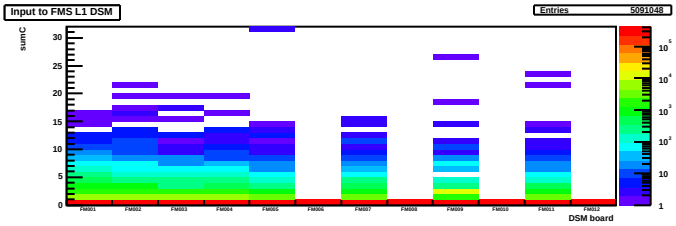

Input to FMS L0 DSM
Entries 30240
HT ADC - simulated
QT board

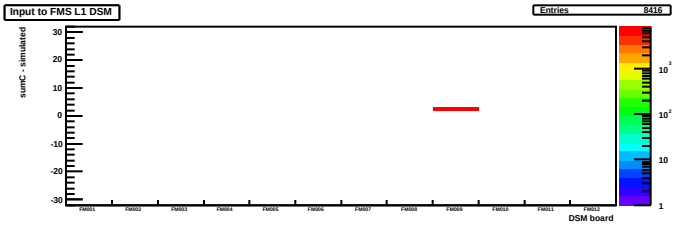

Input to FMS L0 DSM
Entries 2.036419e+07
HT ID
QT board

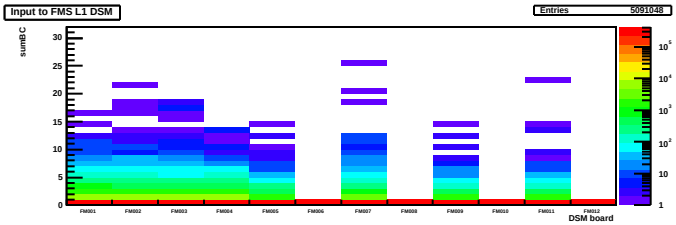

Input to FMS L0 DSM
Entries 459993
HT ID - simulated
QT board

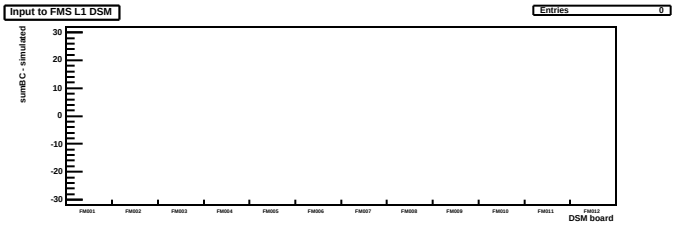

Input to FMS L1 DSM
Entries 5091048
sumD
DSM board

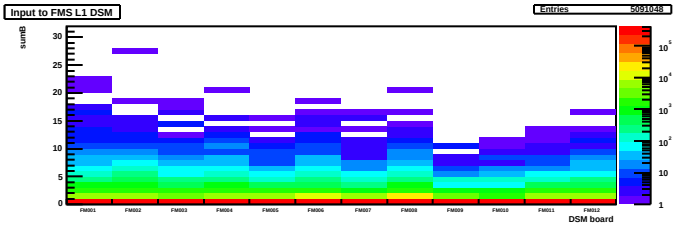

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

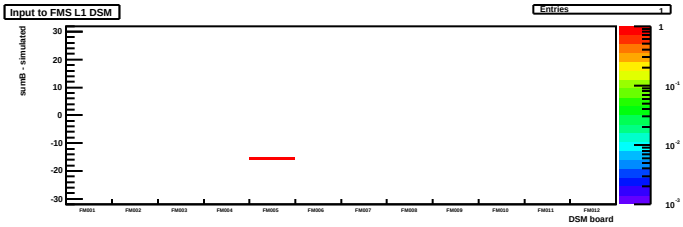

Input to FMS L1 DSM
Entries 5091048
sumC
DSM board

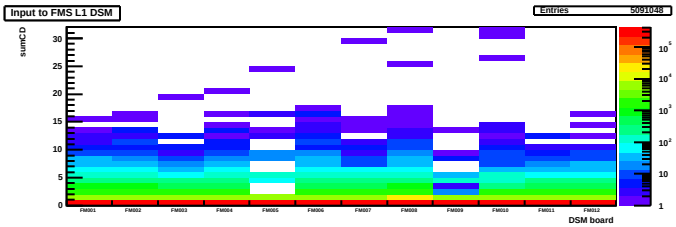

Input to FMS L1 DSM
Entries 8416
sumC - simulated
DSM board

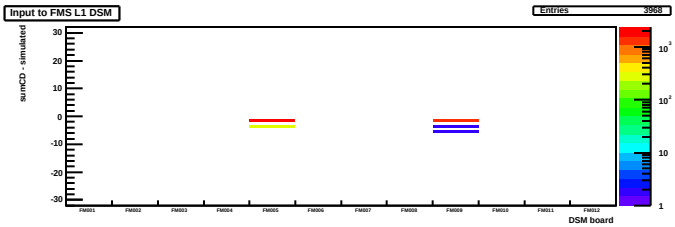

Input to FMS L1 DSM
Entries 5091048
sumBC
DSM board

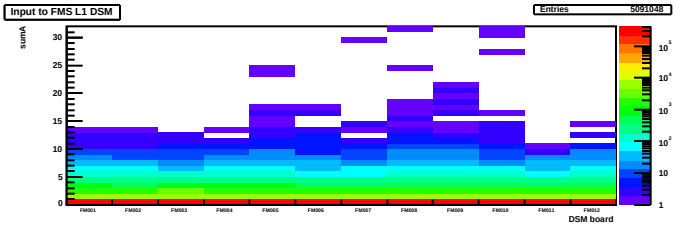

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

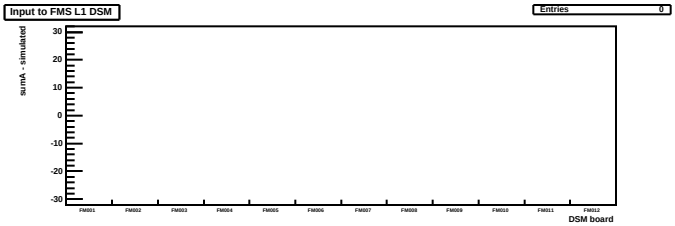

Input to FMS L1 DSM
Entries 5091048
sumB
DSM board

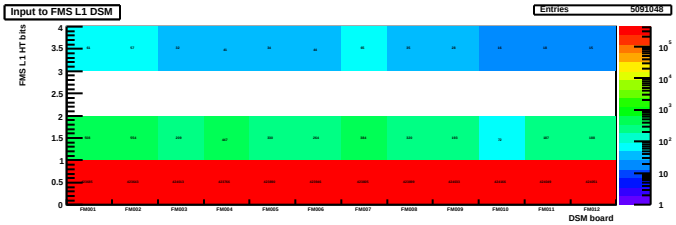

Input to FMS L1 DSM
Entries 1
sumB - simulated
DSM board

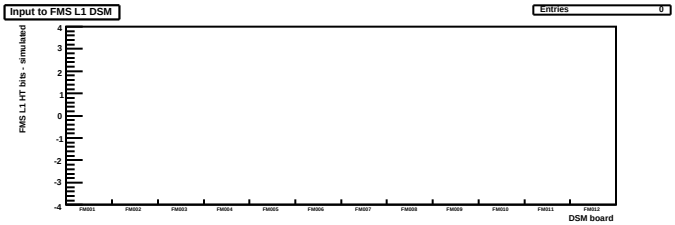

Input to FMS L1 DSM
Entries 5091048
sumCD
DSM board

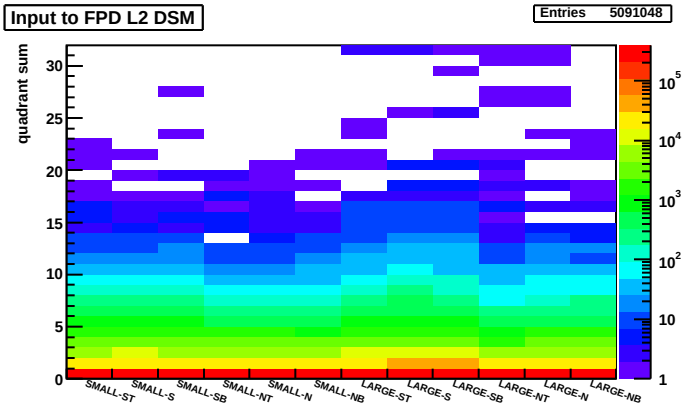

Input to FMS L1 DSM
Entries 3968
sumCD - simulated
DSM board

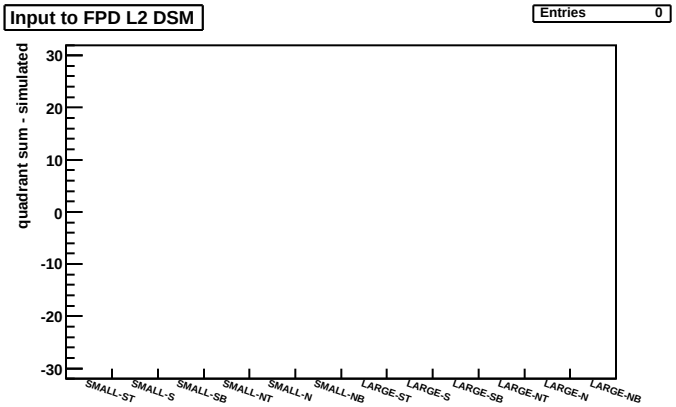

Input to FMS L1 DSM
Entries 5091048
sumA
DSM board

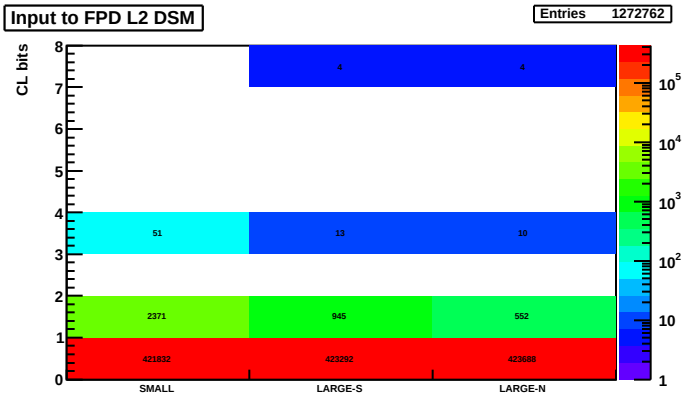

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

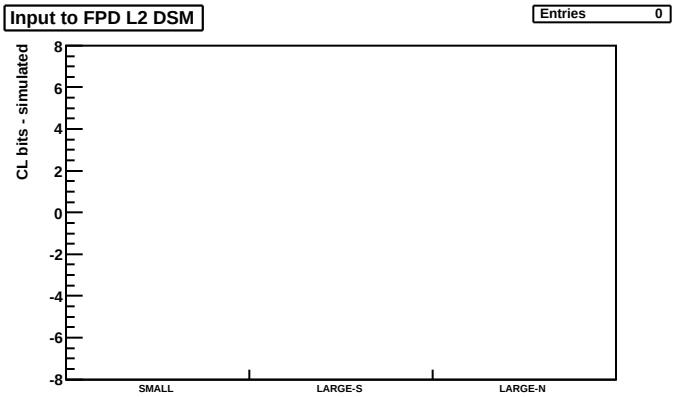

Input to FMS L1 DSM
Entries 5091048
FMS L1 HT bits
DSM board

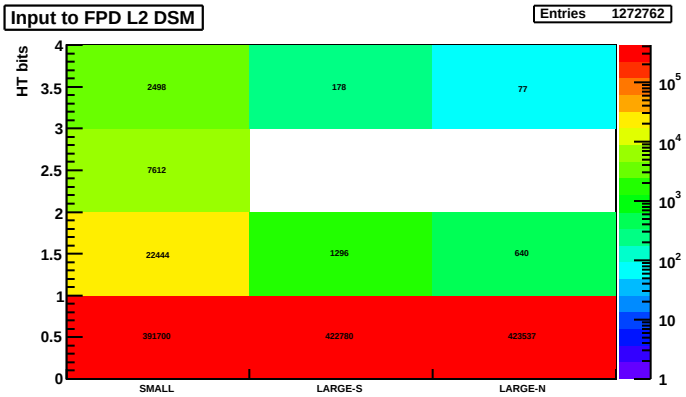

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

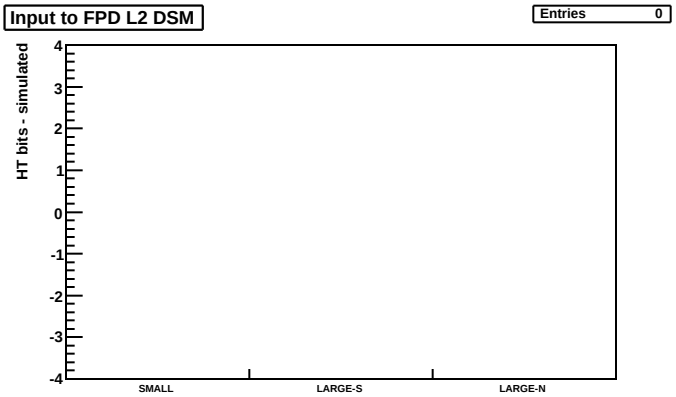

Input to FPD L2 DSM
Entries 5091048
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

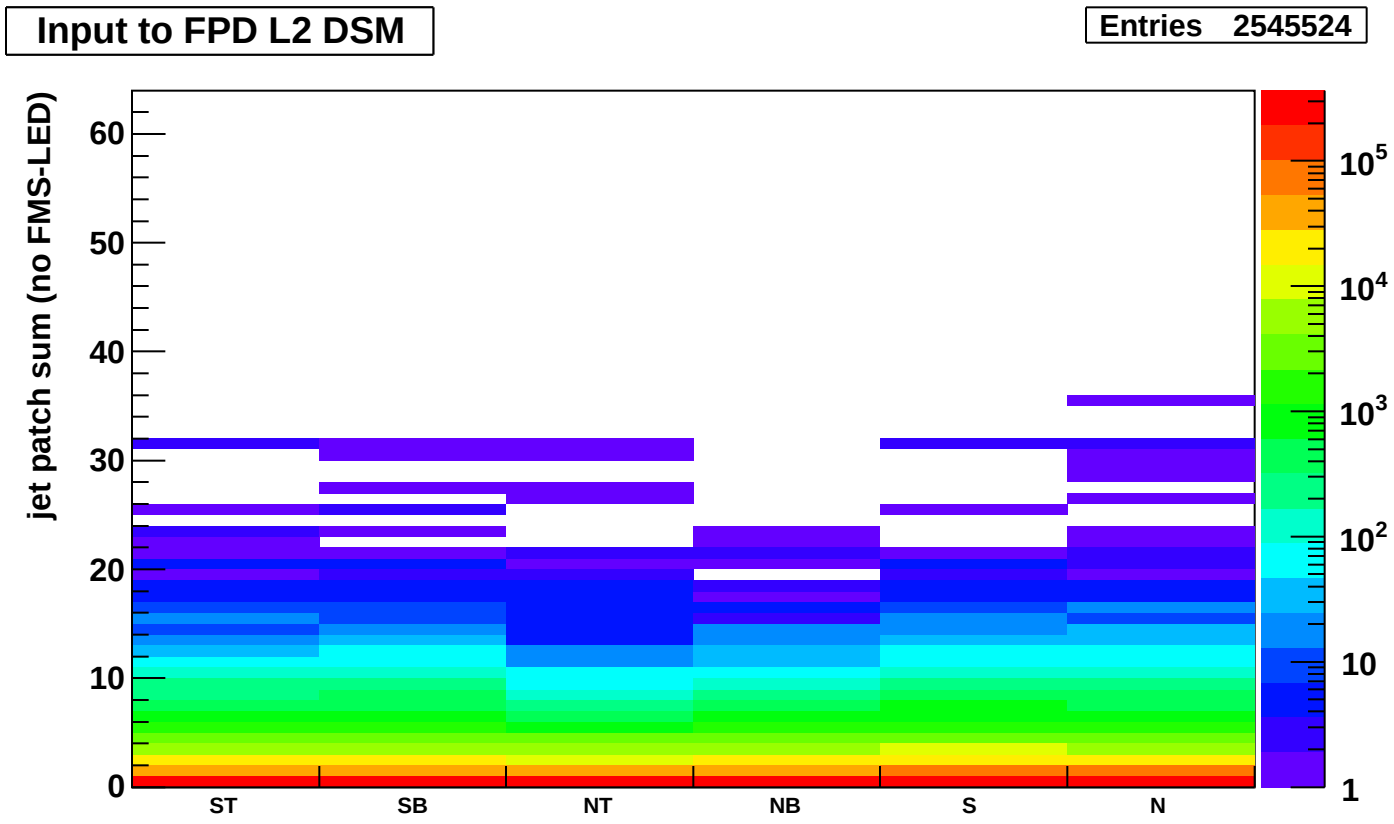

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

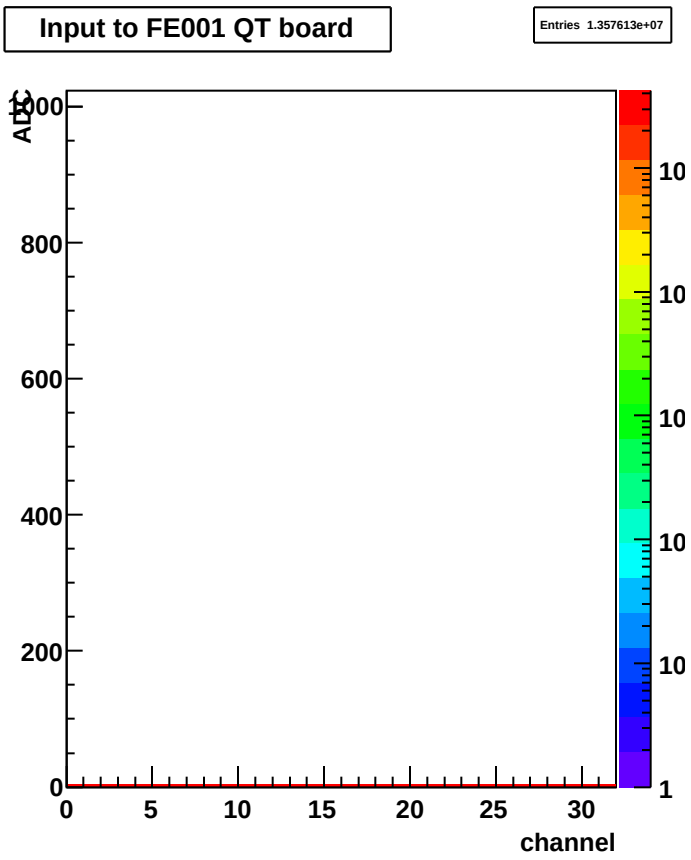

Input to FPD L2 DSM
Entries 1272762
CL bits
4
4
51
13
10
2371
945
552
421832
423292
423688
SMALL
LARGE-S
LARGE-N

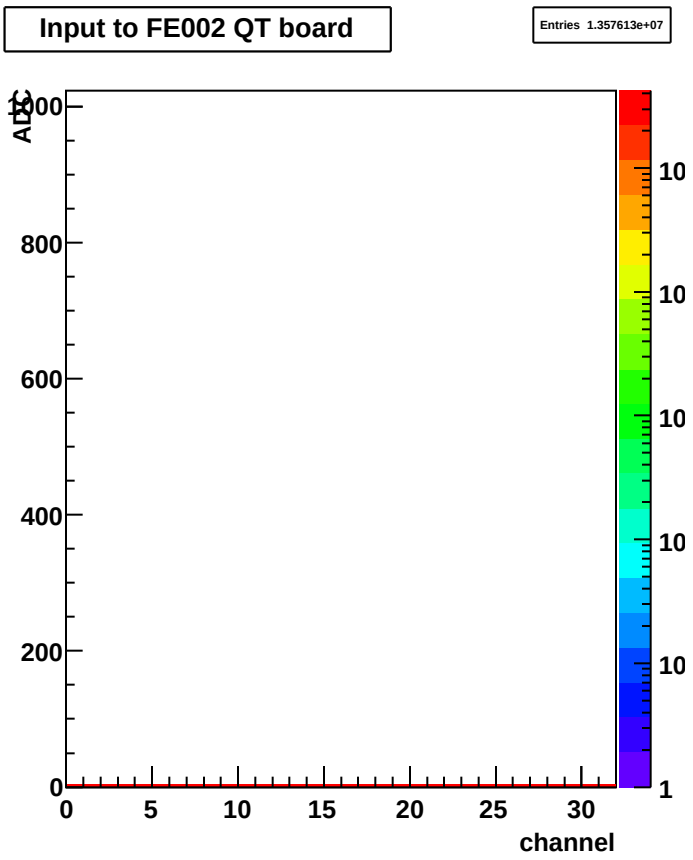

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

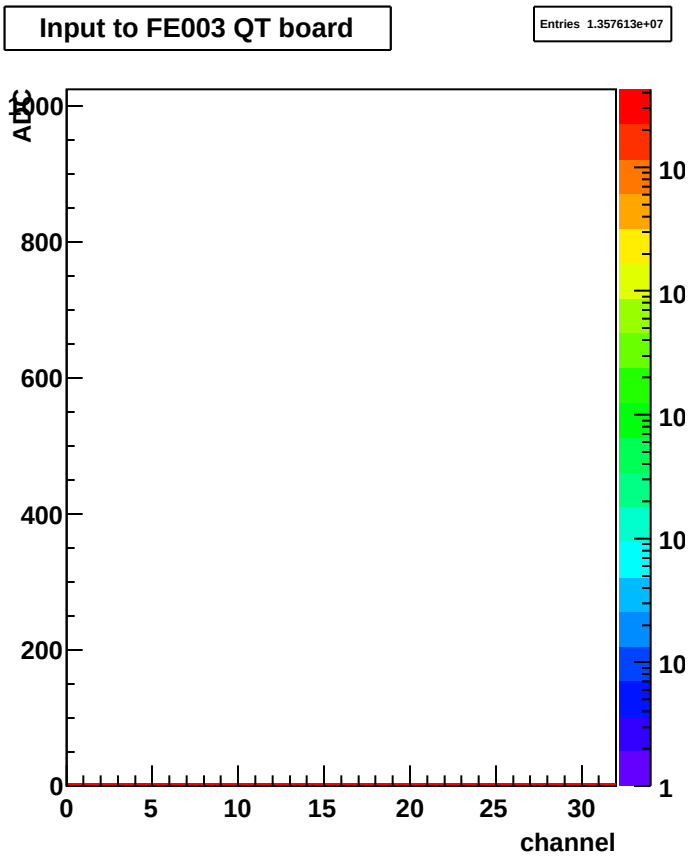

Input to FPD L2 DSM
Entries 1272762
HT bits
2498
178
77
7612
22444
1296
640
391700
422780
423537
SMALL
LARGE-S
LARGE-N

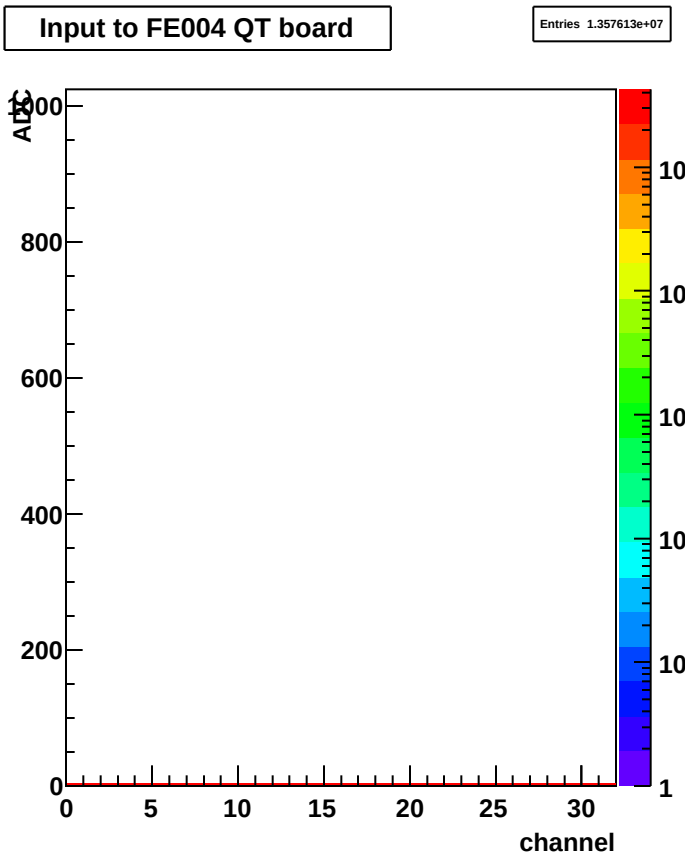

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

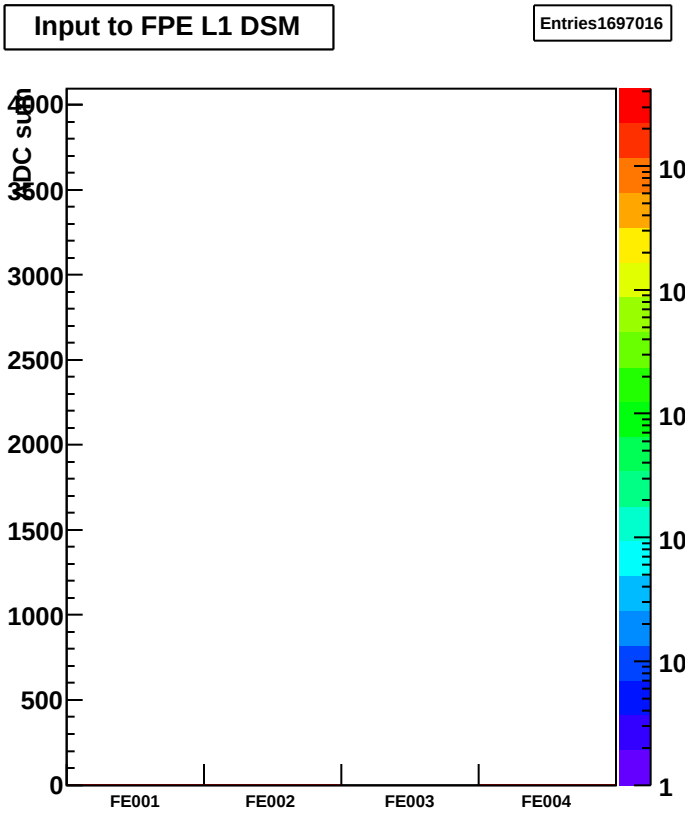

Input to FPD L2 DSM
Entries 2545524
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

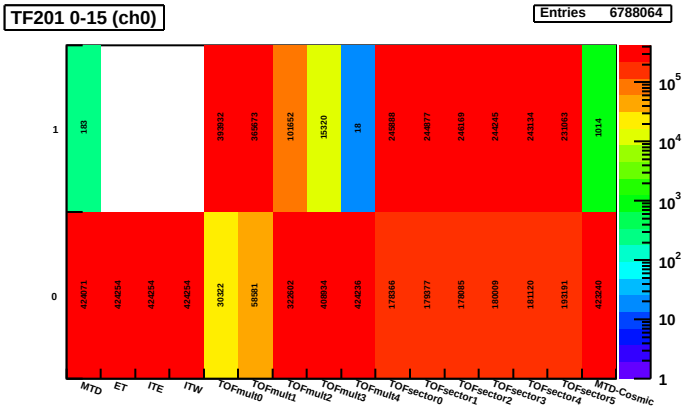

Input to FE001 QT board
Entries 1.357613e+07
ADC
channel

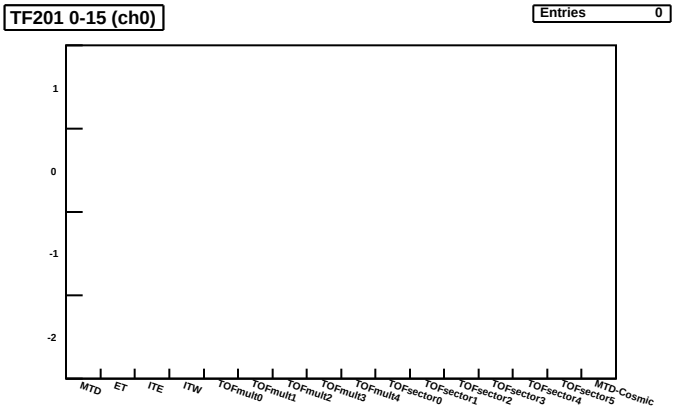

Input to FE002 QT board
Entries 1.357613e+07
ADC
channel

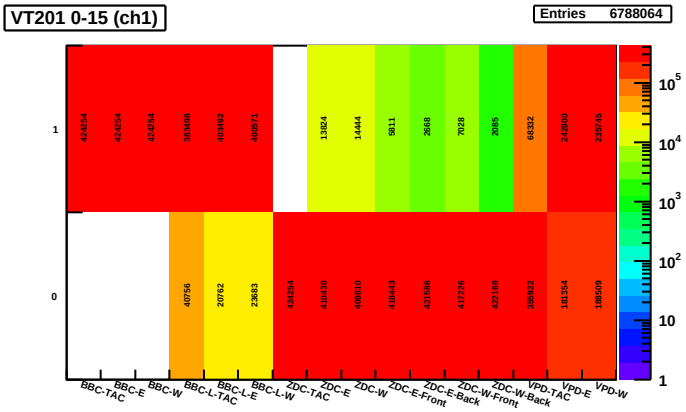

Input to FE003 QT board
Entries 1.357613e+07
ADC
channel

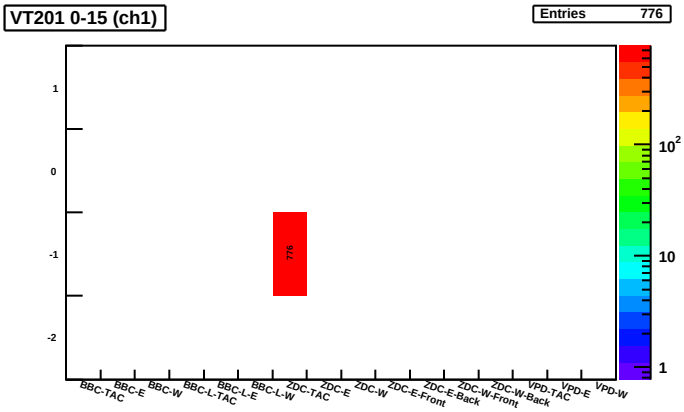

Input to FE004 QT board
Entries 1.357613e+07
ADC
channel

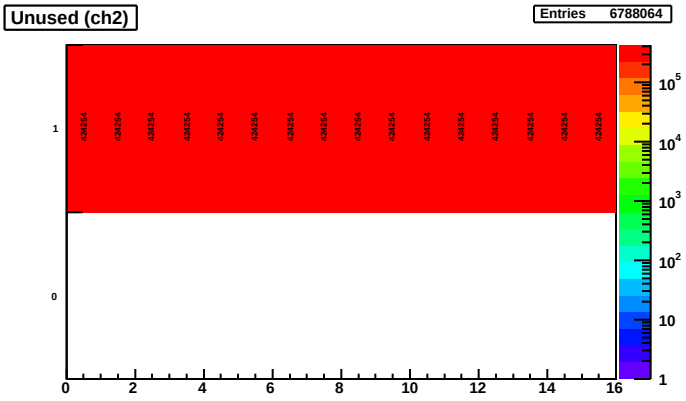

Input to FPE L1 DSM
Entries1697016
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

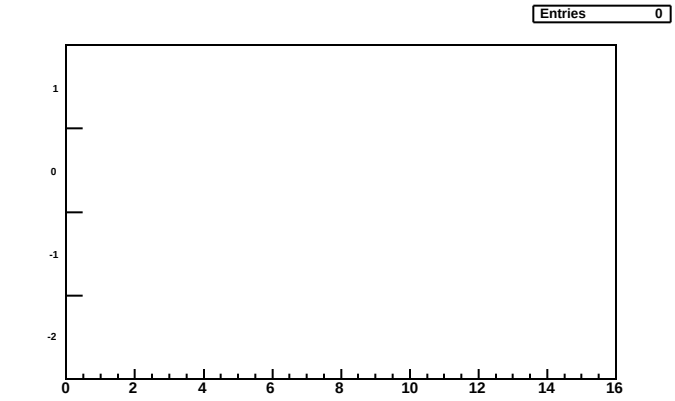

TF201 0-15 (ch0)
Entries 6788064
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

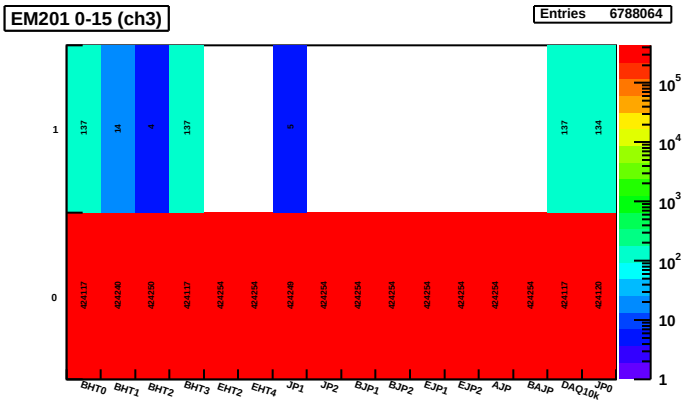

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

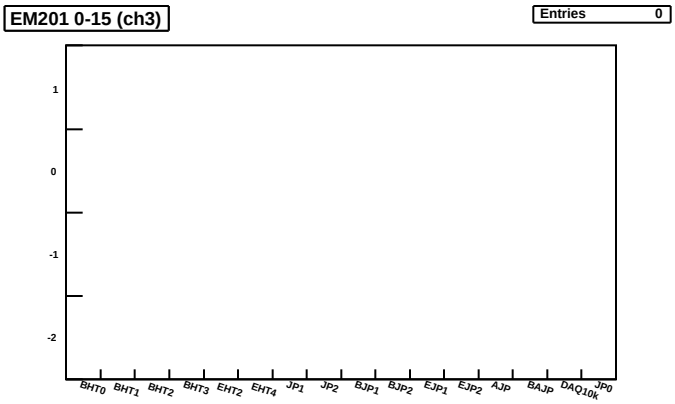

VT201 0-15 (ch1)
Entries 6788064
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 776
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

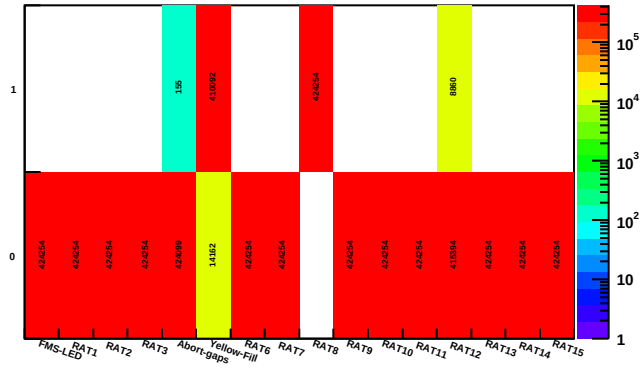

Unused (ch2)
Entries 6788064

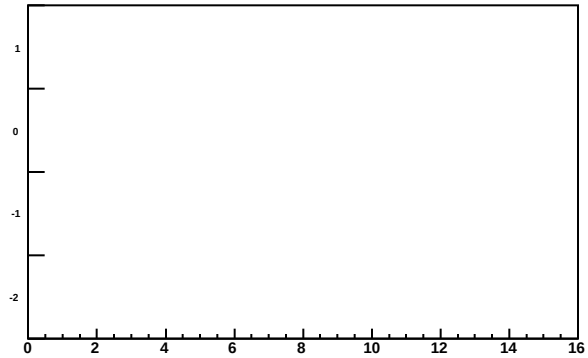

Entries 0


EM201 0-15 (ch3)
Entries 6788064
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

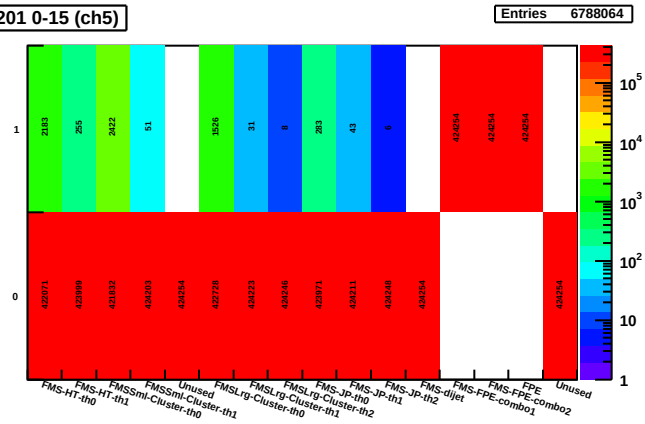

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

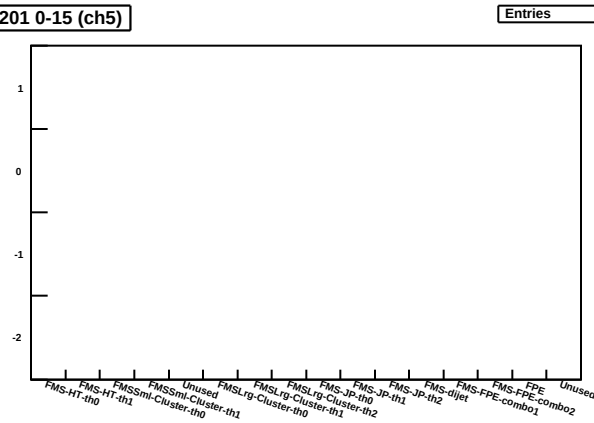




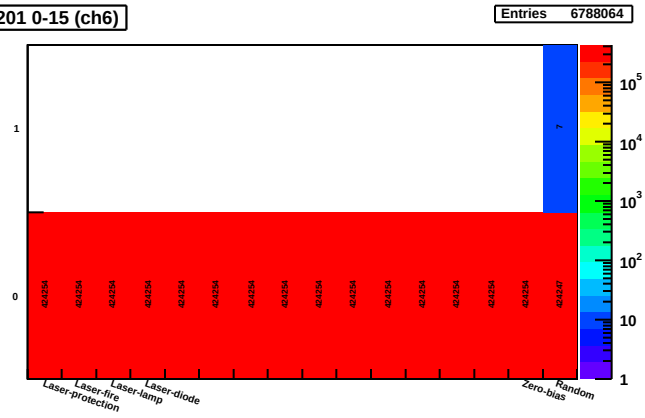
FP201 0-15 (ch5)

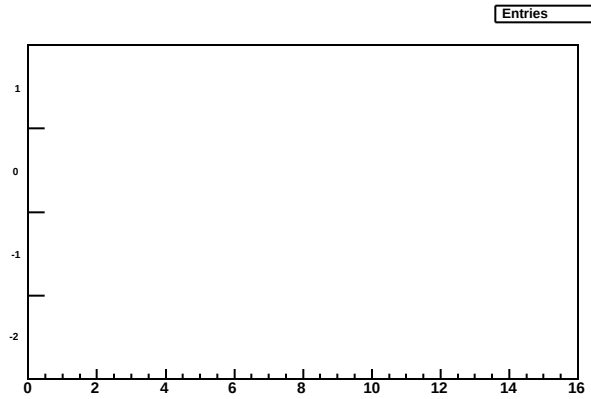
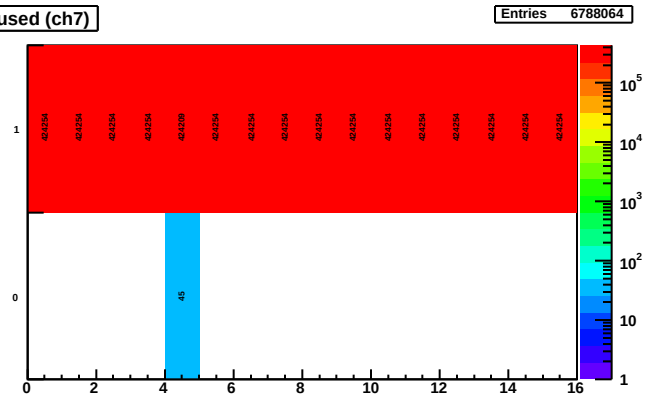
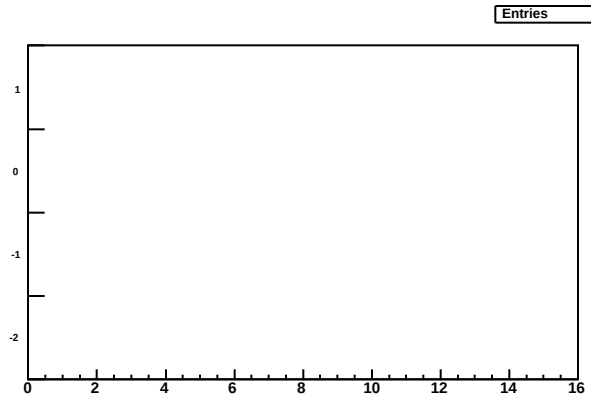


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)