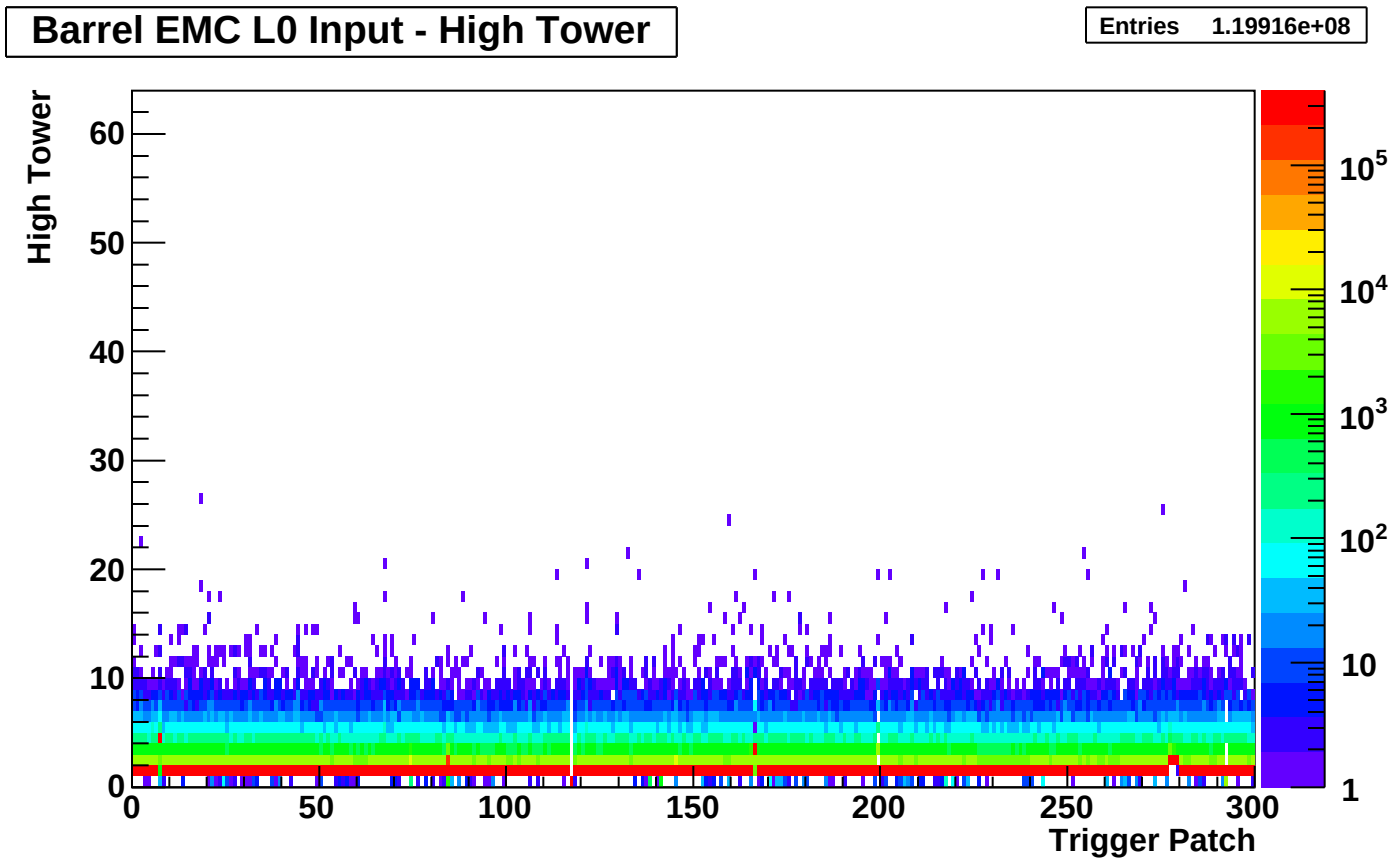
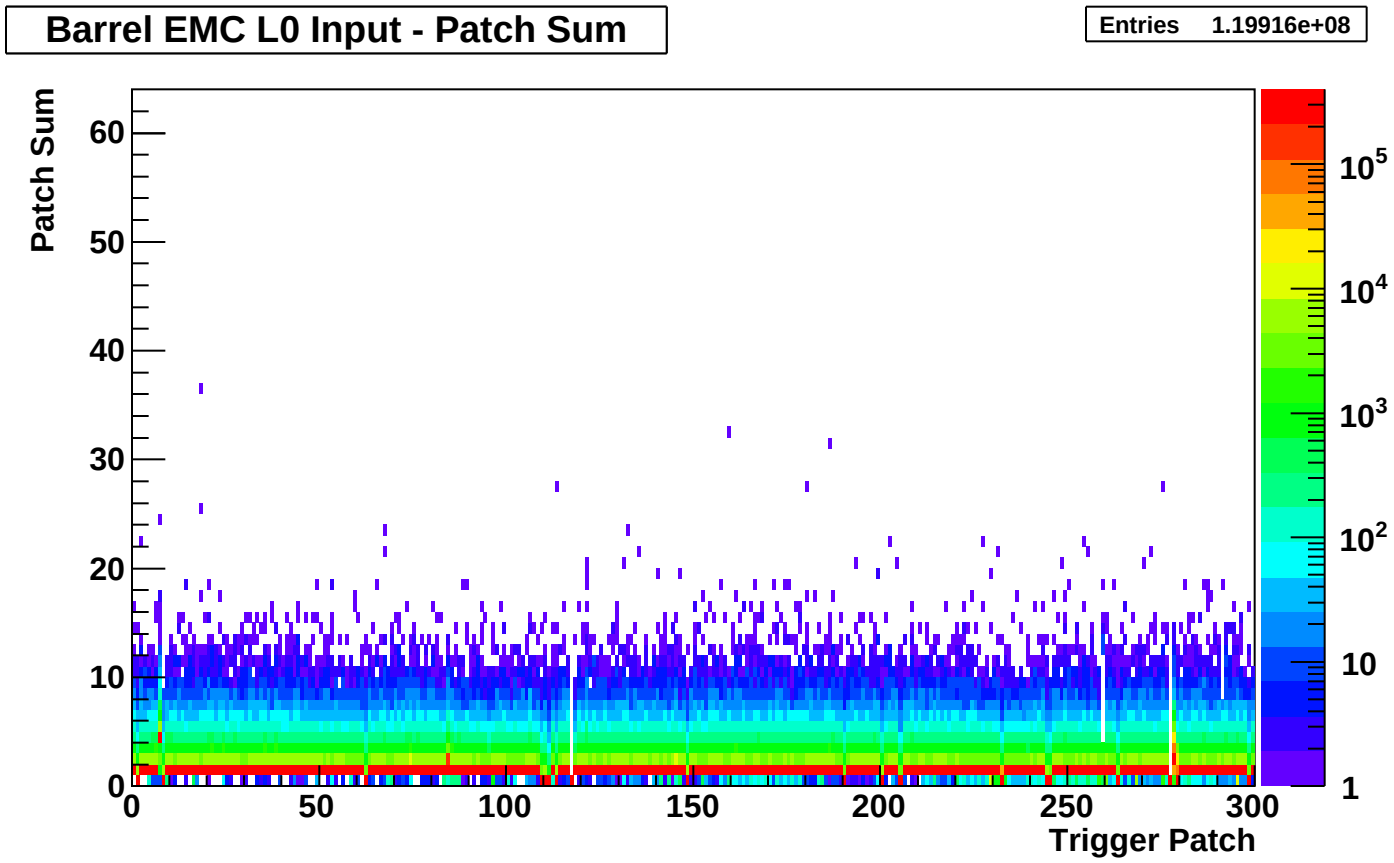
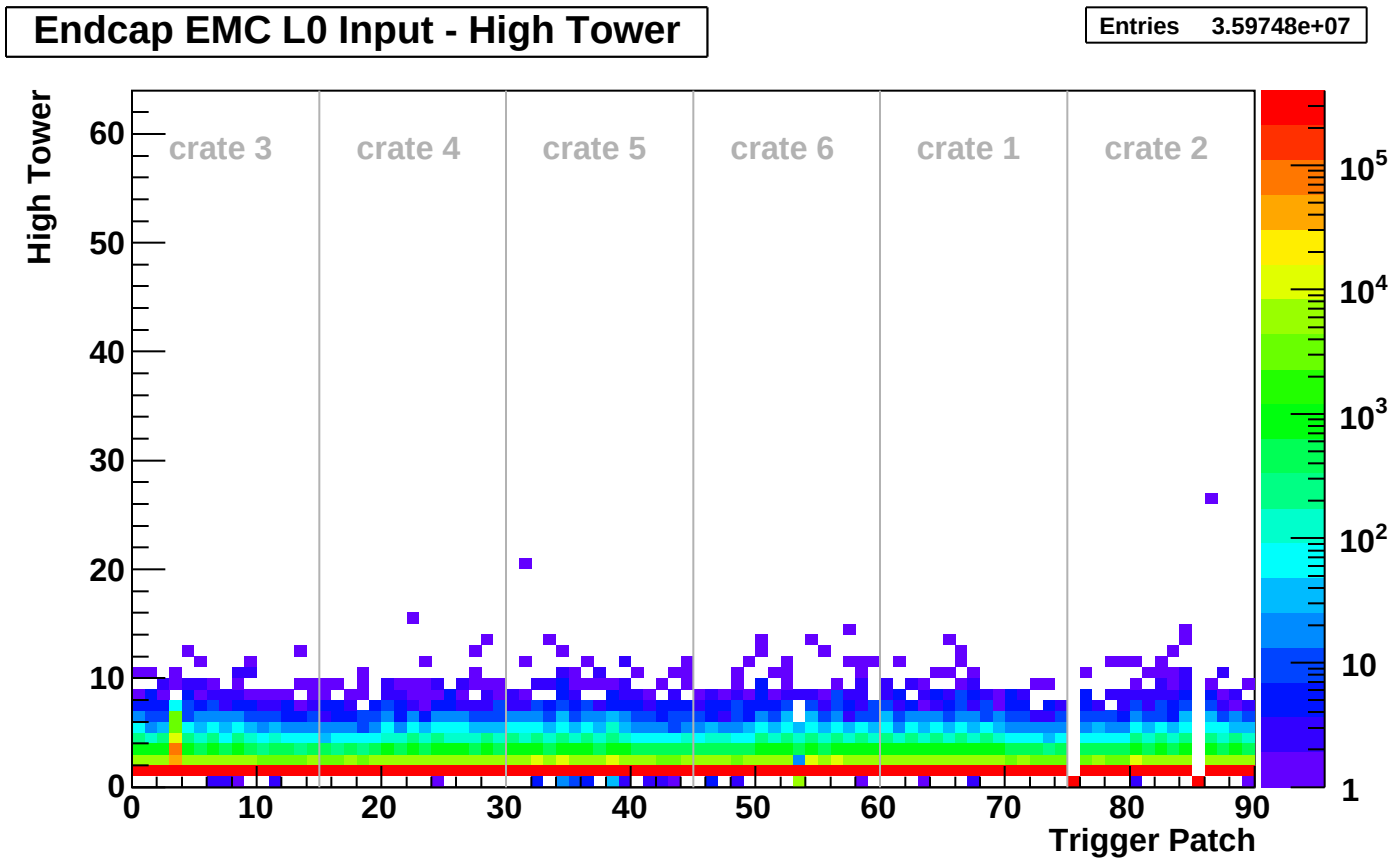
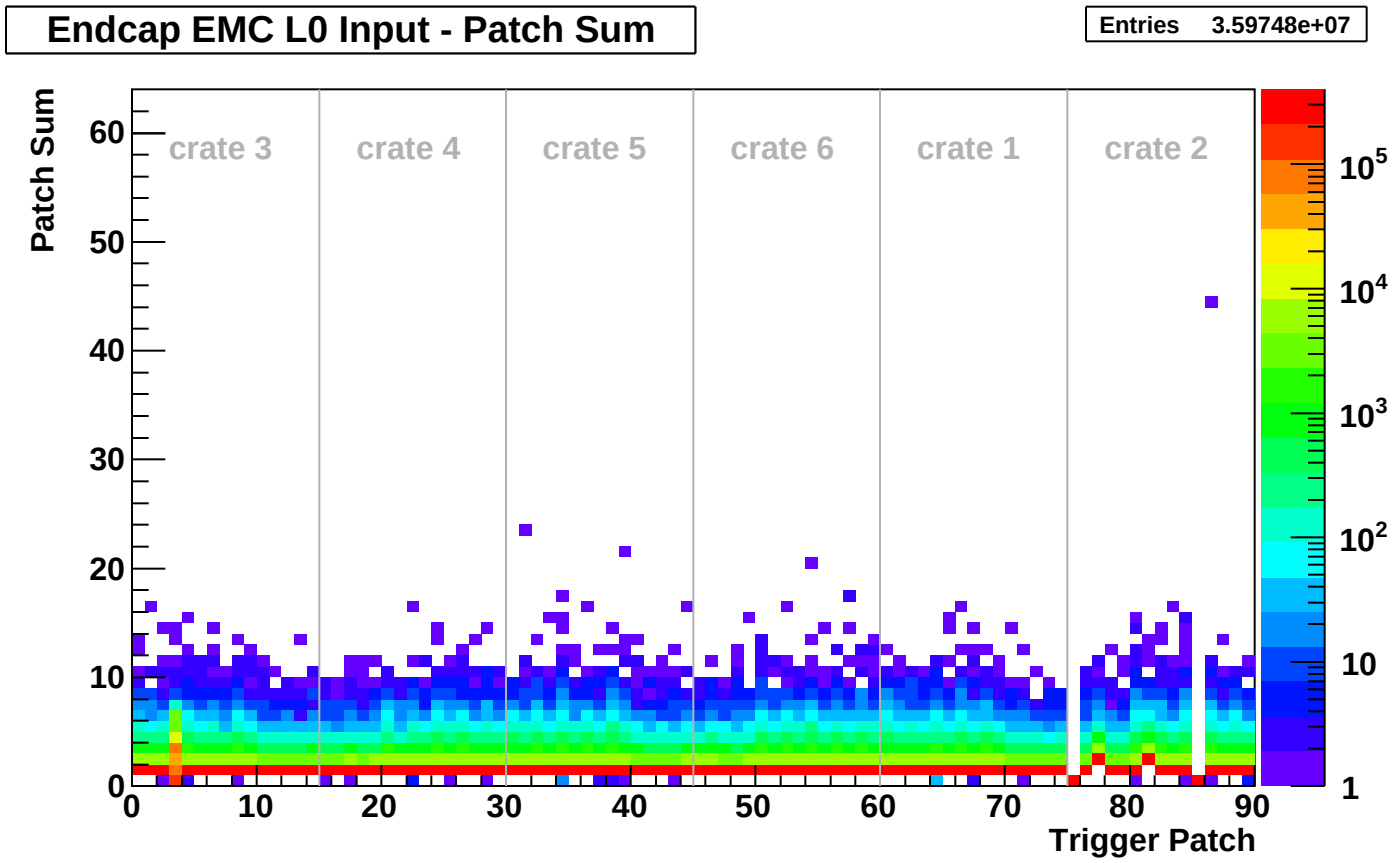
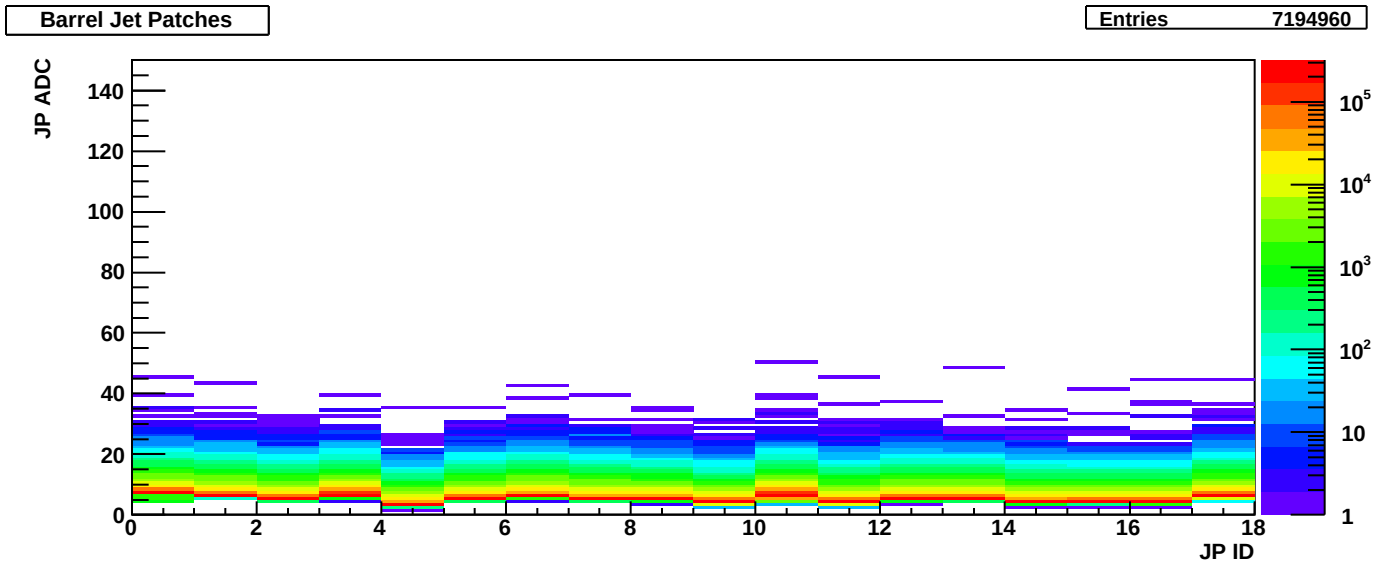
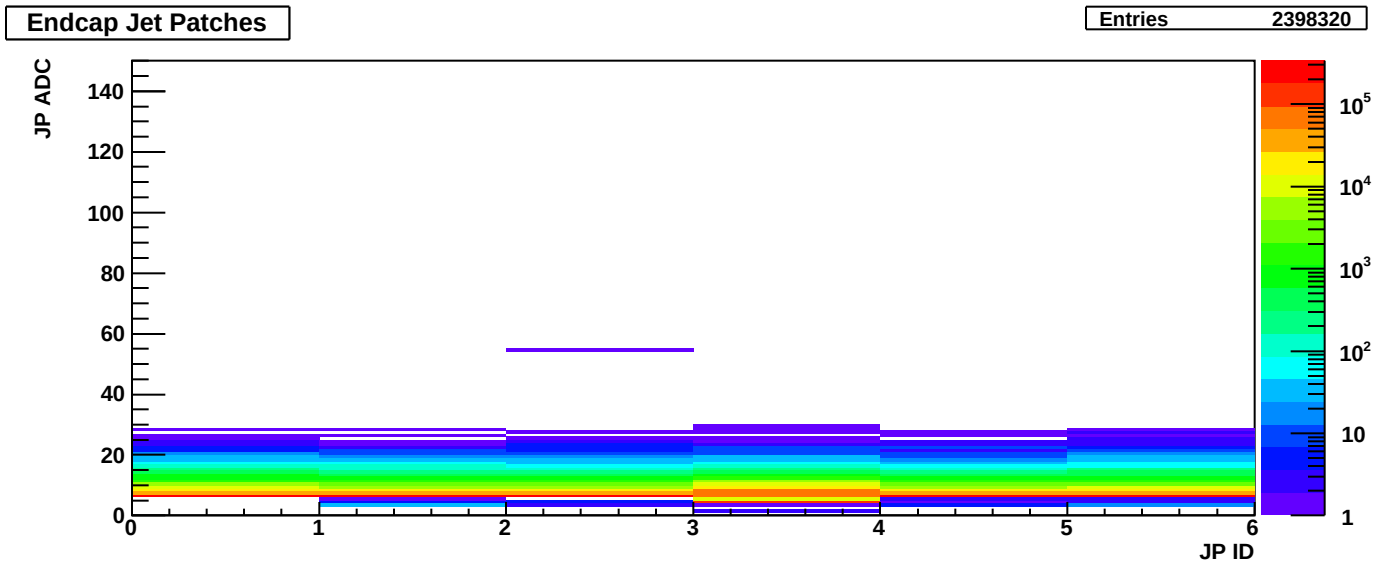

Barrel EMC L0 Input - High Tower
Entries 1.19916e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

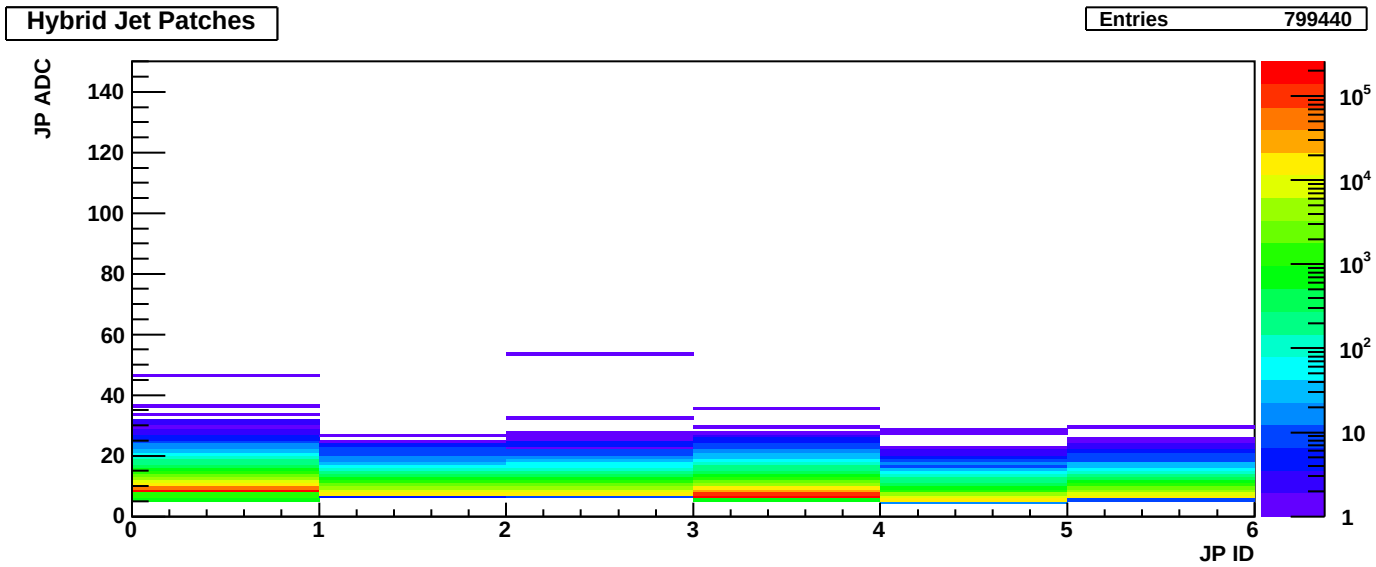

Barrel EMC L0 Input - Patch Sum
Entries 1.19916e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

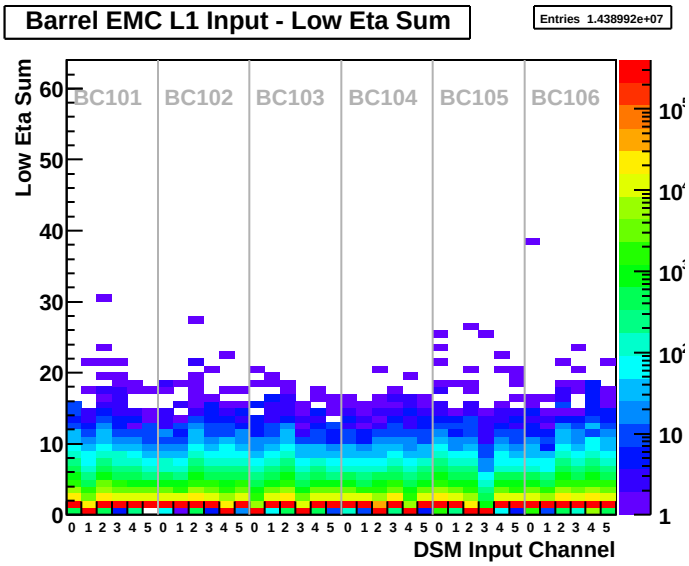

Endcap EMC L0 Input - High Tower
Entries 3.59748e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

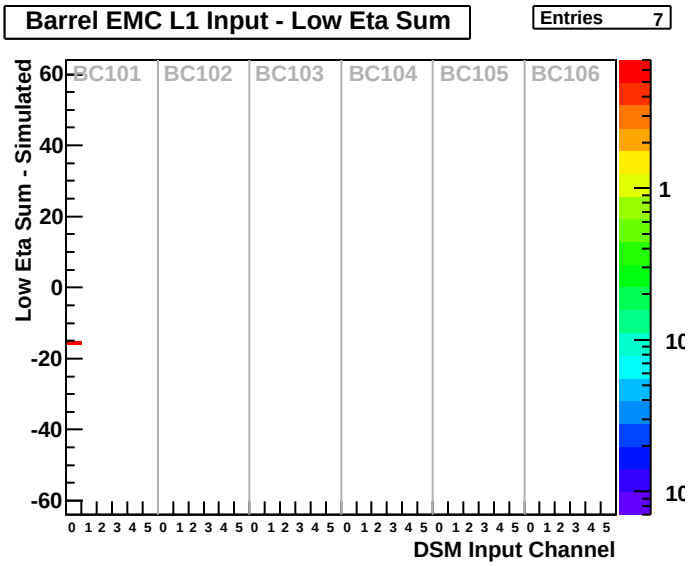

Endcap EMC L0 Input - Patch Sum
Entries 3.59748e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

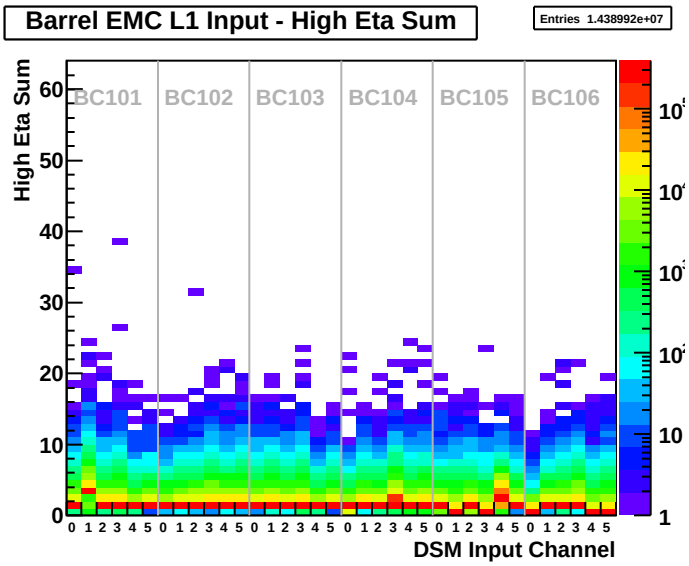

Barrel Jet Patches
Entries 7194960
JP ADC
JP ID

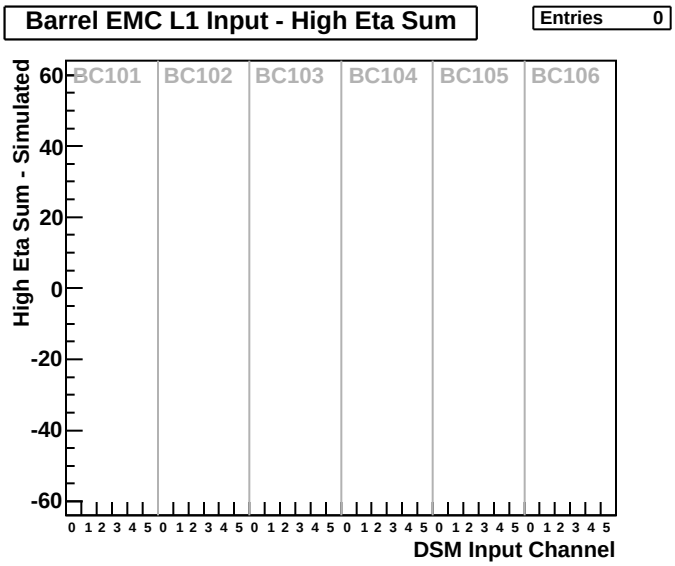

Endcap Jet Patches
Entries 2398320
JP ADC
JP ID

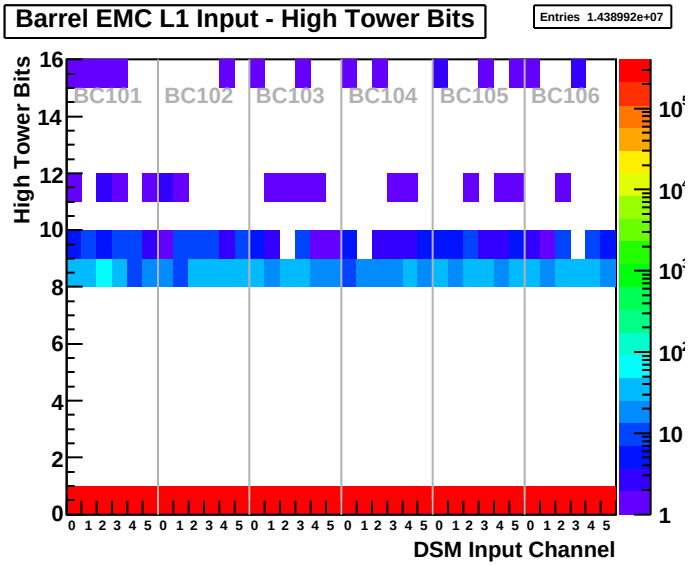

Hybrid Jet Patches
Entries 799440
JP ADC
JP ID

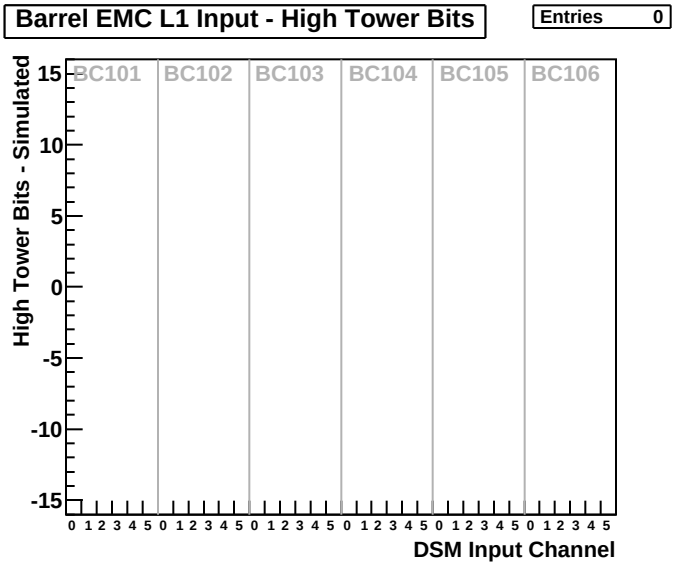

Barrel EMC L1 Input - Low Eta Sum
Entries 1.438992e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

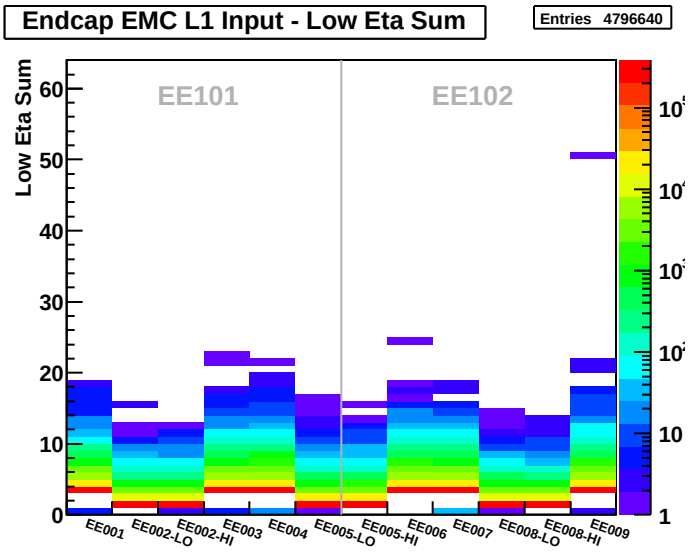

Barrel EMC L1 Input - Low Eta Sum
Entries 7
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

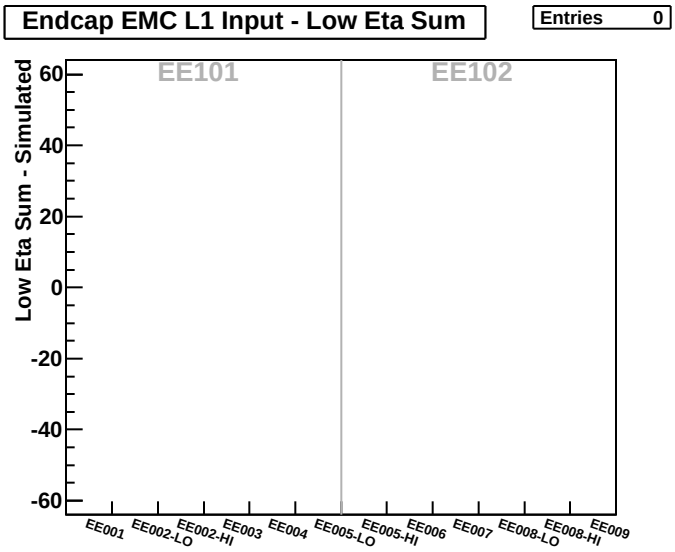

Barrel EMC L1 Input - High Eta Sum
Entries 1.438992e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

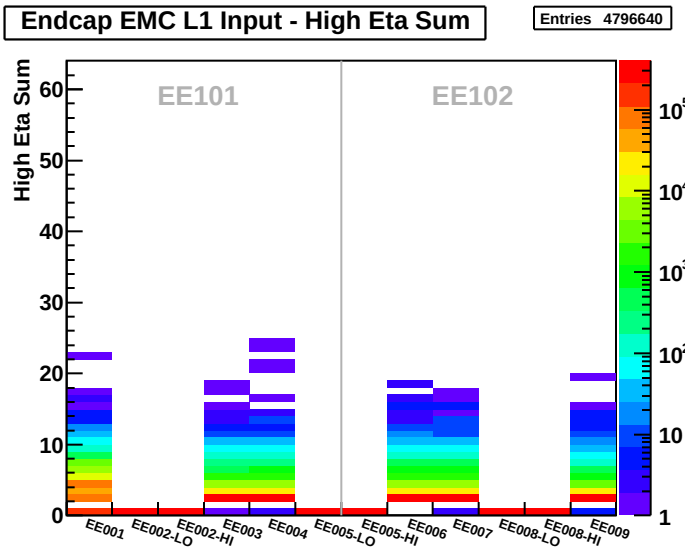

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

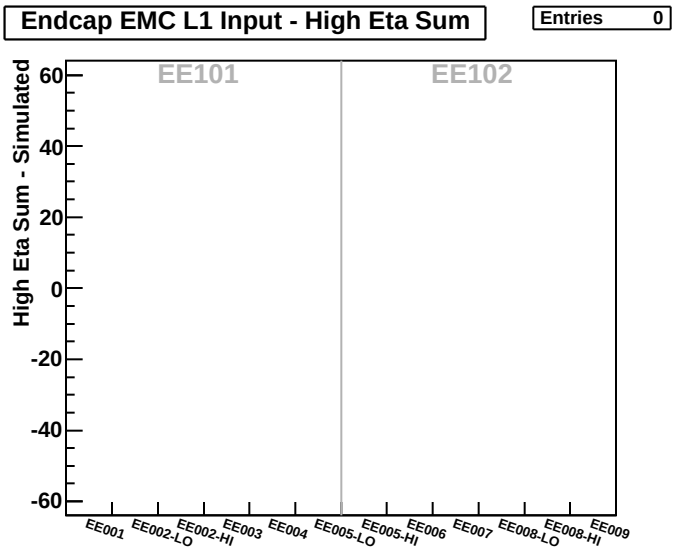

Barrel EMC L1 Input - High Tower Bits
Entries 1.438992e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

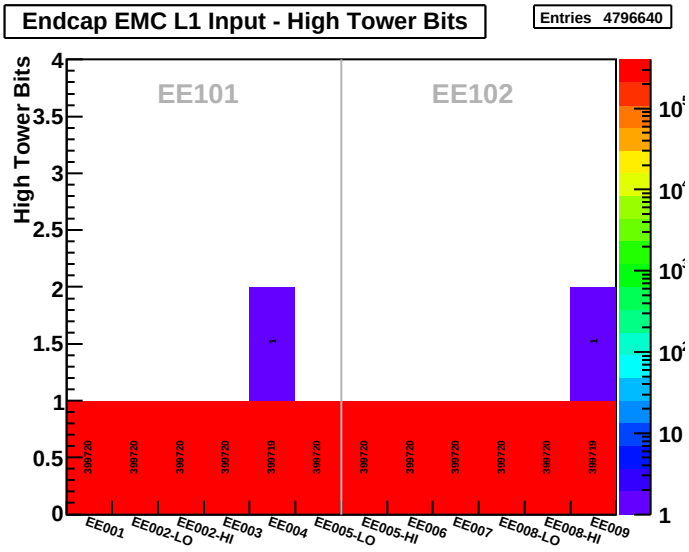

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

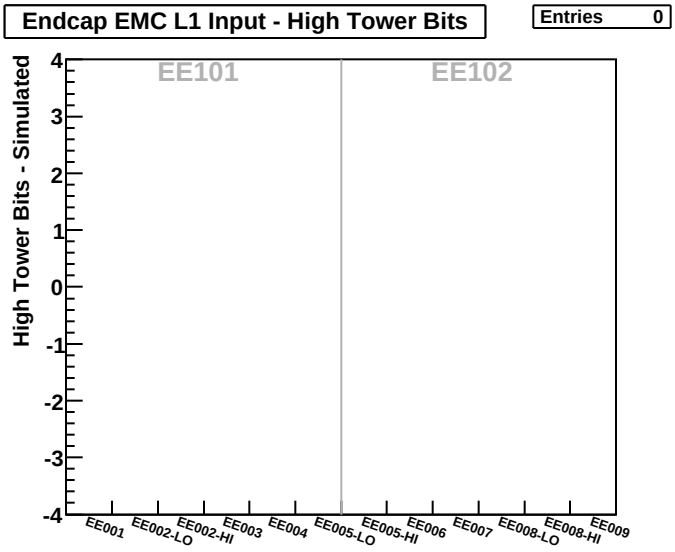

Endcap EMC L1 Input - Low Eta Sum
Entries 4796640
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

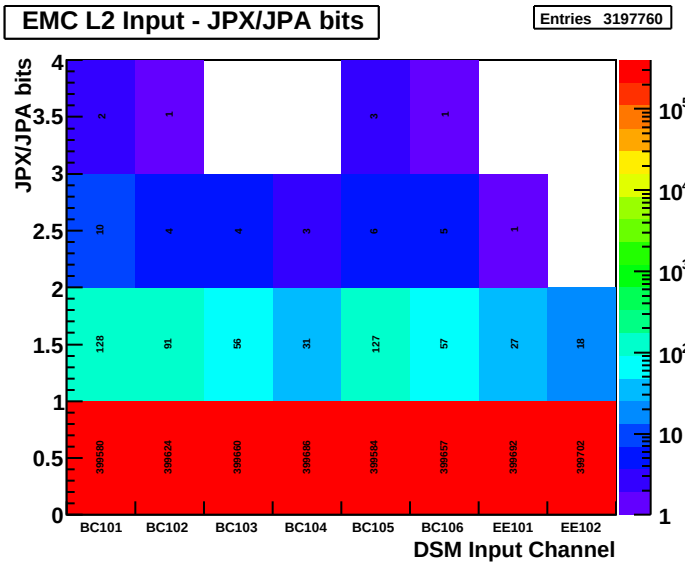

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

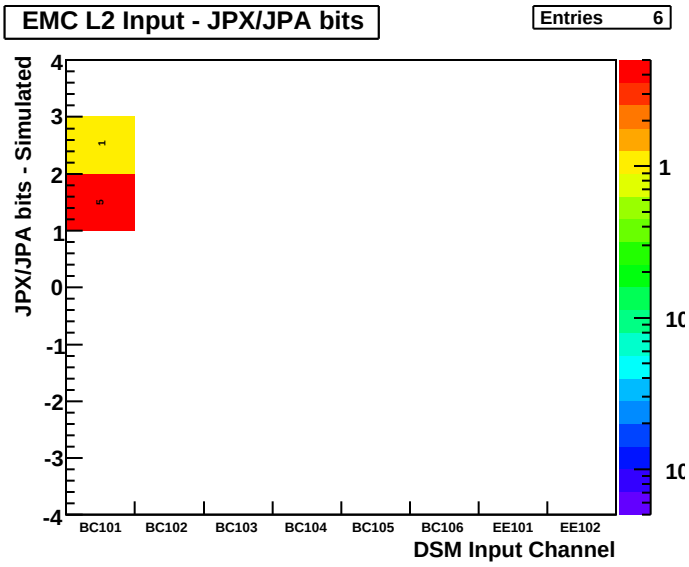

Endcap EMC L1 Input - High Eta Sum
Entries 4796640
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

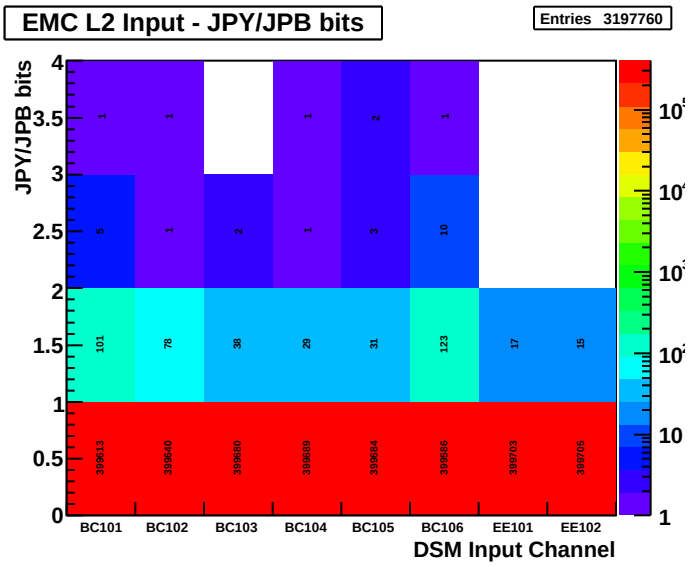

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

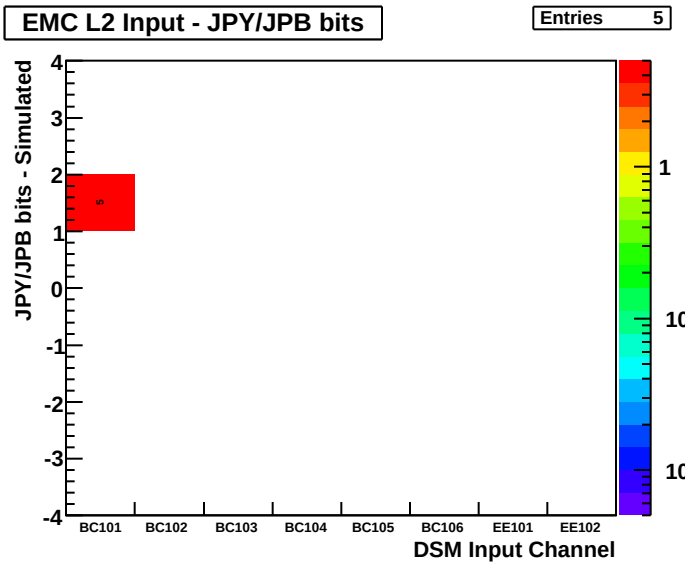

Endcap EMC L1 Input - High Tower Bits
Entries 4796640
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

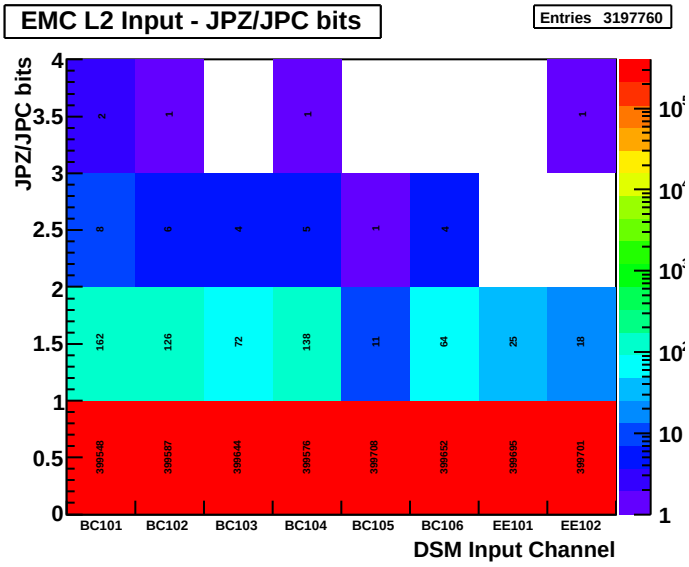

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

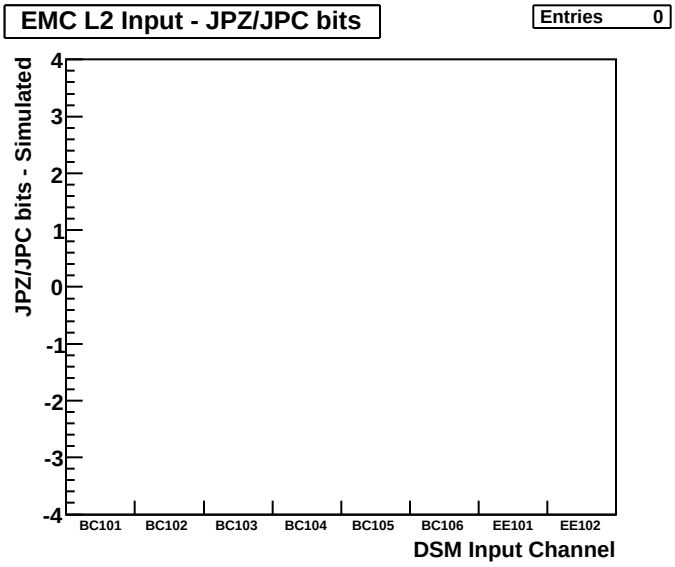

EMC L2 Input - JPX/JPA bits
Entries 3197760
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

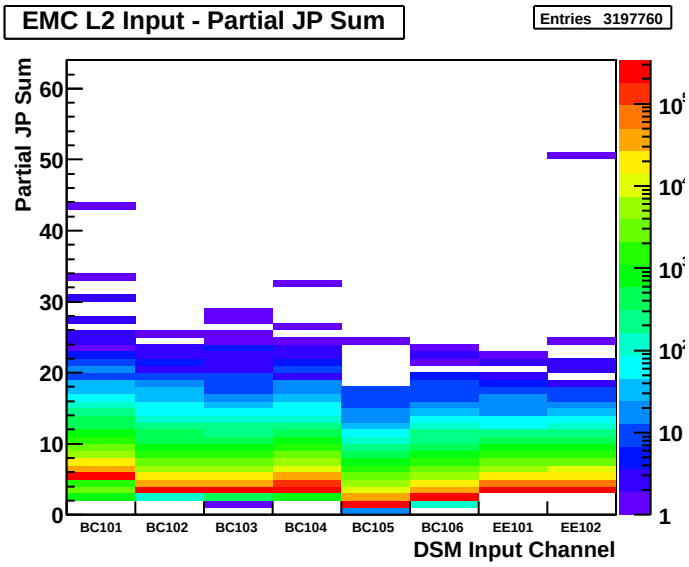

EMC L2 Input - JPX/JPA bits
Entries 6
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

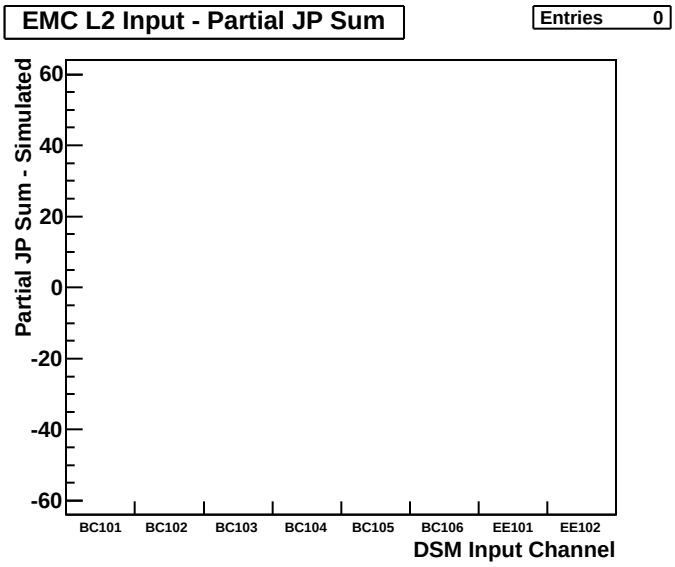

EMC L2 Input - JPY/JPB bits
Entries 3197760
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

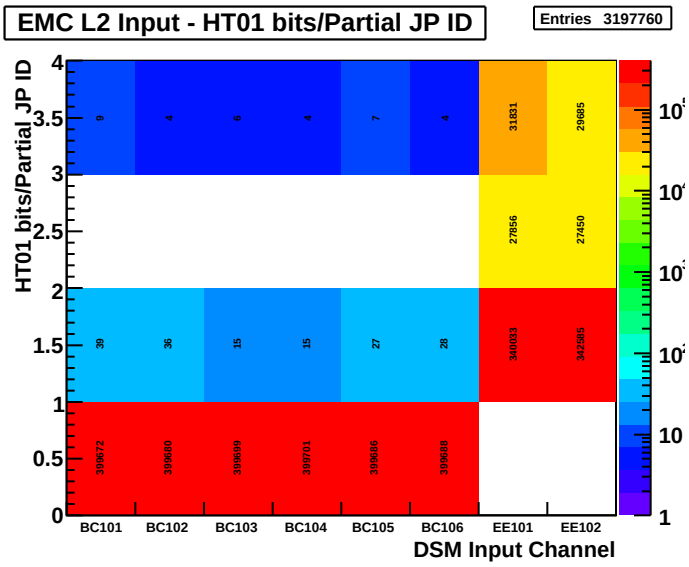

EMC L2 Input - JPY/JPB bits
Entries 5
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

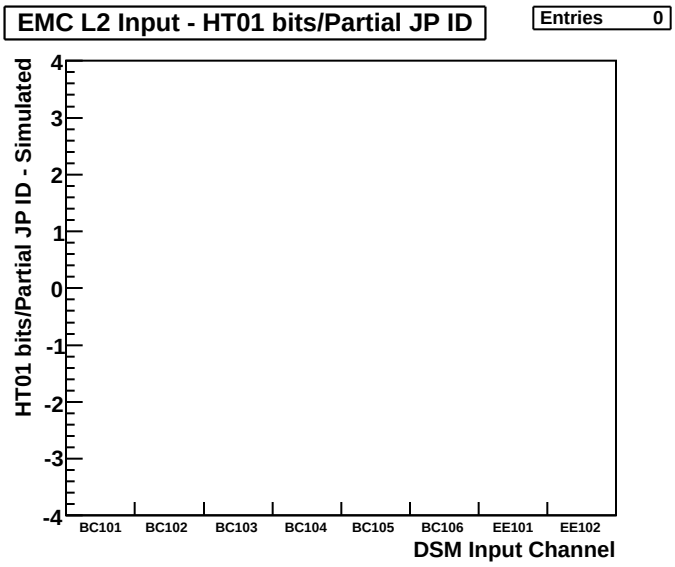

EMC L2 Input - JPZ/JPC bits
Entries 3197760
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

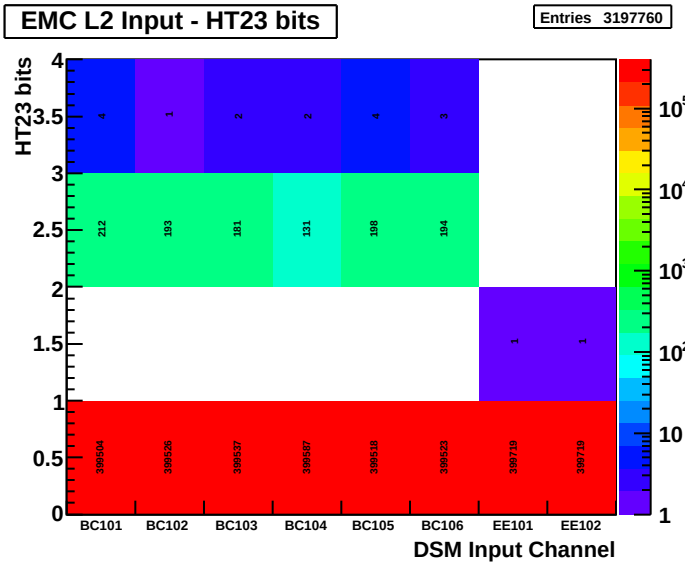

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

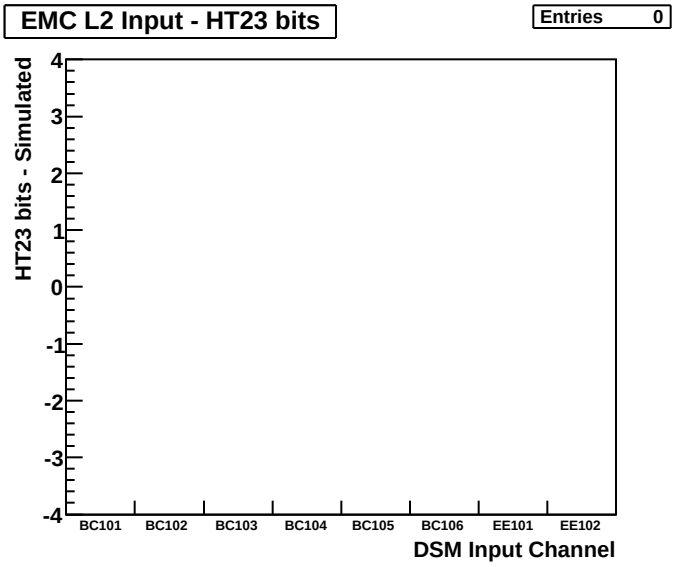

EMC L2 Input - Partial JP Sum
Entries 3197760
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

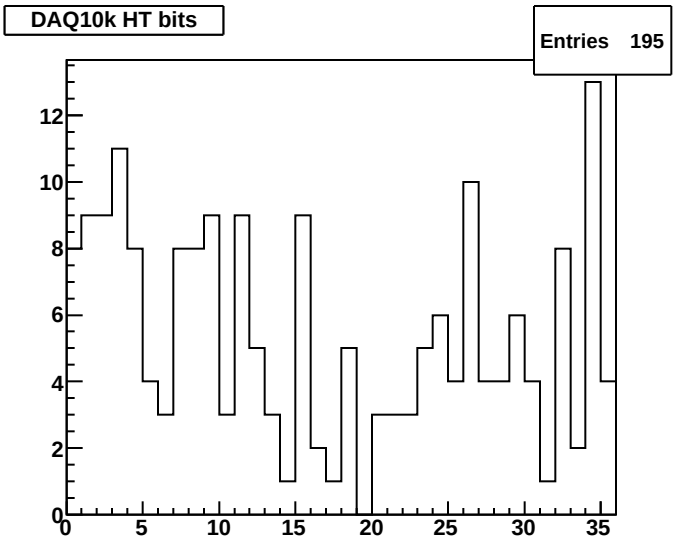

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

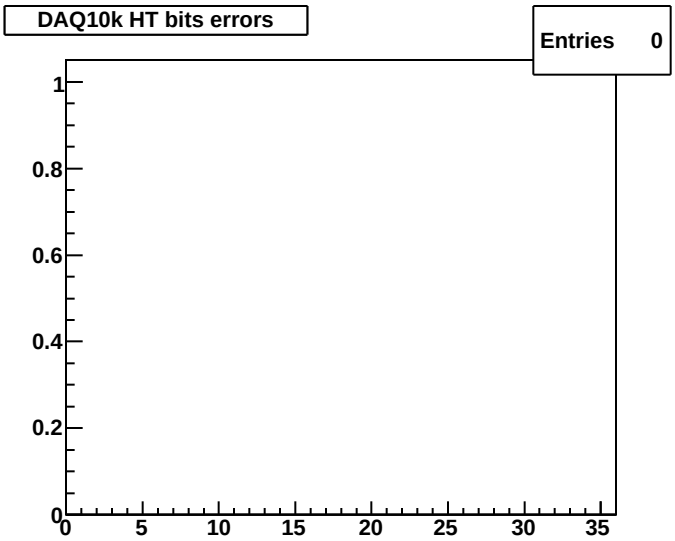

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3197760
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

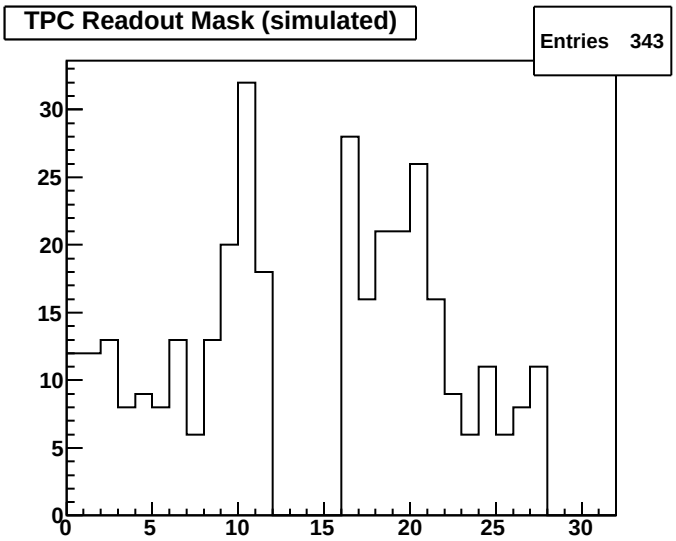

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

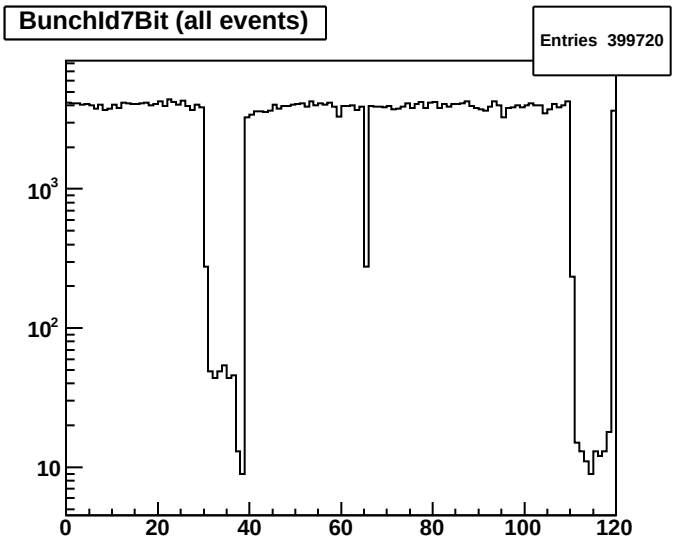

EMC L2 Input - HT23 bits
Entries 3197760
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

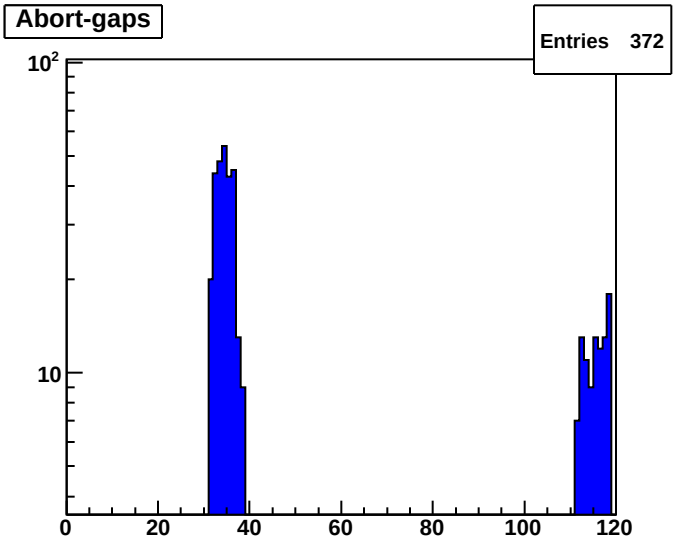

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

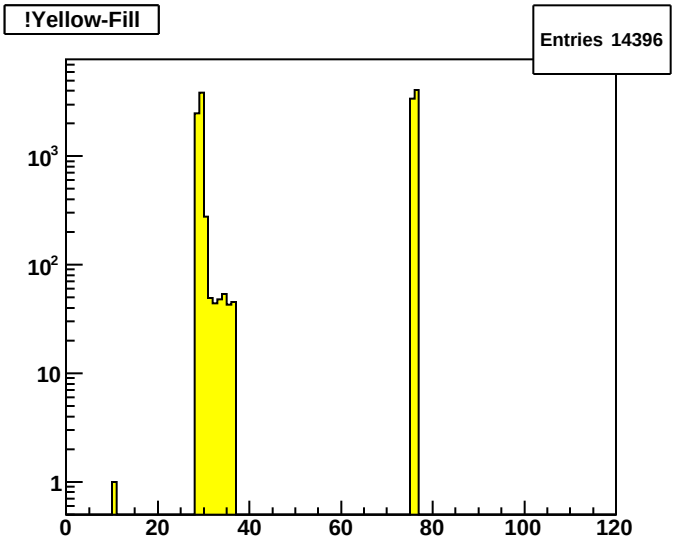

DAQ10k HT bits
Entries 195

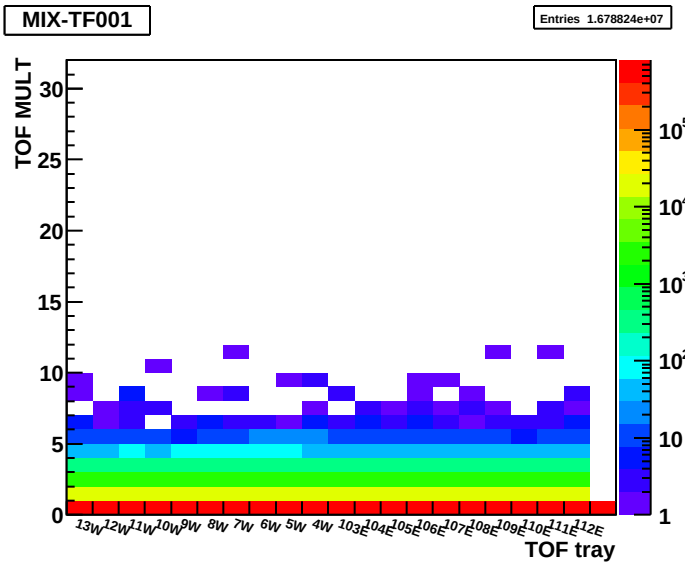

DAQ10k HT bits errors
Entries 0

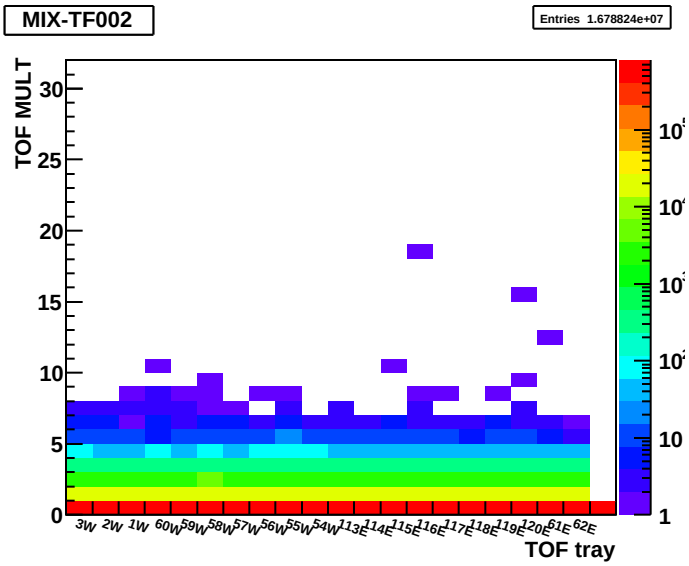

TPC Readout Mask (simulated)
Entries 343

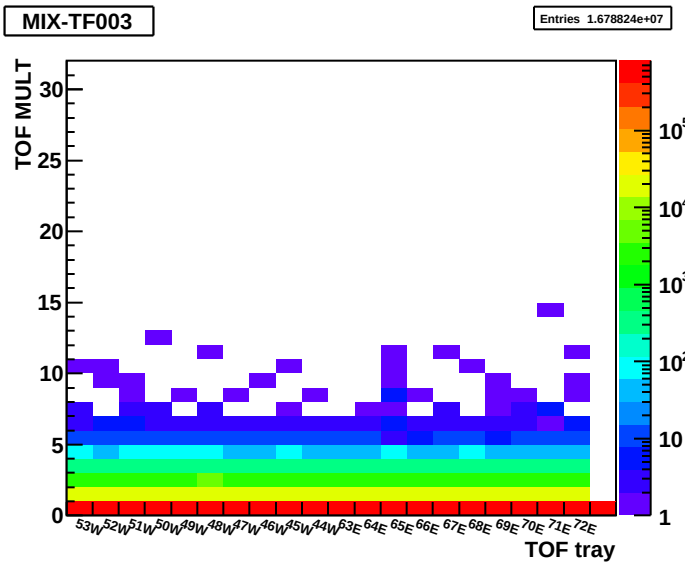

BunchId7Bit (all events)
Entries 399720

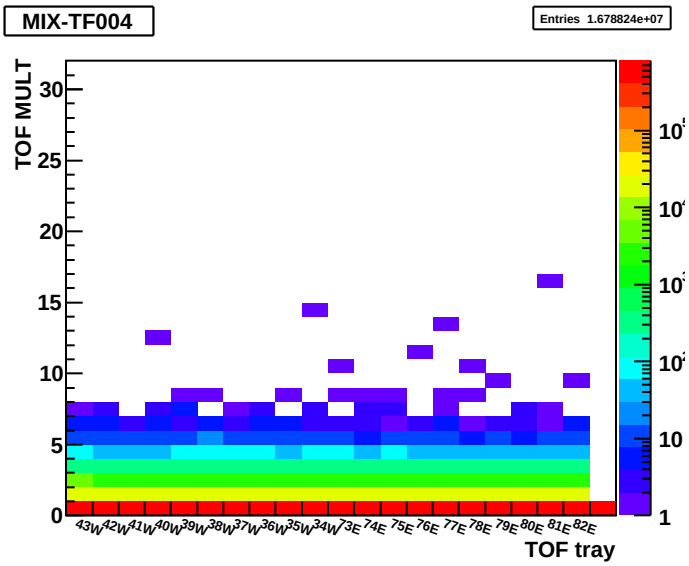

Abort-gaps
Entries 372

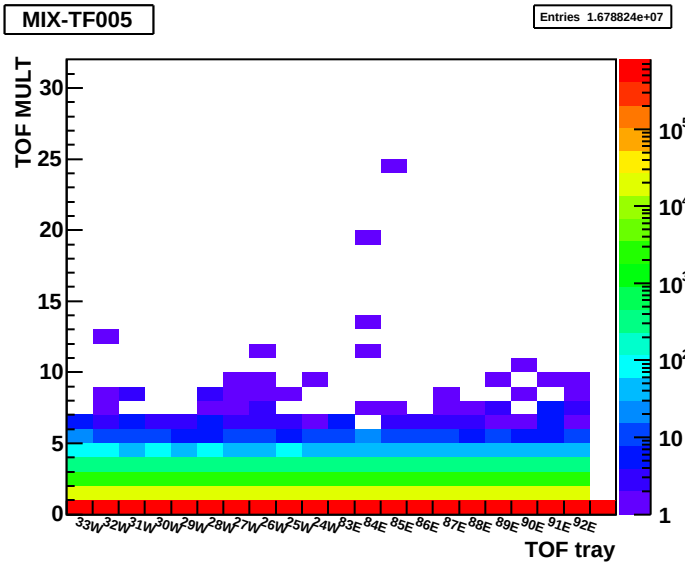

!Yellow-Fill
Entries 14396

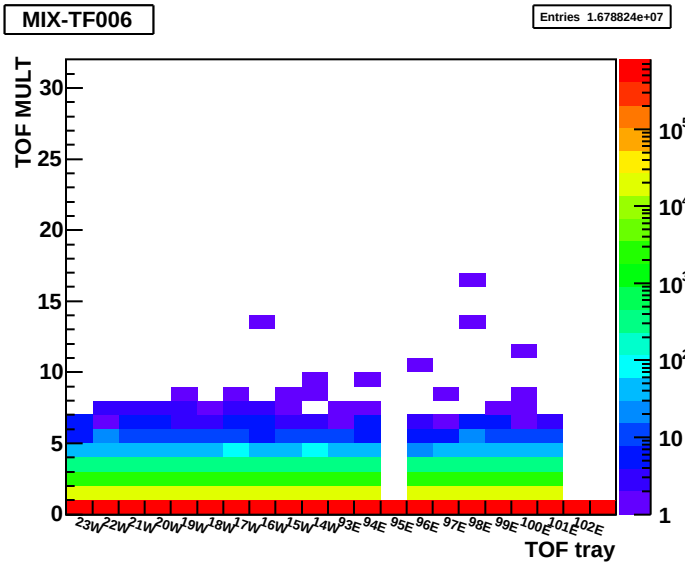

MIX-TF001
Entries 1.678824e+07
TOF MULT
TOF tray

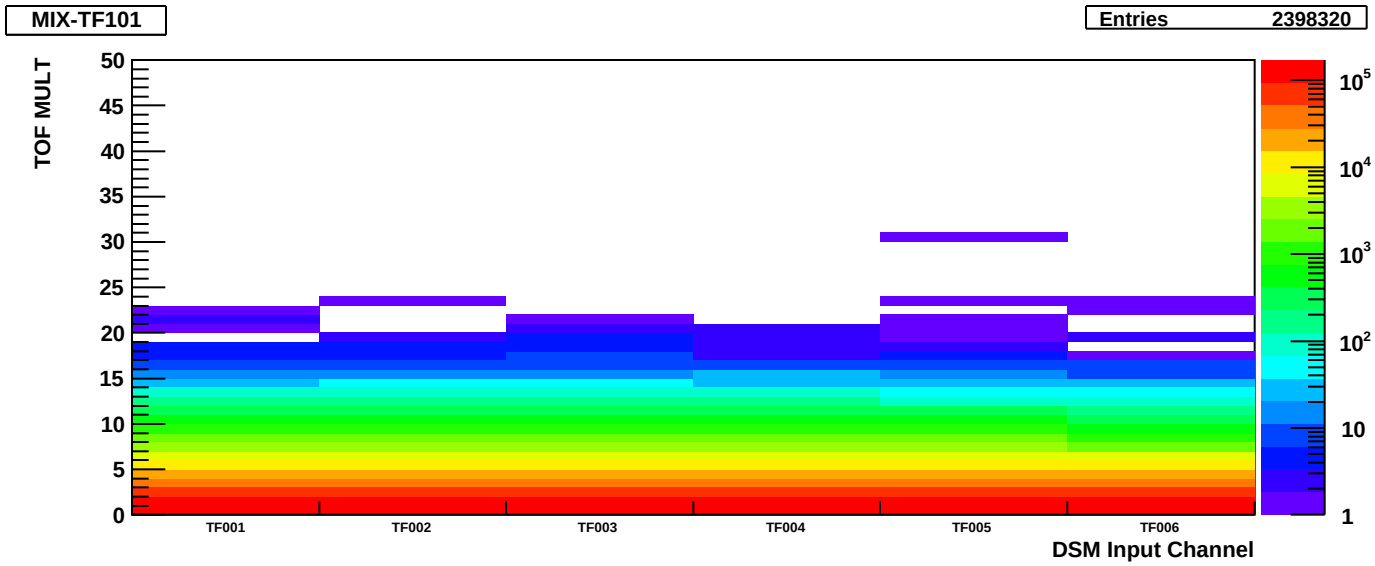

MIX-TF002
Entries 1.678824e+07
TOF MULT
TOF tray

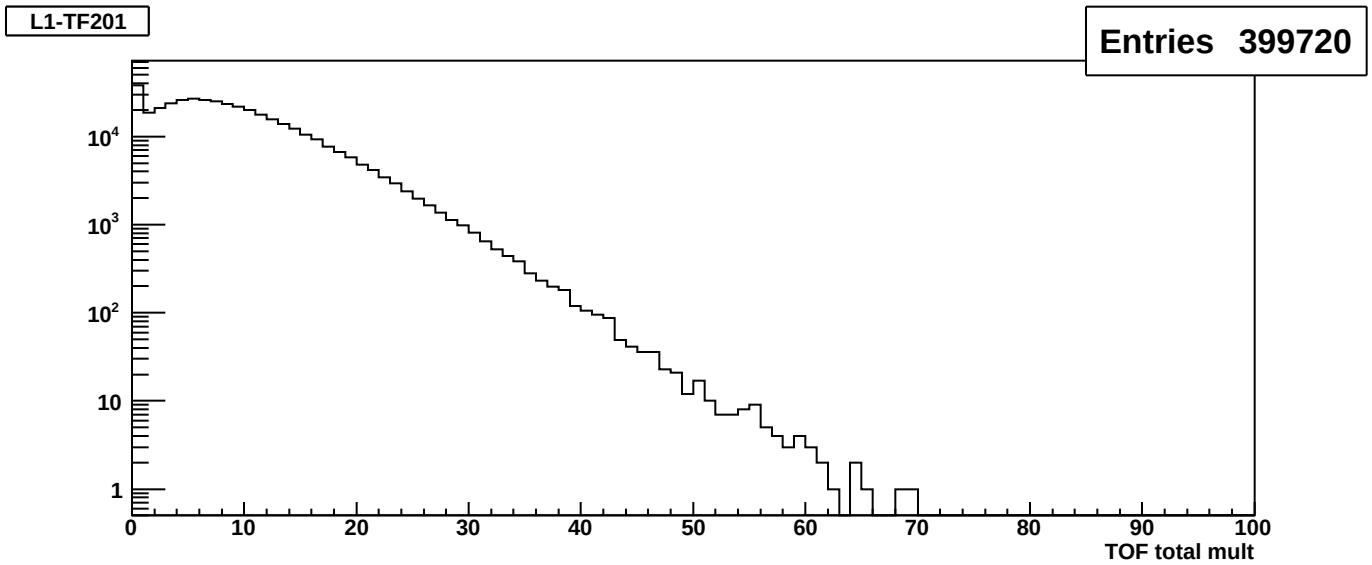

MIX-TF003
Entries 1.678824e+07
TOF MULT
TOF tray

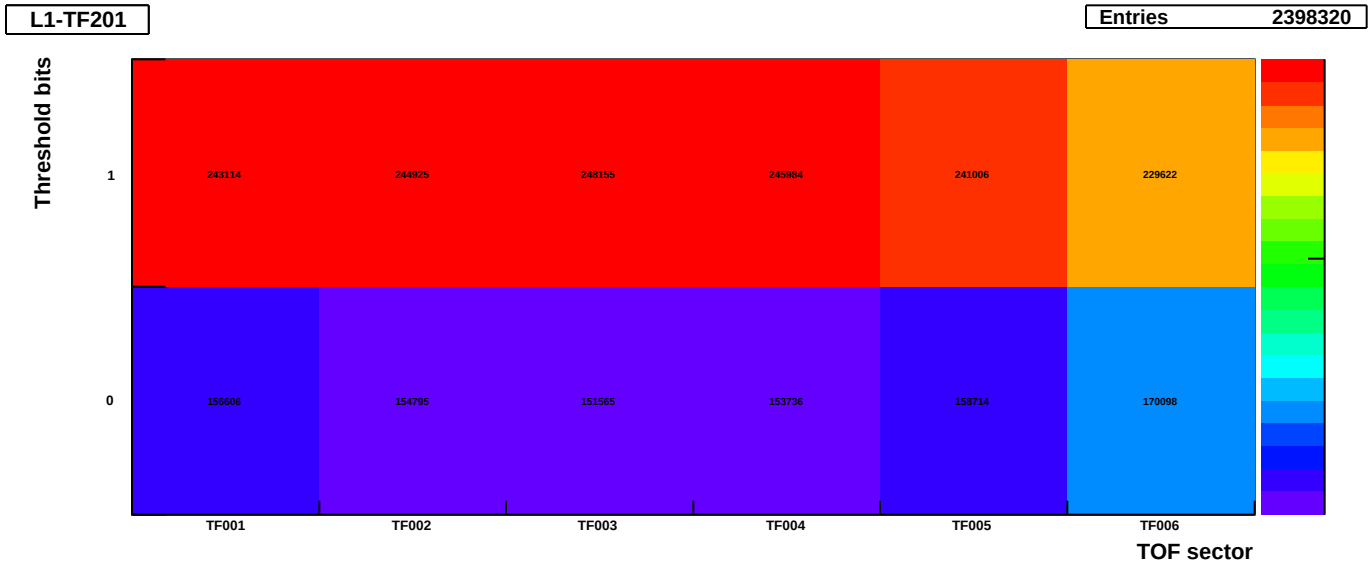

MIX-TF004
Entries 1.678824e+07
TOF MULT
TOF tray

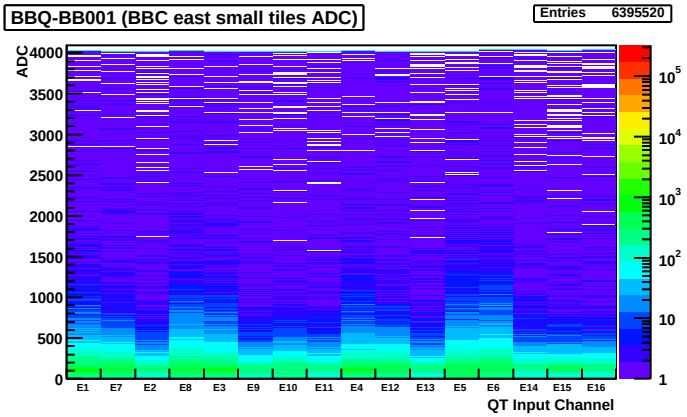

MIX-TF005
Entries 1.678824e+07
TOF MULT
TOF tray

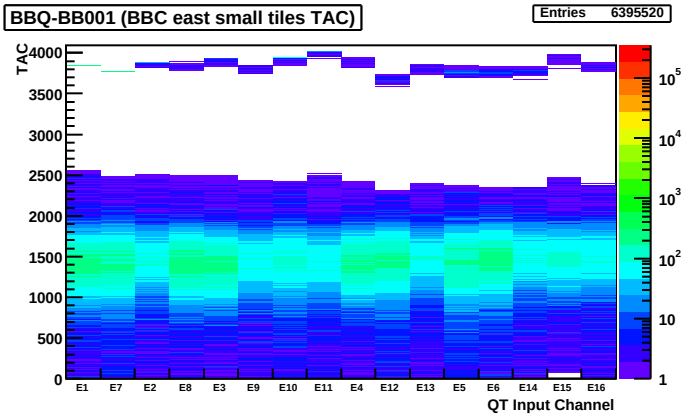

MIX-TF006
Entries 1.678824e+07
TOF MULT
TOF tray

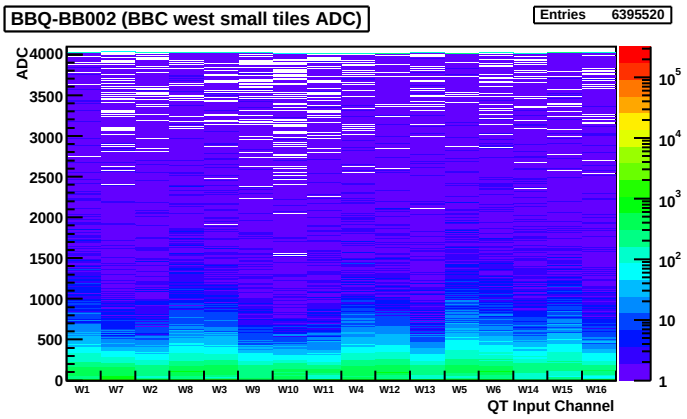

MIX-TF101
Entries 2398320
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

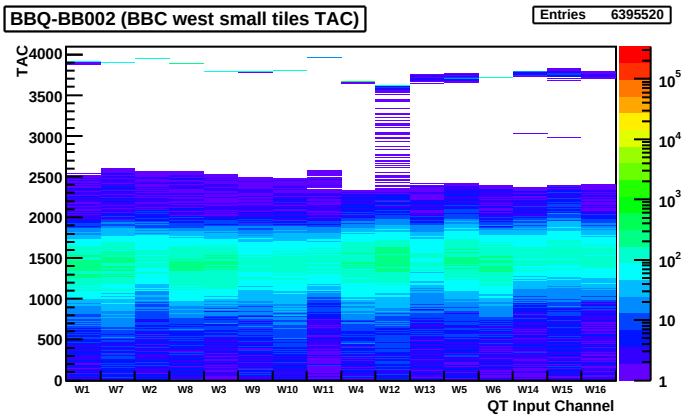

L1-TF201
Entries 399720
TOF total mult

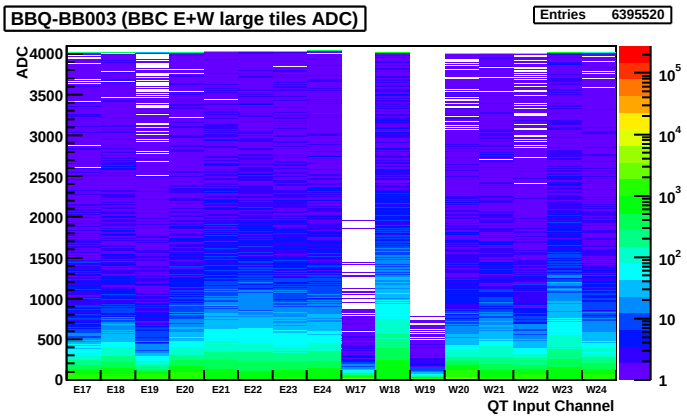

L1-TF201
Entries 2398320
Threshold bits
243114
244925
248155
245984
241006
229622
156606
154795
151565
153736
158714
170098
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

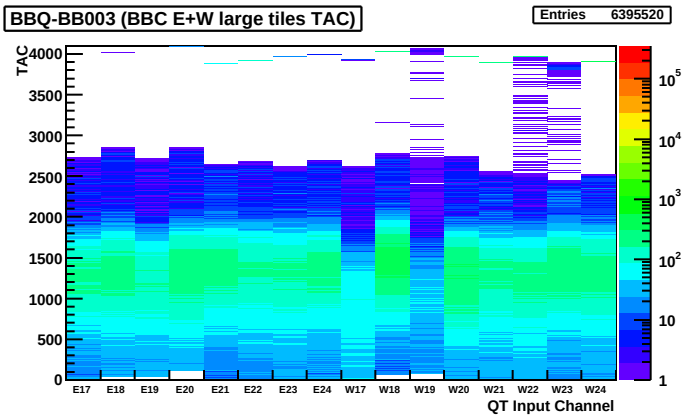

BBQ-BB001 (BBC east small tiles ADC)
Entries 6395520
ADC
QT Input Channel

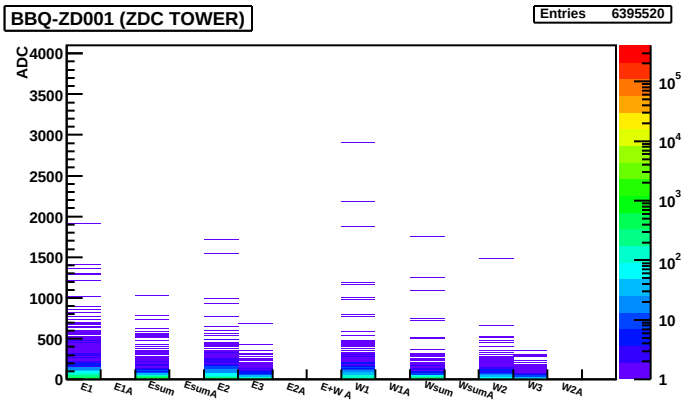

BBQ-BB001 (BBC east small tiles TAC)
Entries 6395520
TAC
QT Input Channel

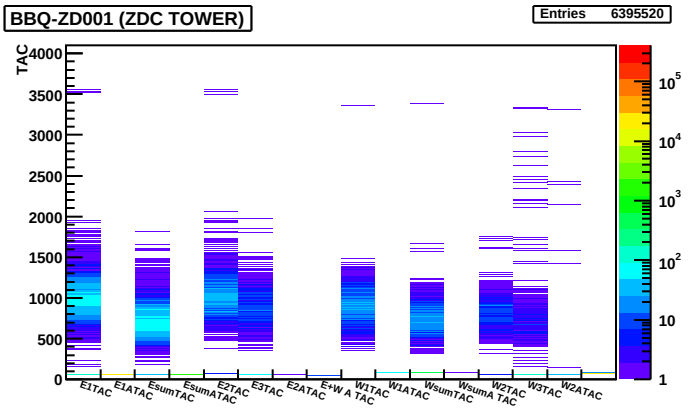

BBQ-BB002 (BBC west small tiles ADC)
Entries 6395520
ADC
QT Input Channel

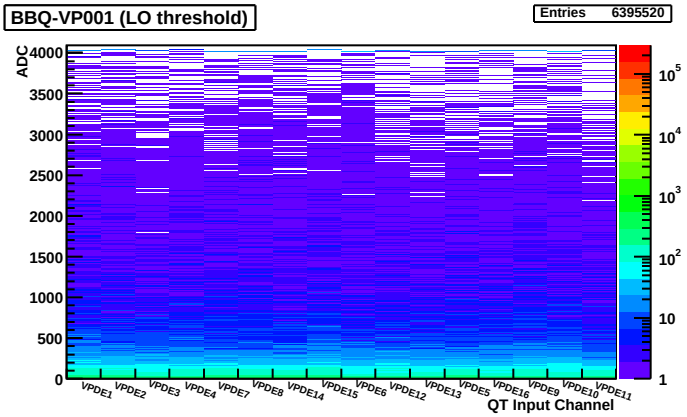

BBQ-BB002 (BBC west small tiles TAC)
Entries 6395520
TAC
QT Input Channel

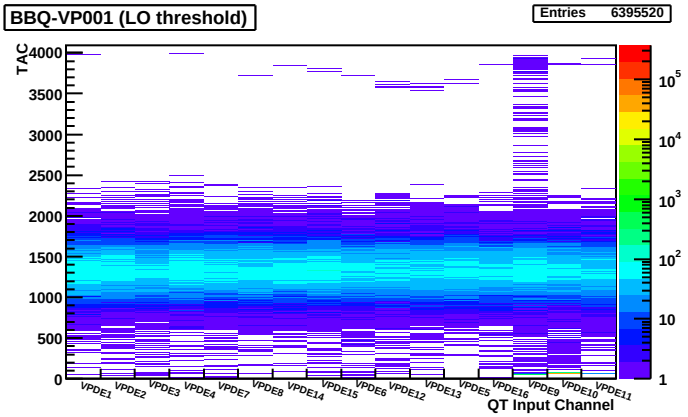

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6395520
ADC
QT Input Channel

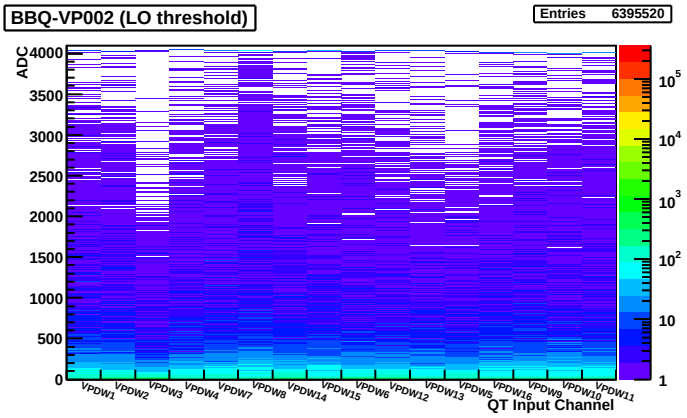

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6395520
TAC
QT Input Channel

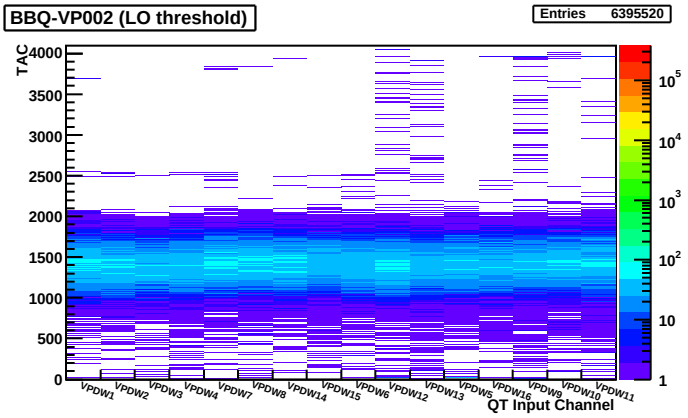

BBQ-ZD001 (ZDC TOWER)
Entries 6395520
ADC

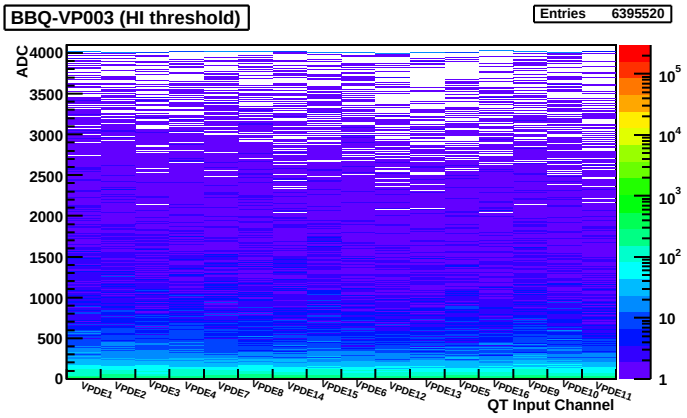

BBQ-ZD001 (ZDC TOWER)
Entries 6395520
TAC

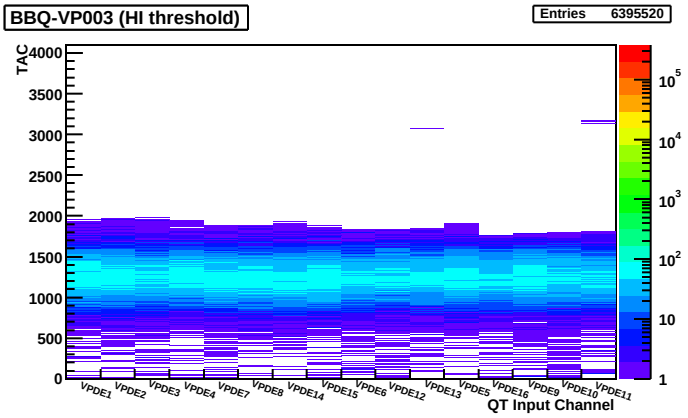

BBQ-VP001 (LO threshold)
Entries 6395520
ADC
QT Input Channel

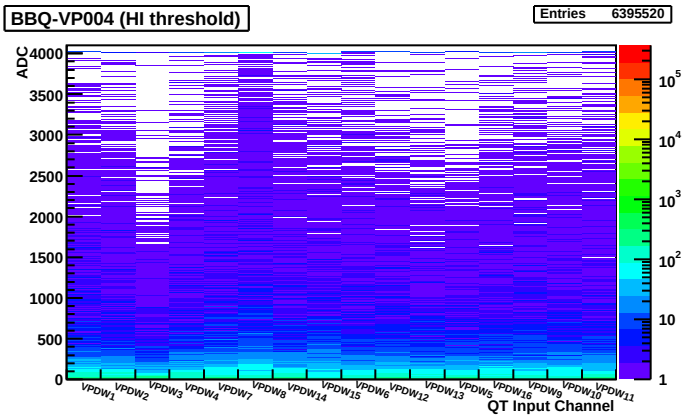

BBQ-VP001 (LO threshold)
Entries 6395520
TAC
QT Input Channel

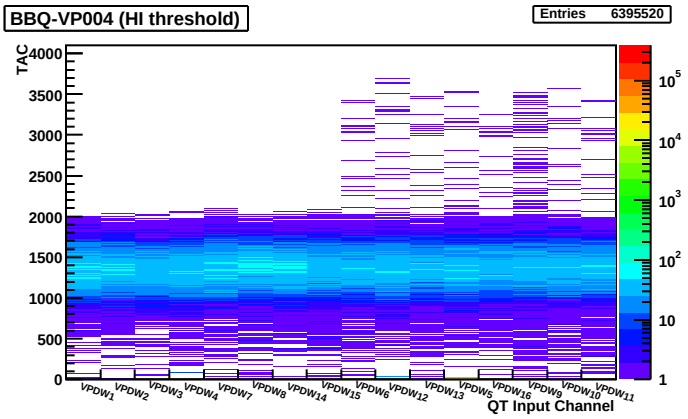

BBQ-VP002 (LO threshold)
Entries 6395520
ADC
QT Input Channel

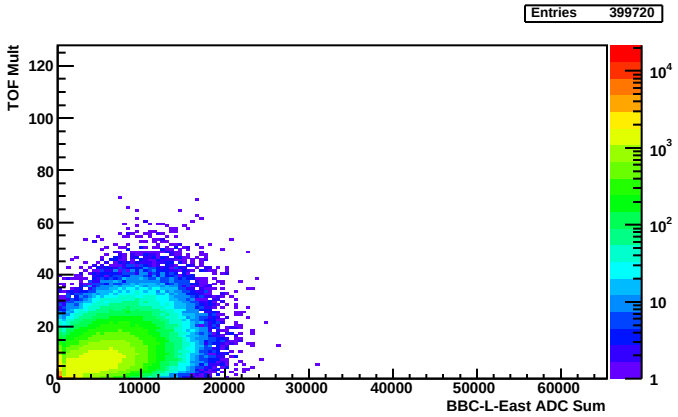

BBQ-VP002 (LO threshold)
Entries 6395520
TAC
QT Input Channel

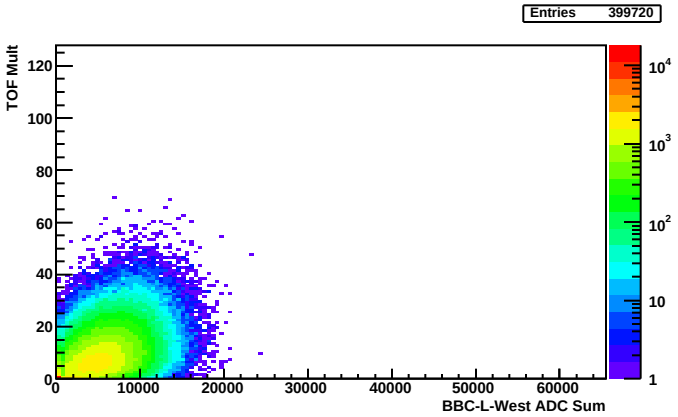

BBQ-VP003 (HI threshold)
Entries 6395520
ADC
QT Input Channel

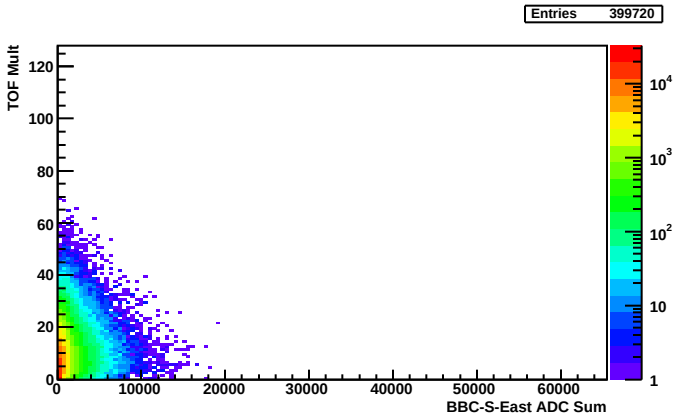

BBQ-VP003 (HI threshold)
Entries 6395520
TAC
QT Input Channel

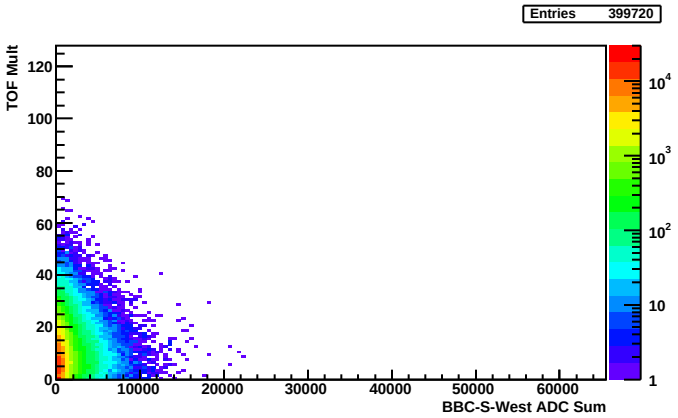

BBQ-VP004 (HI threshold)
Entries 6395520
ADC
QT Input Channel

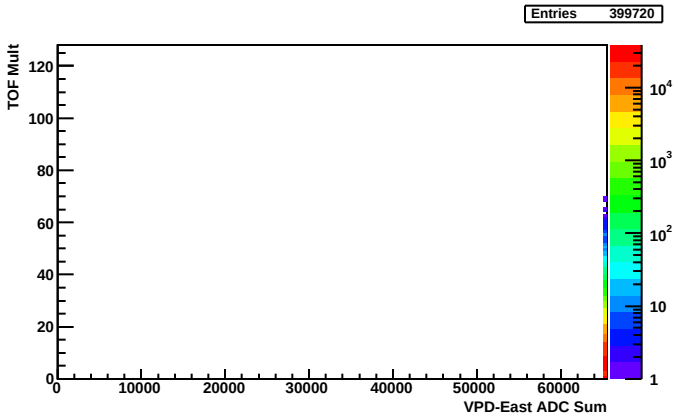

BBQ-VP004 (HI threshold)
Entries 6395520
TAC
QT Input Channel

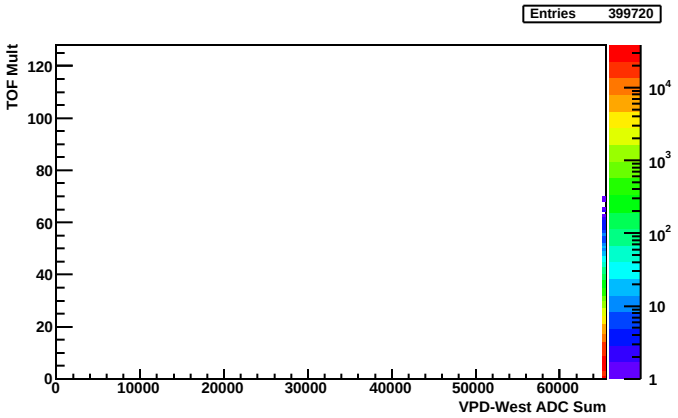

Entries 399720
TOF Mult
BBC-L-East ADC Sum

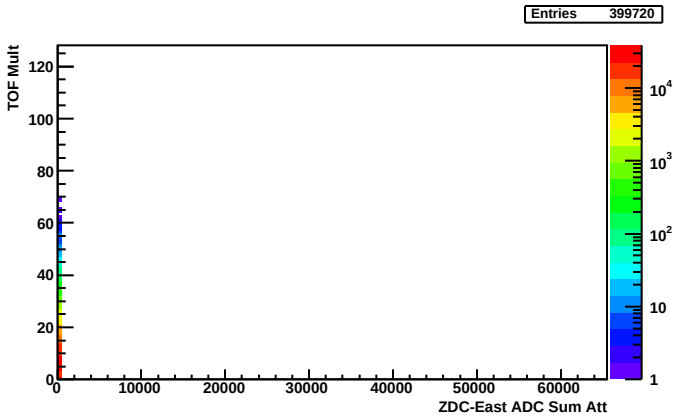

Entries 399720
TOF Mult
BBC-L-West ADC Sum

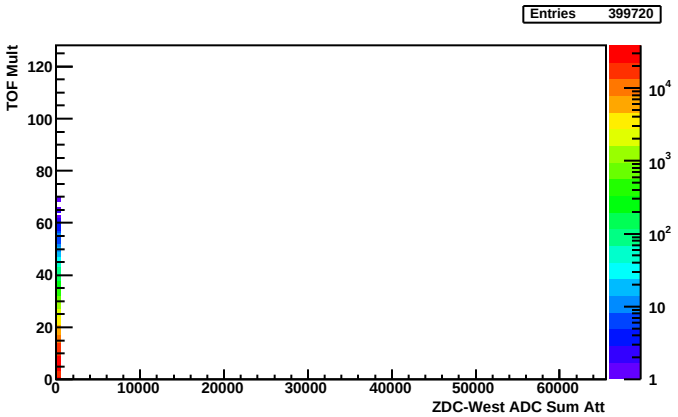

Entries 399720
TOF Mult
BBC-S-East ADC Sum

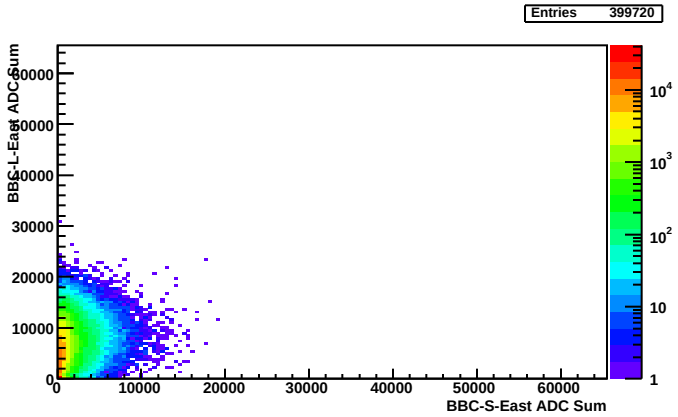

Entries 399720
TOF Mult
BBC-S-West ADC Sum

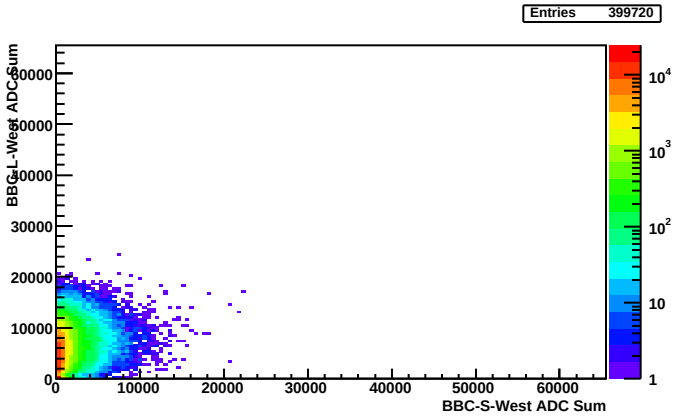

Entries 399720
TOF Mult
VPD-East ADC Sum

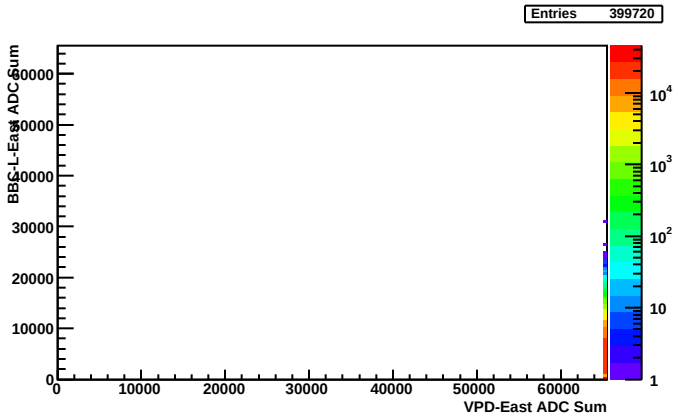

Entries 399720
TOF Mult
VPD-West ADC Sum

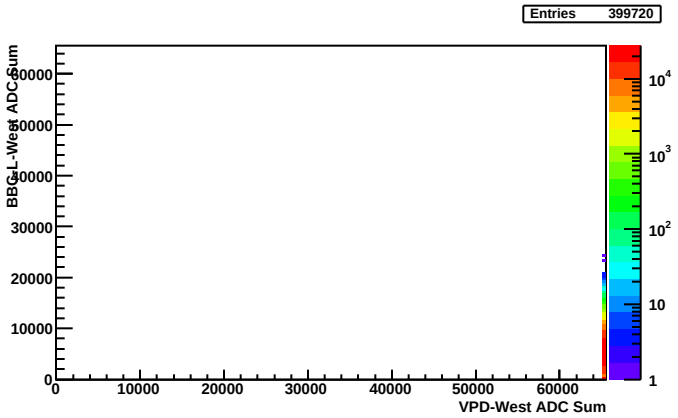

Entries 399720
TOF Mult
ZDC-East ADC Sum Att

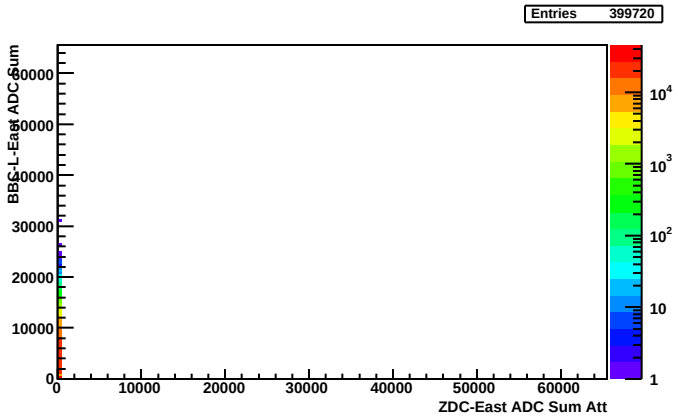

Entries 399720
TOF Mult
ZDC-West ADC Sum Att


Entries 399720
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 399720
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 399720
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 399720
BBC-L-West ADC Sum
VPD-West ADC Sum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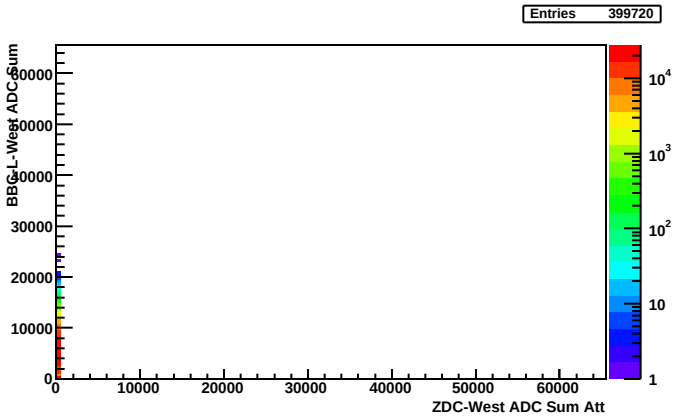
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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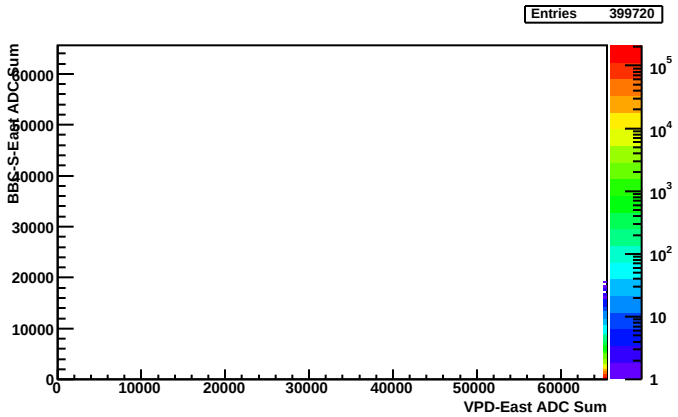
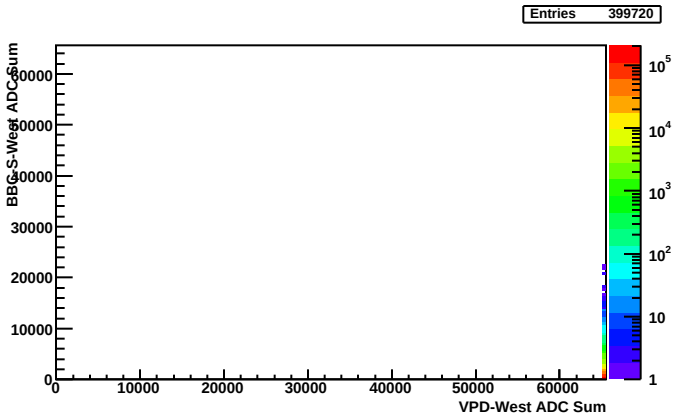
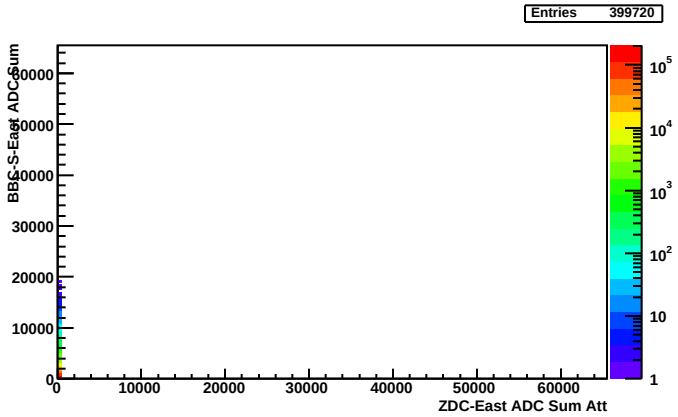
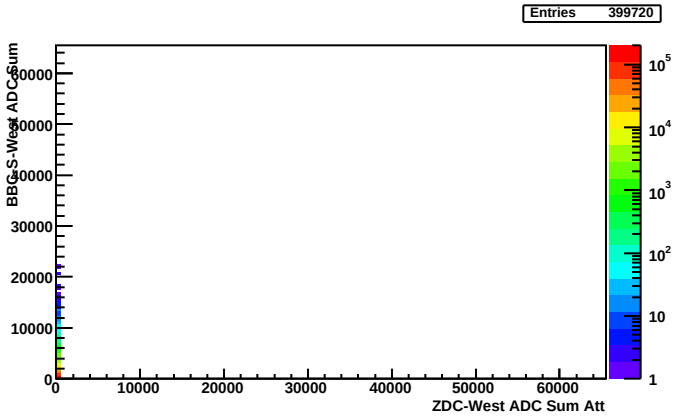
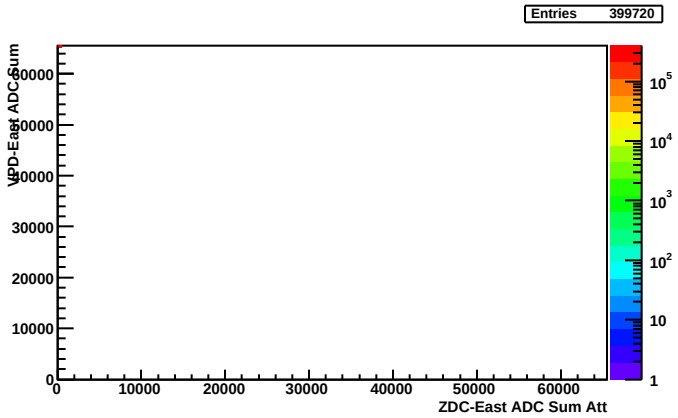
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

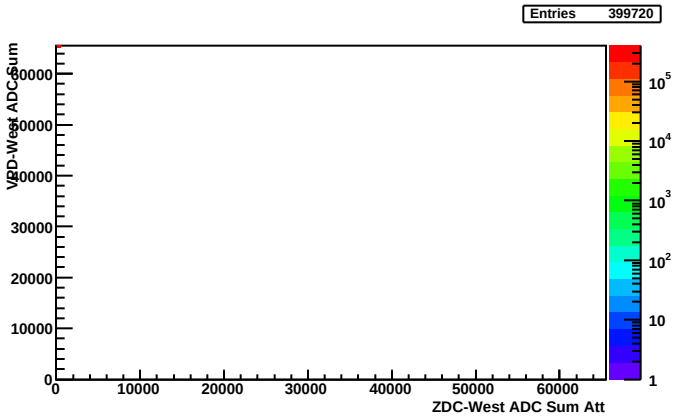

Entries 399720
BBC-S-East ADC Sum
VPD-East ADC Sum

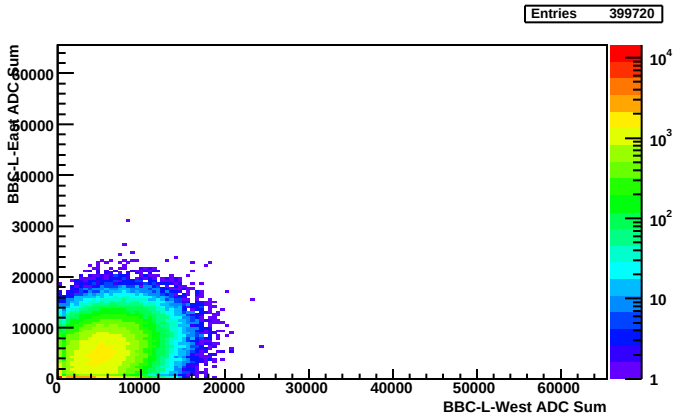

Entries 399720
BBC-S-West ADC Sum
VPD-West ADC Sum

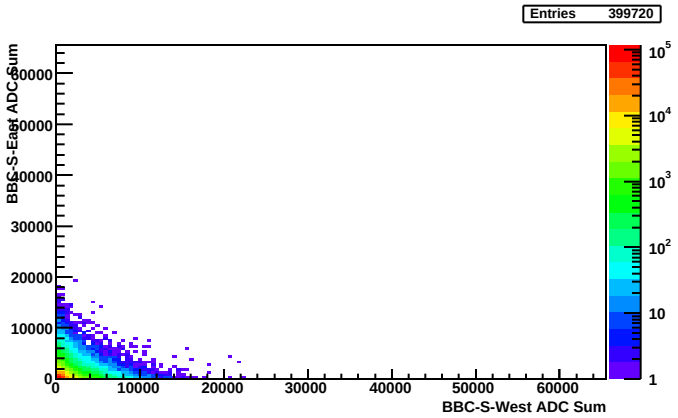

Entries 399720
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

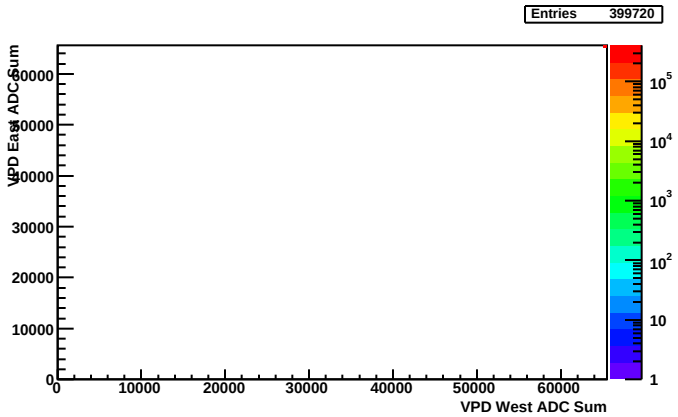

Entries 399720
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

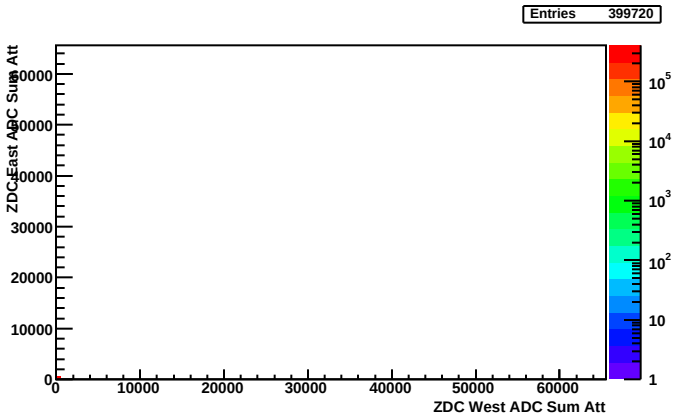

Entries 399720
VPD-East ADC Sum
ZDC-East ADC Sum Att

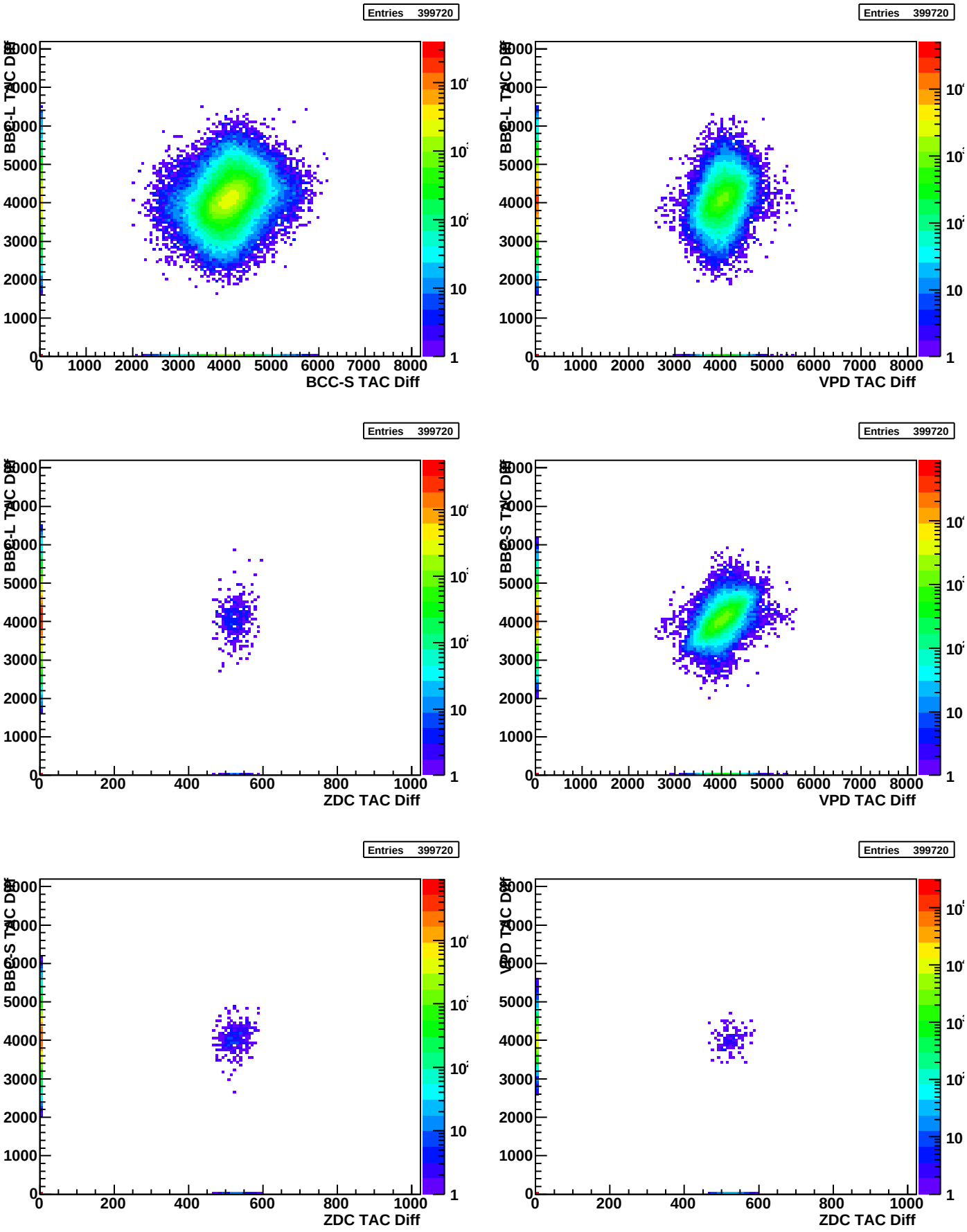

Entries 399720
VPD-West ADC Sum
ZDC-West ADC Sum Att

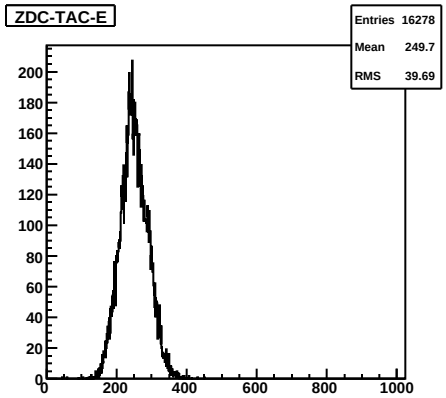

Entries 399720
BBC-L-East ADC Sum
BBC-L-West ADC Sum

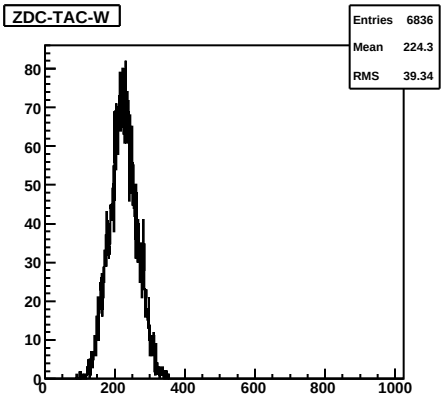

Entries 399720
BBC-S-East ADC Sum
BBC-S-West ADC Sum

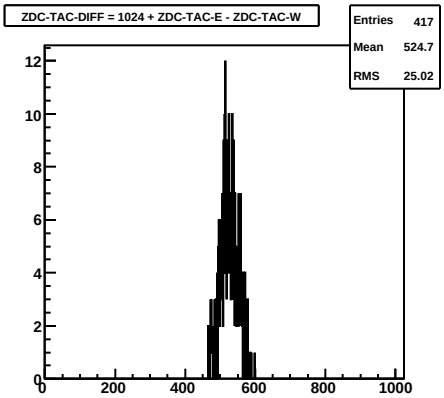

Entries 399720
VPD East ADC Sum
VPD West ADC Sum

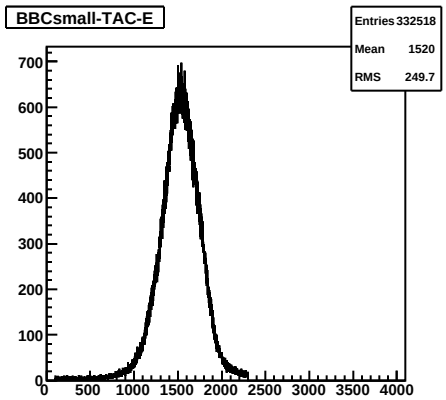

Entries 399720
ZDC East ADC Sum Att
ZDC West ADC Sum Att

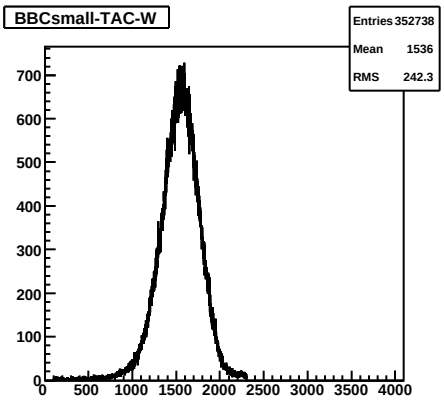

Entries 399720
BBC-L TAC Diff
BCC-S TAC Diff
Entries 399720
BBC-L TAC Diff
VPD TAC Diff
Entries 399720
BBC-L TAC Diff
ZDC TAC Diff
Entries 399720
BBC-S TAC Diff
VPD TAC Diff
Entries 399720
BBC-S TAC Diff
ZDC TAC Diff
Entries 399720
VPD TAC Diff
ZDC TAC Diff

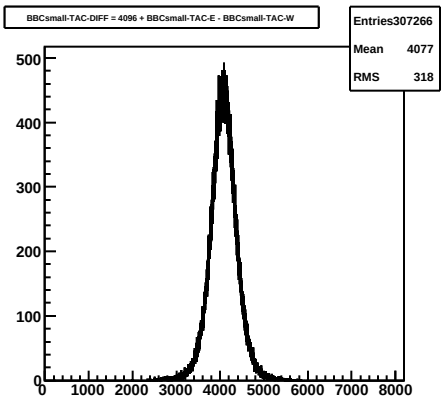

ZDC-TAC-E
Entries 16278
Mean 249.7
RMS 39.69

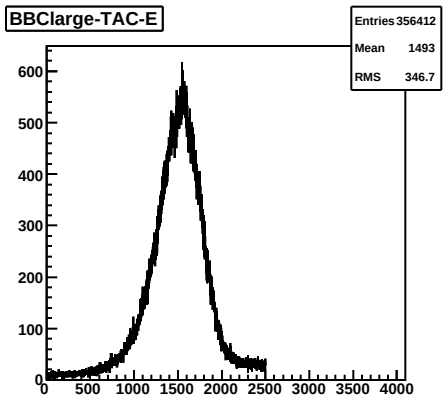

ZDC-TAC-W
Entries 6836
Mean 224.3
RMS 39.34

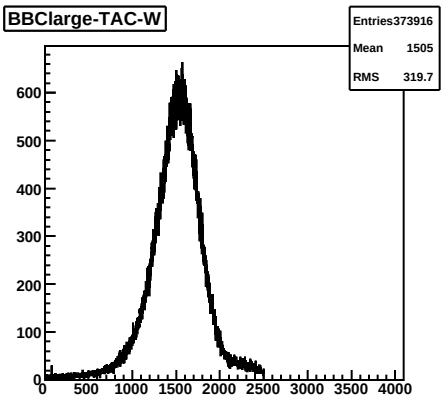

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 417
Mean 524.7
RMS 25.02

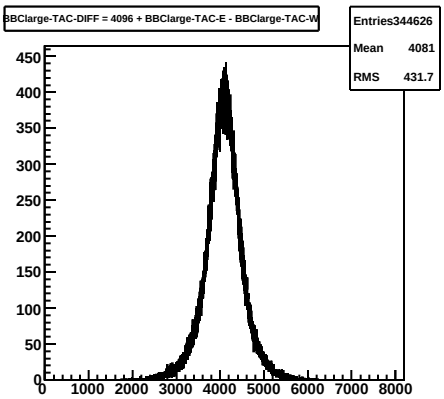

BBCsmall-TAC-E
Entries 332518
Mean 1520
RMS 249.7

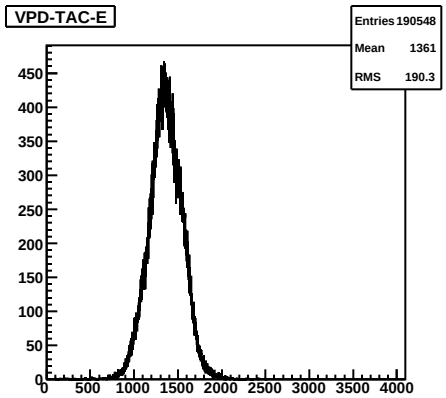

BBCsmall-TAC-W
Entries 352738
Mean 1536
RMS 242.3

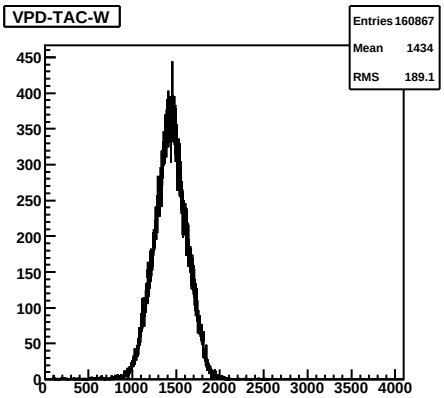

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 307266
Mean 4077
RMS 318

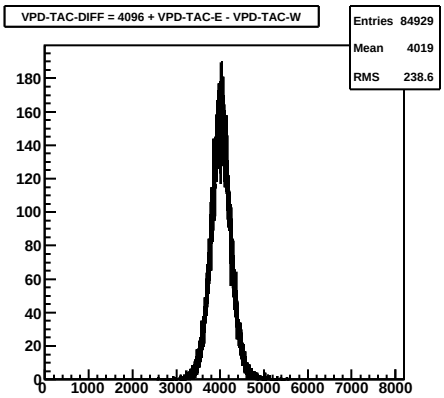

BBClarge-TAC-E
Entries 356412
Mean 1493
RMS 346.7

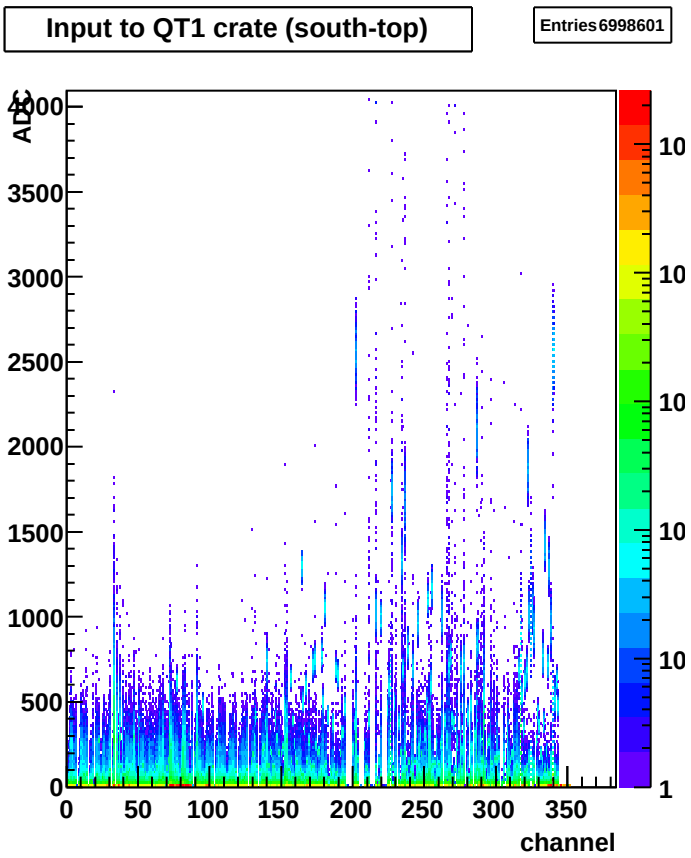

BBClarge-TAC-W
Entries 373916
Mean 1505
RMS 319.7

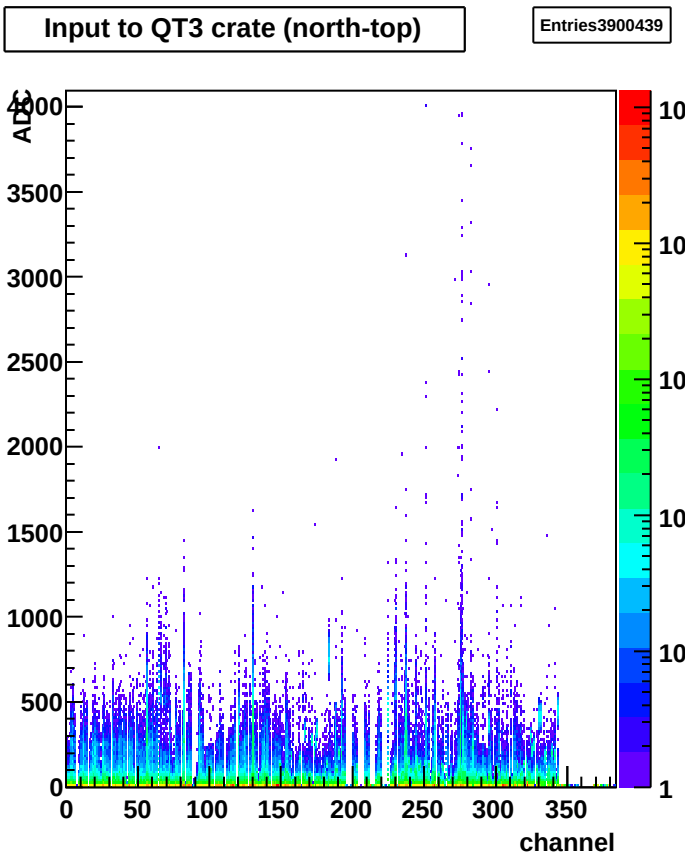

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 344626
Mean 4081
RMS 431.7

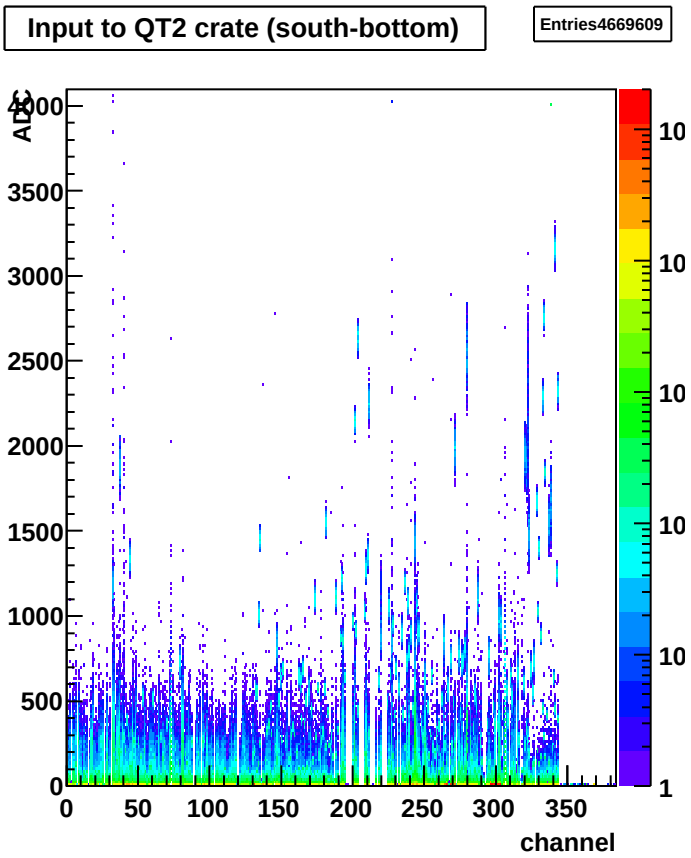

VPD-TAC-E
Entries 190548
Mean 1361
RMS 190.3

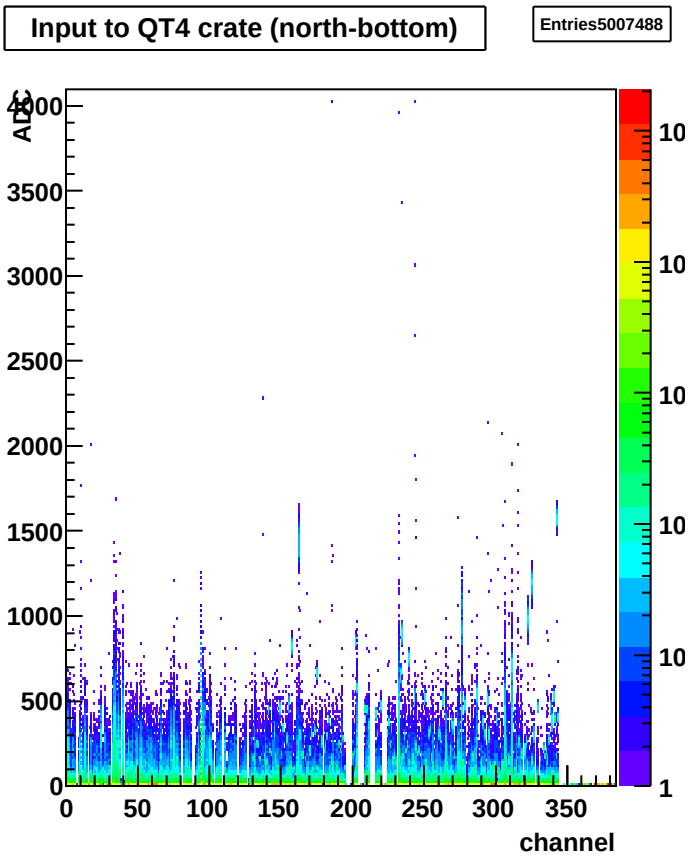

VPD-TAC-W
Entries 160867
Mean 1434
RMS 189.1

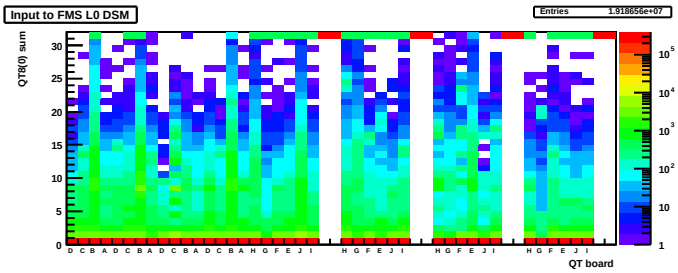

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 84929
Mean 4019
RMS 238.6

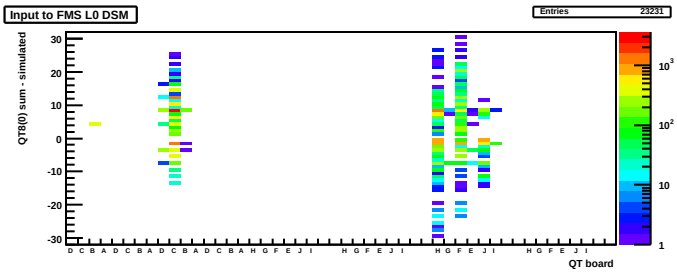

Input to QT1 crate (south-top)
Entries 6998601
ADC
channel

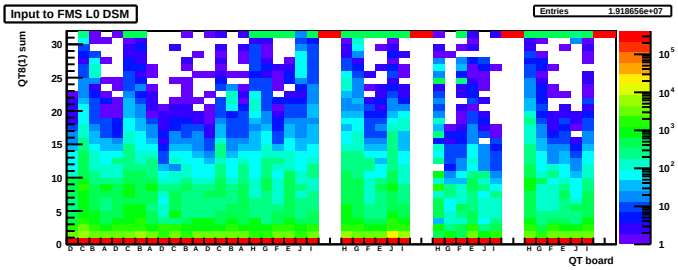

Input to QT3 crate (north-top)
Entries 3900439
ADC
channel

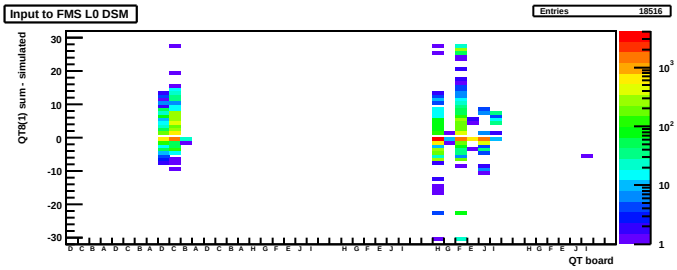

Input to QT2 crate (south-bottom)
Entries 4669609
ADC
channel

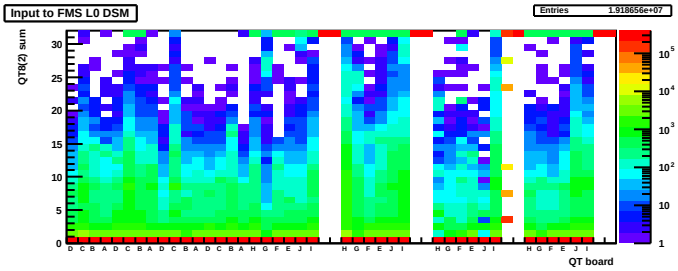

Input to QT4 crate (north-bottom)
Entries 5007488
ADC
channel

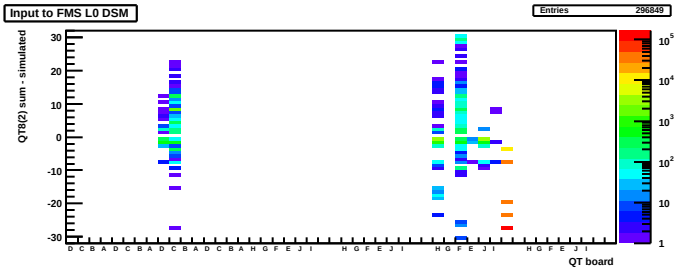

Input to FMS L0 DSM
Entries 1.918656e+07
QT8(0) sum
QT board

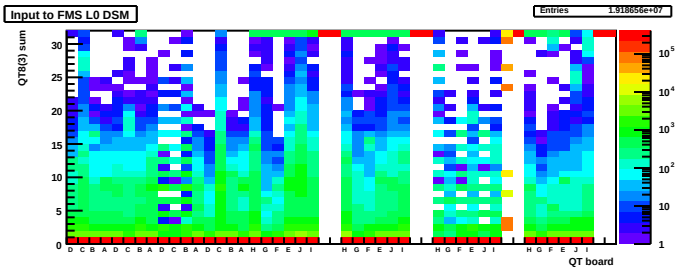

Input to FMS L0 DSM
Entries 23231
QT8(0) sum - simulated
QT board

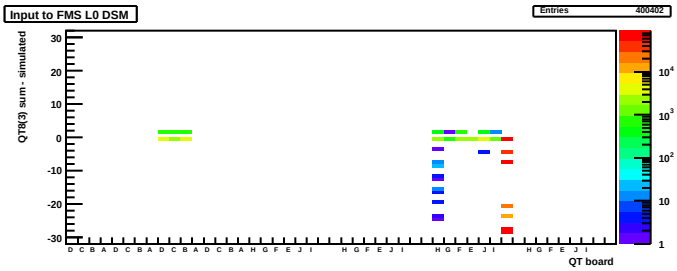

Input to FMS L0 DSM
Entries 1.918656e+07
QT8(1) sum
QT board

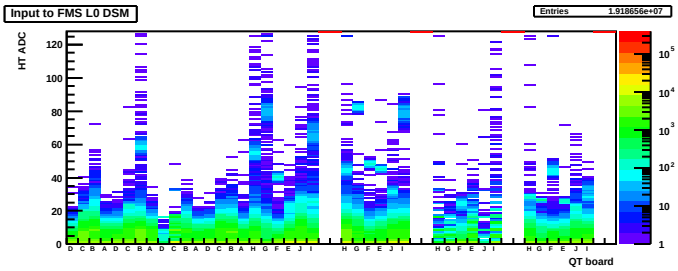

Input to FMS L0 DSM
Entries 18516
QT8(1) sum - simulated
QT board

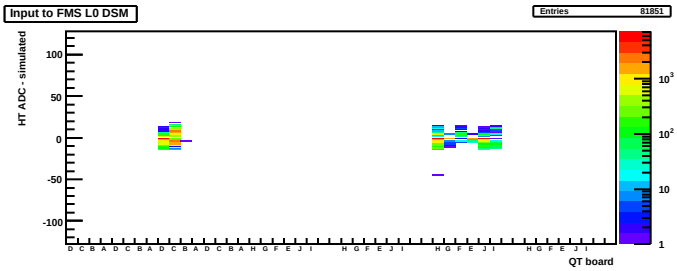

Input to FMS L0 DSM
Entries 1.918656e+07
QT8(2) sum
QT board

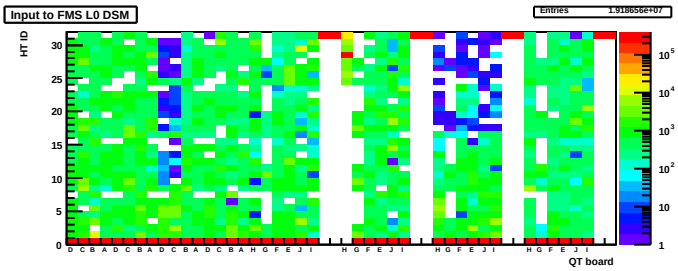

Input to FMS L0 DSM
Entries 296849
QT8(2) sum - simulated
QT board

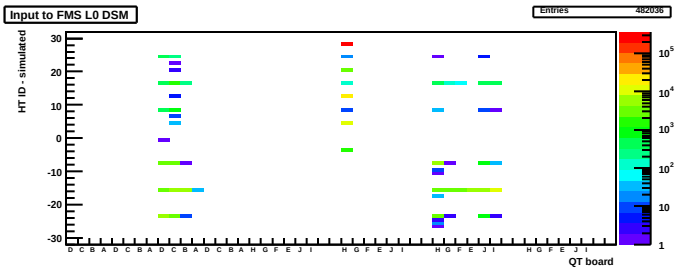

Input to FMS L0 DSM
Entries 1.918656e+07
QT8(3) sum
QT board

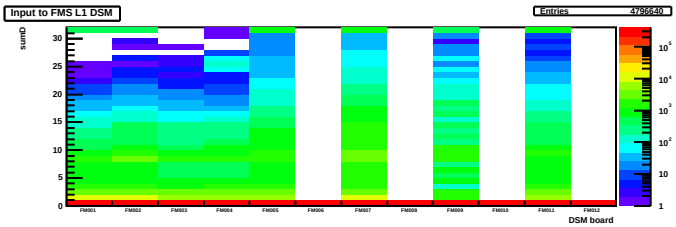

Input to FMS L0 DSM
Entries 400402
QT8(3) sum - simulated
QT board

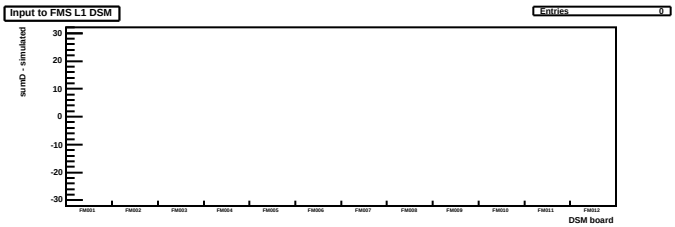

Input to FMS L0 DSM
Entries 1.918656e+07
HT ADC
QT board

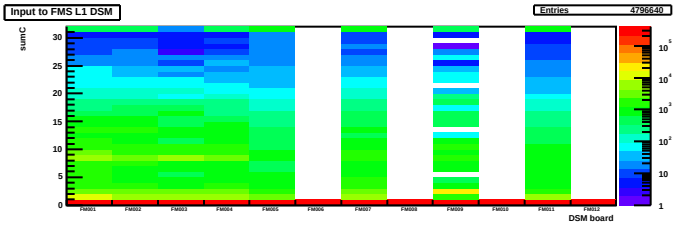

Input to FMS L0 DSM
Entries 81851
HT ADC - simulated
QT board

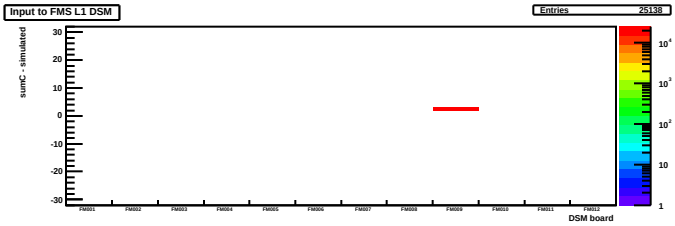

Input to FMS L0 DSM
Entries 1.918656e+07
HT ID
QT board

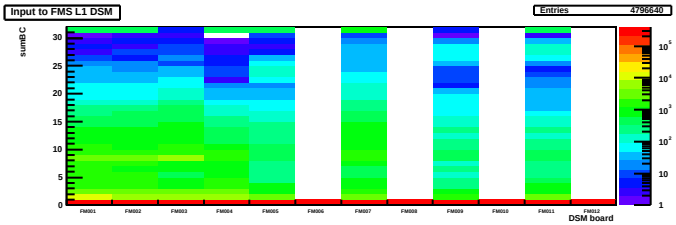

Input to FMS L0 DSM
Entries 482036
HT ID - simulated
QT board

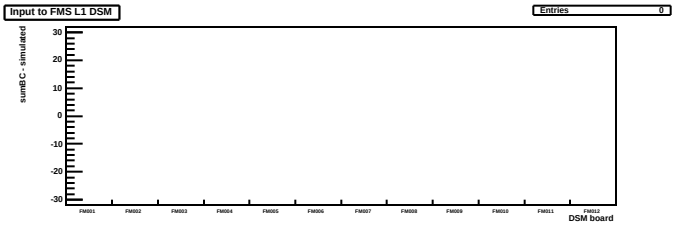

Input to FMS L1 DSM
Entries 4786640
sumD
DSM board

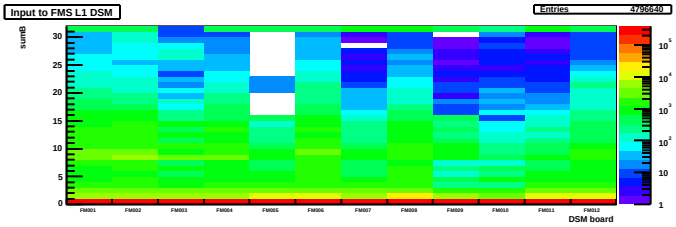

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

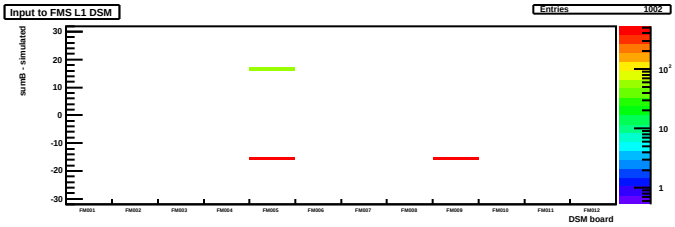

Input to FMS L1 DSM
Entries 4786640
sumC
DSM board

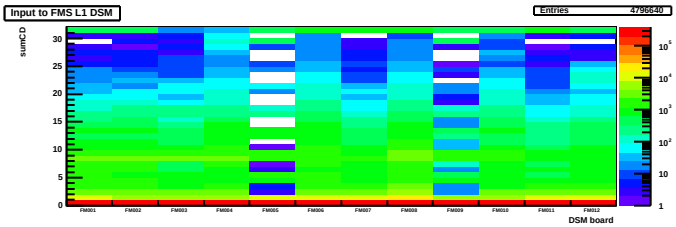

Input to FMS L1 DSM
Entries 25138
sumC - simulated
DSM board

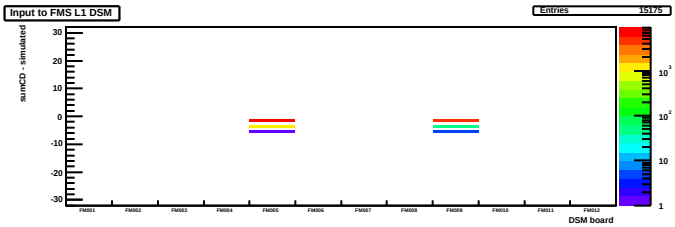

Input to FMS L1 DSM
Entries 4786640
sumBC
DSM board

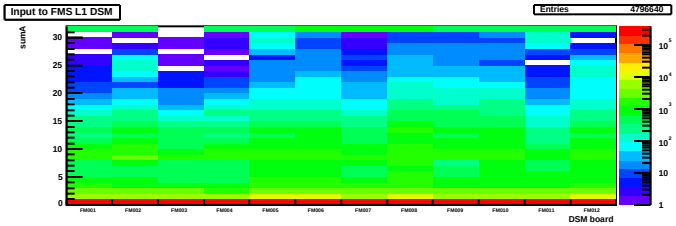

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

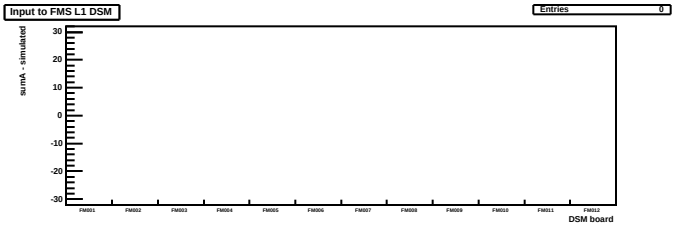

Input to FMS L1 DSM
Entries 4786640
sumB
DSM board

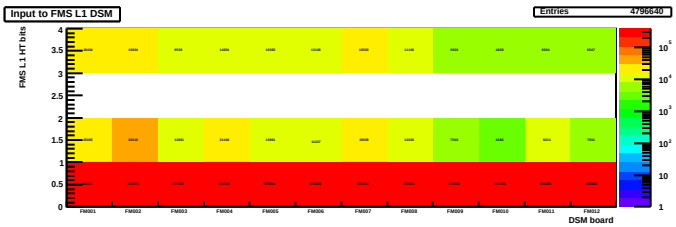

Input to FMS L1 DSM
Entries 1002
sumB - simulated
DSM board

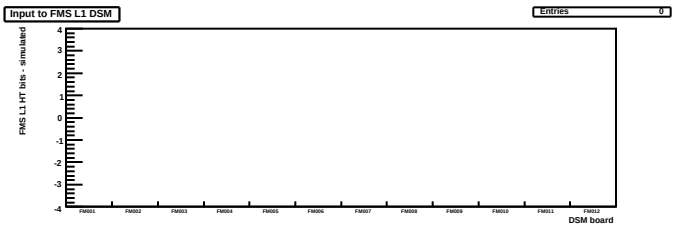

Input to FMS L1 DSM
Entries 4786640
sumCD
DSM board

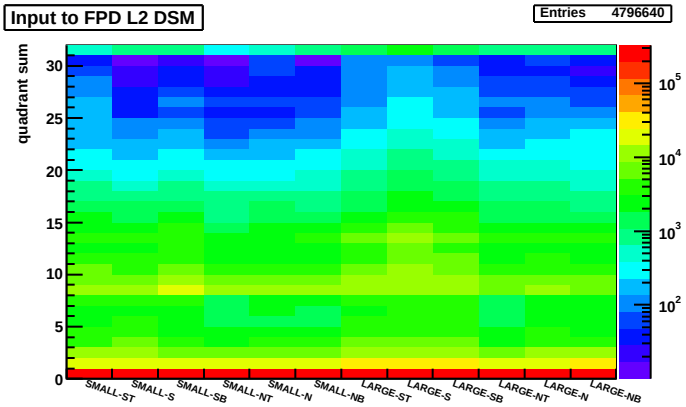

Input to FMS L1 DSM
Entries 15175
sumCD - simulated
DSM board

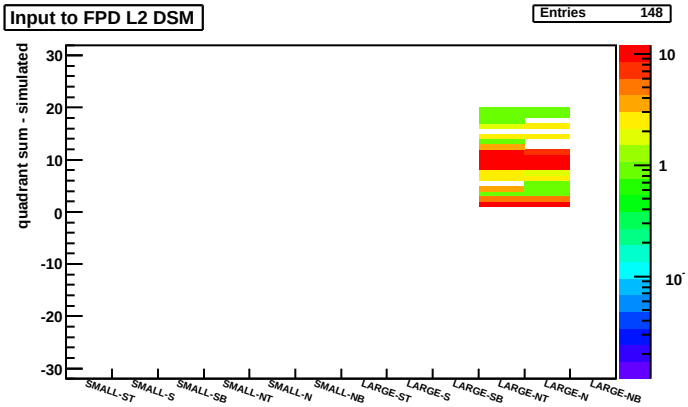

Input to FMS L1 DSM
Entries 4786640
sumA
DSM board

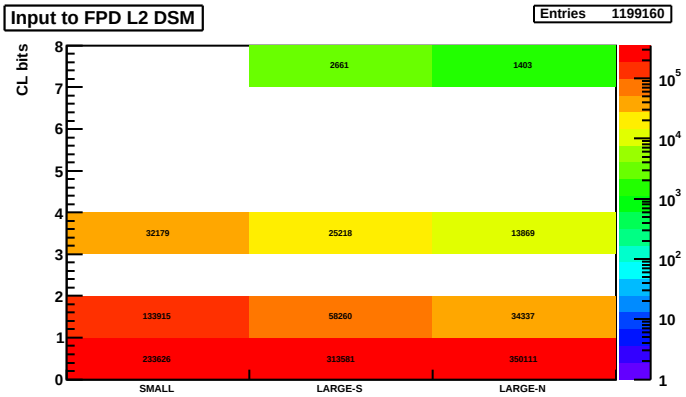

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

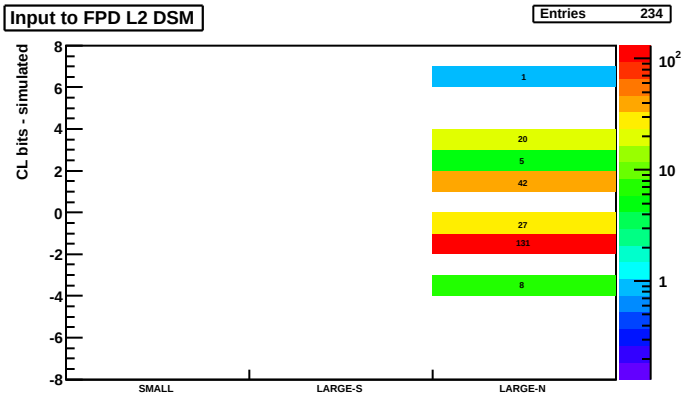

Input to FMS L1 DSM
Entries 4786640
FMS L1 HT bits
DSM board

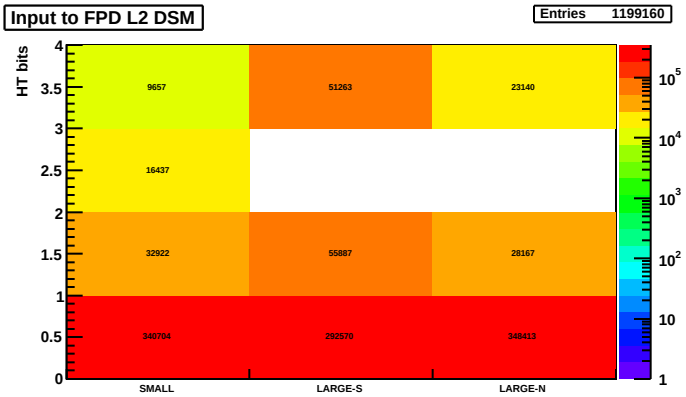

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

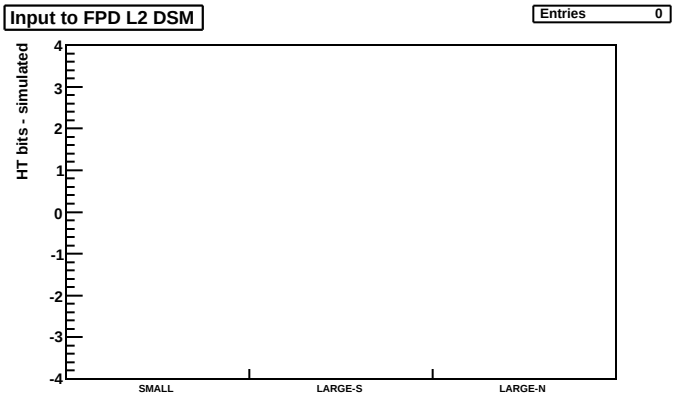

Input to FPD L2 DSM
Entries 4796640
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

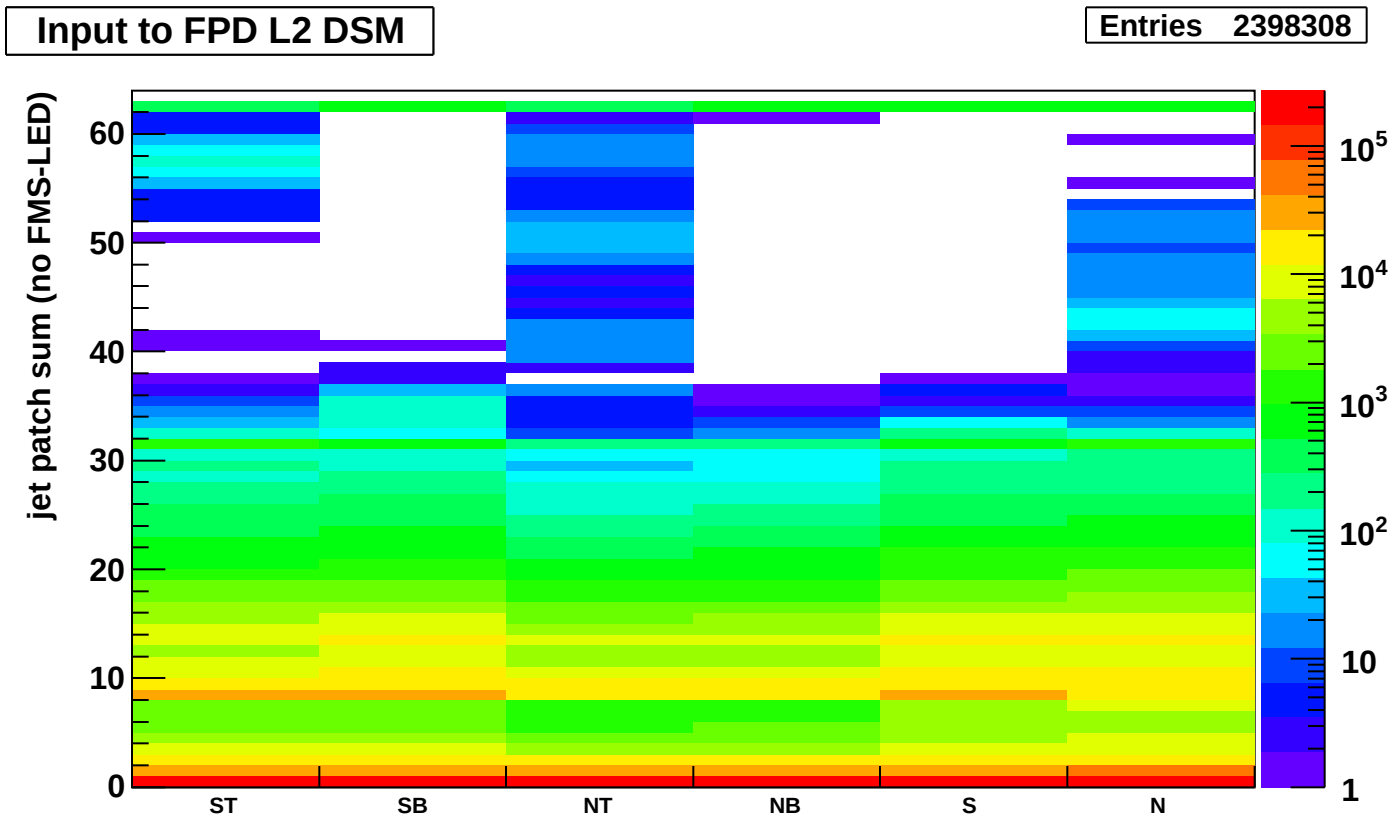

Input to FPD L2 DSM
Entries 148
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

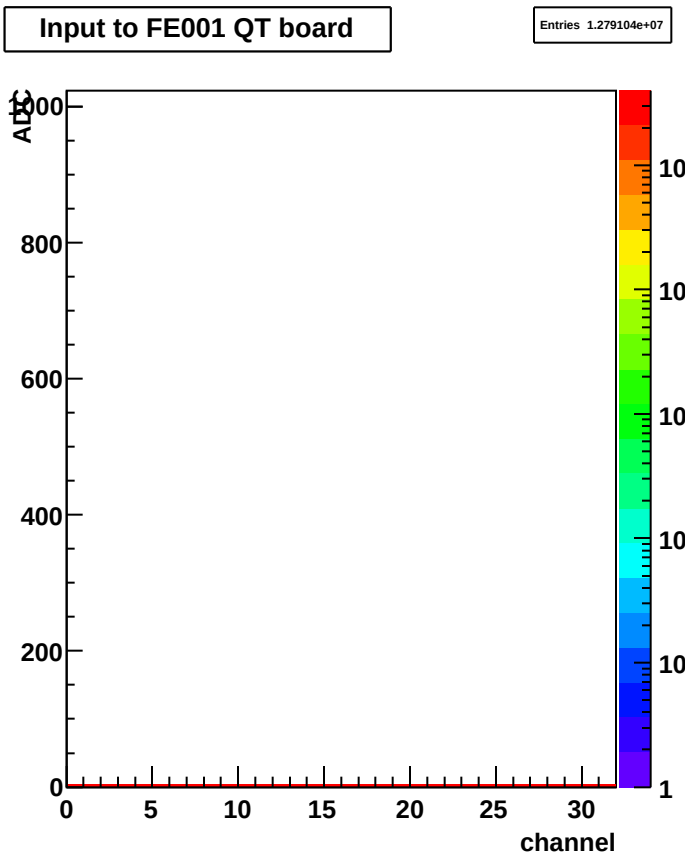

Input to FPD L2 DSM
Entries 1199160
CL bits
2661
1403
32179
25218
13869
133915
58260
34337
233626
313581
350111
SMALL
LARGE-S
LARGE-N

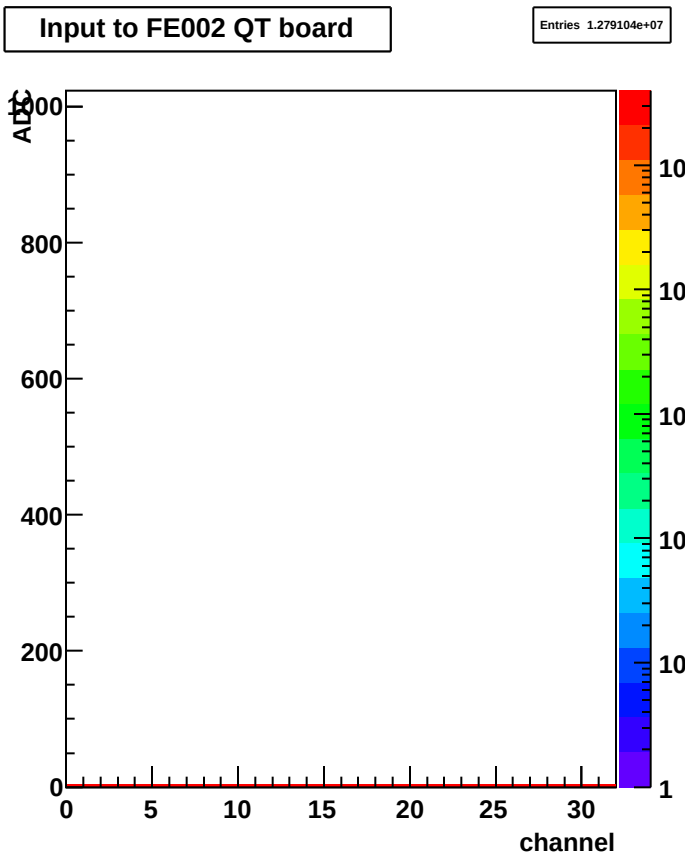

Input to FPD L2 DSM
Entries 234
CL bits - simulated
1
20
5
42
27
131
8
SMALL
LARGE-S
LARGE-N

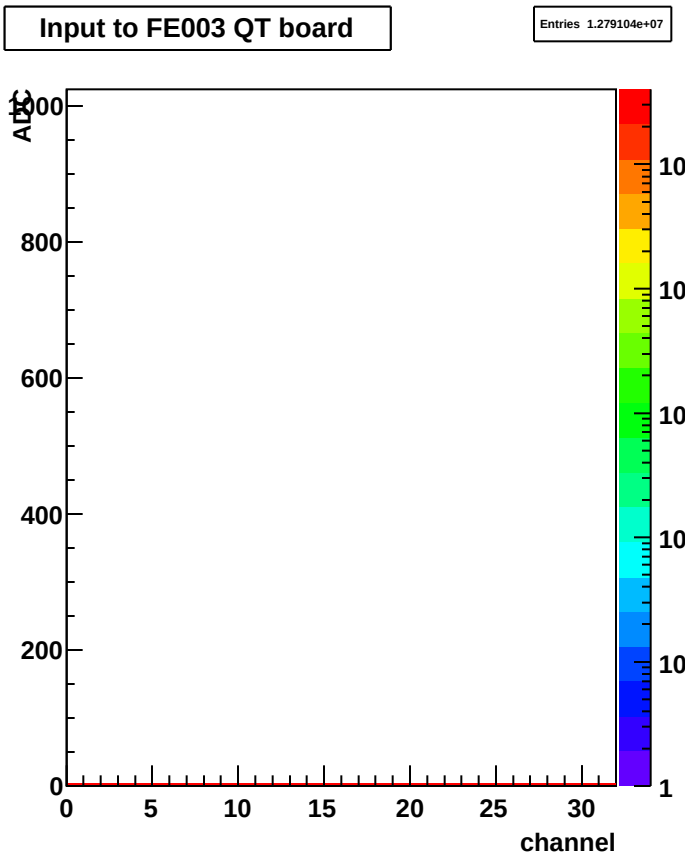

Input to FPD L2 DSM
Entries 1199160
HT bits
9657
51263
23140
16437
32922
55887
28167
340704
292570
348413
SMALL
LARGE-S
LARGE-N

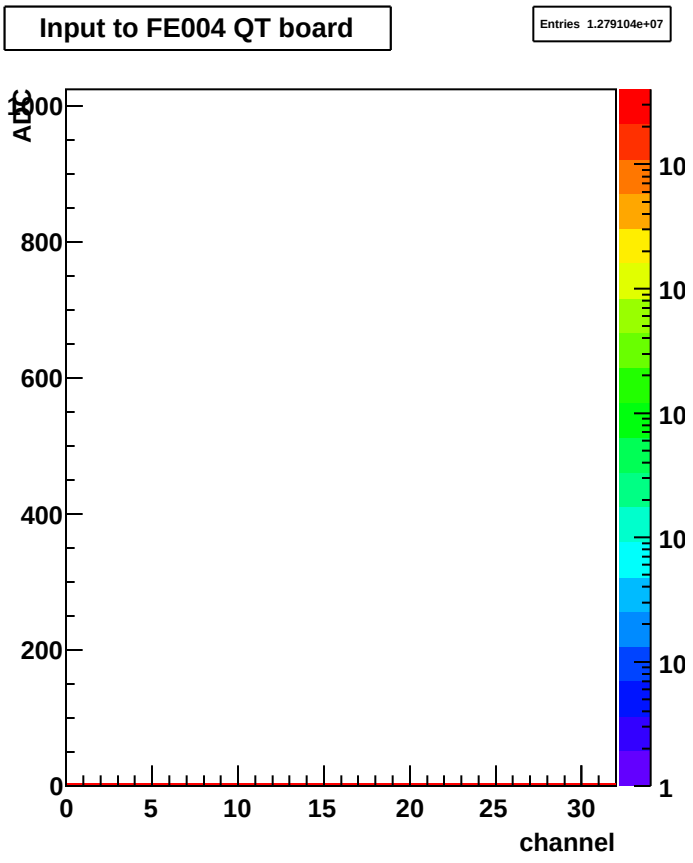

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

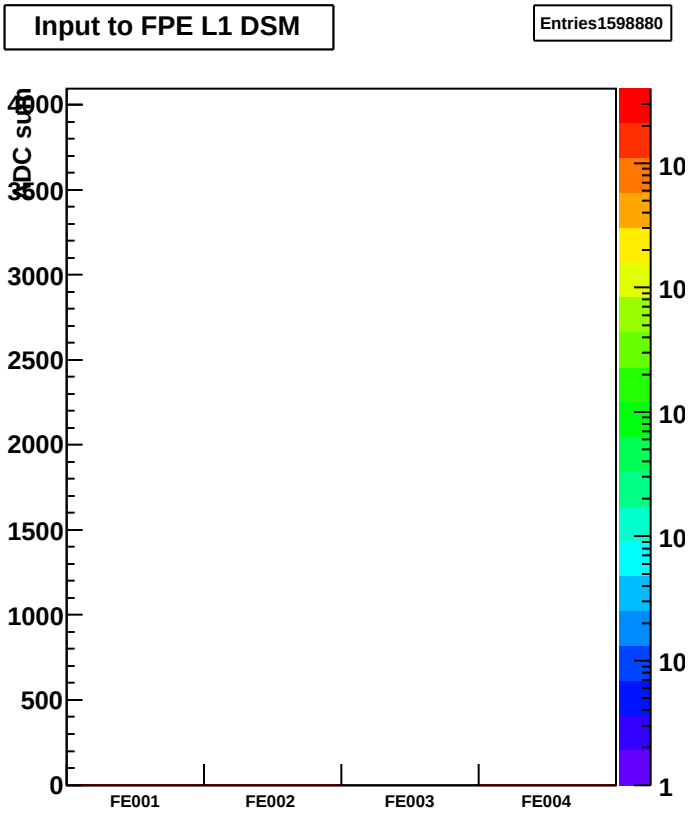

Input to FPD L2 DSM
Entries 2398308
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

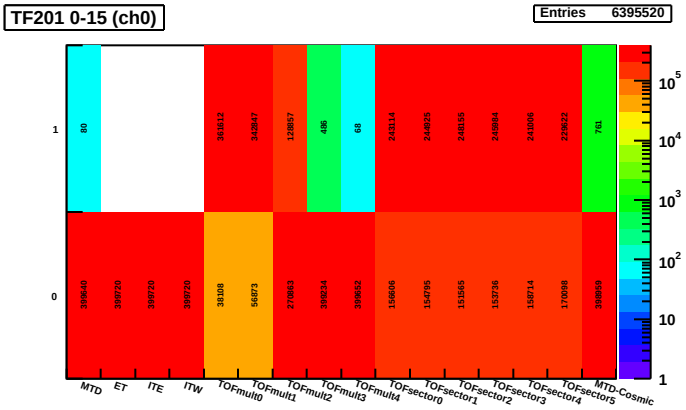

Input to FE001 QT board
Entries 1.279104e+07
ADC
channel

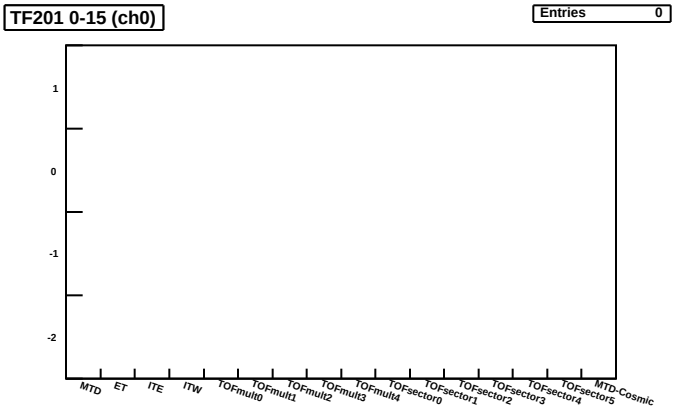

Input to FE002 QT board
Entries 1.279104e+07
ADC
channel

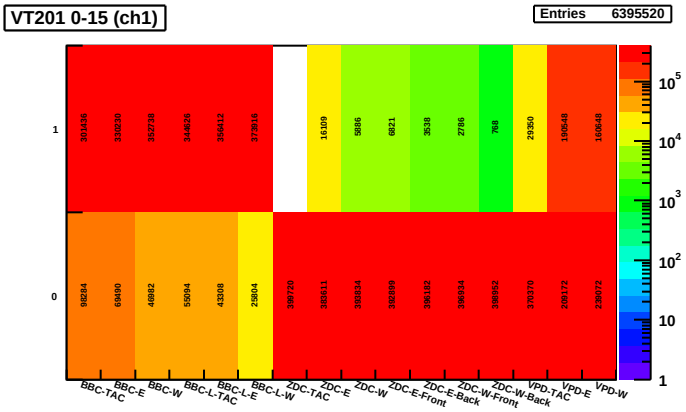

Input to FE003 QT board
Entries 1.279104e+07
ADC
channel

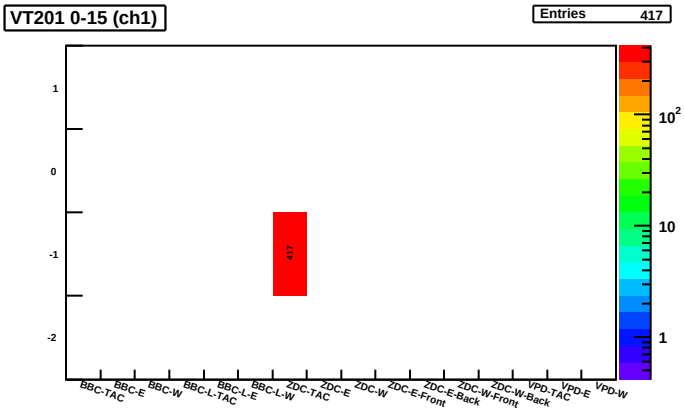

Input to FE004 QT board
Entries 1.279104e+07
ADC
channel

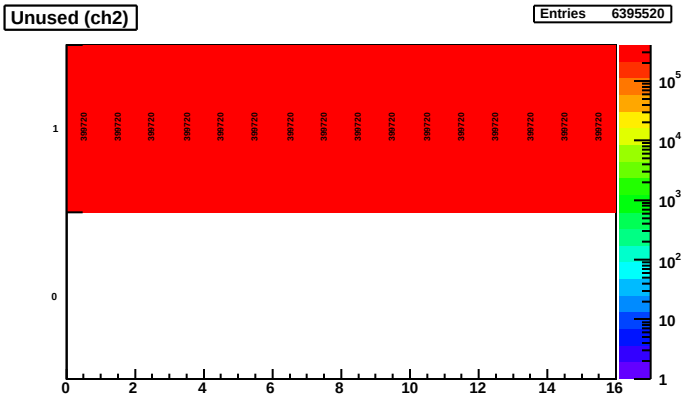

Input to FPE L1 DSM
Entries1598880
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

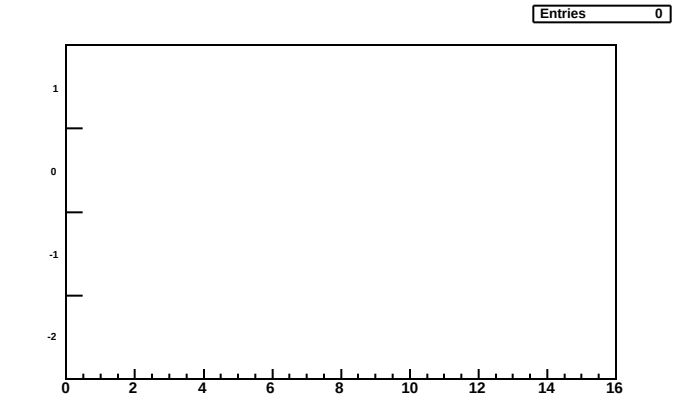

TF201 0-15 (ch0)
Entries 6395520
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 6395520
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 417
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 6395520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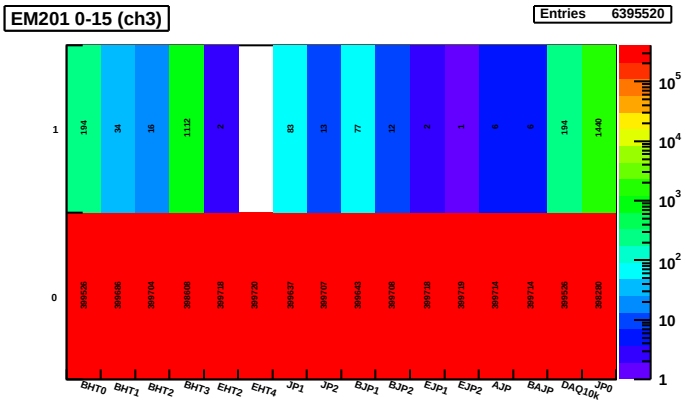
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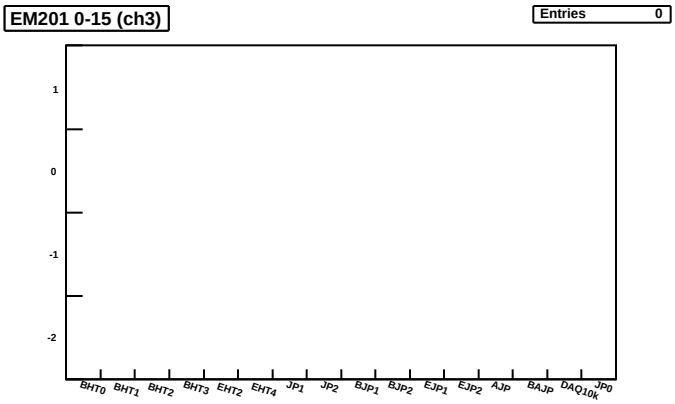
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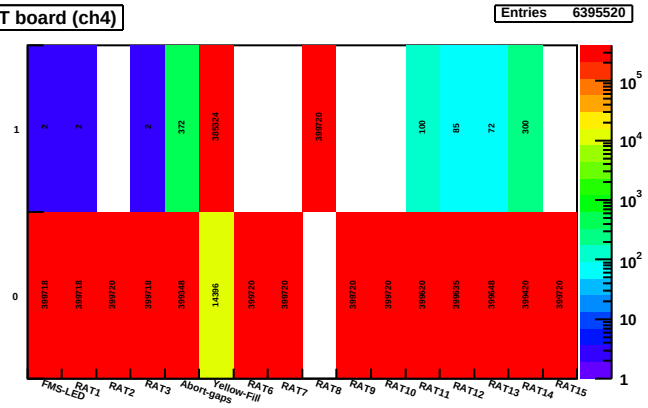
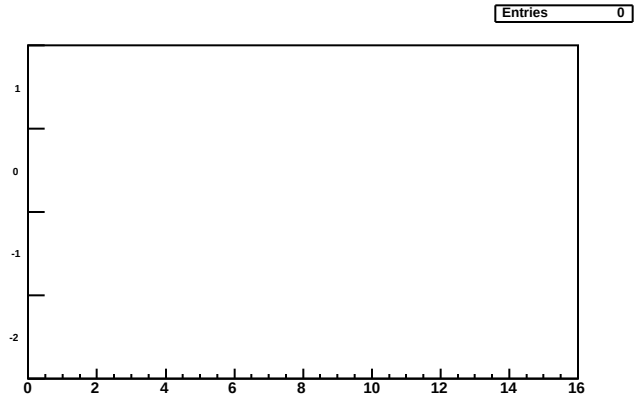
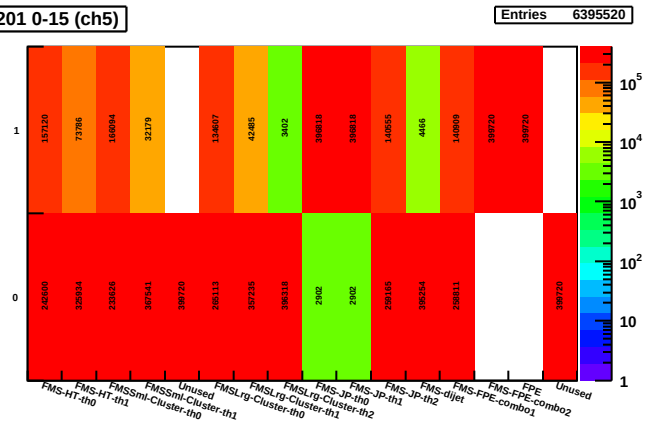
RAT board (ch4)





FP201 0-15 (ch5)



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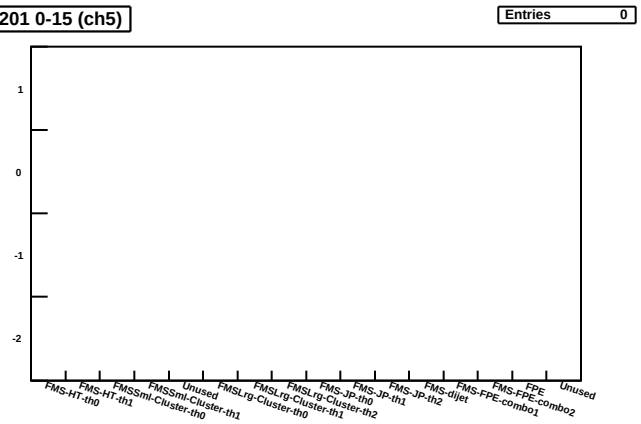

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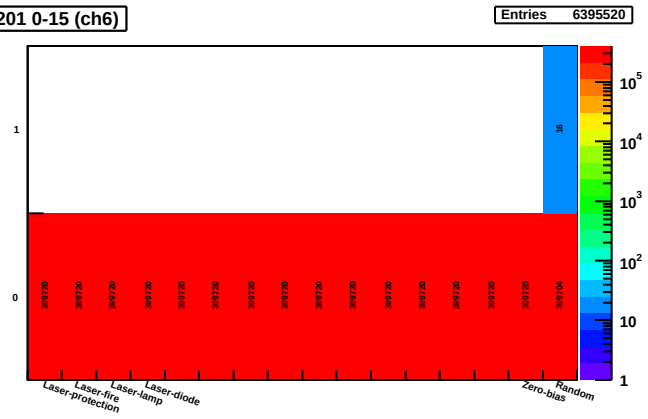

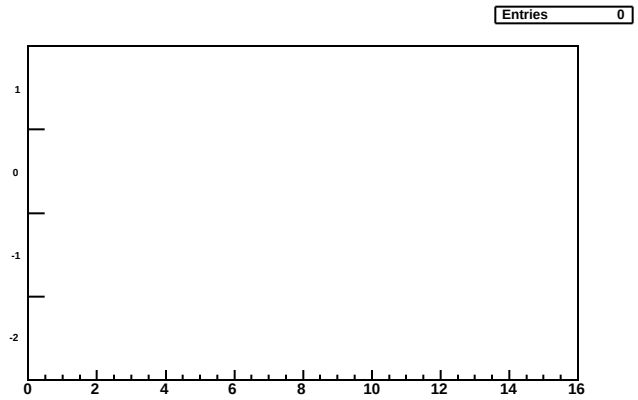
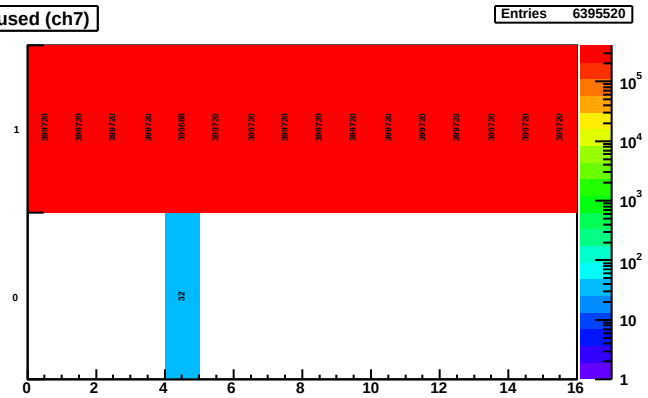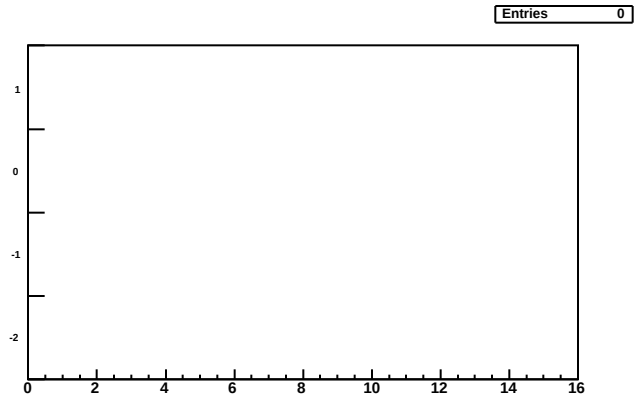
Unused (ch7)