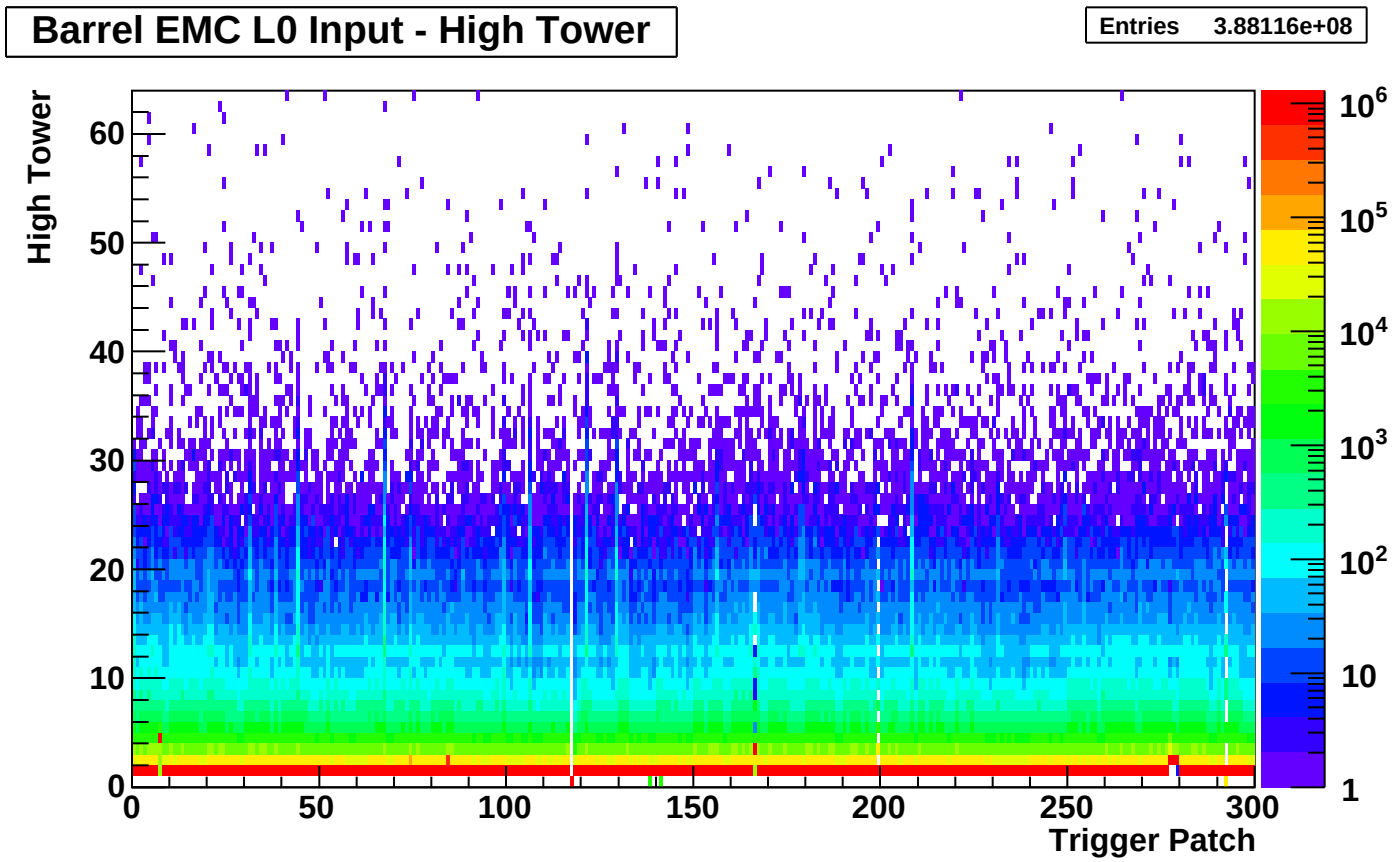
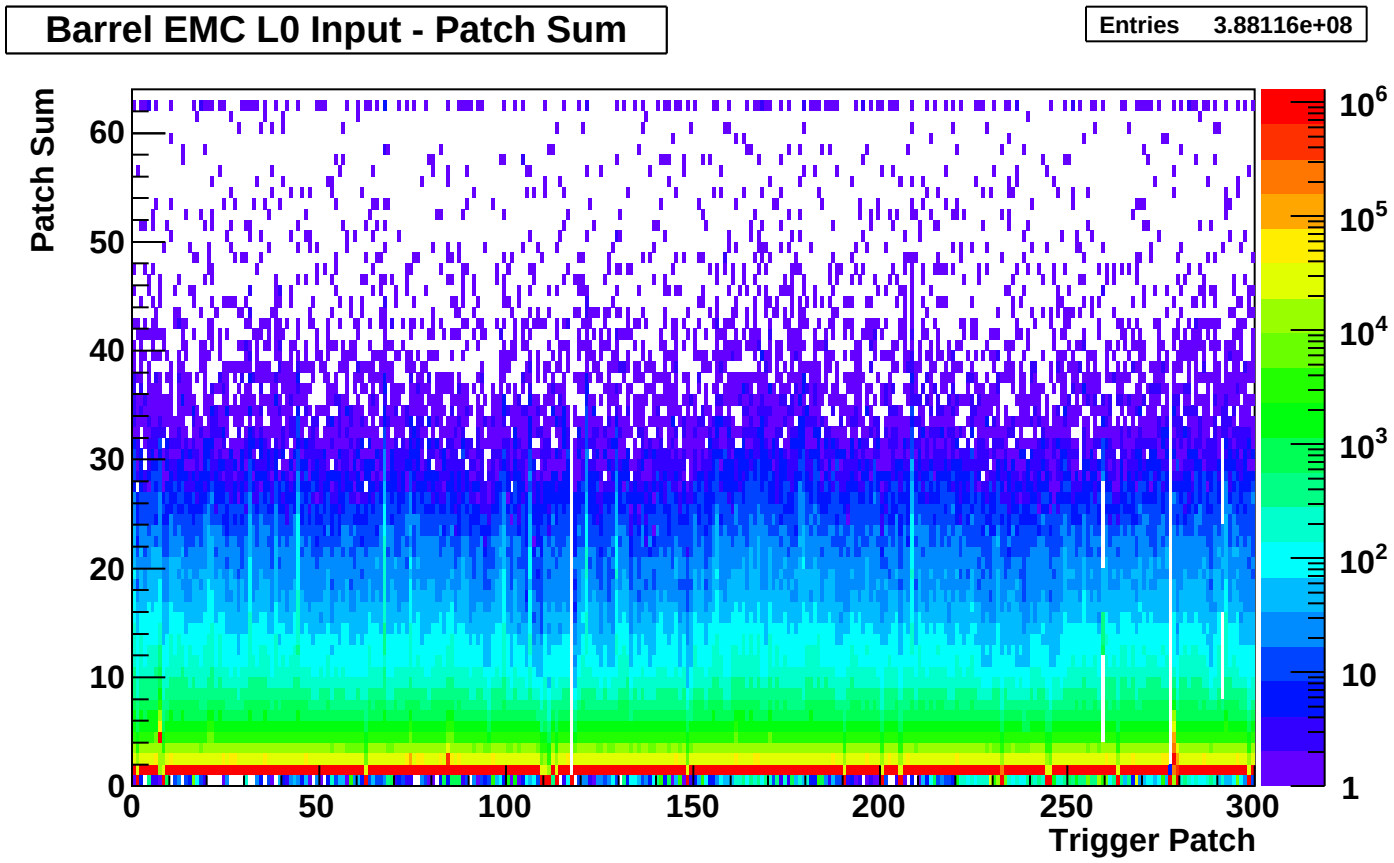
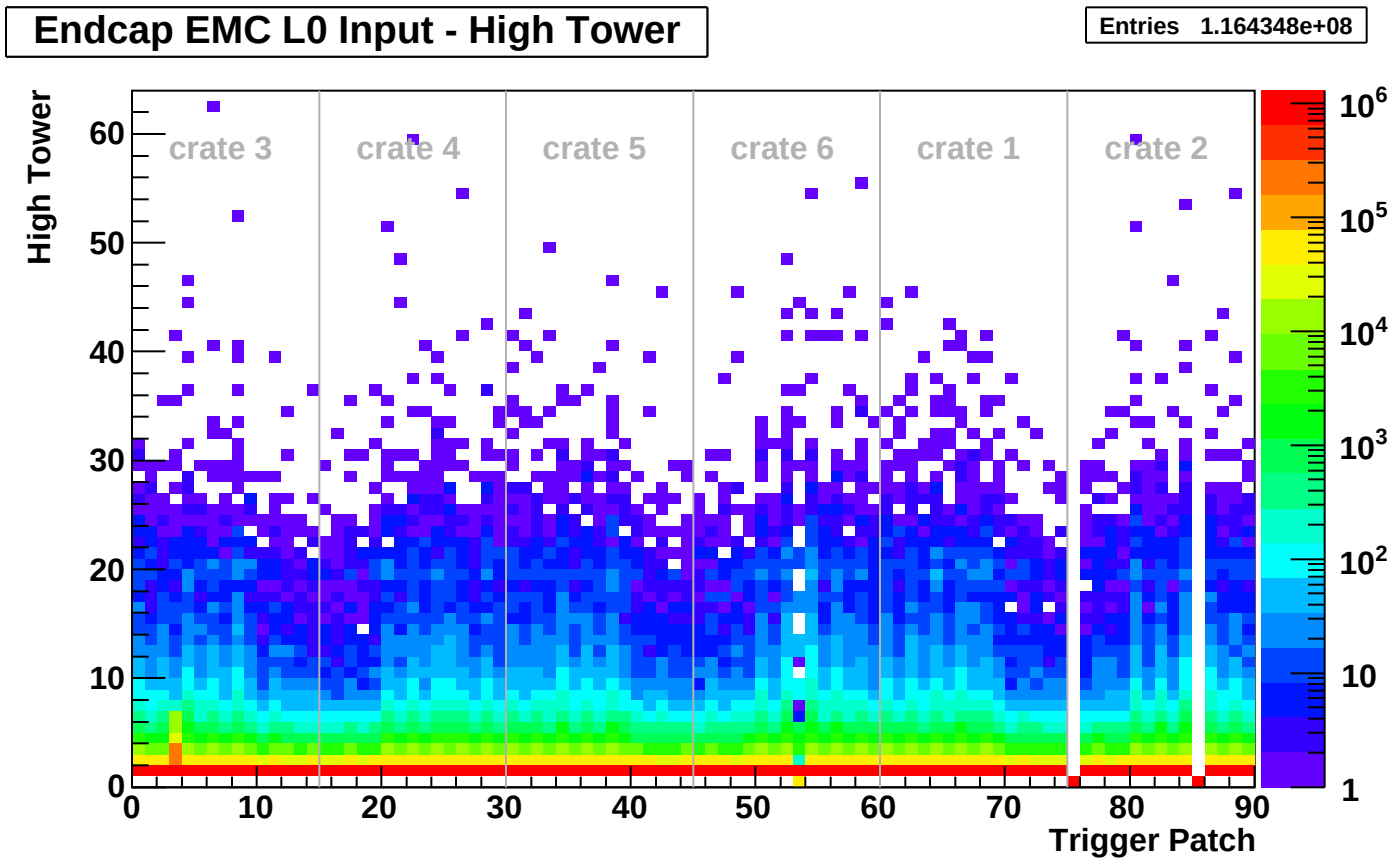
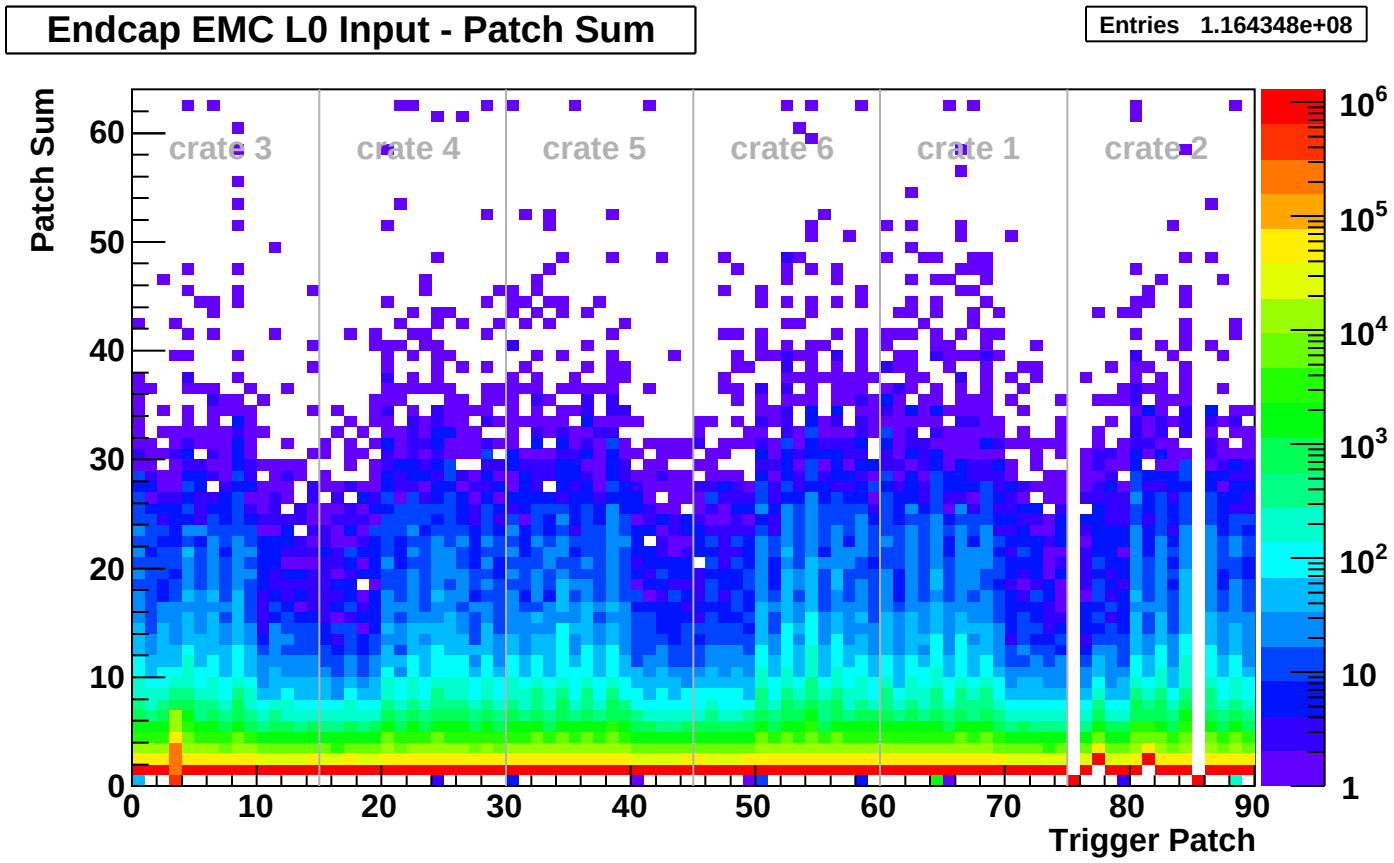
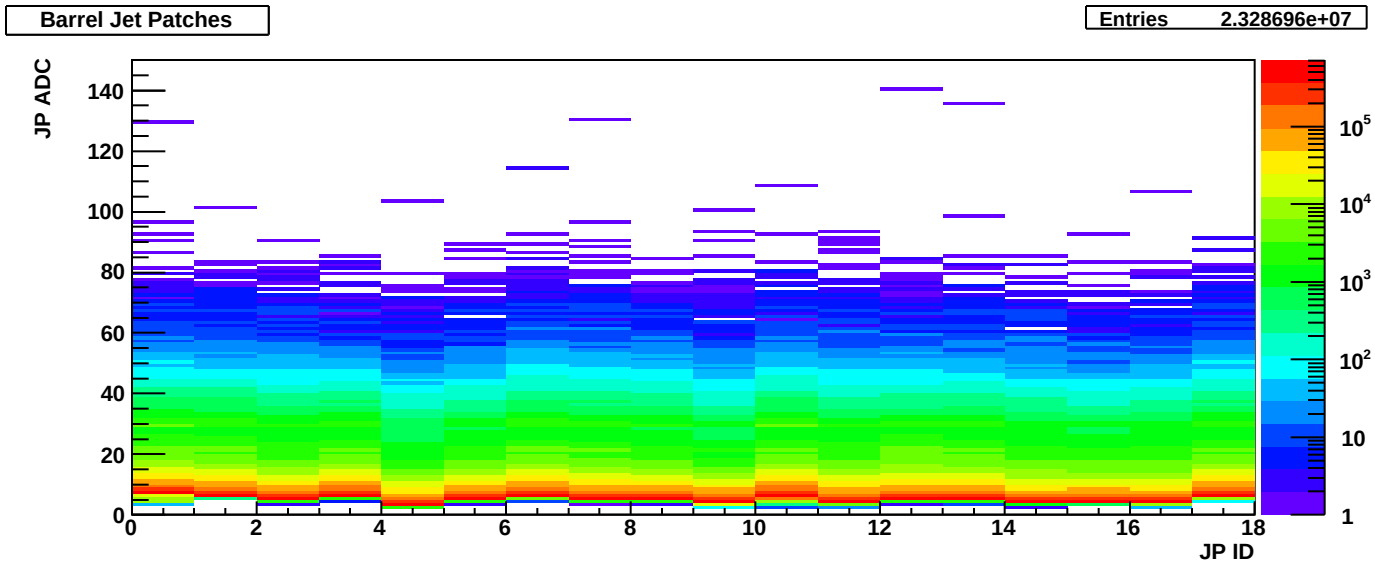
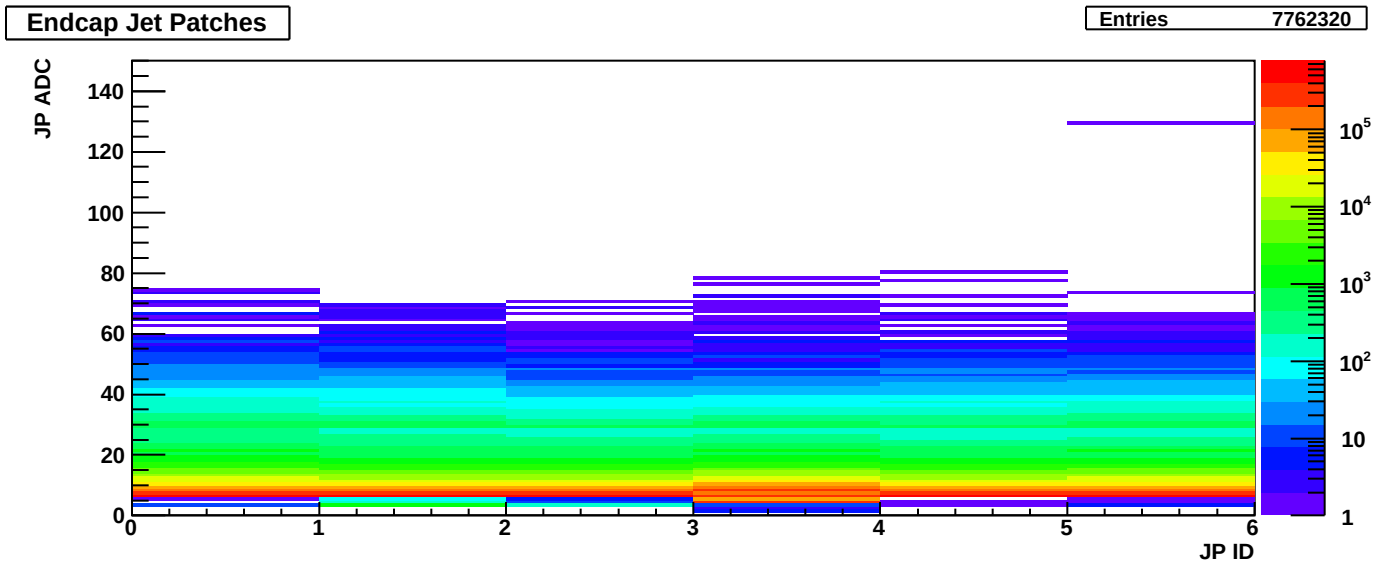

Barrel EMC L0 Input - High Tower
Entries 3.88116e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

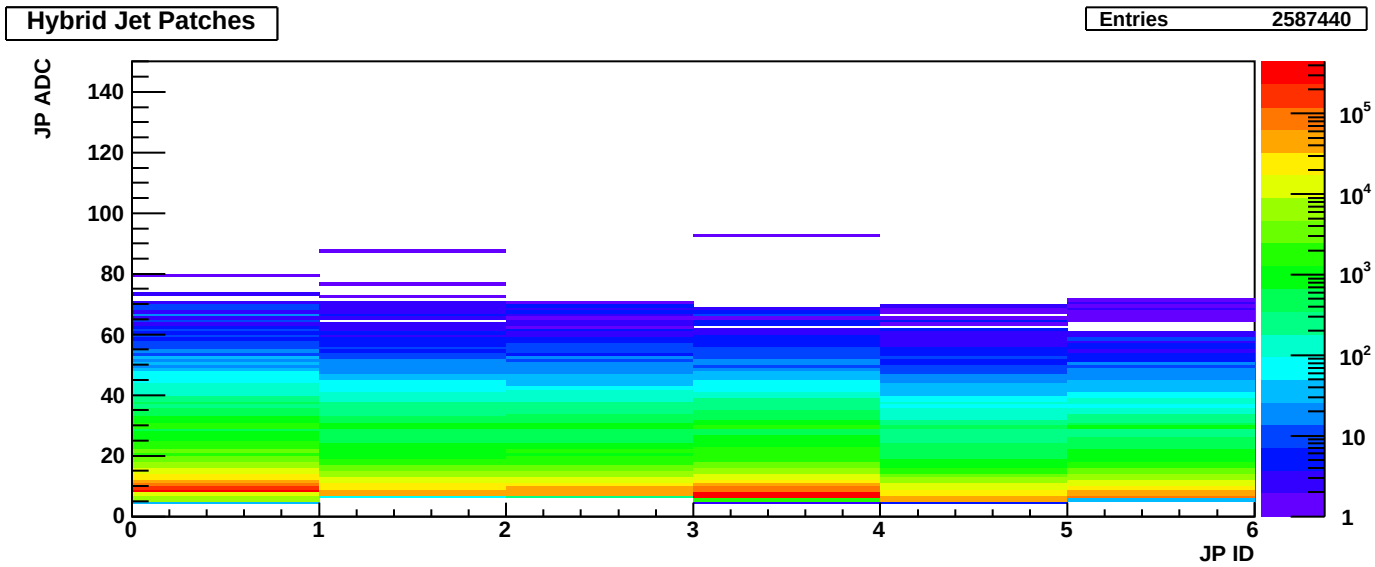

Barrel EMC L0 Input - Patch Sum
Entries 3.88116e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

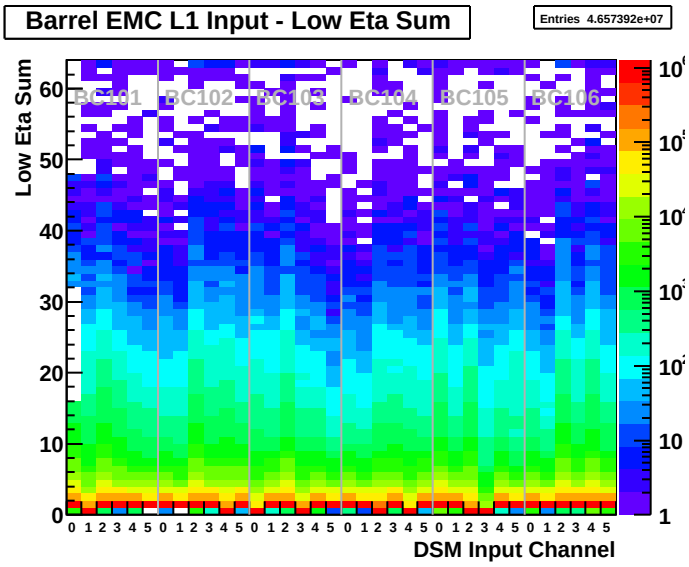

Endcap EMC L0 Input - High Tower
Entries 1.164348e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

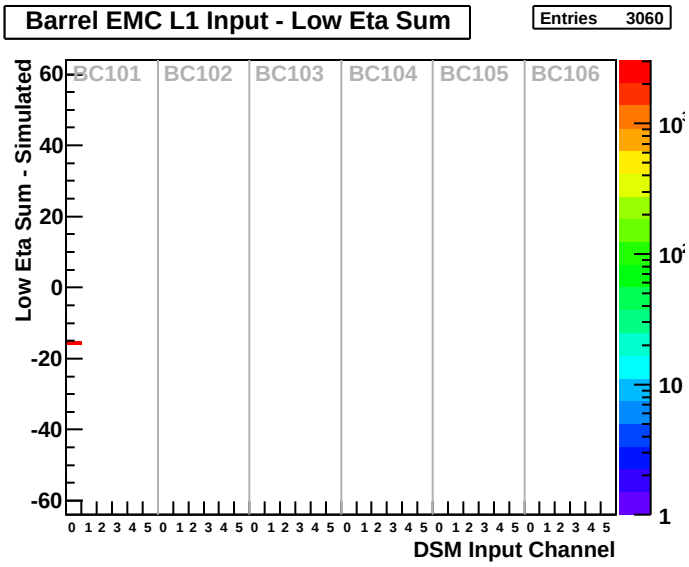

Endcap EMC L0 Input - Patch Sum
Entries 1.164348e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

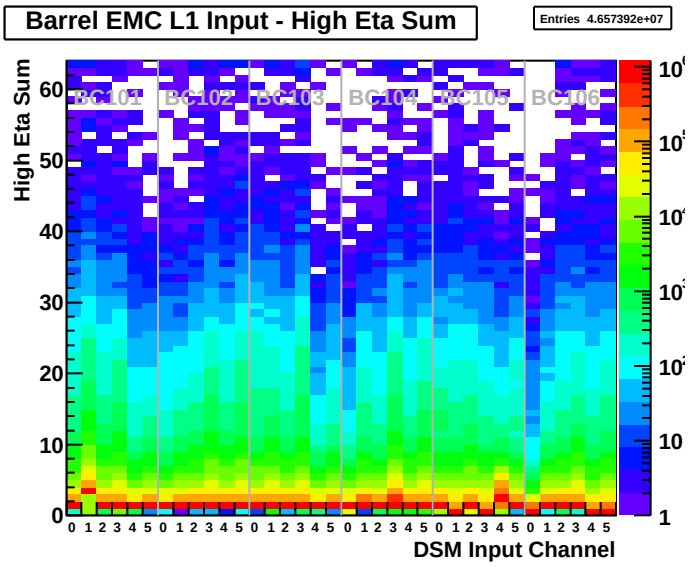

Barrel Jet Patches
Entries 2.328696e+07
JP ADC
JP ID

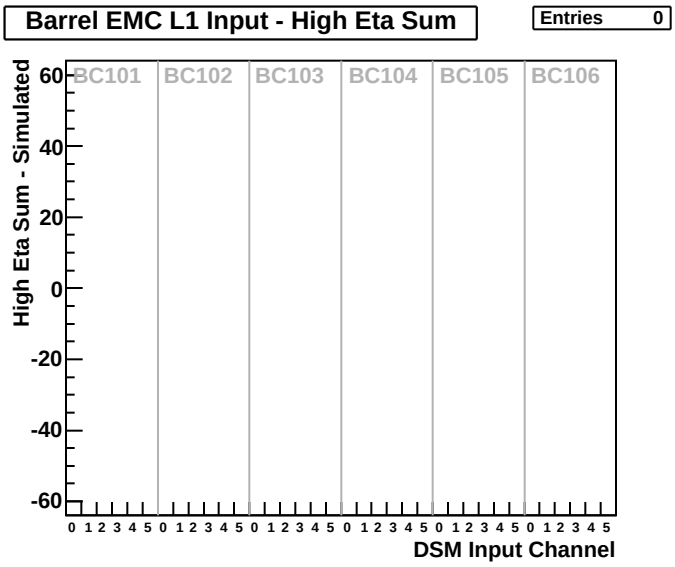

Endcap Jet Patches
Entries 7762320
JP ADC
JP ID

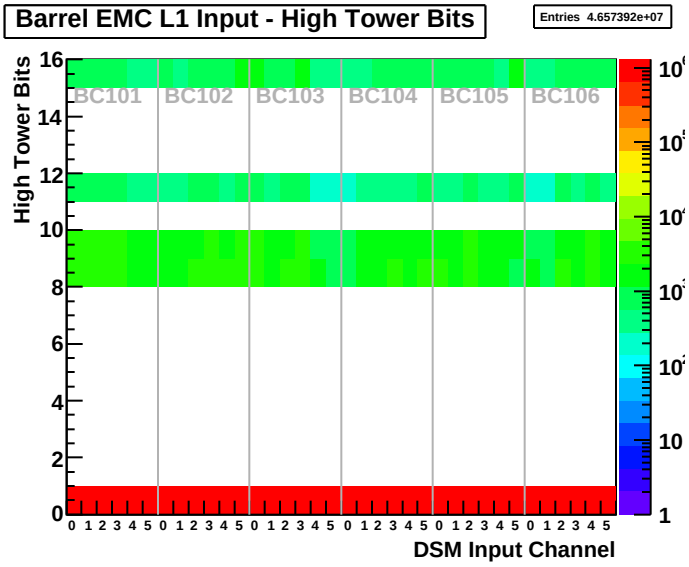

Hybrid Jet Patches
Entries 2587440
JP ADC
JP ID

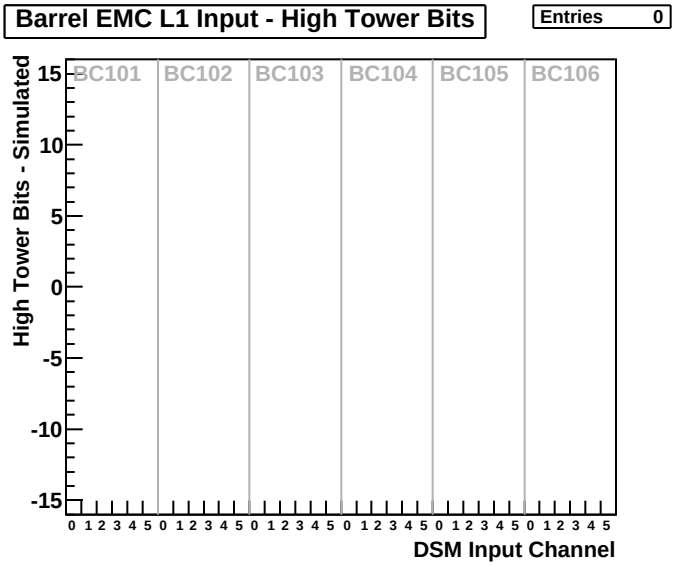

Barrel EMC L1 Input - Low Eta Sum
Entries 4.657392e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

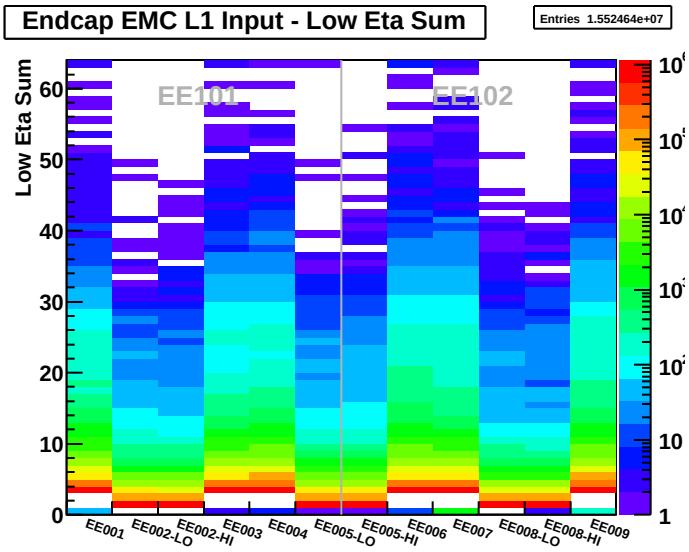

Barrel EMC L1 Input - Low Eta Sum
Entries 3060
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

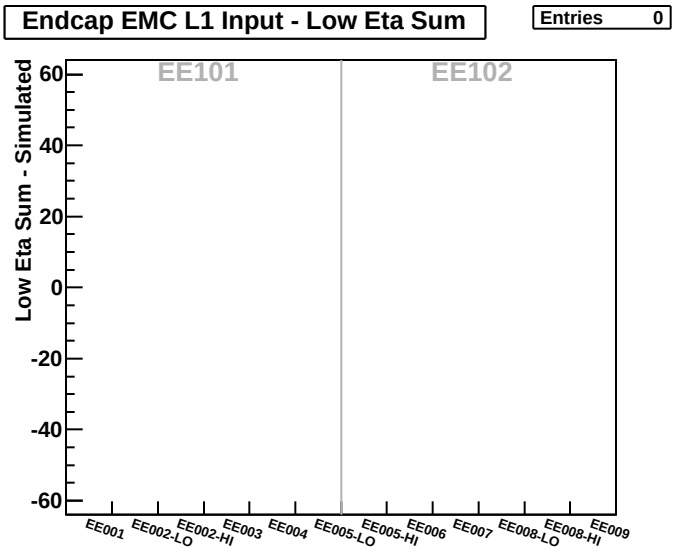

Barrel EMC L1 Input - High Eta Sum
Entries 4.657392e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

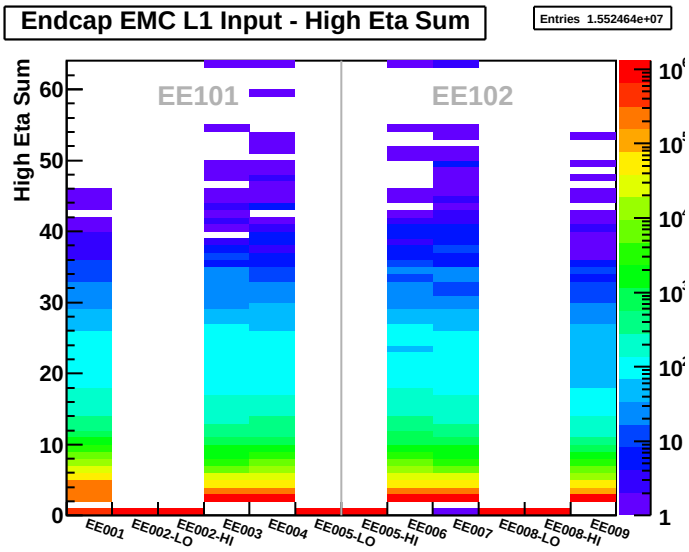

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

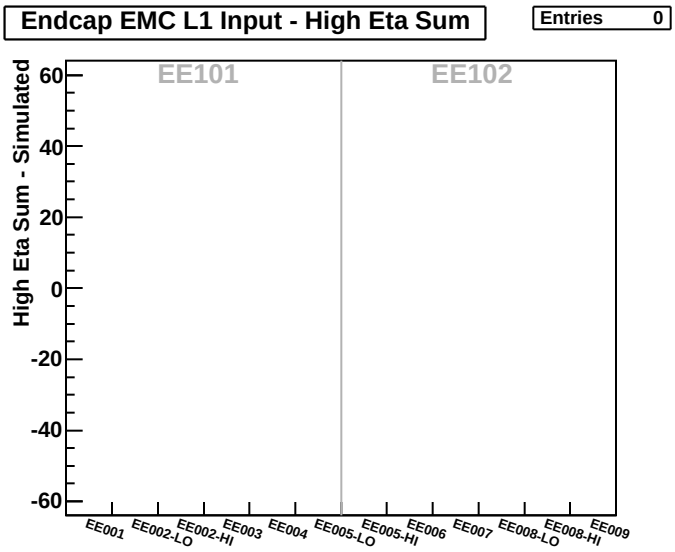

Barrel EMC L1 Input - High Tower Bits
Entries 4.657392e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

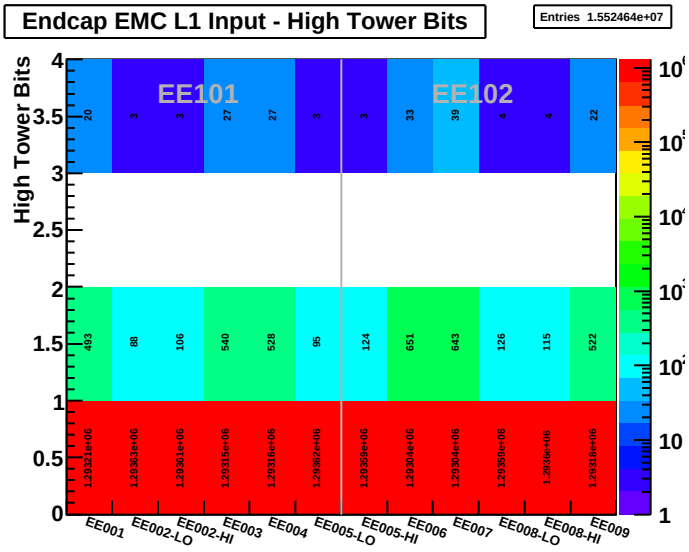

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

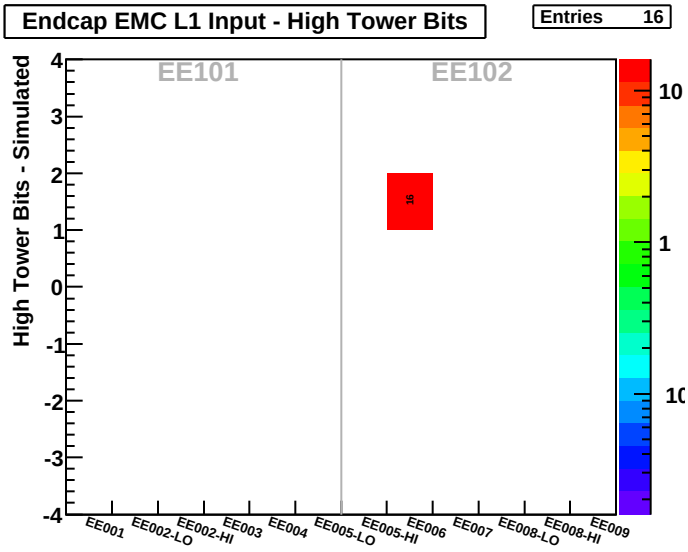

Endcap EMC L1 Input - Low Eta Sum
Entries 1.552464e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

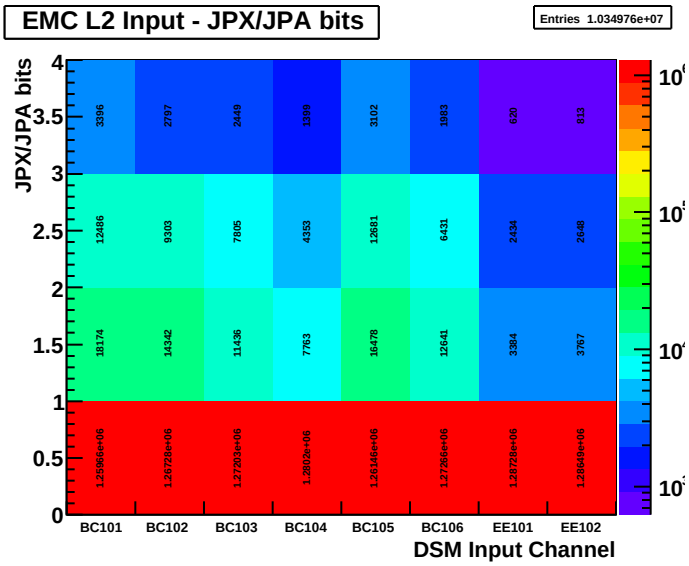

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

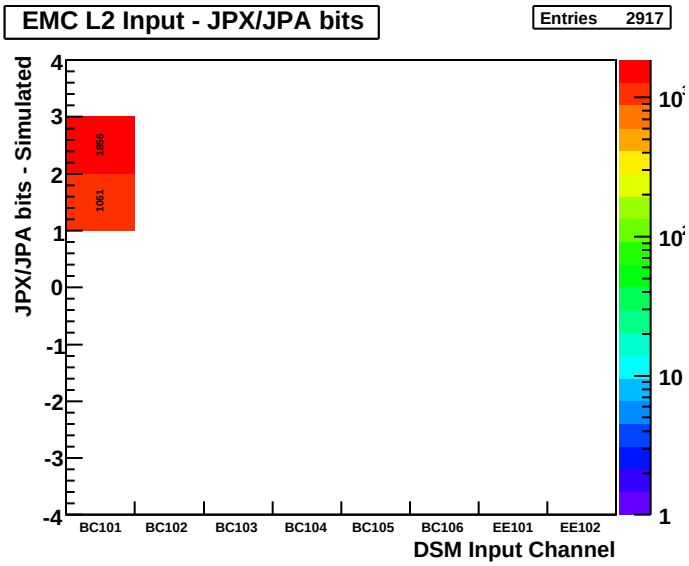

Endcap EMC L1 Input - High Eta Sum
Entries 1.552464e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

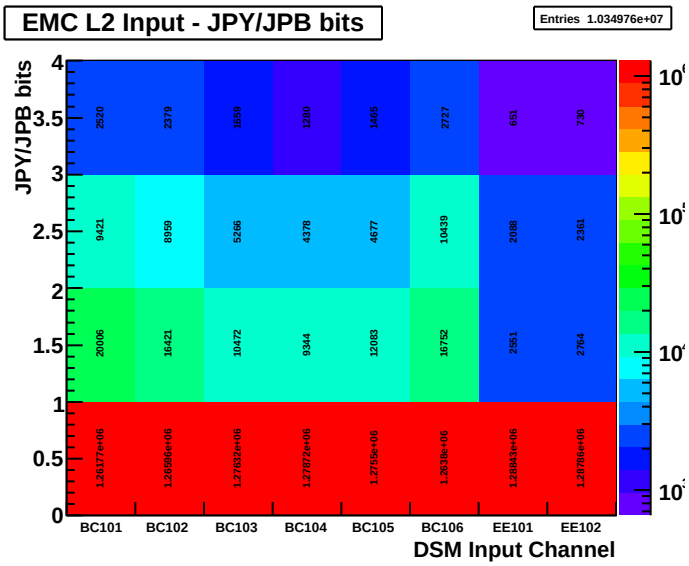

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

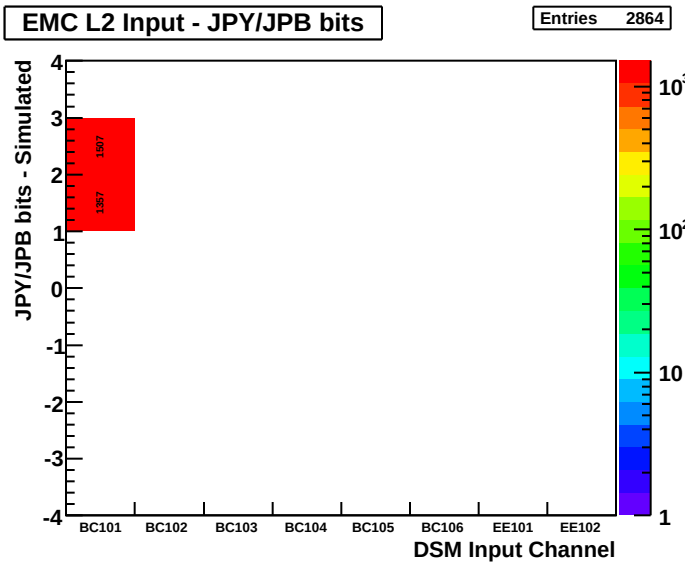

Endcap EMC L1 Input - High Tower Bits
Entries 1.552464e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

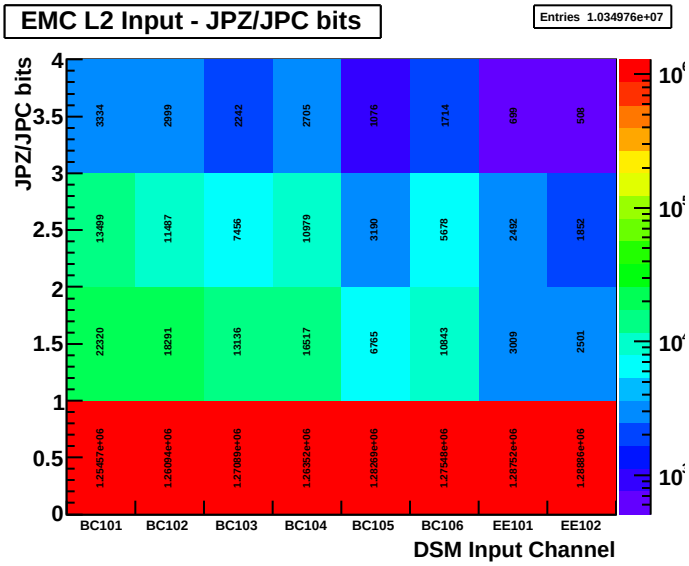

Endcap EMC L1 Input - High Tower Bits
Entries 16
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

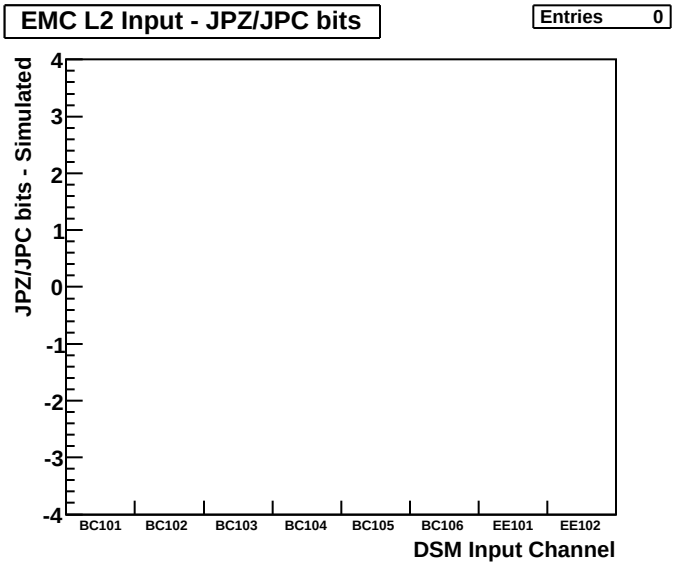

EMC L2 Input - JPX/JPA bits
Entries 1.034976e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3396 2797 2449 1389 3102 1983 620 813
12486 9303 7805 4353 12681 6431 2434 2648
18174 14342 11436 7763 16478 12641 3384 3767
1.25956e+06 1.26728e+06 1.27303e+06 1.28092e+06 1.26146e+06 1.27356e+06 1.28728e+06 1.28649e+06

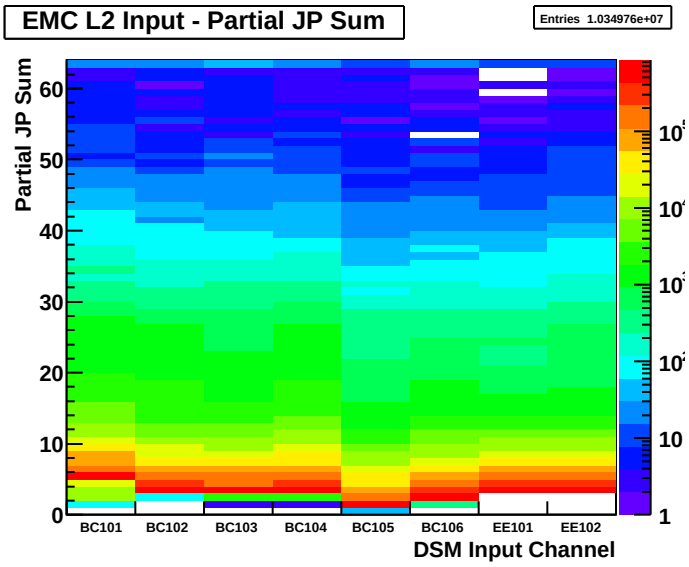

EMC L2 Input - JPX/JPA bits
Entries 2917
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1856
1061

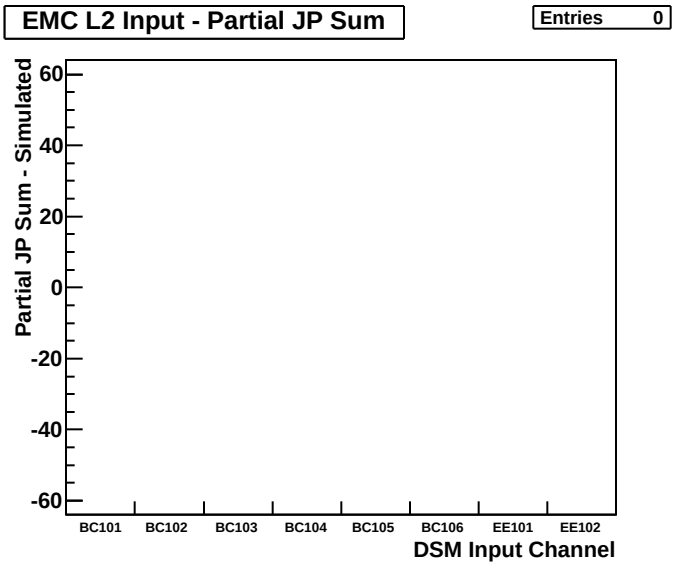

EMC L2 Input - JPY/JPB bits
Entries 1.034976e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2520 2379 1659 1280 1465 2727 651 730
9421 8959 5266 4378 4677 10439 2088 2361
20006 16421 10472 9344 12083 16752 2551 2764
1.26177e+06 1.26596e+06 1.27632e+06 1.27872e+06 1.2755e+06 1.2638e+06 1.28943e+06 1.28786e+06

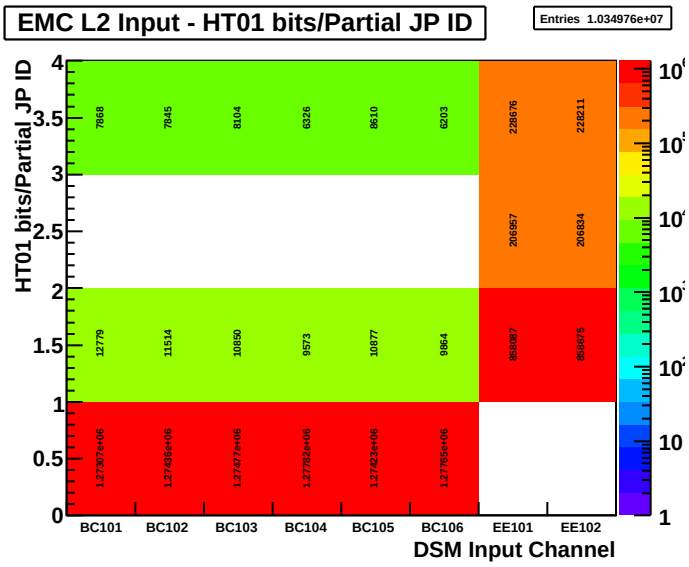

EMC L2 Input - JPY/JPB bits
Entries 2864
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1507
1357

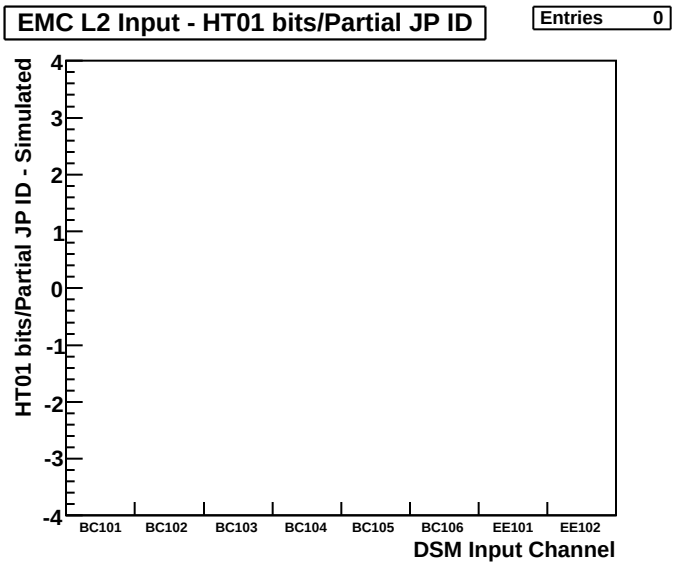

EMC L2 Input - JPZ/JPC bits
Entries 1.034976e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3334 2999 2242 2705 1076 1714 699 508
13499 11487 7456 10979 3190 5678 2492 1852
22320 18291 13136 16517 6765 10843 3009 2501
1.25457e+06 1.26094e+06 1.27093e+06 1.26352e+06 1.28269e+06 1.27548e+06 1.28752e+06 1.28886e+06

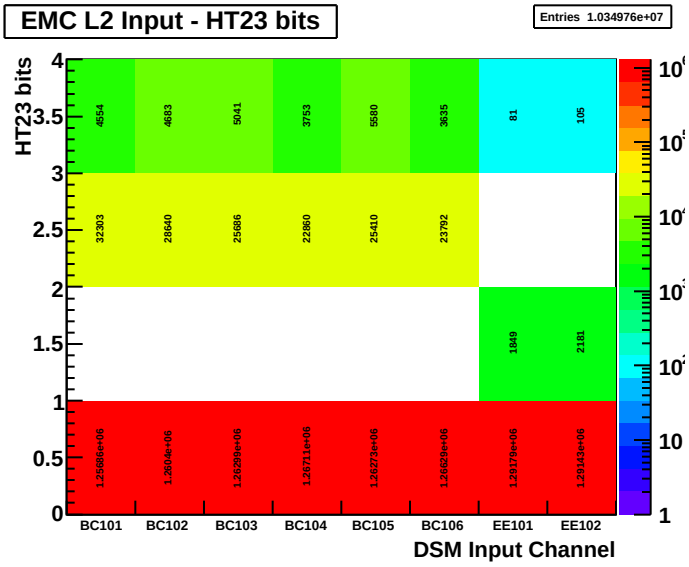

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

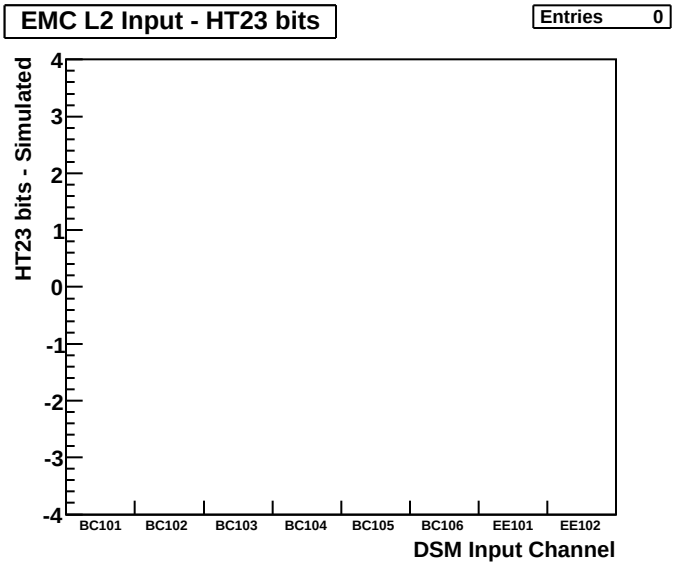

EMC L2 Input - Partial JP Sum
Entries 1.034976e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

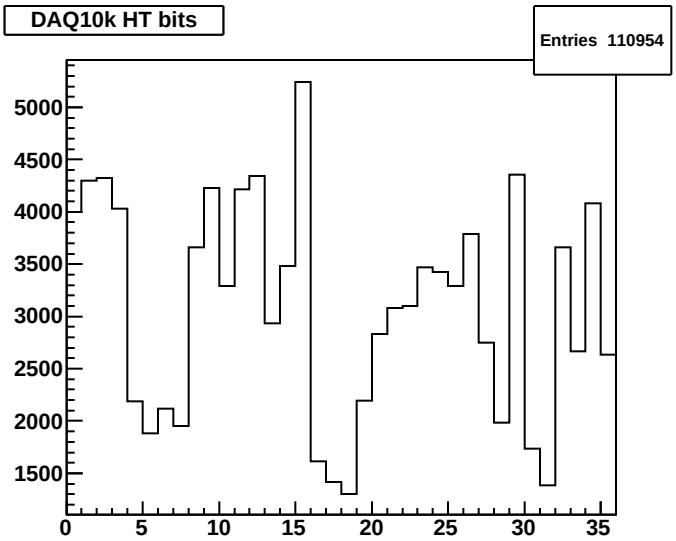

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

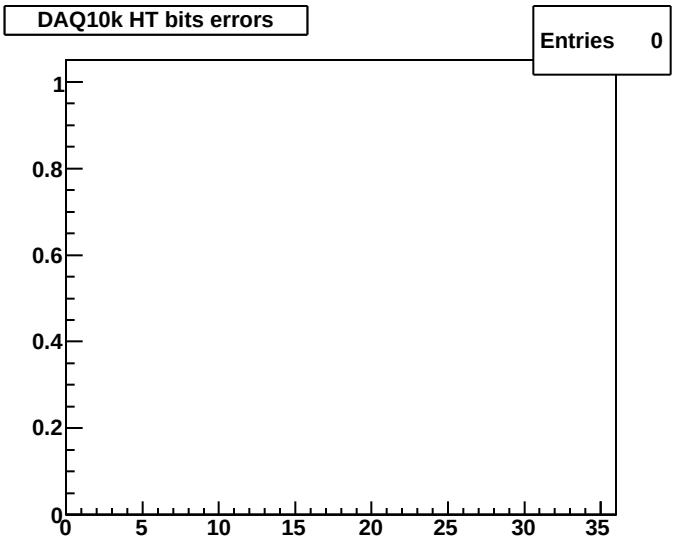

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.034976e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

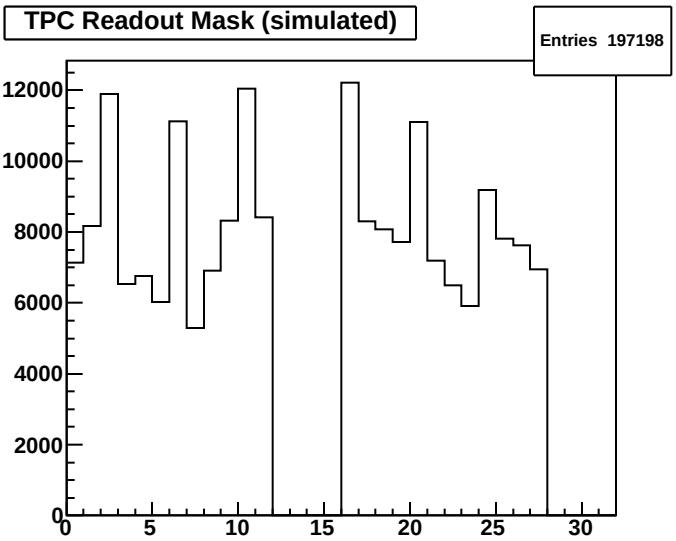

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

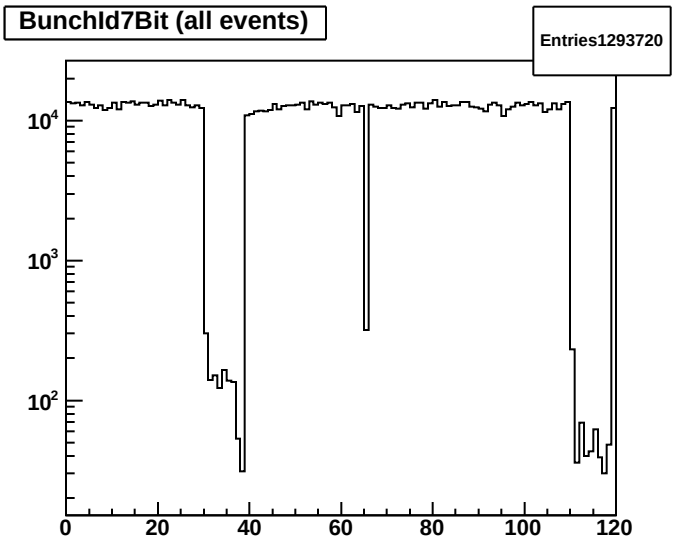

EMC L2 Input - HT23 bits
Entries 1.034976e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

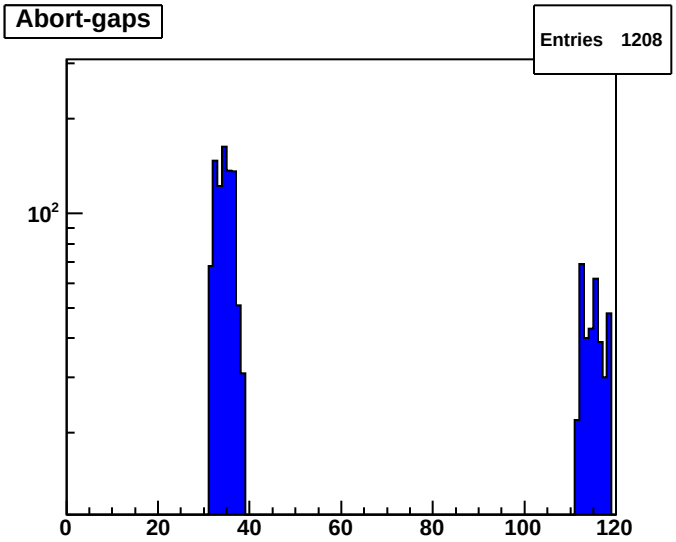

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

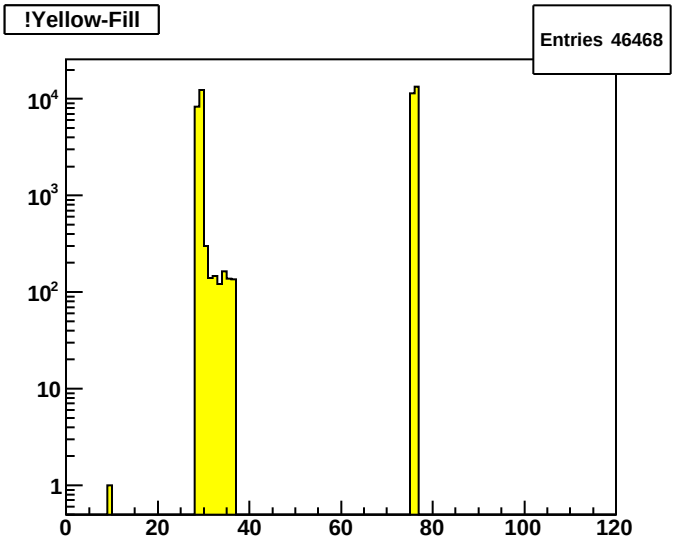

DAQ10k HT bits
Entries 110954

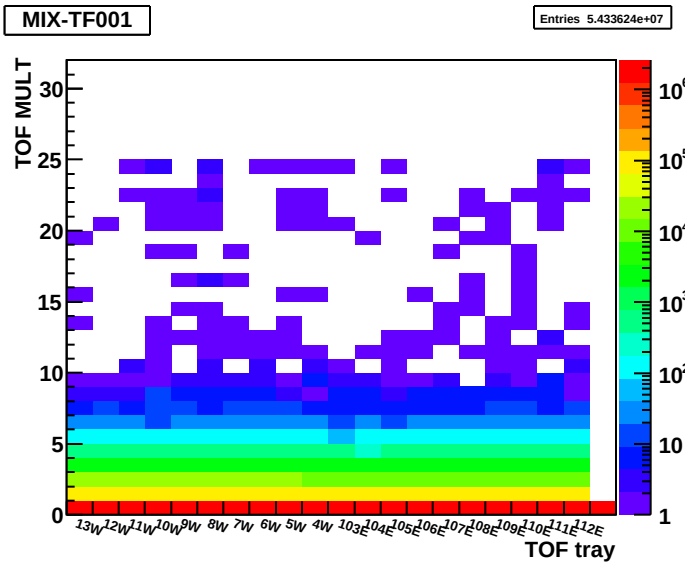

DAQ10k HT bits errors
Entries 0

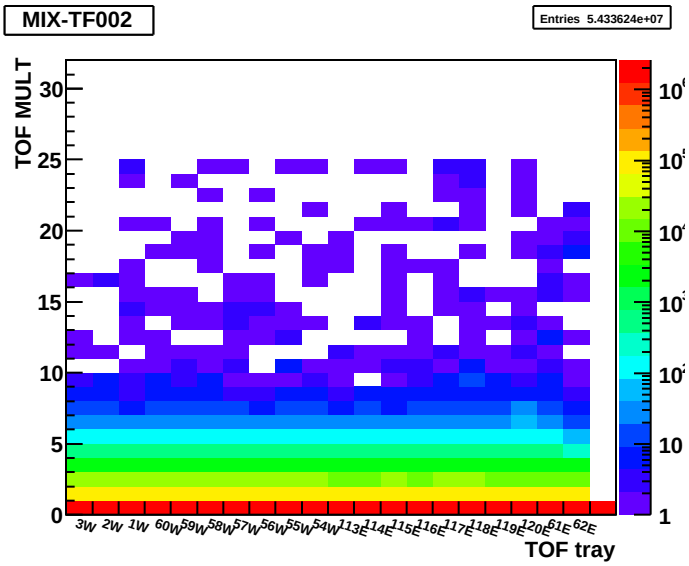

TPC Readout Mask (simulated)
Entries 197198

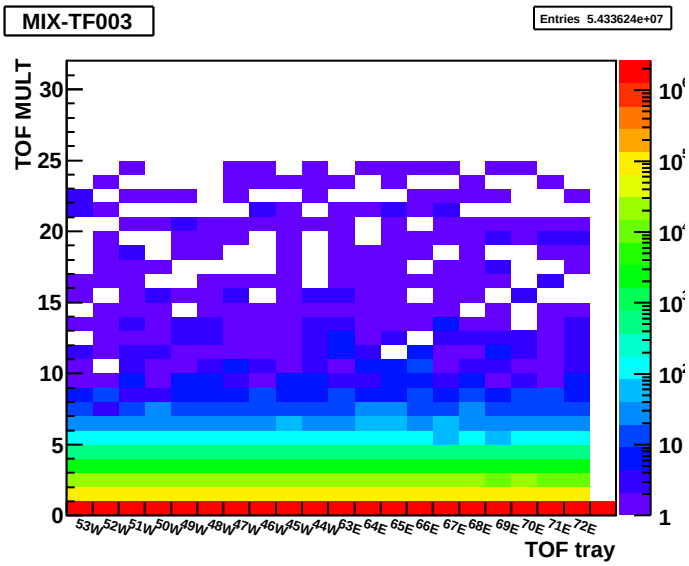

BunchId7Bit (all events)
Entries1293720

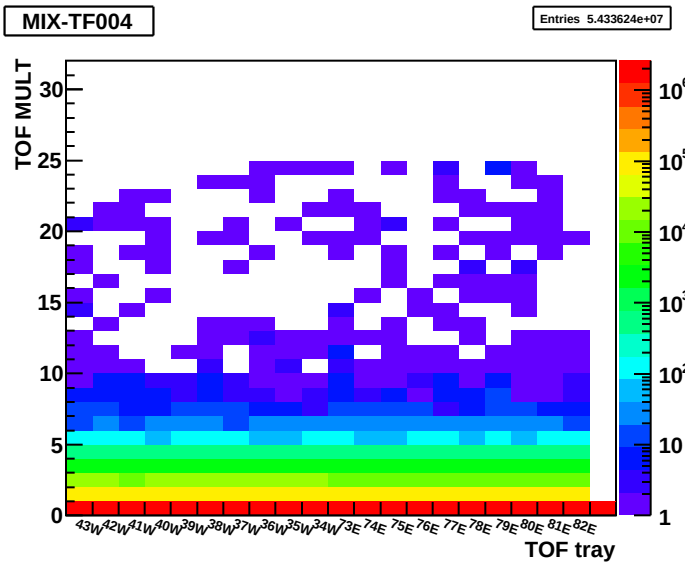

Abort-gaps
Entries 1208

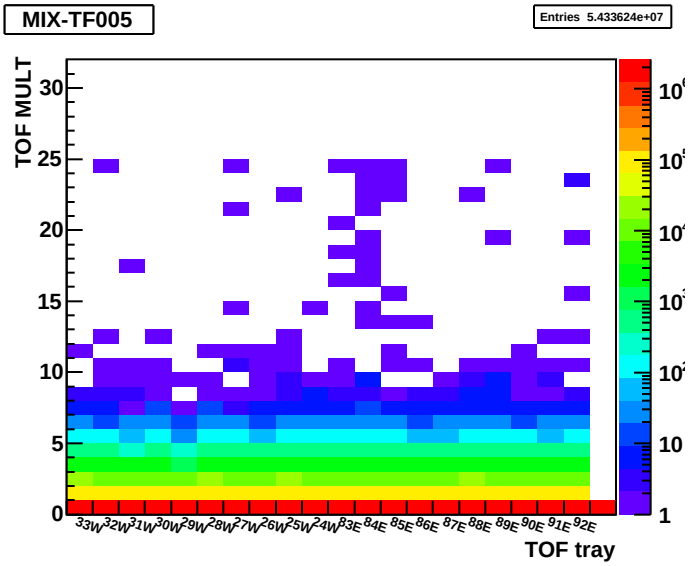

!Yellow-Fill
Entries 46468

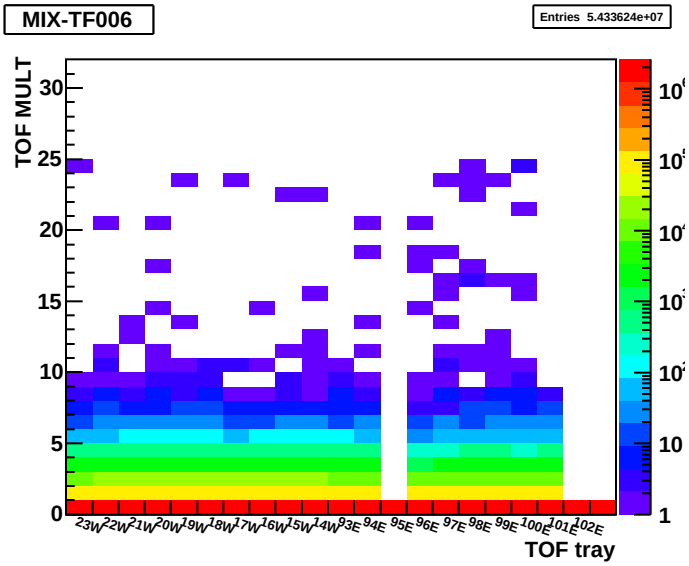

MIX-TF001
Entries 5.433624e+07
TOF MULT
TOF tray

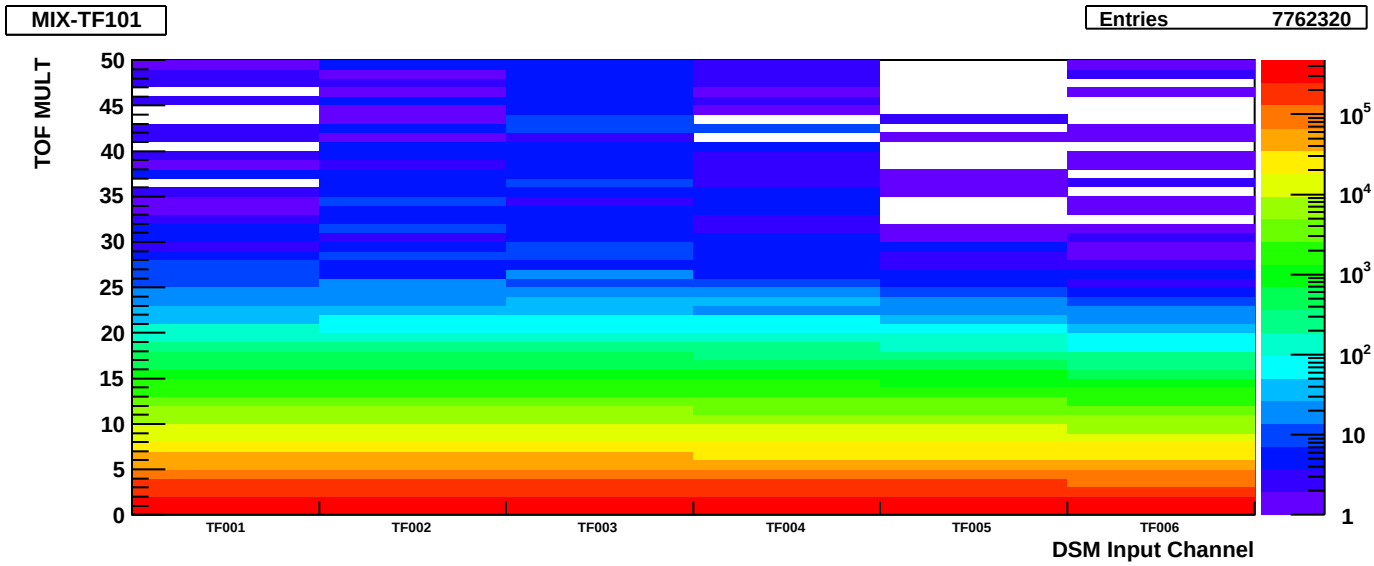

MIX-TF002
Entries 5.433624e+07
TOF MULT
TOF tray

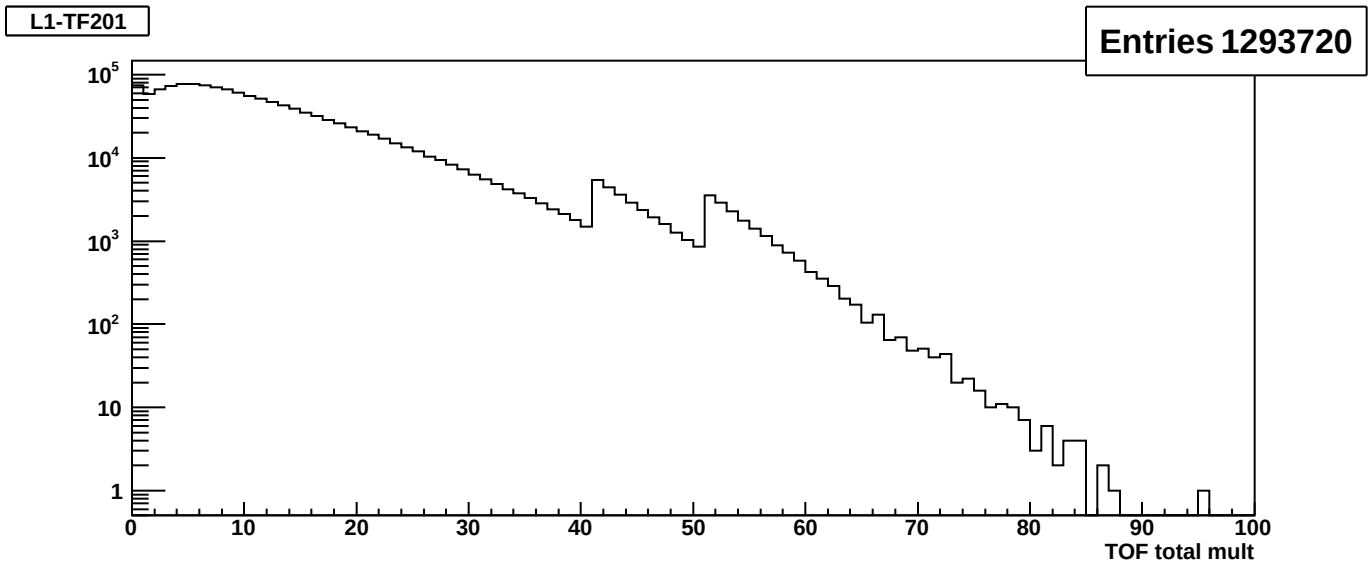

MIX-TF003
Entries 5.433624e+07
TOF MULT
TOF tray

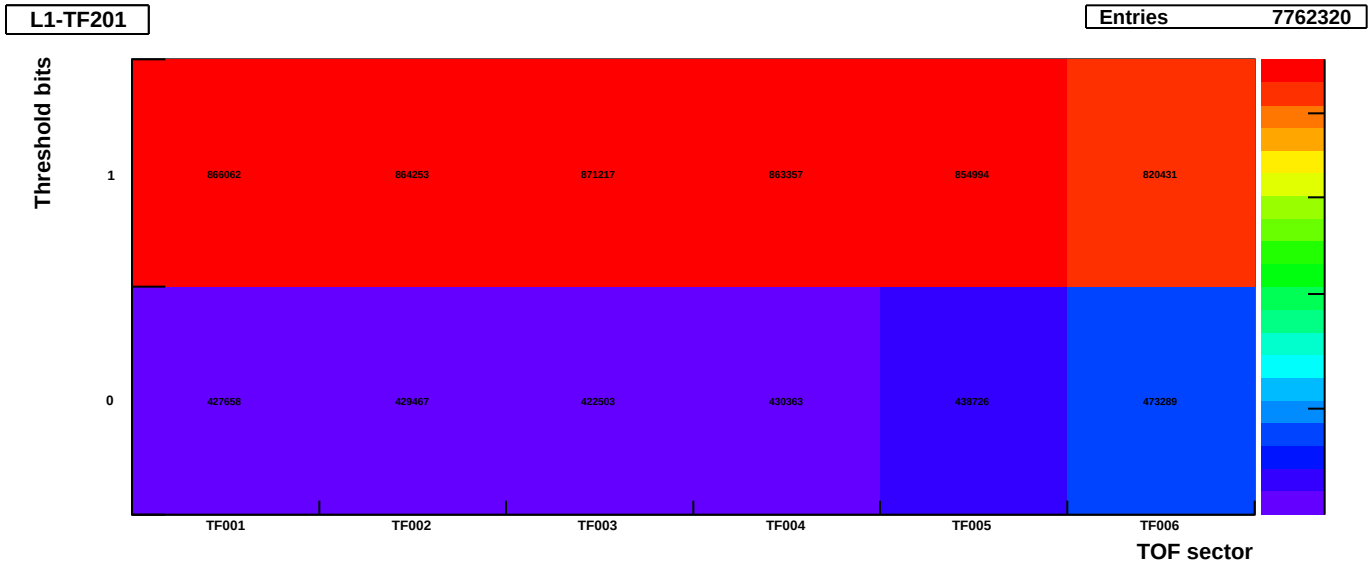

MIX-TF004
Entries 5.433624e+07
TOF MULT
TOF tray

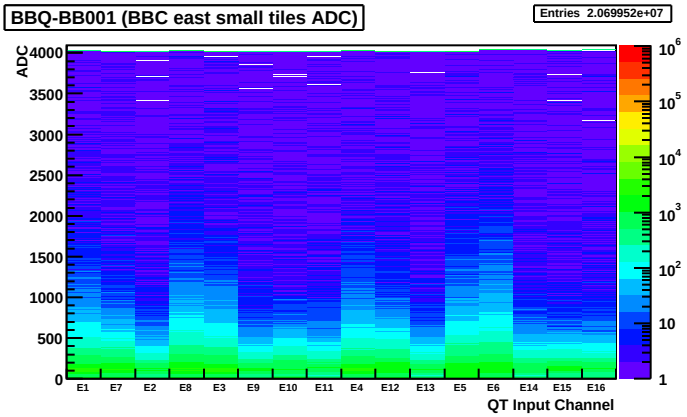

MIX-TF005
Entries 5.433624e+07
TOF MULT
TOF tray

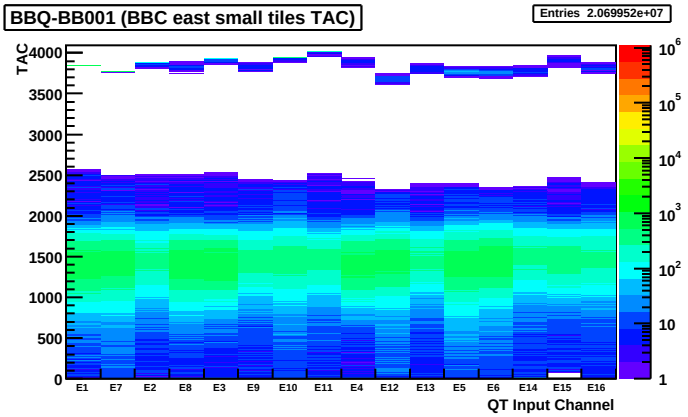

MIX-TF006
Entries 5.433624e+07
TOF MULT
TOF tray

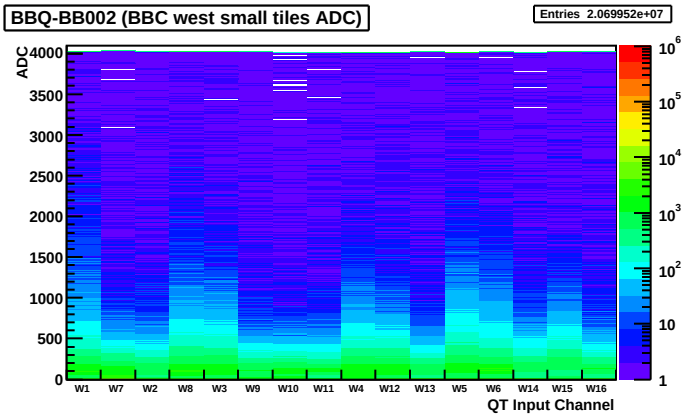

MIX-TF101
Entries 7762320
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

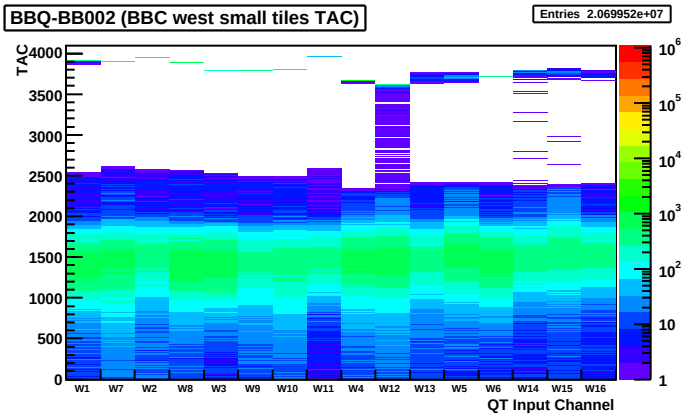

L1-TF201
Entries 1293720
TOF total mult

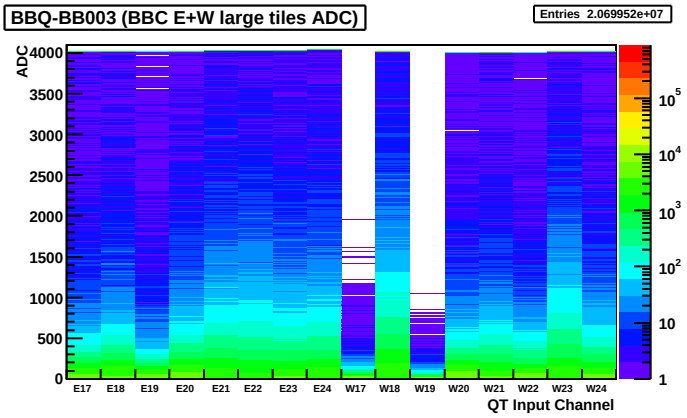

L1-TF201
Entries 7762320
Threshold bits
866062
864253
871217
863357
854994
820431
427658
429467
422503
430363
438726
473289
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

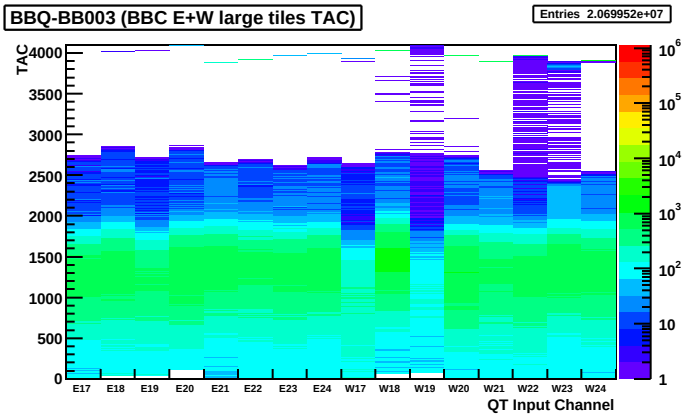

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.069952e+07
ADC
QT Input Channel

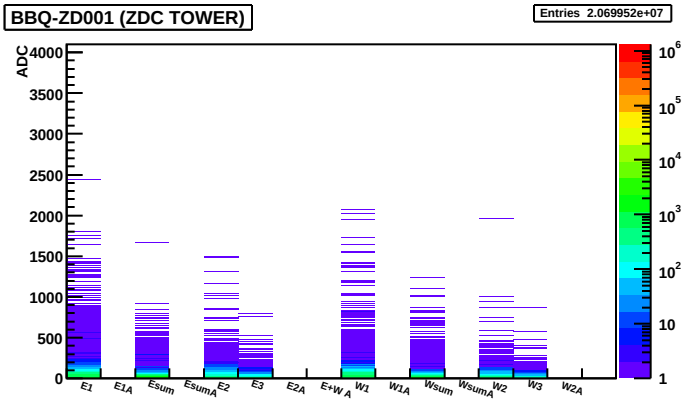

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.069952e+07
TAC
QT Input Channel

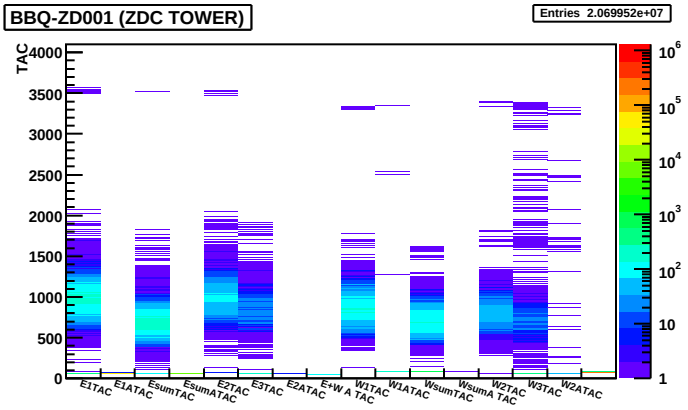

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.069952e+07
ADC
QT Input Channel

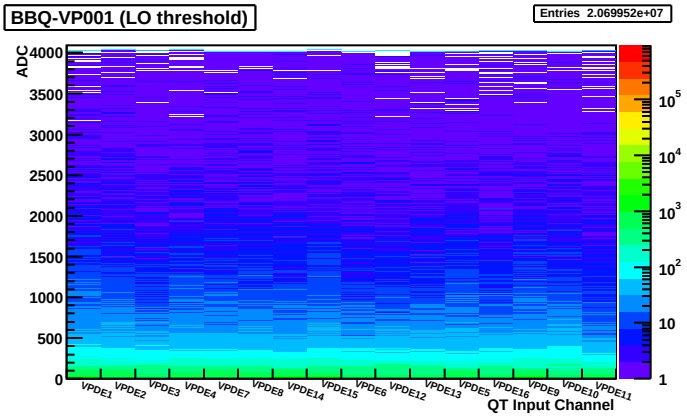

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.069952e+07
TAC
QT Input Channel

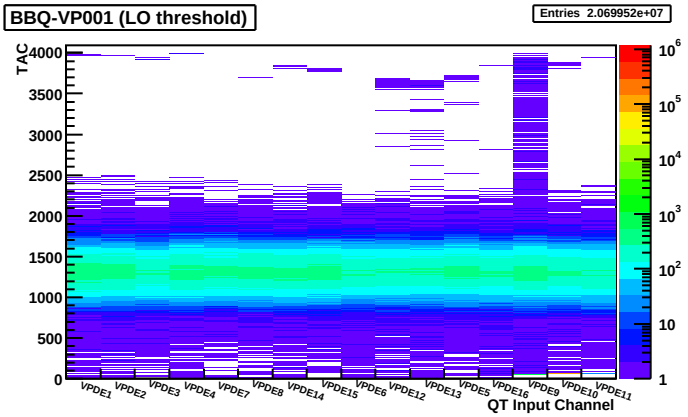

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.069952e+07
ADC
QT Input Channel

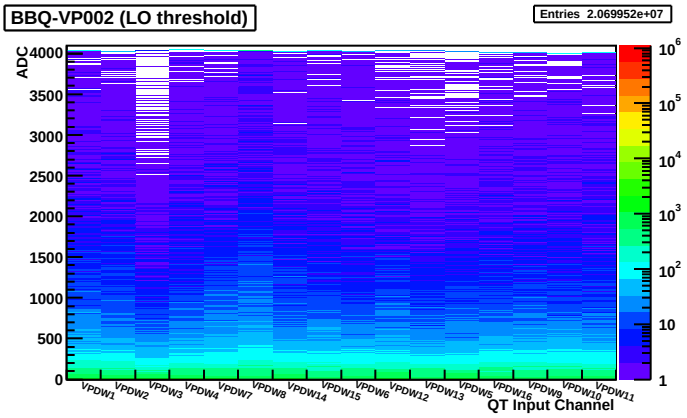

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.069952e+07
TAC
QT Input Channel

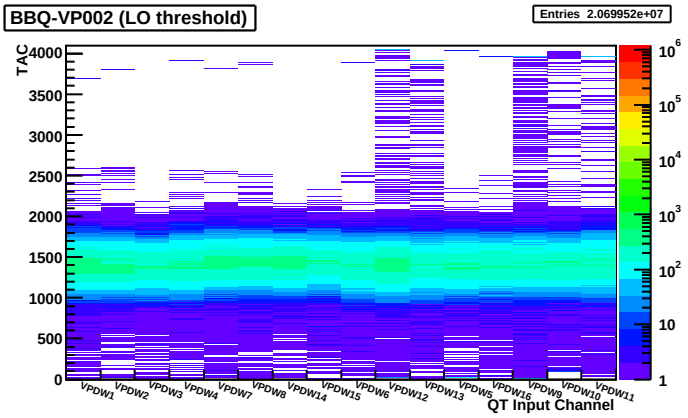

BBQ-ZD001 (ZDC TOWER)
Entries 2.069952e+07
ADC

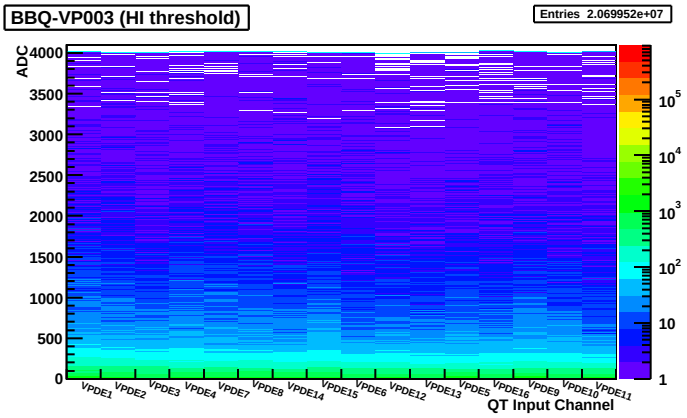

BBQ-ZD001 (ZDC TOWER)
Entries 2.069952e+07
TAC

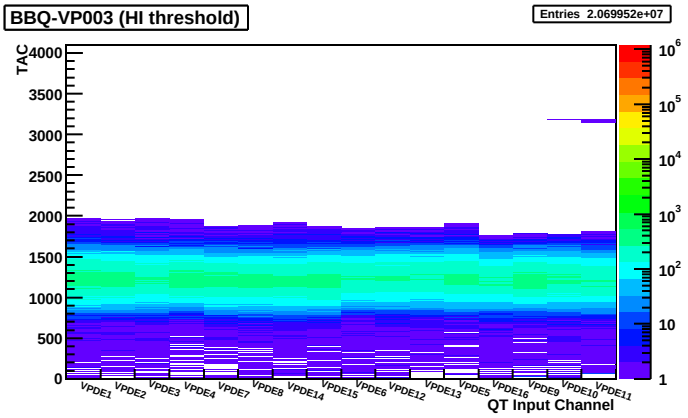

BBQ-VP001 (LO threshold)
Entries 2.069952e+07
ADC
QT Input Channel

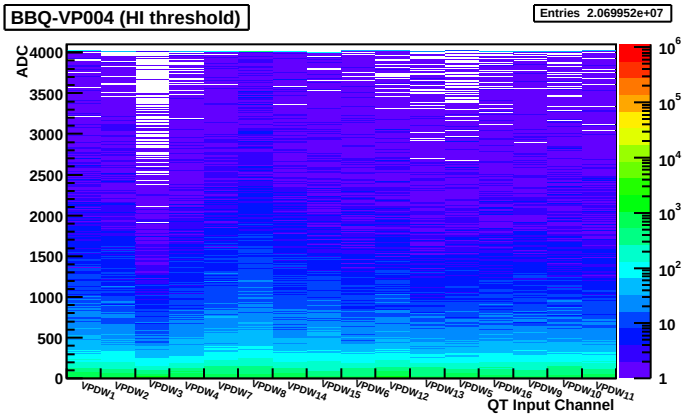

BBQ-VP001 (LO threshold)
Entries 2.069952e+07
TAC
QT Input Channel

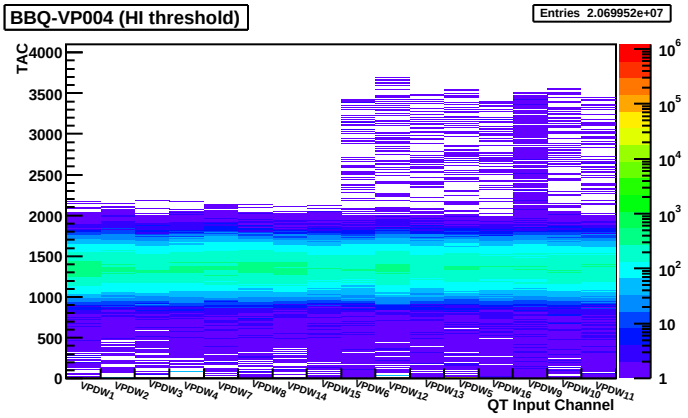

BBQ-VP002 (LO threshold)
Entries 2.069952e+07
ADC
QT Input Channel

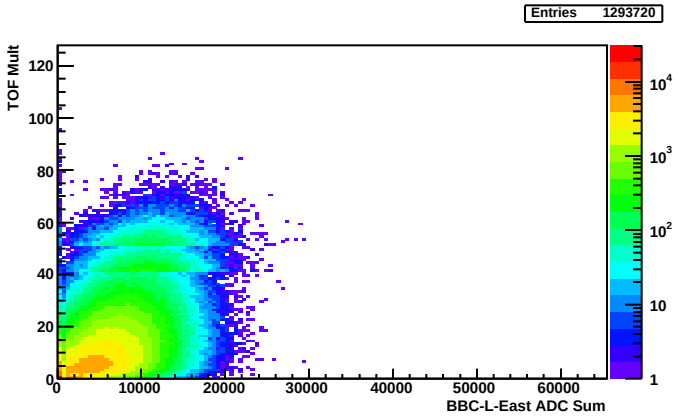

BBQ-VP002 (LO threshold)
Entries 2.069952e+07
TAC
QT Input Channel

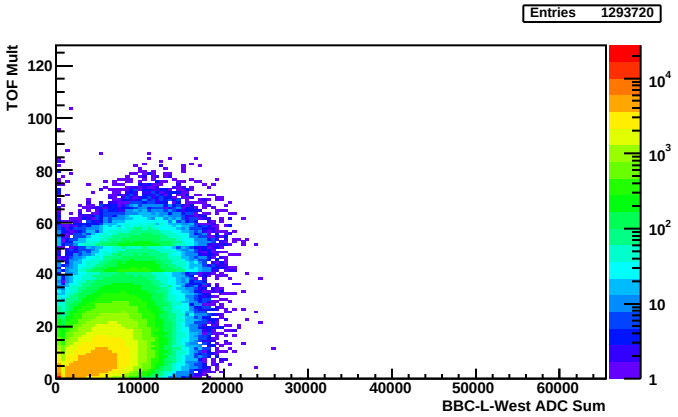

BBQ-VP003 (HI threshold)
Entries 2.069952e+07
ADC
QT Input Channel

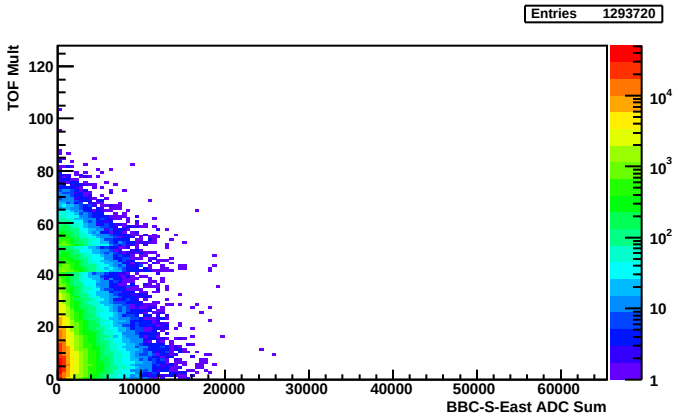

BBQ-VP003 (HI threshold)
Entries 2.069952e+07
TAC
QT Input Channel

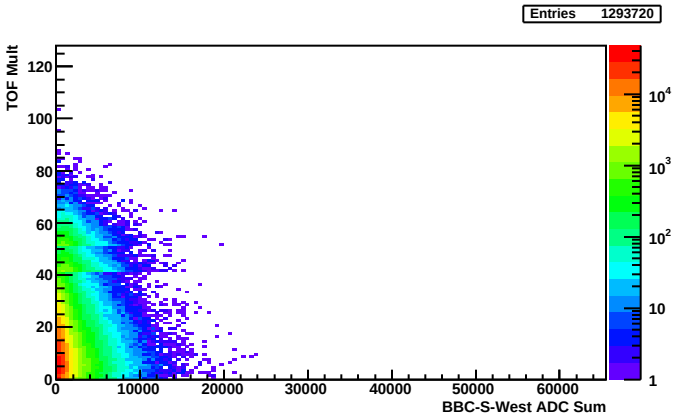

BBQ-VP004 (HI threshold)
Entries 2.069952e+07
ADC
QT Input Channel

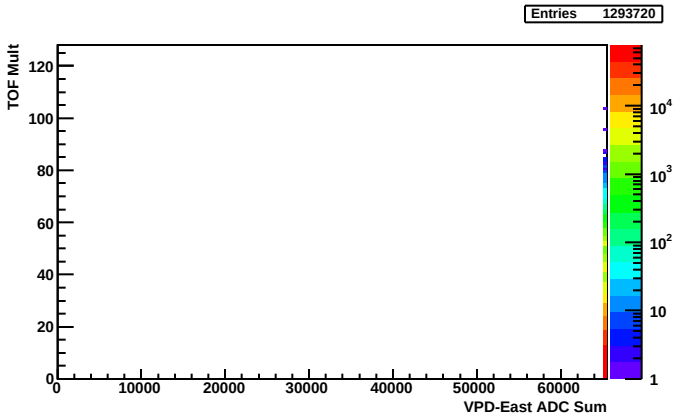

BBQ-VP004 (HI threshold)
Entries 2.069952e+07
TAC
QT Input Channel

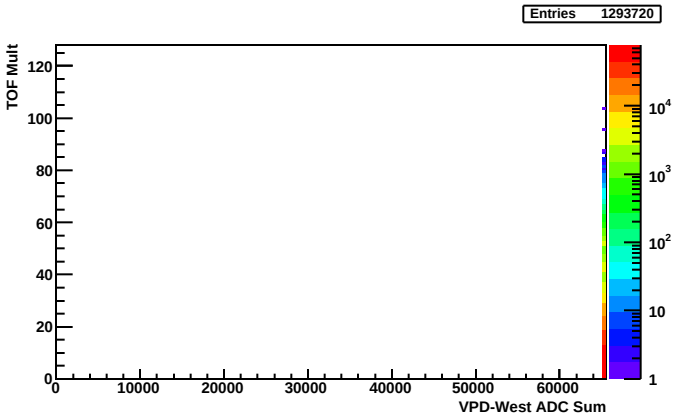

Entries 1293720
TOF Mult
BBC-L-East ADC Sum

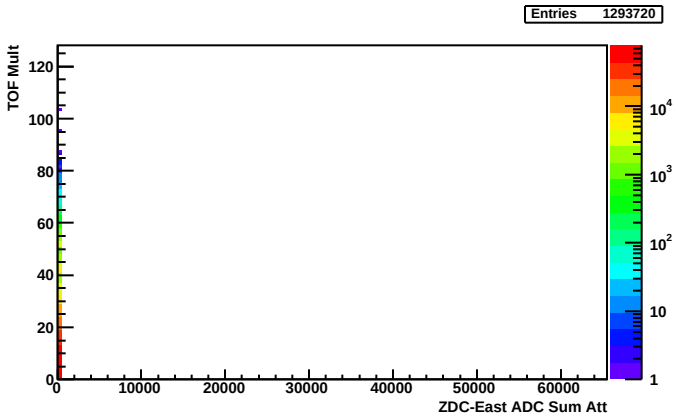

Entries 1293720
TOF Mult
BBC-L-West ADC Sum

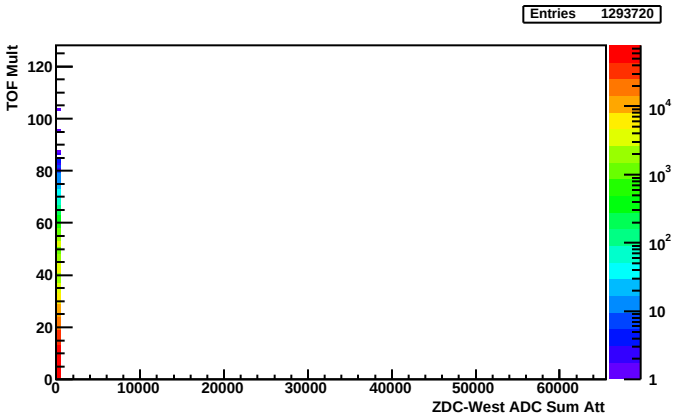

Entries 1293720
TOF Mult
BBC-S-East ADC Sum

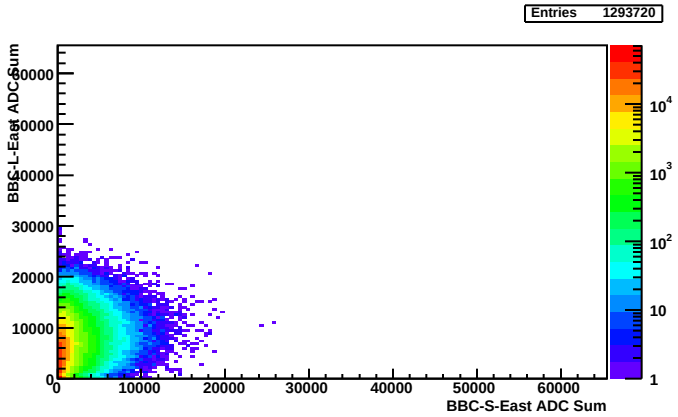

Entries 1293720
TOF Mult
BBC-S-West ADC Sum

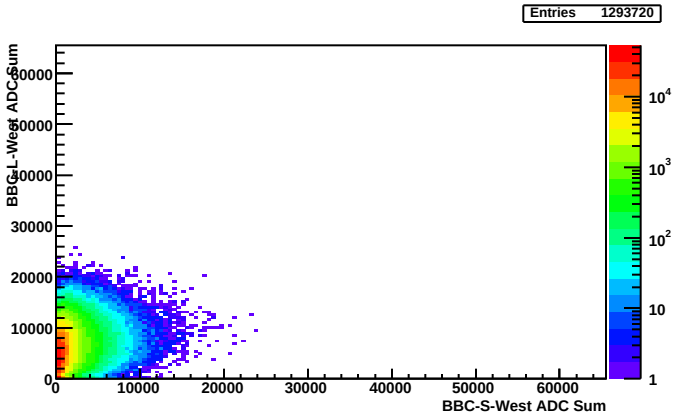

Entries 1293720
TOF Mult
VPD-East ADC Sum

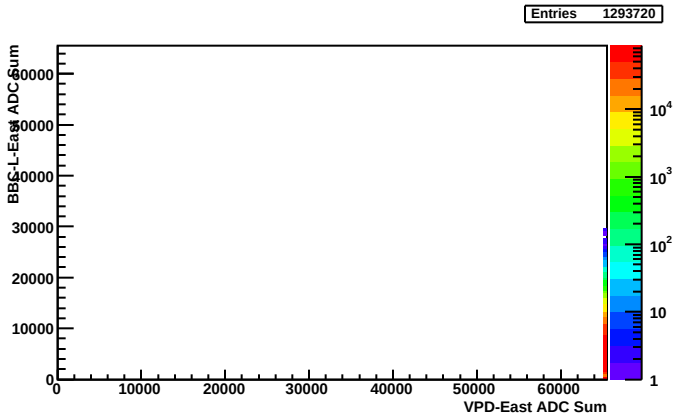

Entries 1293720
TOF Mult
VPD-West ADC Sum

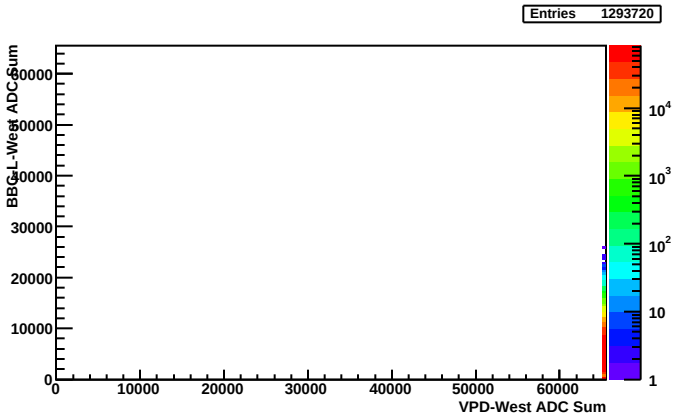

Entries 1293720
TOF Mult
ZDC-East ADC Sum Att

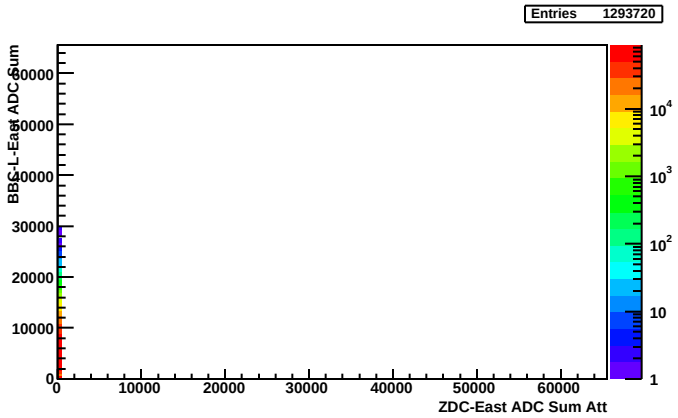

Entries 1293720
TOF Mult
ZDC-West ADC Sum Att

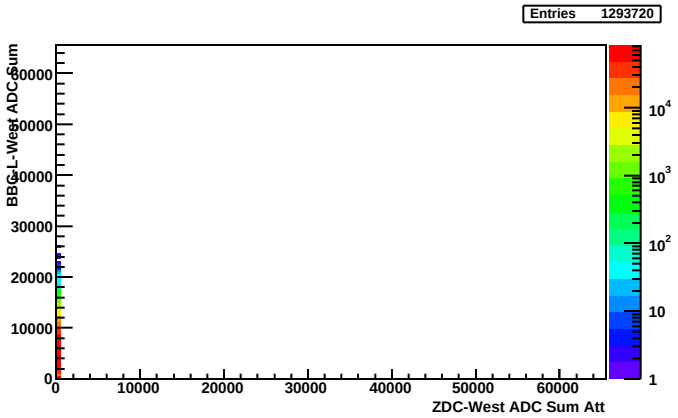

Entries 1293720
BBC-L-East ADC Sum
BBC-S-East ADC Sum

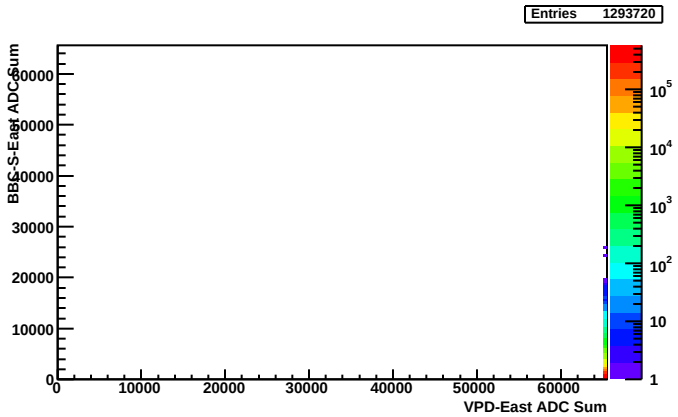

Entries 1293720
BBC-L-West ADC Sum
BBC-S-West ADC Sum

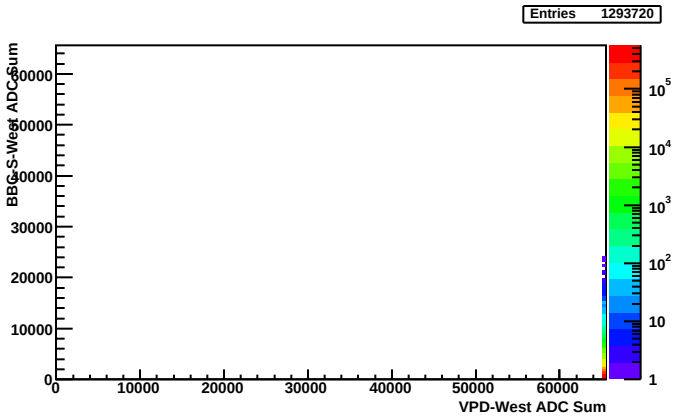

Entries 1293720
BBC-L-East ADC Sum
VPD-East ADC Sum

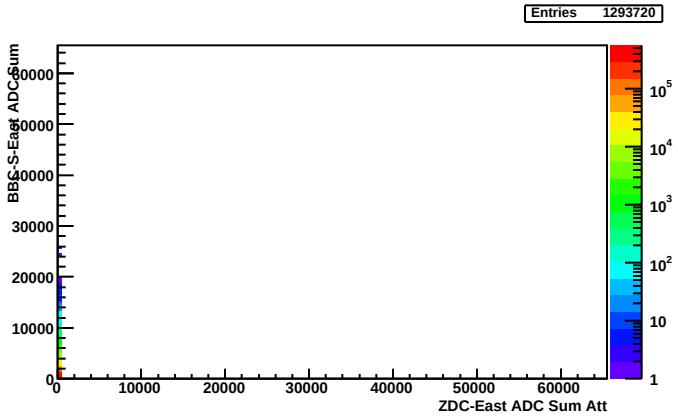

Entries 1293720
BBC-L-West ADC Sum
VPD-West ADC Sum

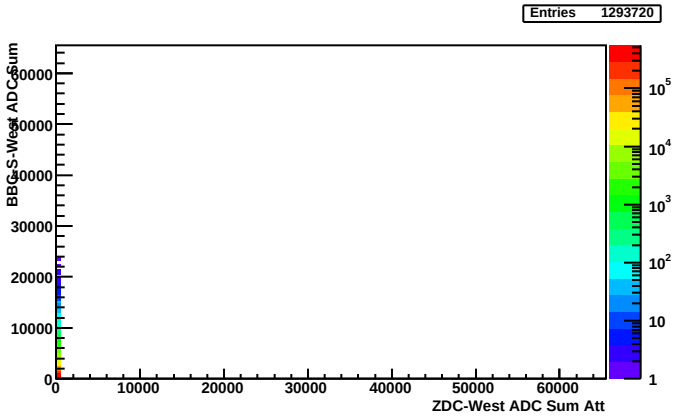

Entries 1293720
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

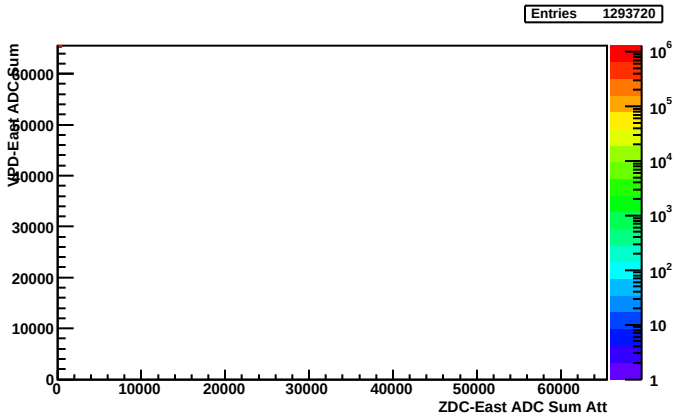

Entries 1293720
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

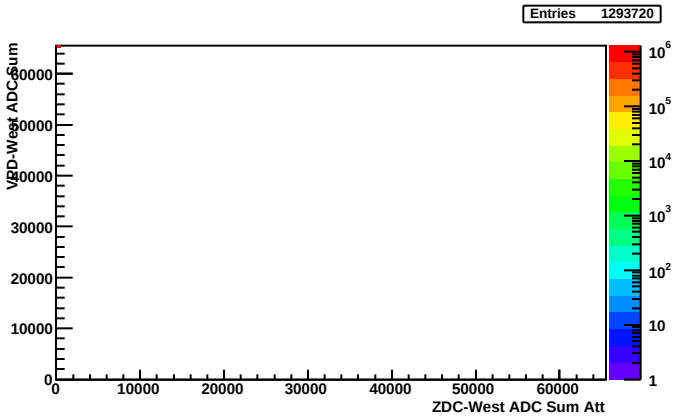

Entries 1293720
BBC-S-East ADC Sum
VPD-East ADC Sum

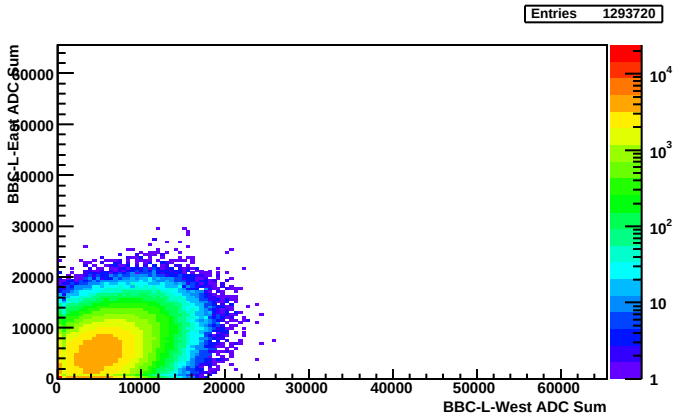

Entries 1293720
BBC-S-West ADC Sum
VPD-West ADC Sum

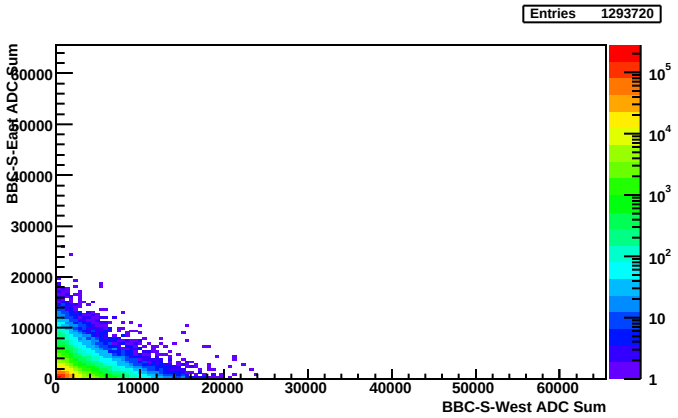

Entries 1293720
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

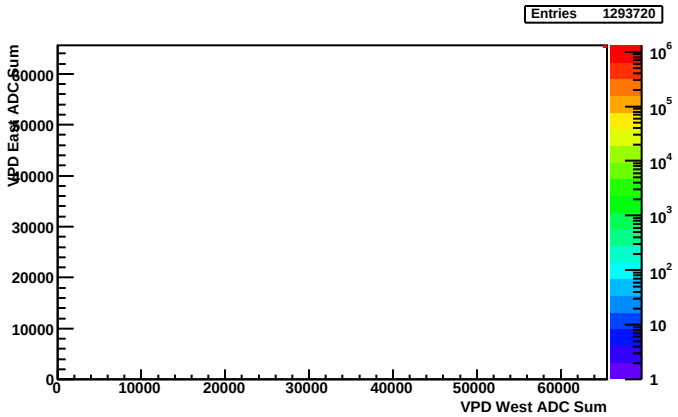

Entries 1293720
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

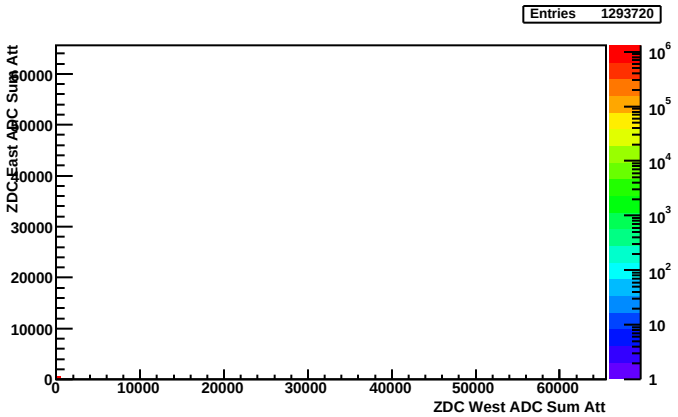

Entries 1293720
VPD-East ADC Sum
ZDC-East ADC Sum Att

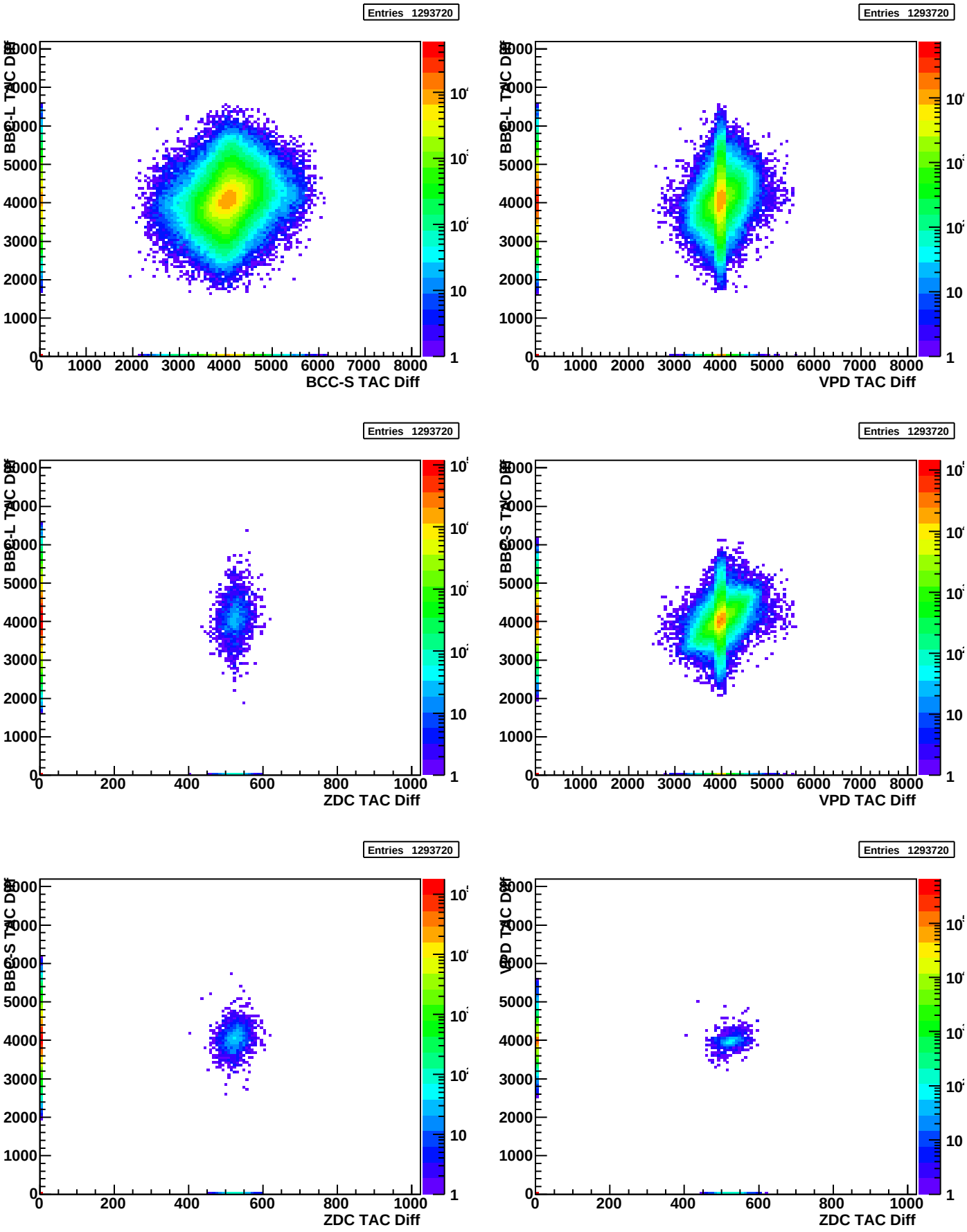

Entries 1293720
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 1293720
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 1293720
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 1293720
VPD East ADC Sum
VPD West ADC Sum


Entries 1293720
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 1293720
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1293720
BBC-L TAC Diff
VPD TAC Diff
Entries 1293720
BBC-L TAC Diff
ZDC TAC Diff
Entries 1293720
BBC-S TAC Diff
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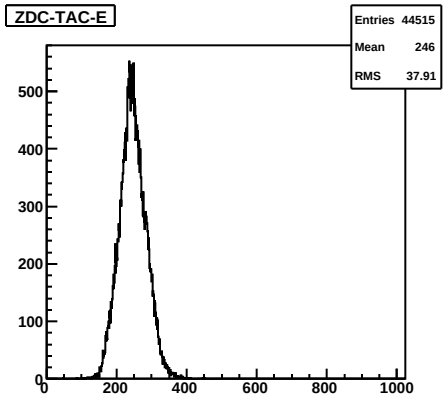
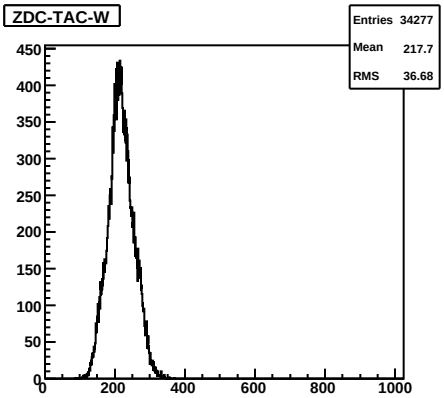
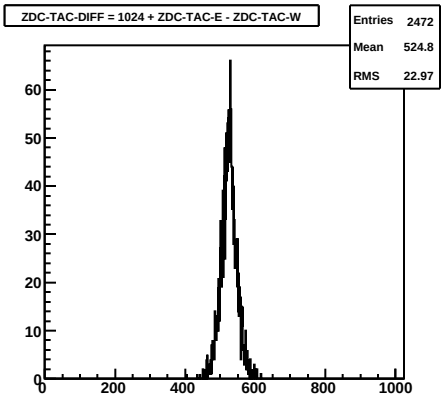
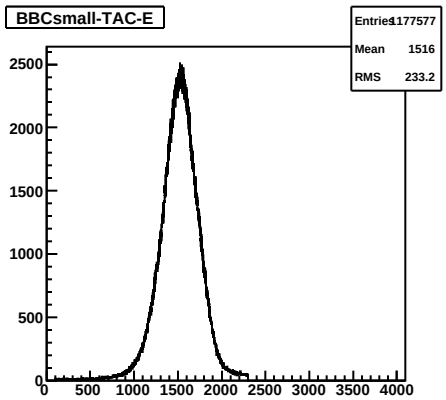
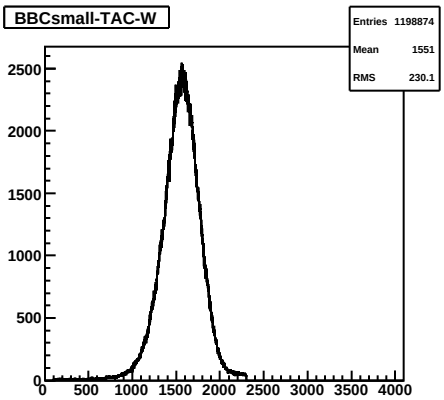
Entries 1293720
BBC-S TAC Diff
ZDC TAC Diff
Entries 1293720
VPD TAC Diff
ZDC TAC Diff

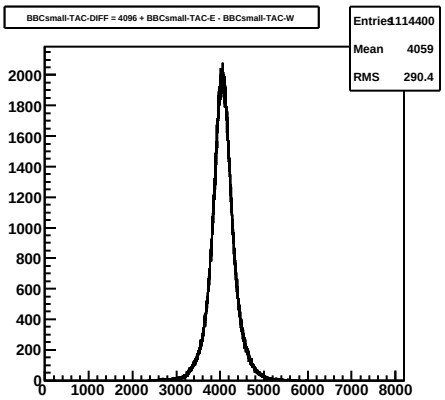

ZDC-TAC-E
Entries 44515
Mean 246
RMS 37.91

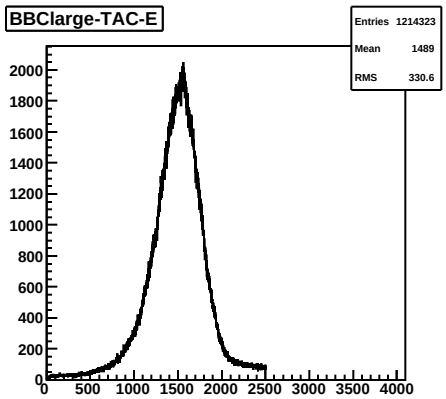

ZDC-TAC-W
Entries 34277
Mean 217.7
RMS 36.68

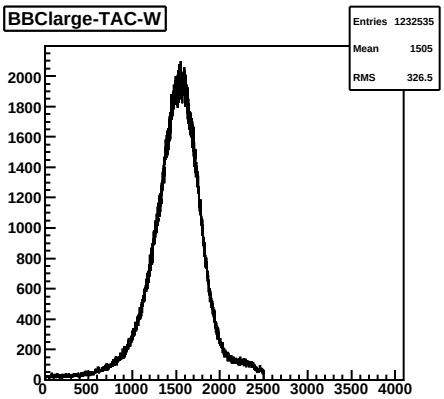

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2472
Mean 524.8
RMS 22.97

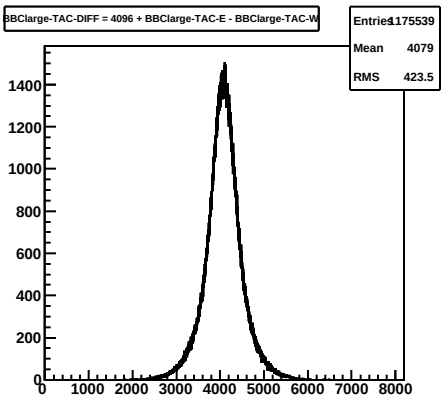

BBCsmall-TAC-E
Entries 1177577
Mean 1516
RMS 233.2

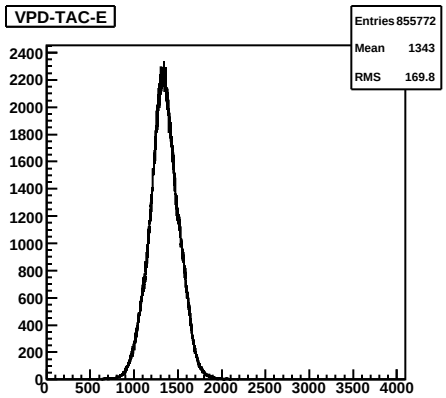

BBCsmall-TAC-W
Entries 1198874
Mean 1551
RMS 230.1

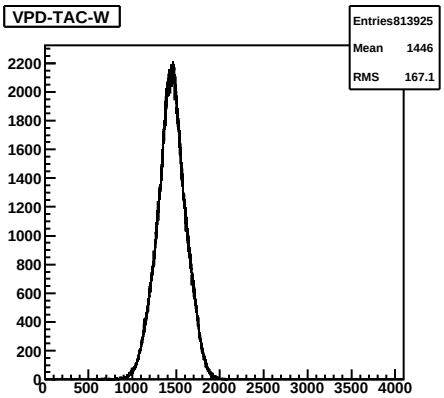

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1114400
Mean 4059
RMS 290.4

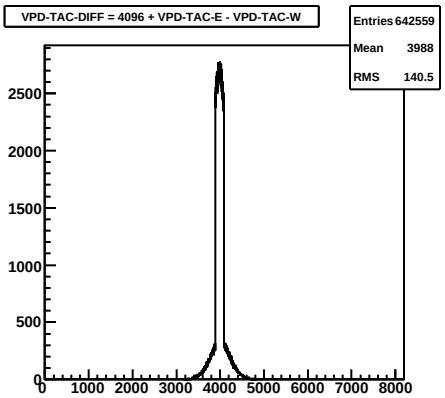

BBClarge-TAC-E
Entries 1214323
Mean 1489
RMS 330.6

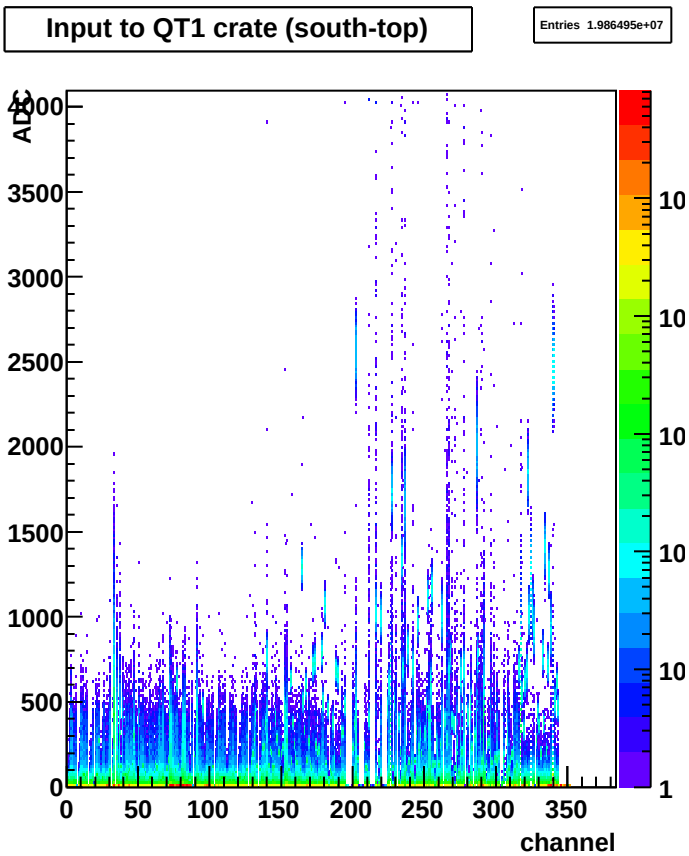

BBClarge-TAC-W
Entries 1232535
Mean 1505
RMS 326.5

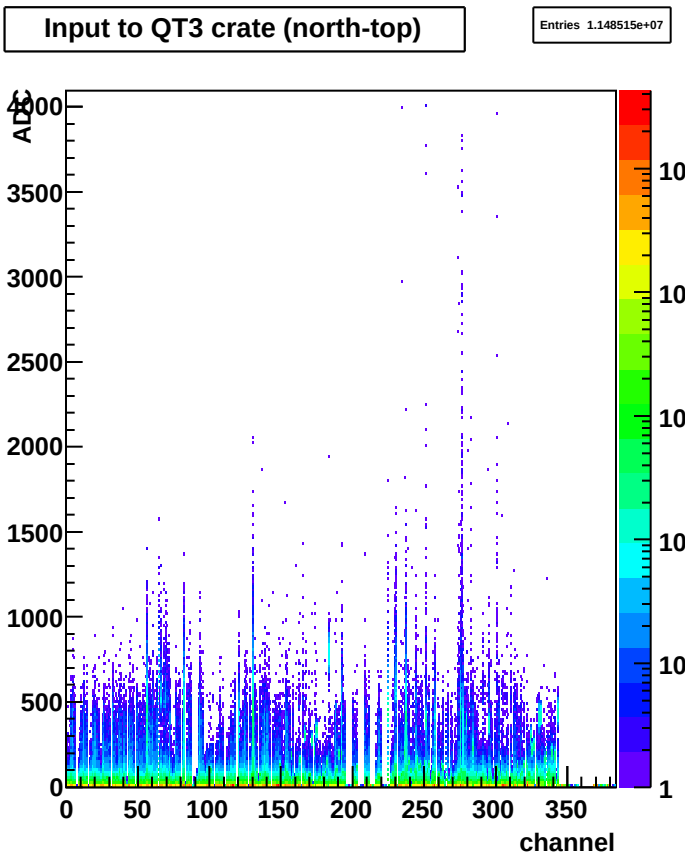

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1175539
Mean 4079
RMS 423.5

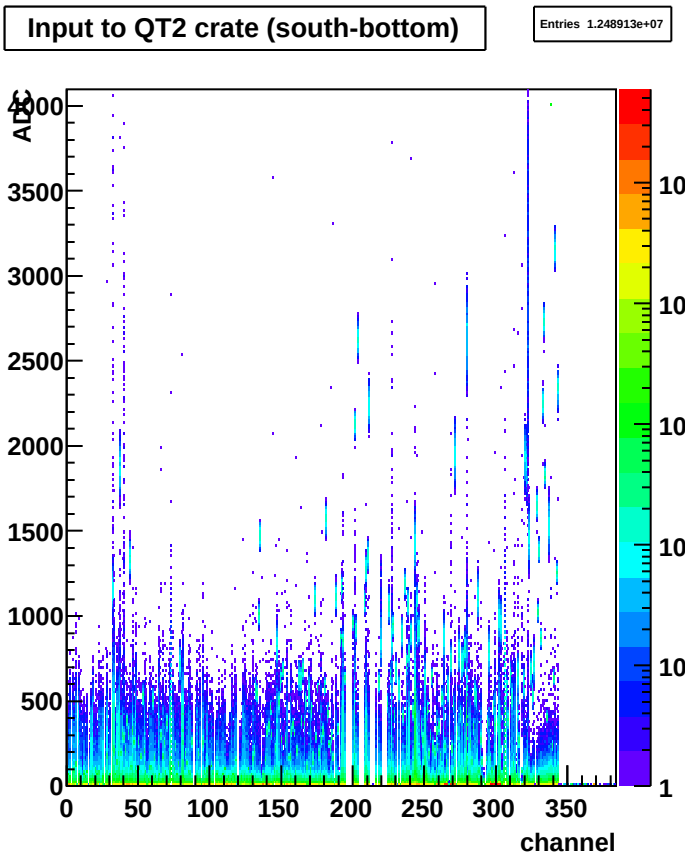

VPD-TAC-E
Entries 855772
Mean 1343
RMS 169.8

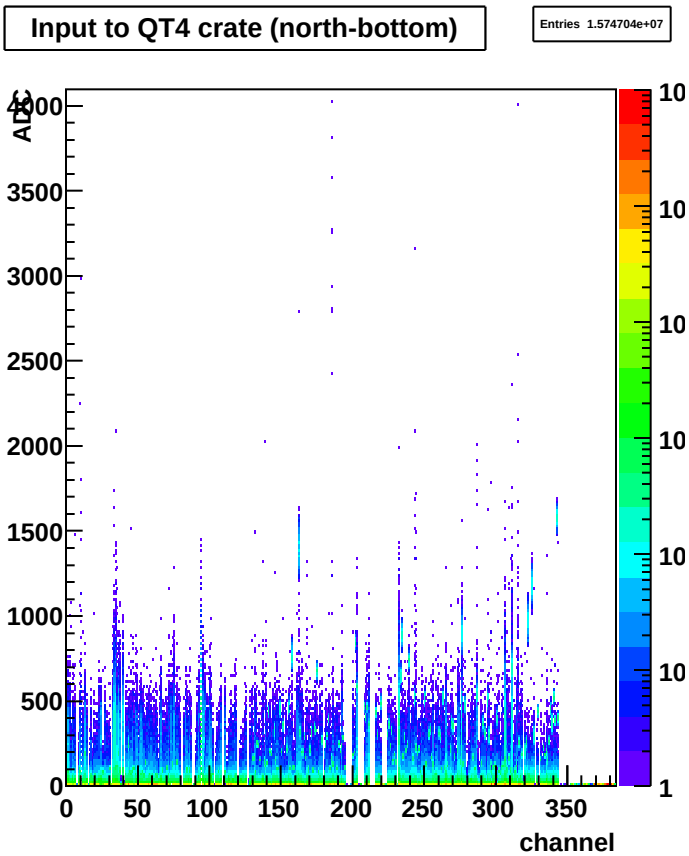

VPD-TAC-W
Entries 813925
Mean 1446
RMS 167.1

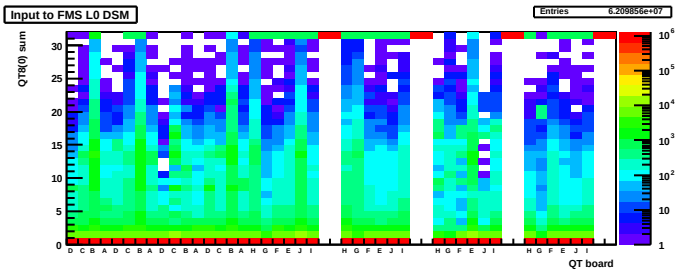

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 642559
Mean 3988
RMS 140.5

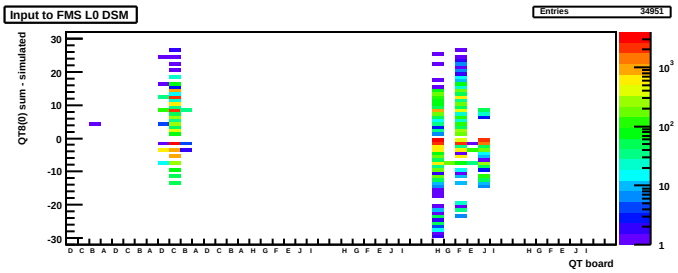

Input to QT1 crate (south-top)
Entries 1.986495e+07
ADC
channel

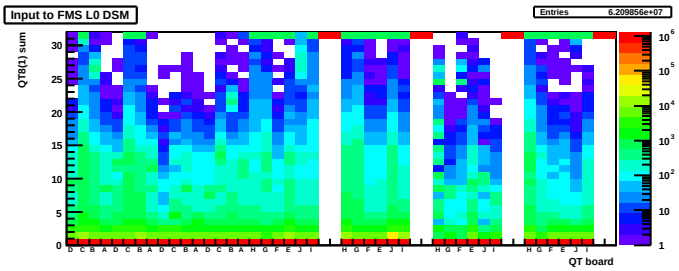

Input to QT3 crate (north-top)
Entries 1.148515e+07
ADC
channel

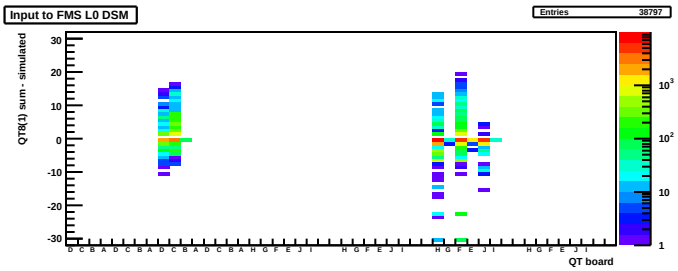

Input to QT2 crate (south-bottom)
Entries 1.248913e+07
ADC
channel

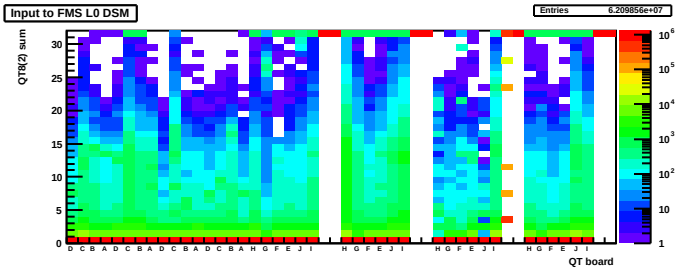

Input to QT4 crate (north-bottom)
Entries 1.574704e+07
ADC
channel

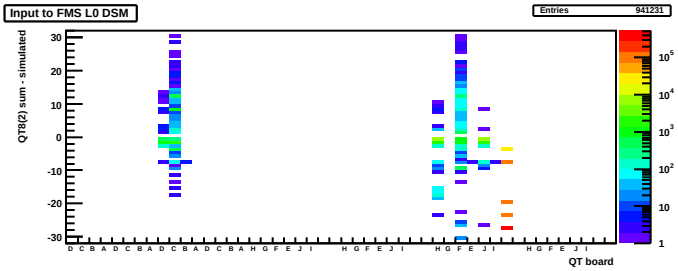

Input to FMS L0 DSM
Entries 6.209856e+07
QT8(0) sum
QT board

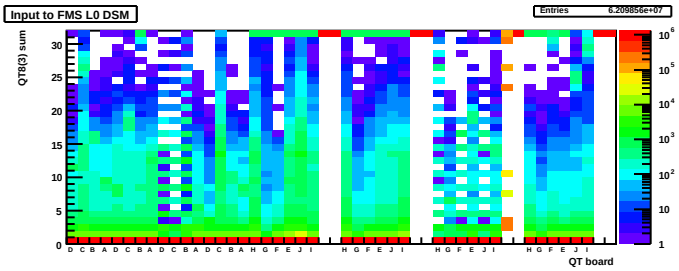

Input to FMS L0 DSM
Entries 34951
QT8(0) sum - simulated
QT board

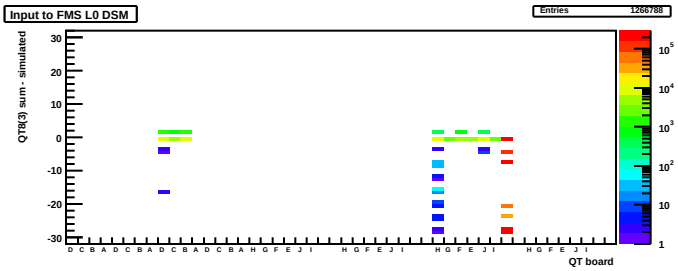

Input to FMS L0 DSM
Entries 6.209856e+07
QT8(1) sum
QT board

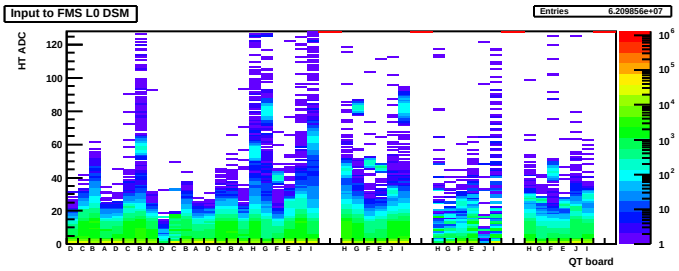

Input to FMS L0 DSM
Entries 38797
QT8(1) sum - simulated
QT board

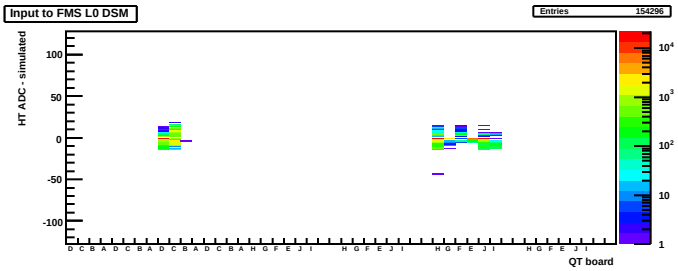

Input to FMS L0 DSM
Entries 6.209856e+07
QT8(2) sum
QT board

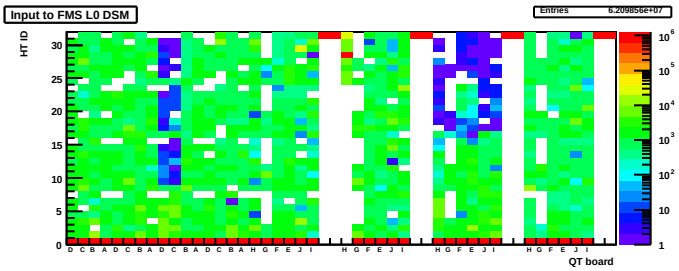

Input to FMS L0 DSM
Entries 941231
QT8(2) sum - simulated
QT board

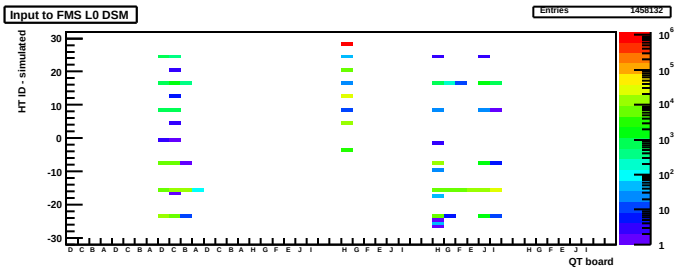

Input to FMS L0 DSM
Entries 6.209856e+07
QT8(3) sum
QT board

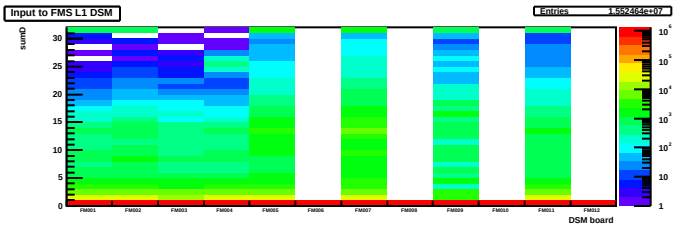

Input to FMS L0 DSM
Entries 1266788
QT8(3) sum - simulated
QT board

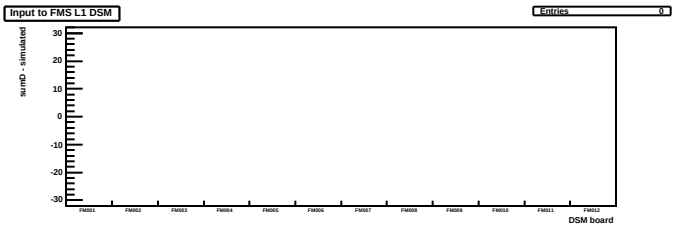

Input to FMS L0 DSM
Entries 6.209856e+07
HT ADC
QT board

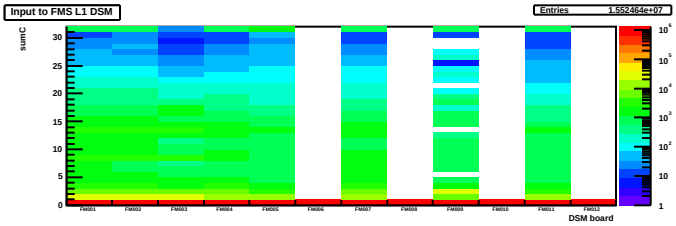

Input to FMS L0 DSM
Entries 154296
HT ADC - simulated
QT board

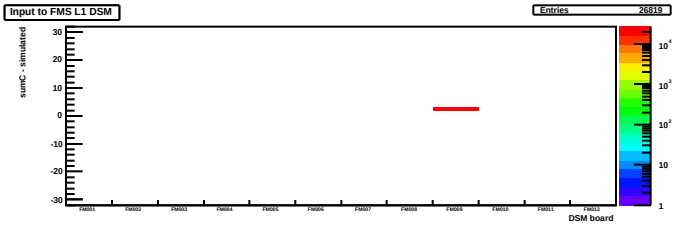

Input to FMS L0 DSM
Entries 6.209856e+07
HT ID
QT board

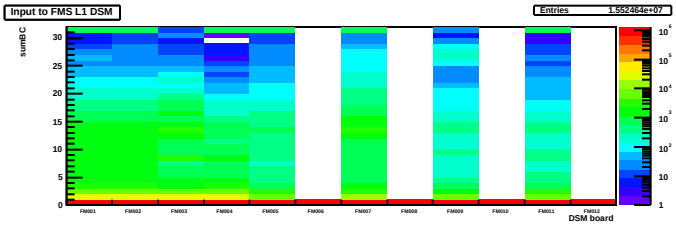

Input to FMS L0 DSM
Entries 1458132
HT ID - simulated
QT board

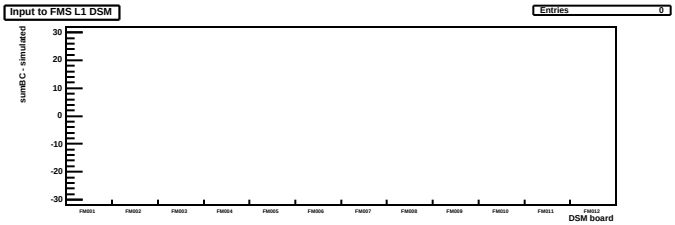

Input to FMS L1 DSM
Entries 1.552464e+07
sumD
DSM board

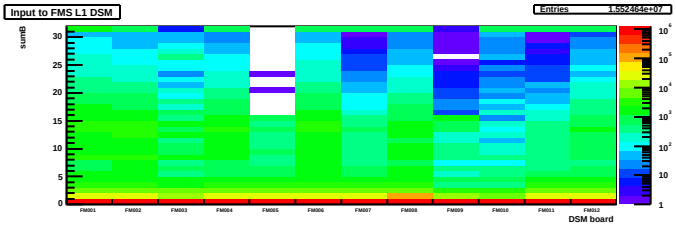

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

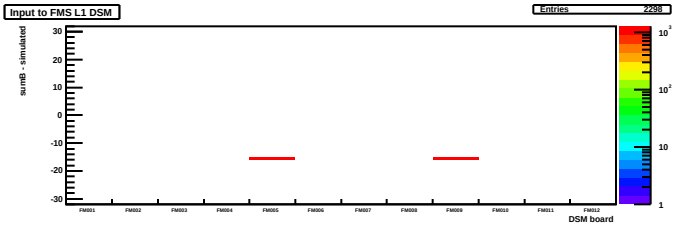

Input to FMS L1 DSM
Entries 1.552464e+07
sumC
DSM board

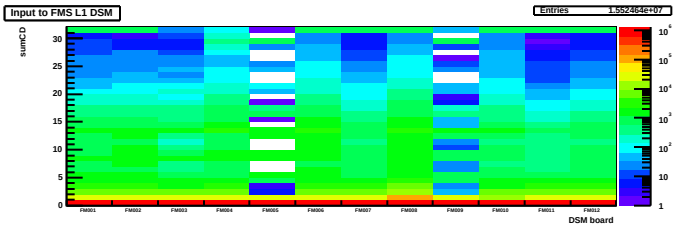

Input to FMS L1 DSM
Entries 26819
sumC - simulated
DSM board

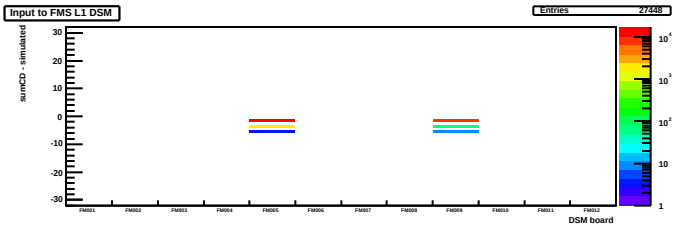

Input to FMS L1 DSM
Entries 1.552464e+07
sumBC
DSM board

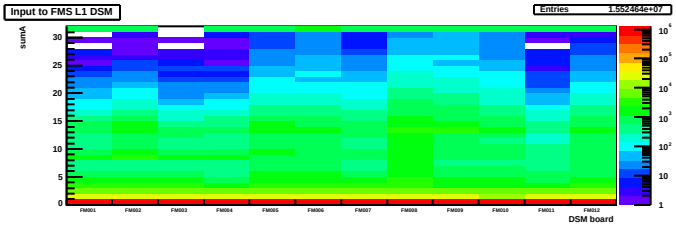

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

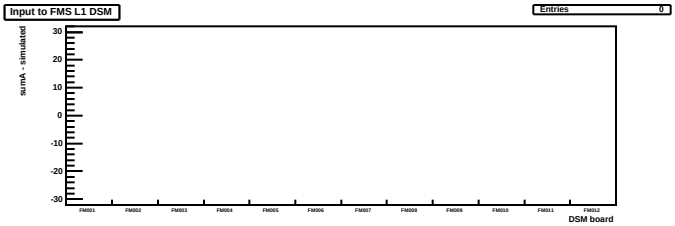

Input to FMS L1 DSM
Entries 1.552464e+07
sumB
DSM board

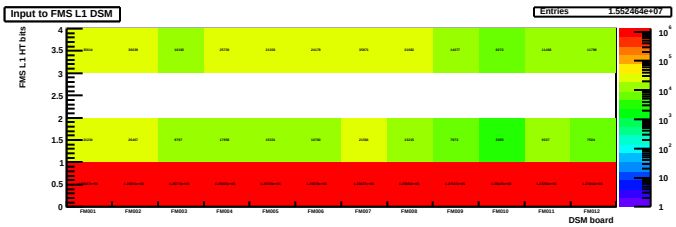

Input to FMS L1 DSM
Entries 2298
sumB - simulated
DSM board

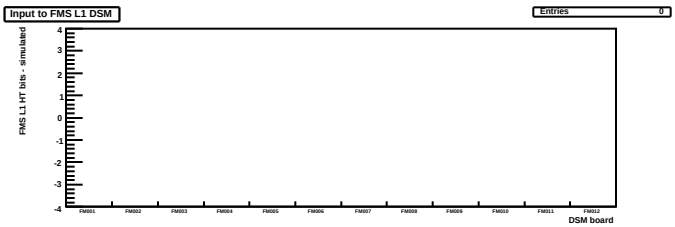

Input to FMS L1 DSM
Entries 1.552464e+07
sumCD
DSM board

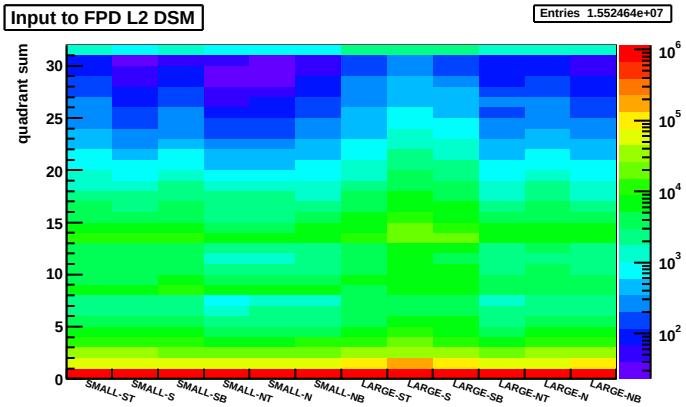

Input to FMS L1 DSM
Entries 27448
sumCD - simulated
DSM board

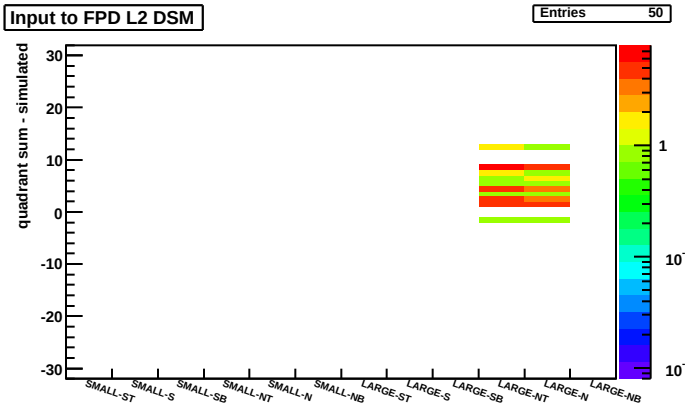

Input to FMS L1 DSM
Entries 1.552464e+07
sumA
DSM board

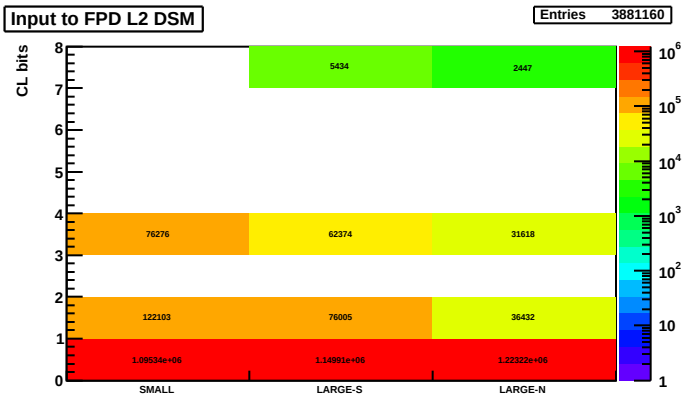

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

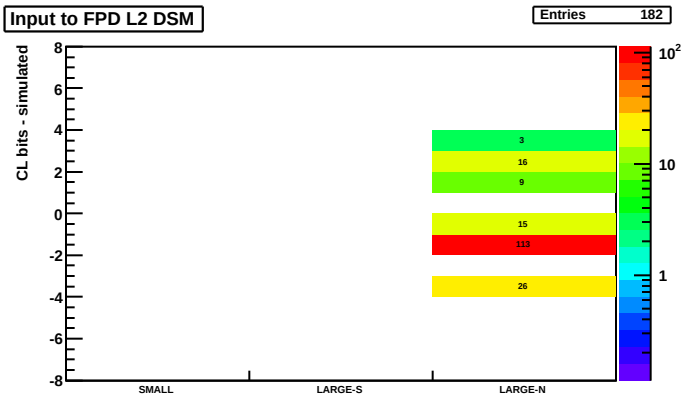

Input to FMS L1 DSM
Entries 1.552464e+07
FMS L1 HT bits
DSM board

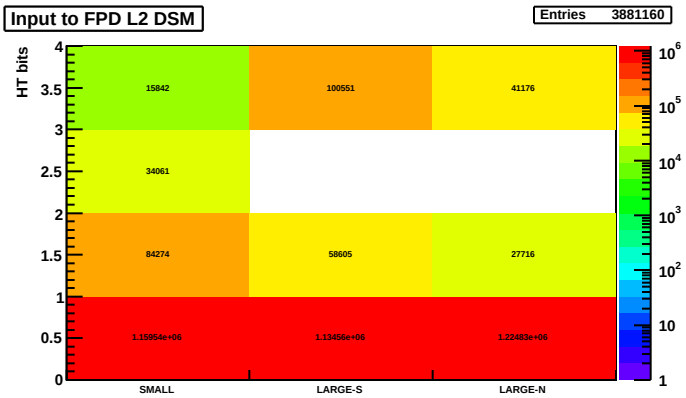

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

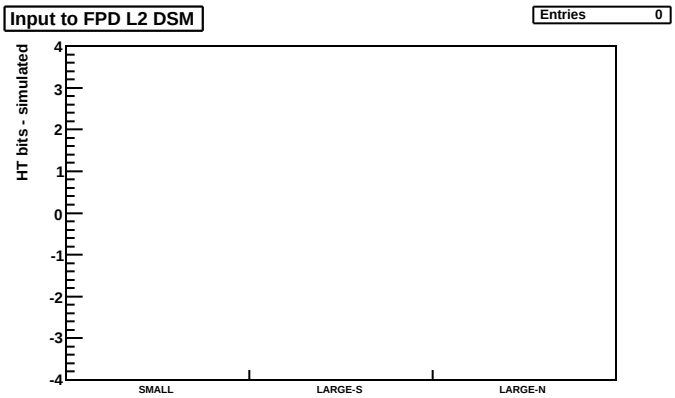

Input to FPD L2 DSM
Entries 1.552464e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

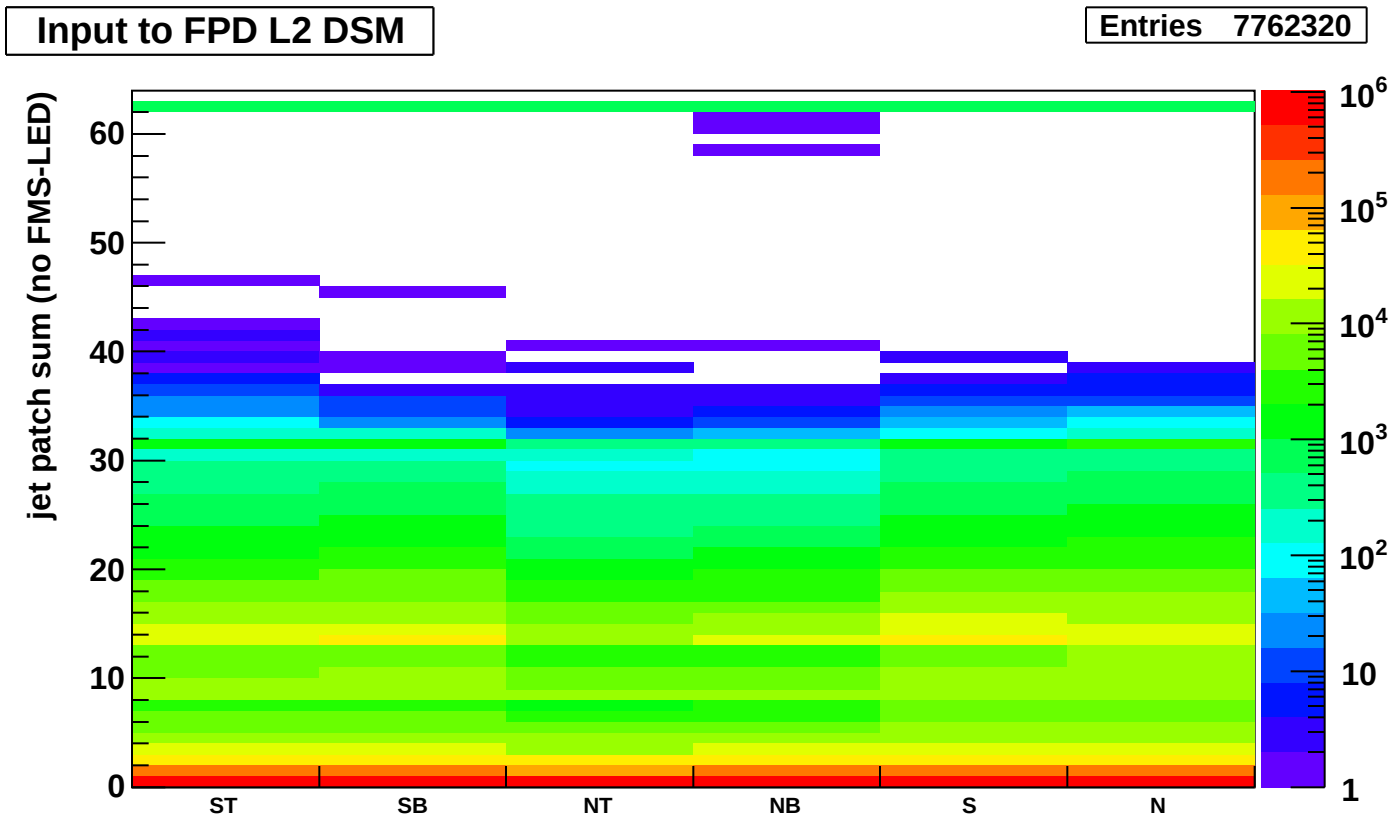

Input to FPD L2 DSM
Entries 50
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

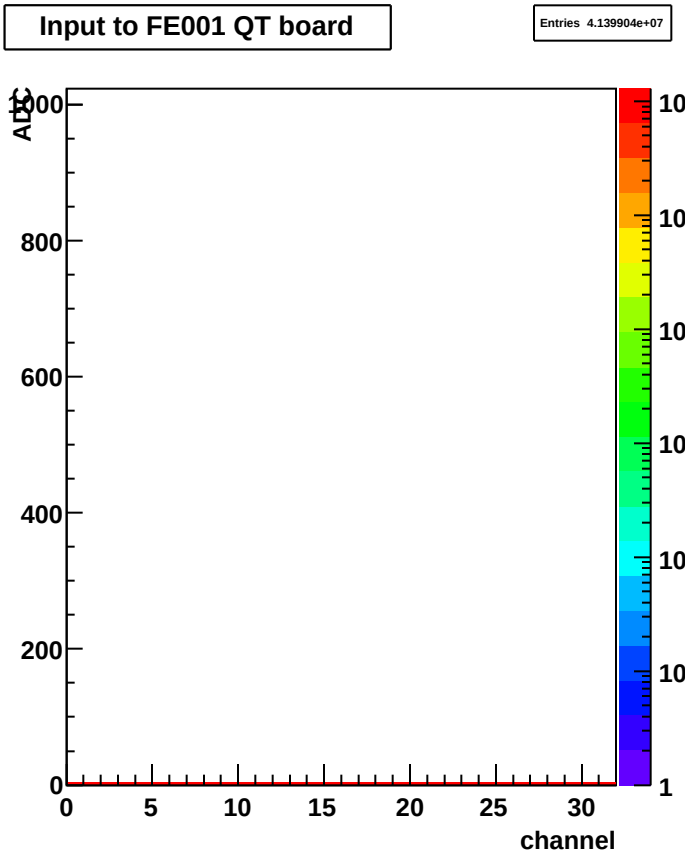

Input to FPD L2 DSM
Entries 3881160
CL bits
5434
2447
76276
62374
31618
122103
76005
36432
1.09534e+06
1.14991e+06
1.22322e+06
SMALL
LARGE-S
LARGE-N

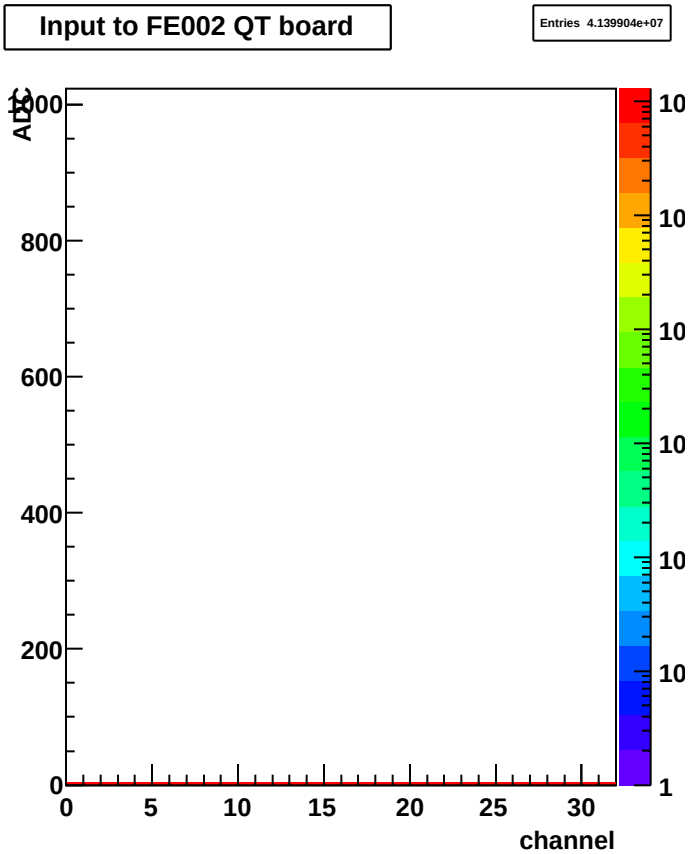

Input to FPD L2 DSM
Entries 182
CL bits - simulated
3
16
9
15
113
26
SMALL
LARGE-S
LARGE-N

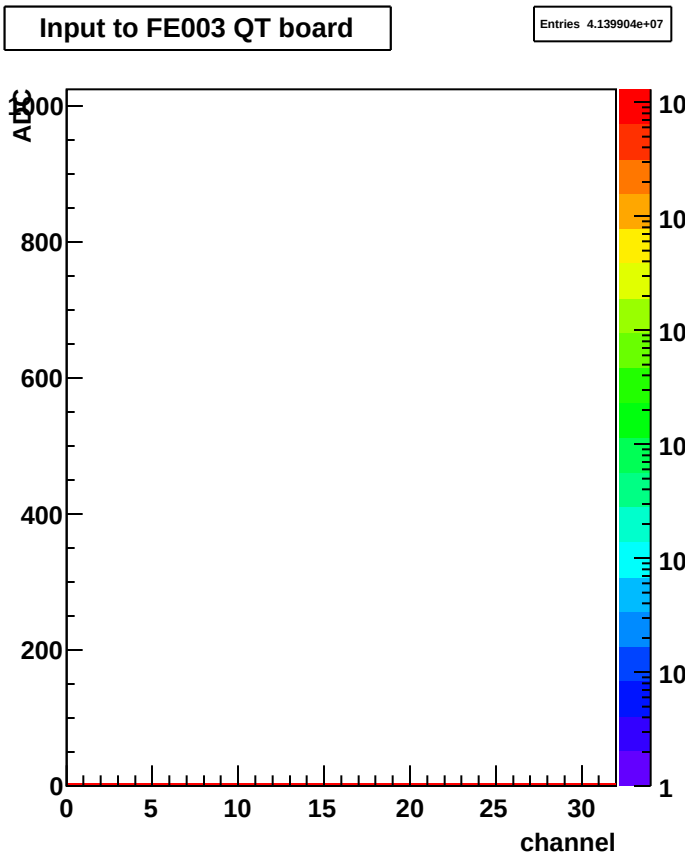

Input to FPD L2 DSM
Entries 3881160
HT bits
15842
100551
41176
34061
84274
58605
27716
1.15954e+06
1.13456e+06
1.22483e+06
SMALL
LARGE-S
LARGE-N

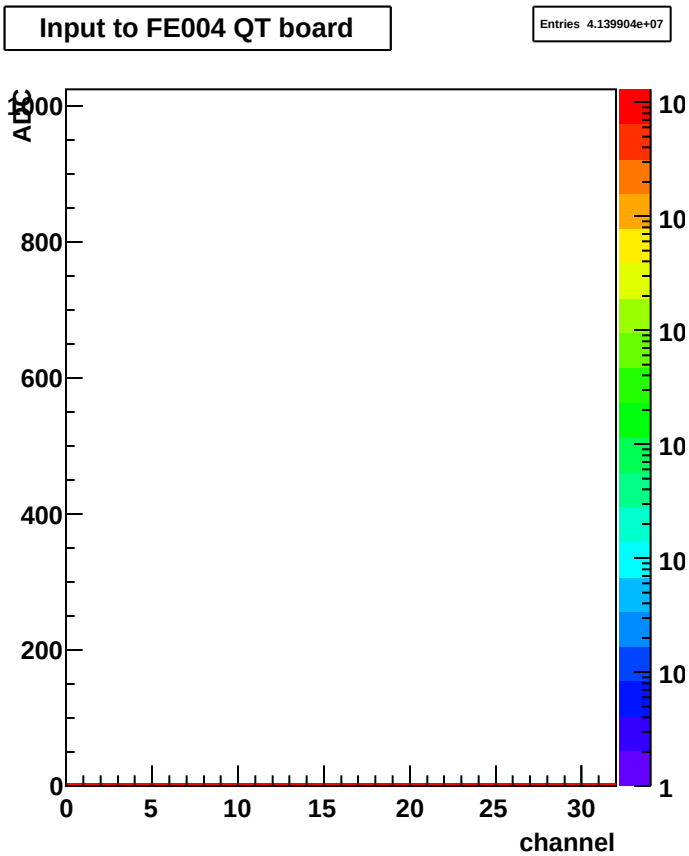

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

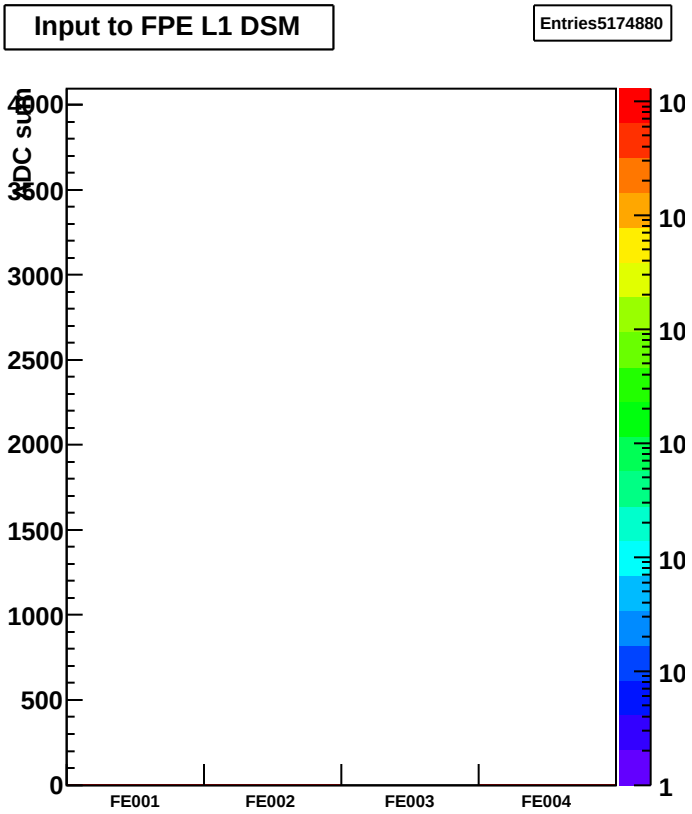

Input to FPD L2 DSM
Entries 7762320
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

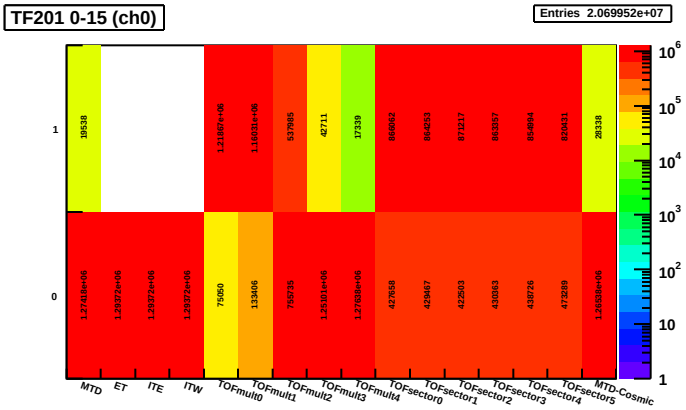

Input to FE001 QT board
Entries 4.139904e+07
ADC
channel

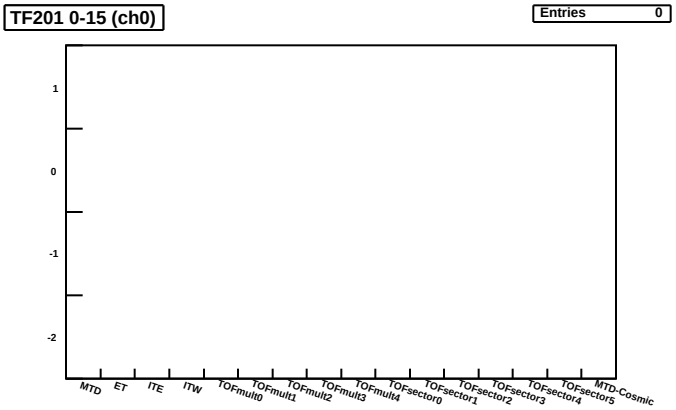

Input to FE002 QT board
Entries 4.139904e+07
ADC
channel

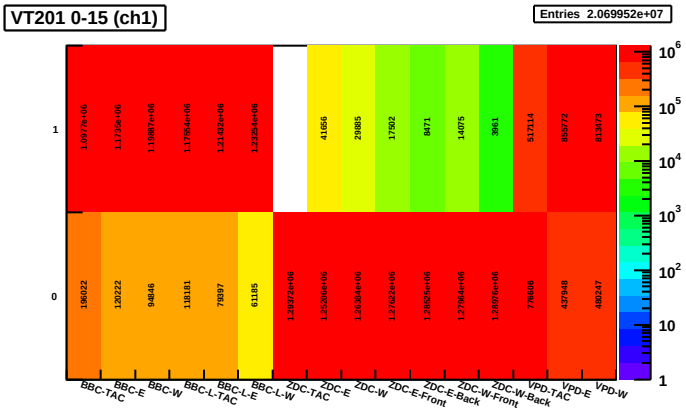

Input to FE003 QT board
Entries 4.139904e+07
ADC
channel


Input to FE004 QT board
Entries 4.139904e+07
ADC
channel


Input to FPE L1 DSM
Entries5174880
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 2.069952e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
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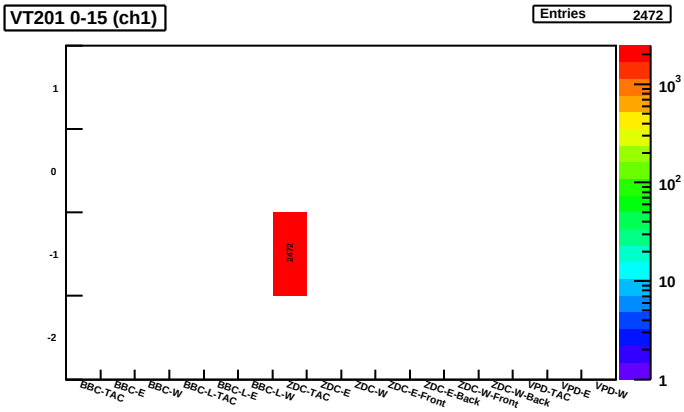
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2472
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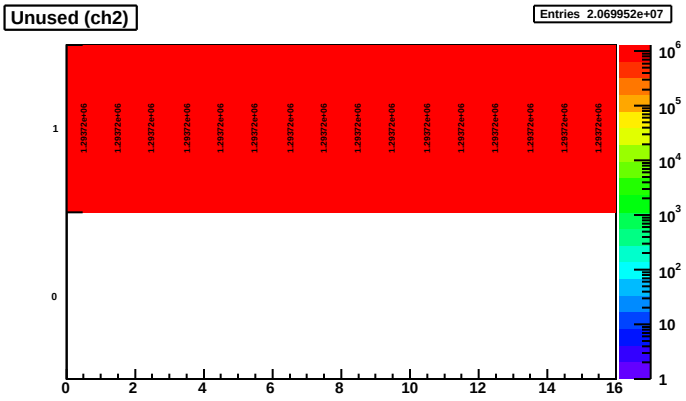
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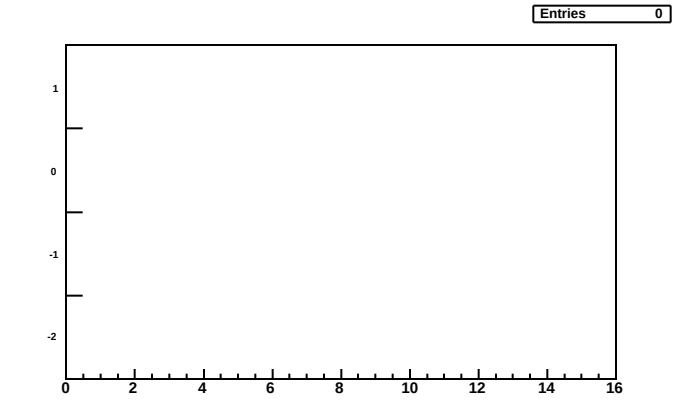
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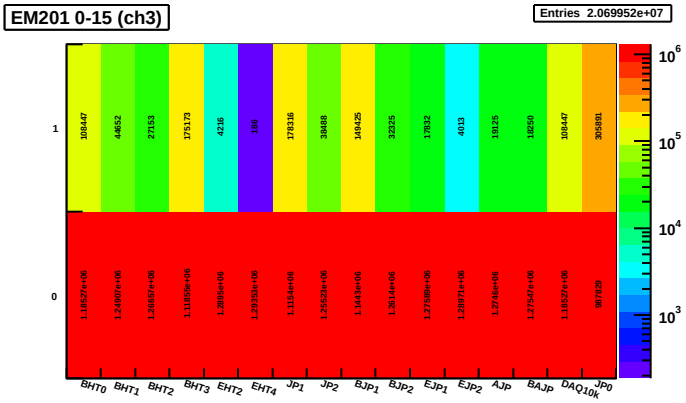
EM201 0-15 (ch3)
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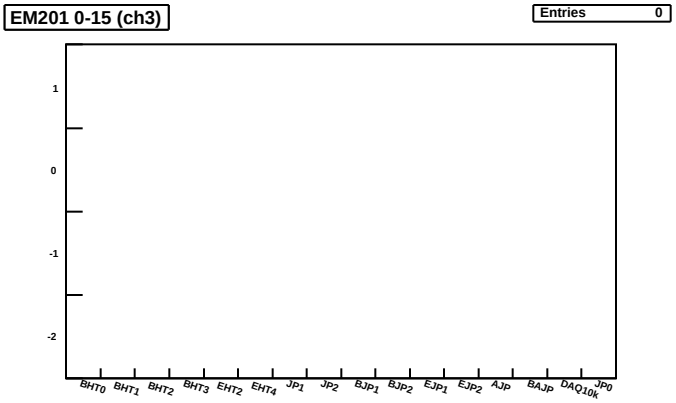
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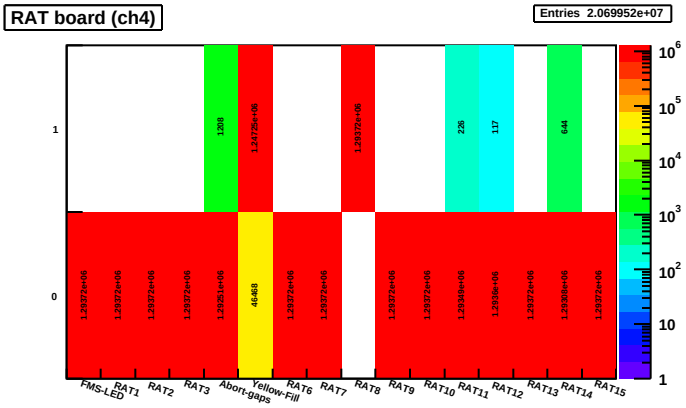
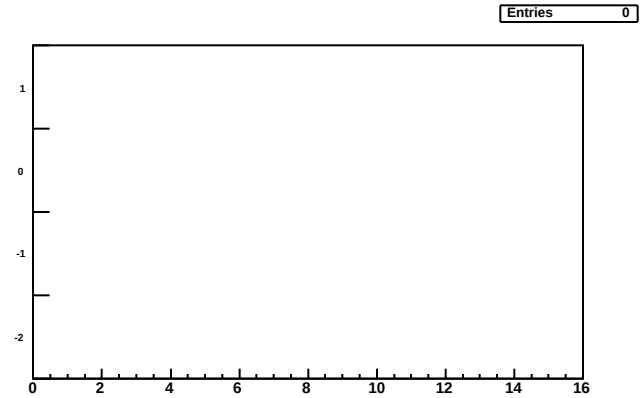
RAT board (ch4)





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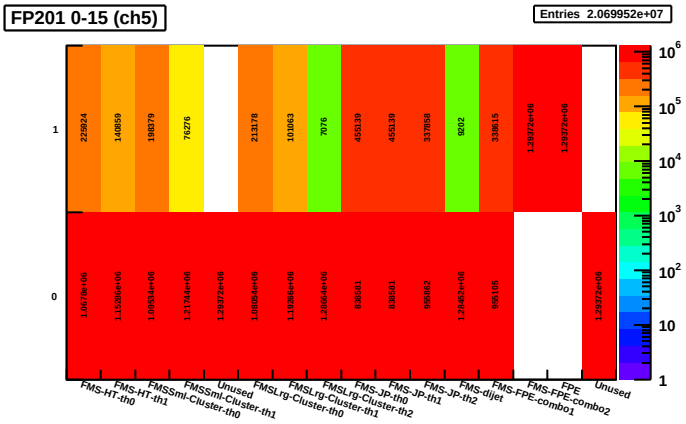

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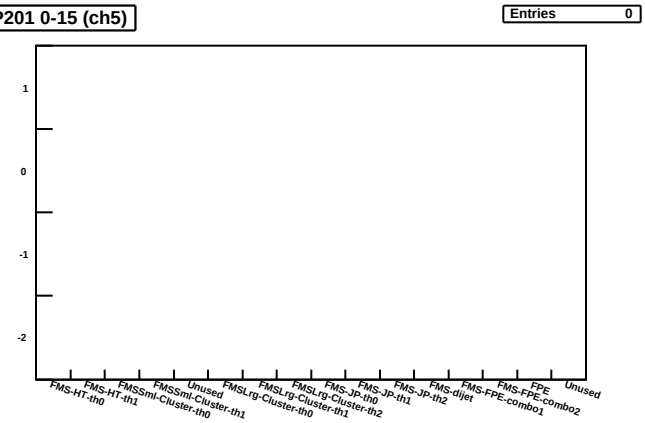
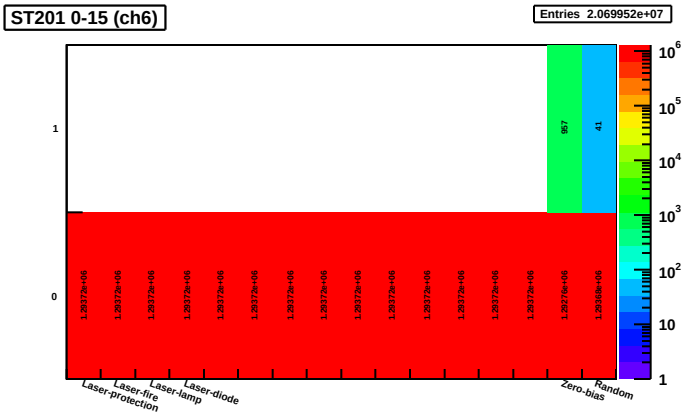
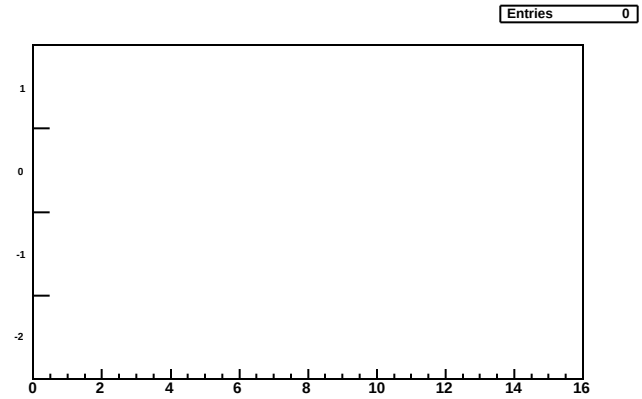
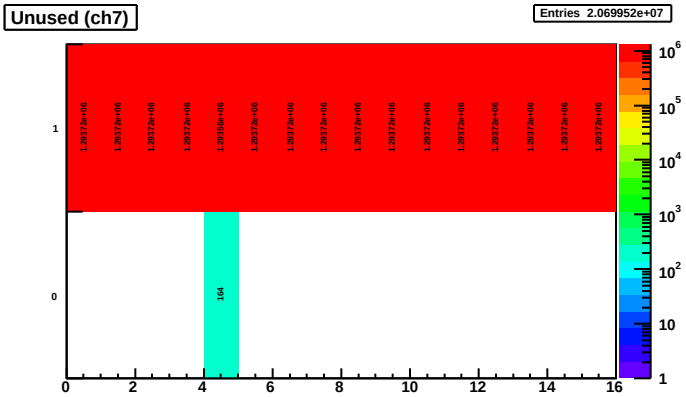
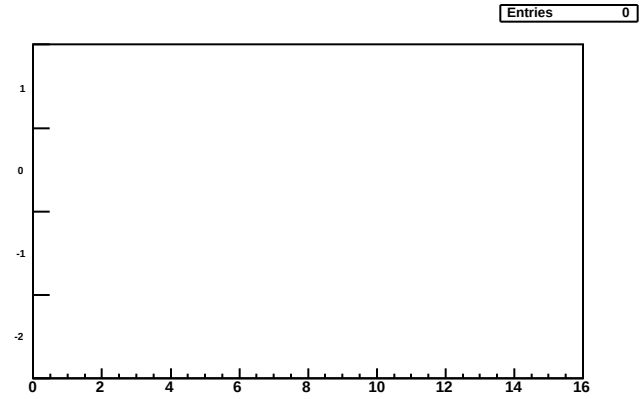


ST201 0-15 (ch6)





Unused (ch7)