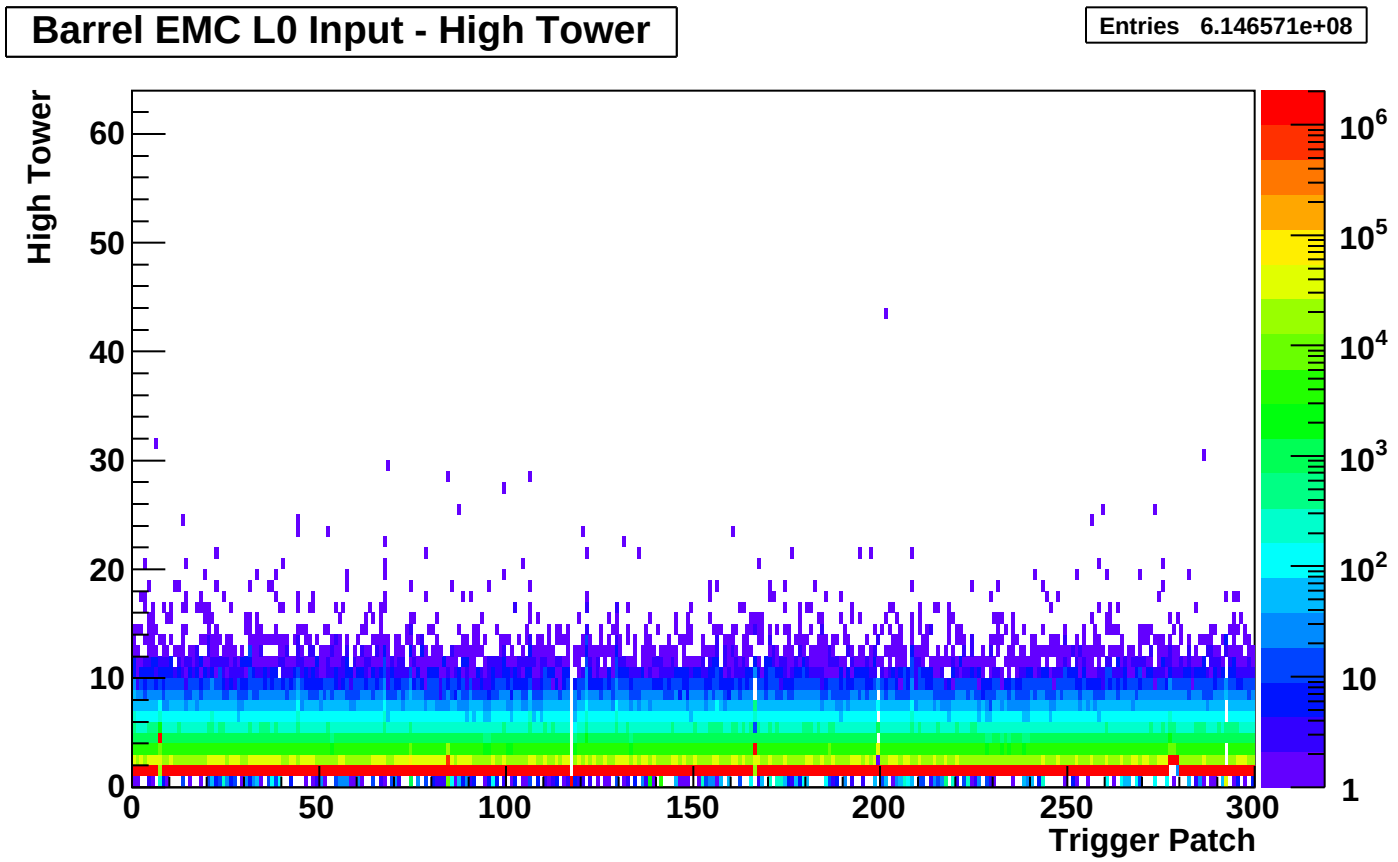
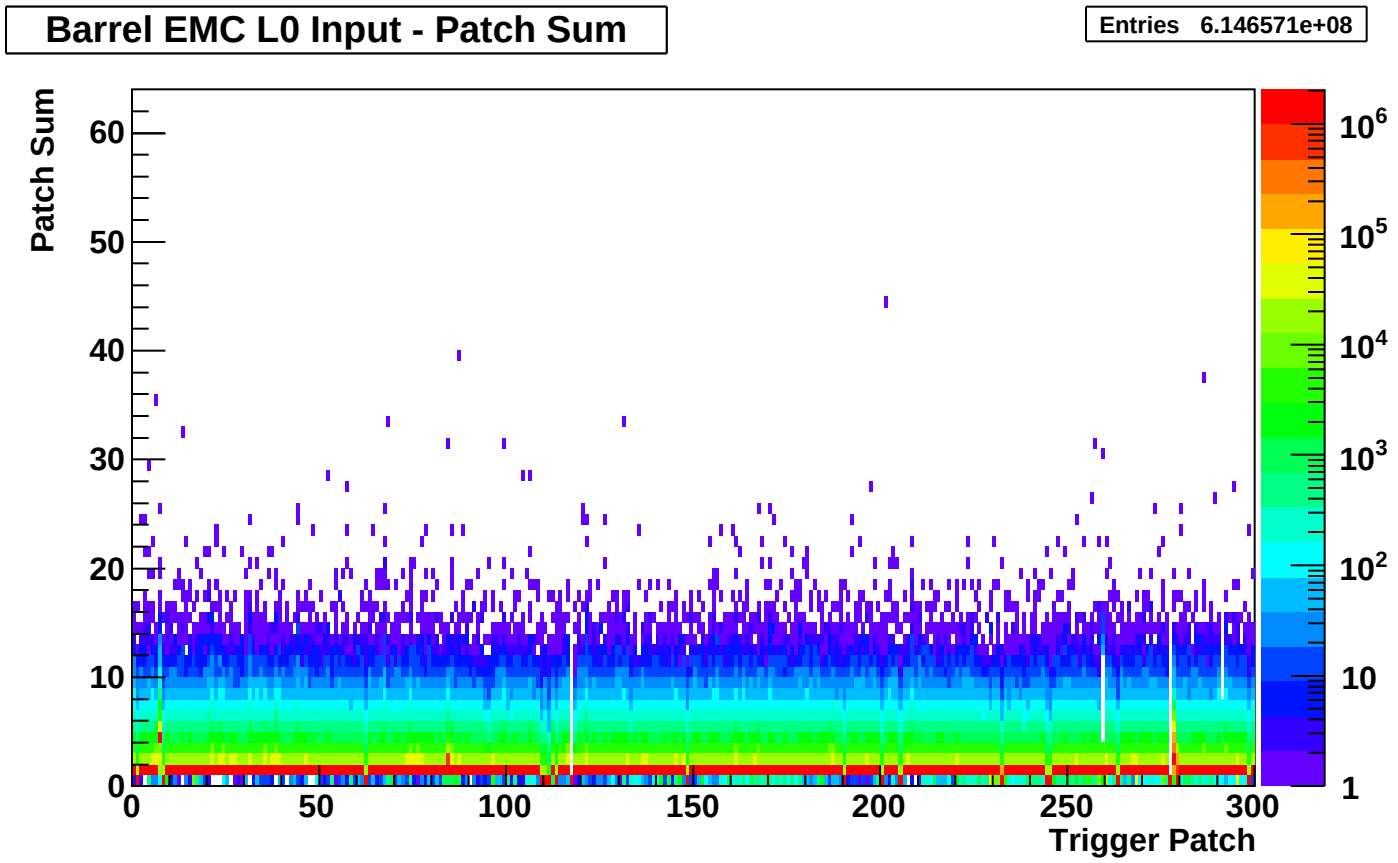
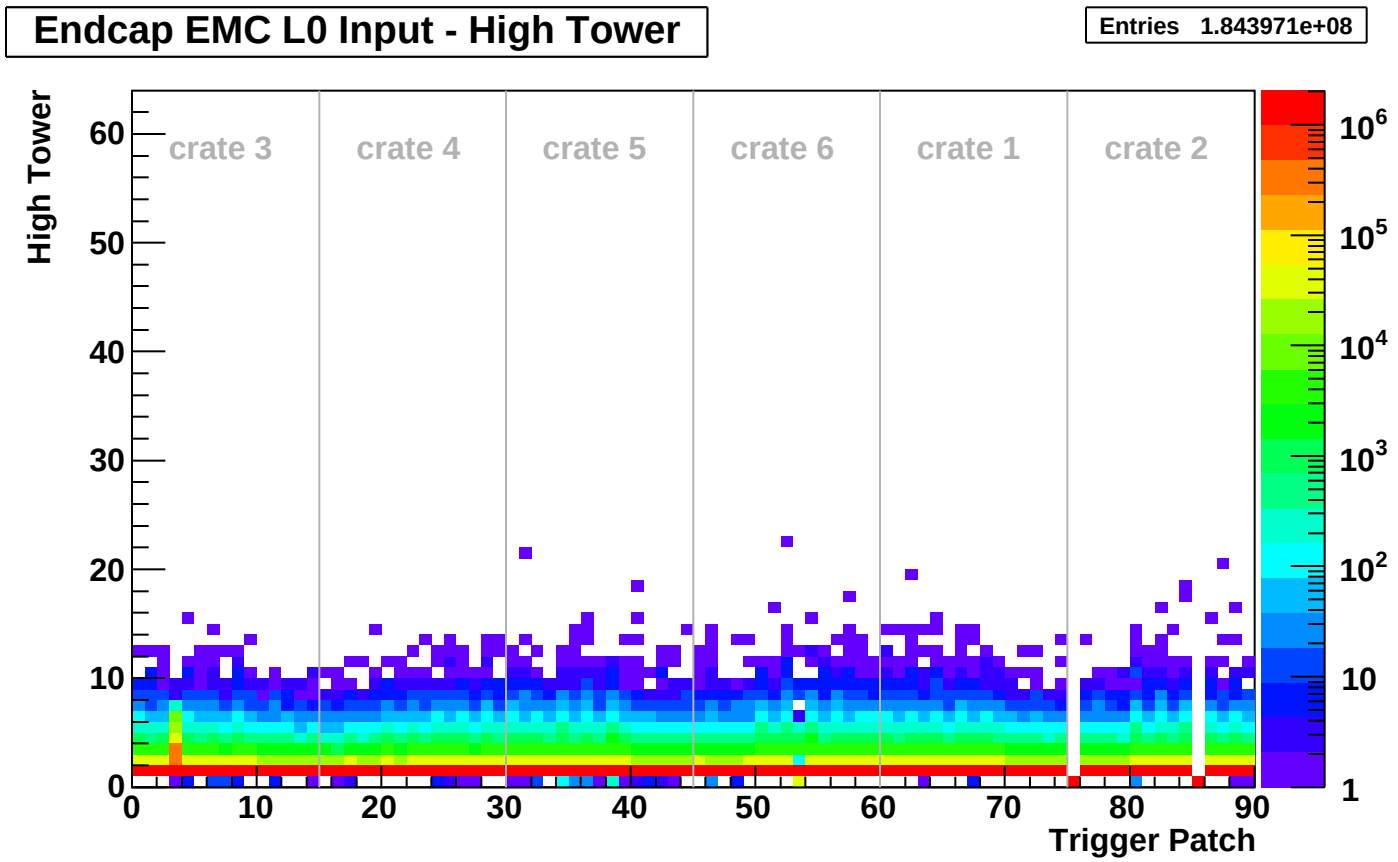
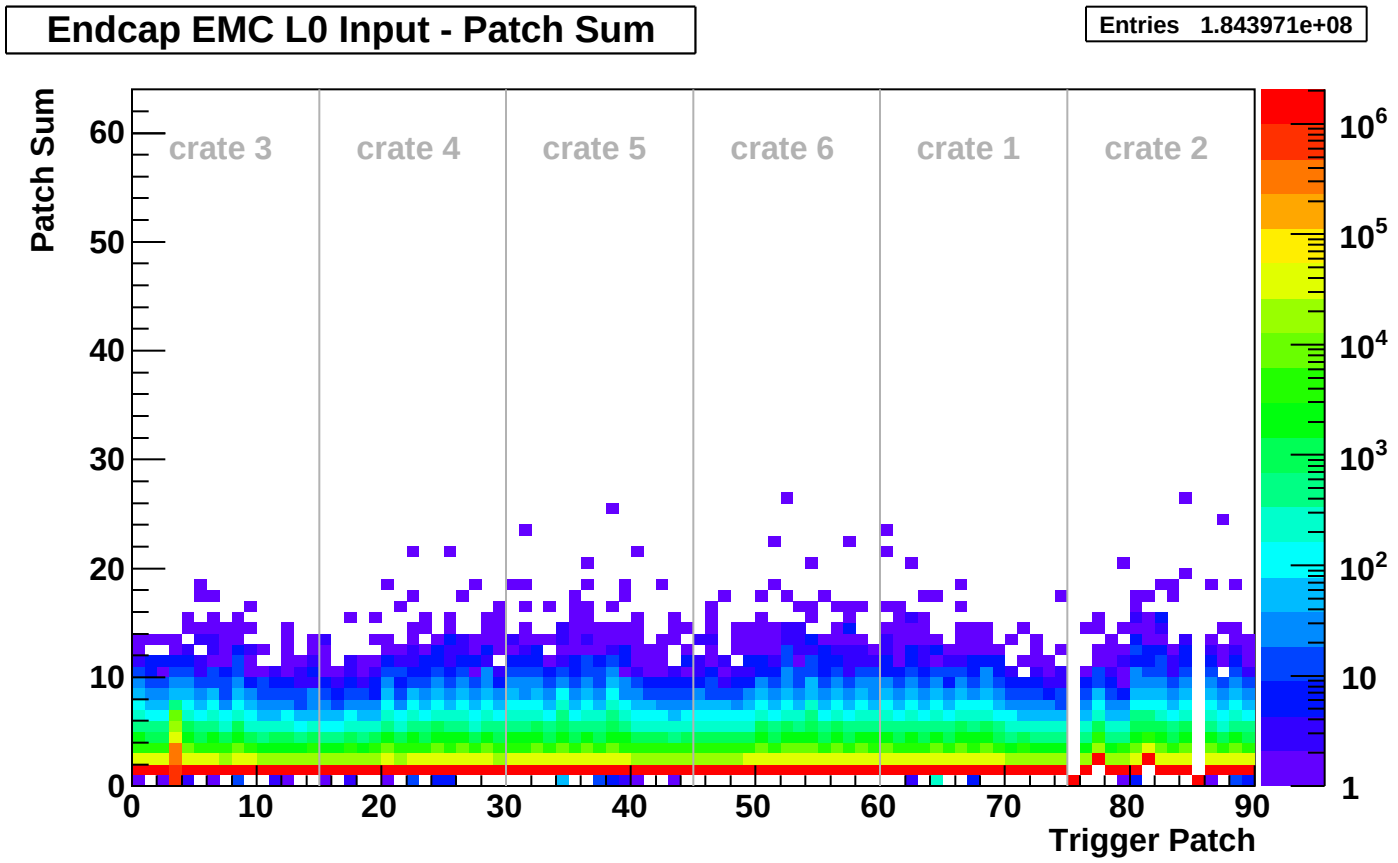
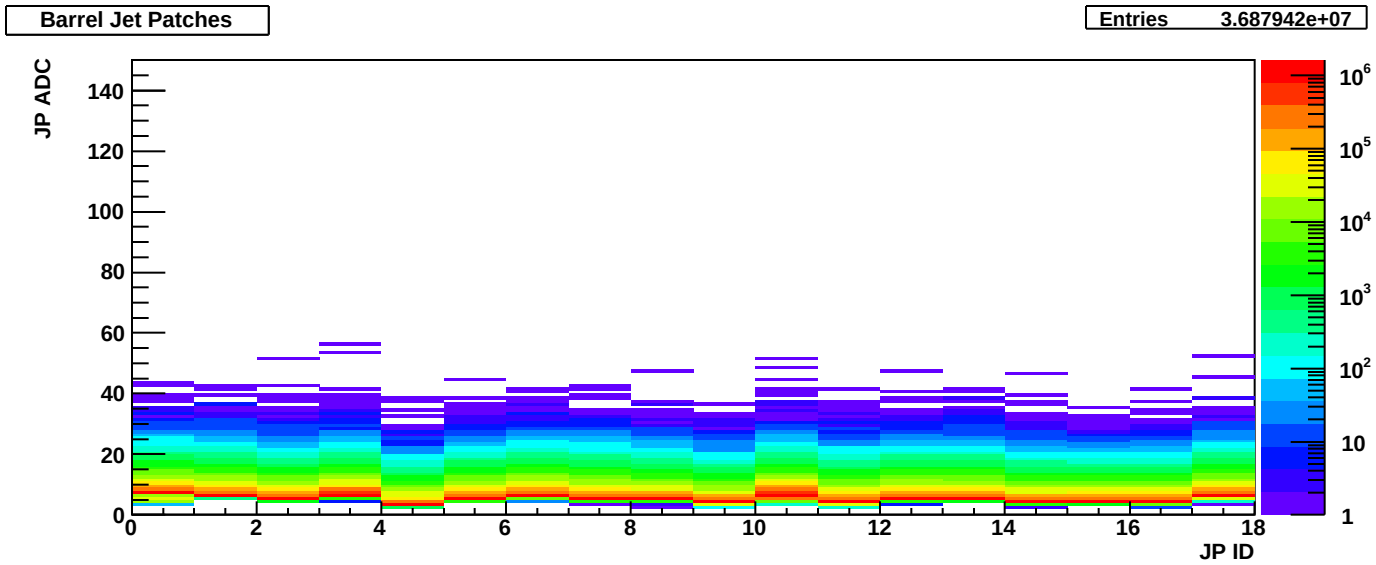
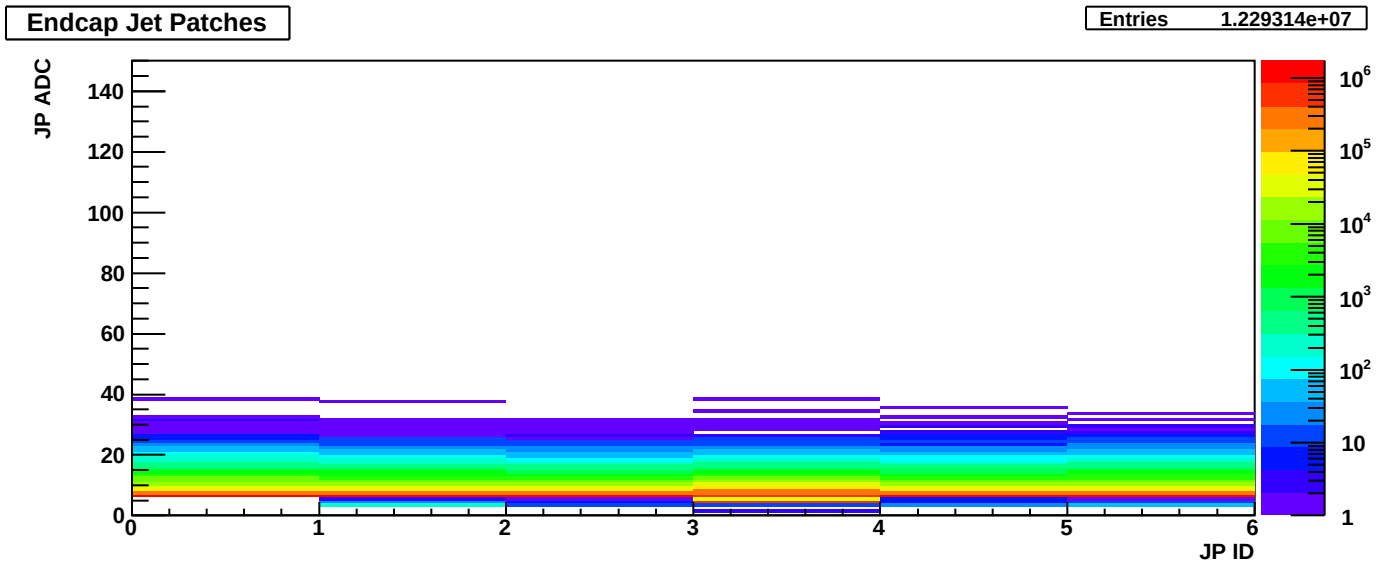

Barrel EMC L0 Input - High Tower
Entries 6.146571e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

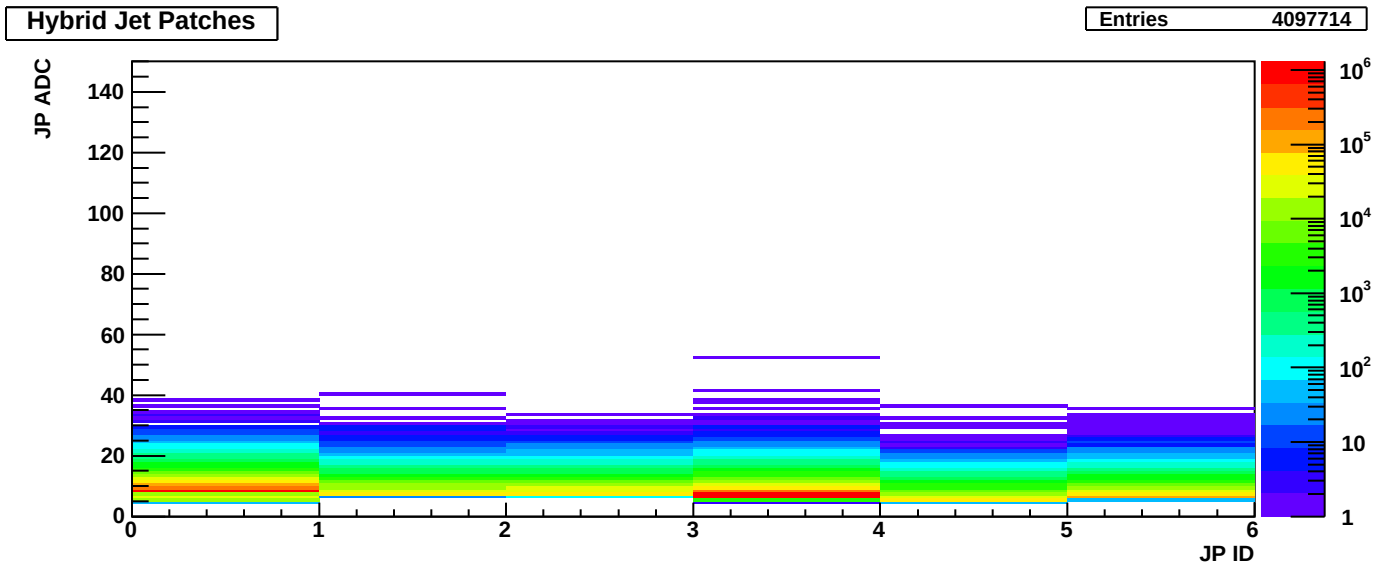

Barrel EMC L0 Input - Patch Sum
Entries 6.146571e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

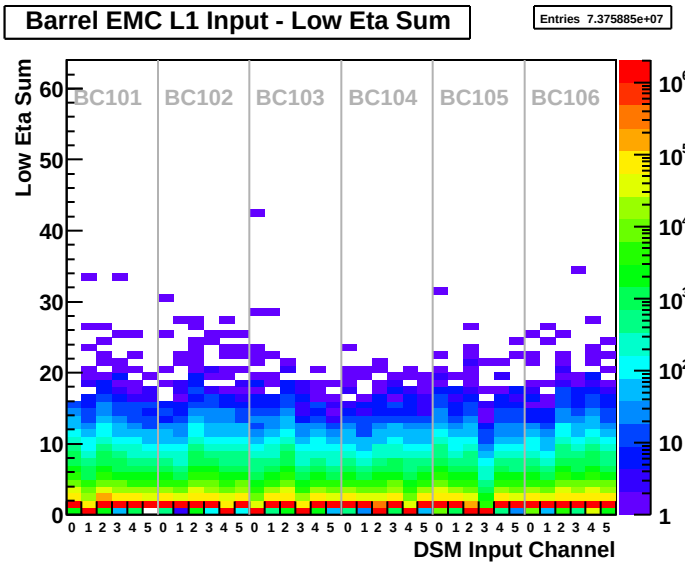

Endcap EMC L0 Input - High Tower
Entries 1.843971e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

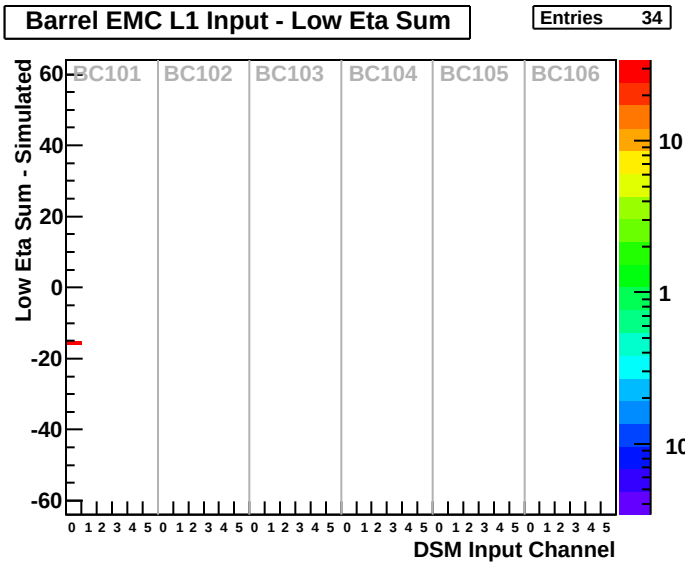

Endcap EMC L0 Input - Patch Sum
Entries 1.843971e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

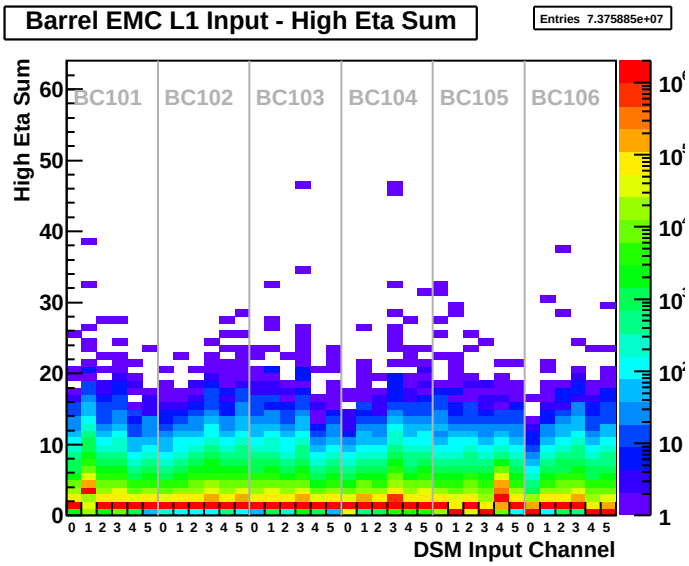

Barrel Jet Patches
Entries 3.687942e+07
JP ADC
JP ID

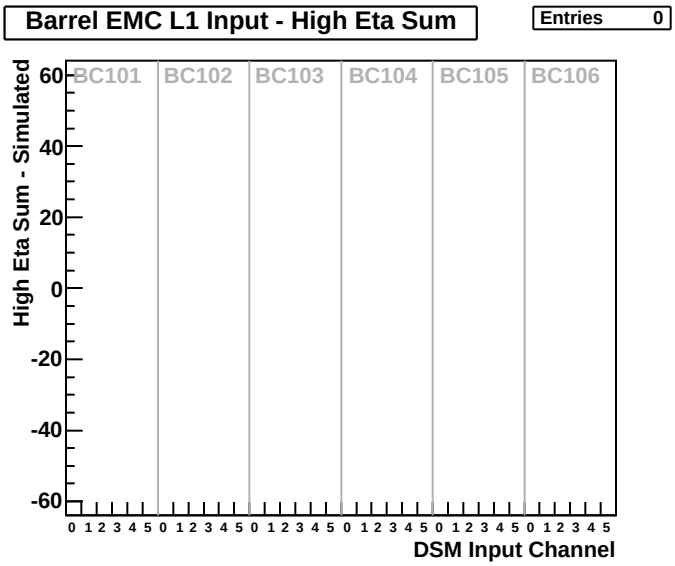

Endcap Jet Patches
Entries 1.229314e+07
JP ADC
JP ID

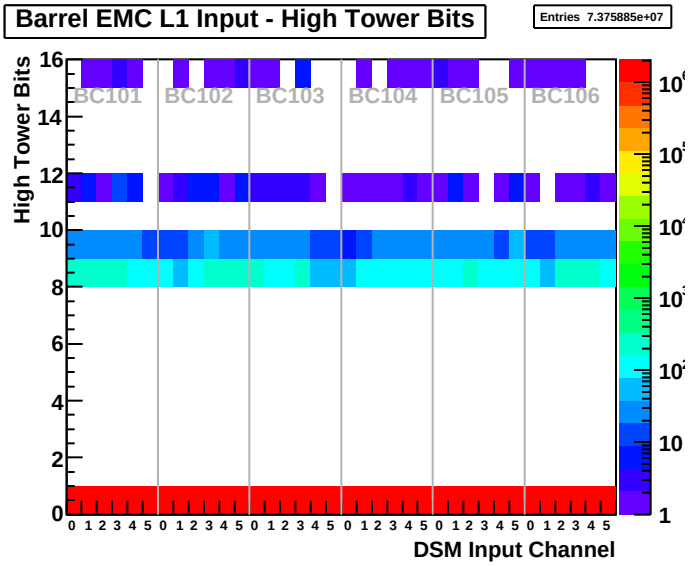

Hybrid Jet Patches
Entries 4097714
JP ADC
JP ID

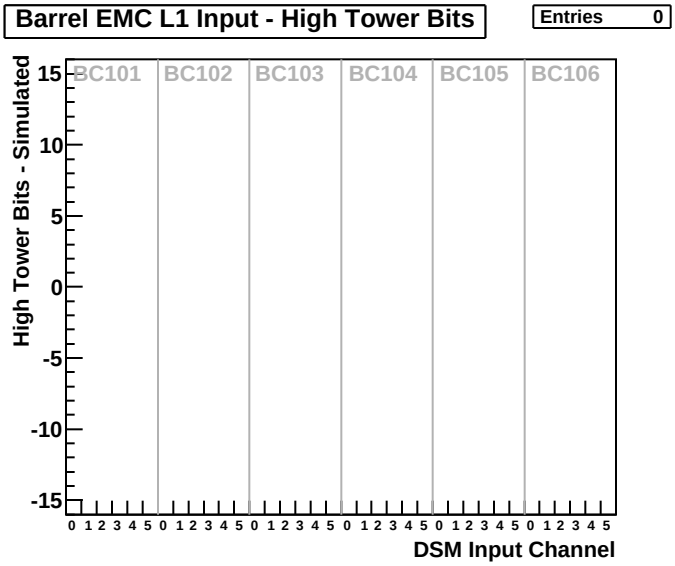

Barrel EMC L1 Input - Low Eta Sum
Entries 7.375885e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

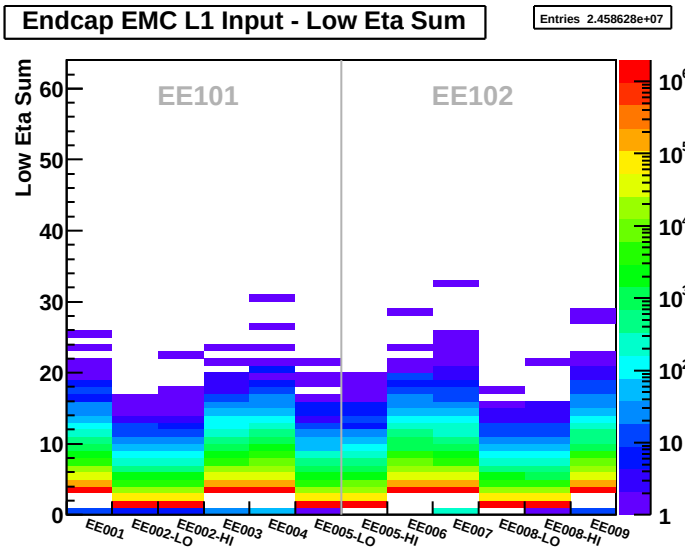

Barrel EMC L1 Input - Low Eta Sum
Entries 34
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

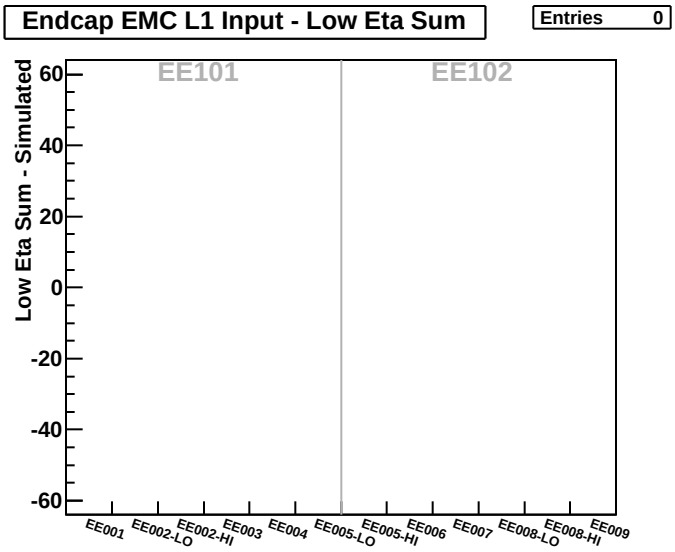

Barrel EMC L1 Input - High Eta Sum
Entries 7.375885e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

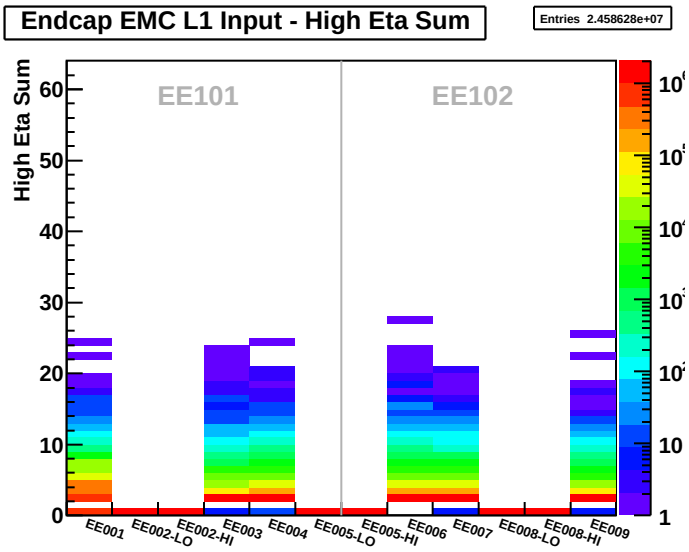

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

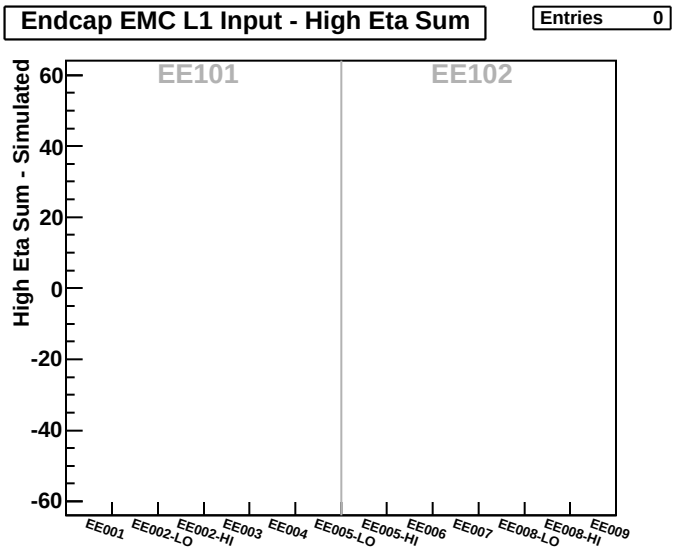

Barrel EMC L1 Input - High Tower Bits
Entries 7.375885e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

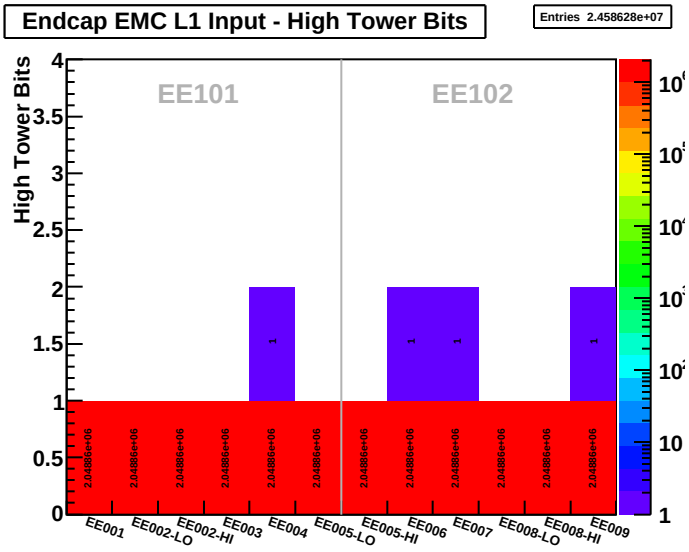

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

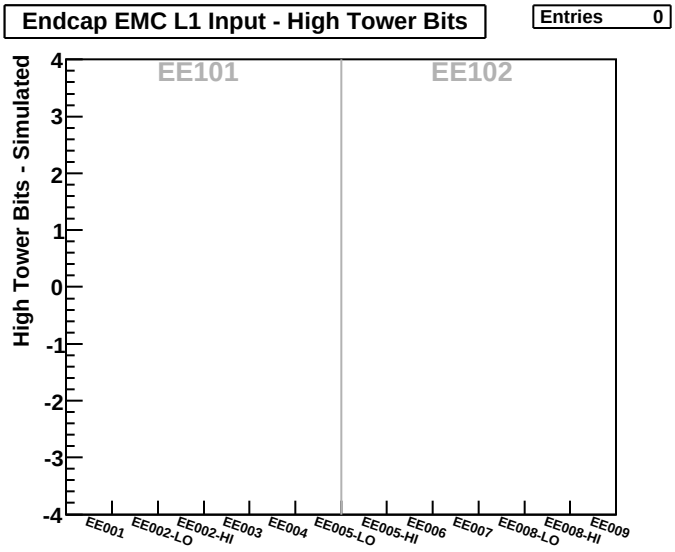

Endcap EMC L1 Input - Low Eta Sum
Entries 2.458628e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

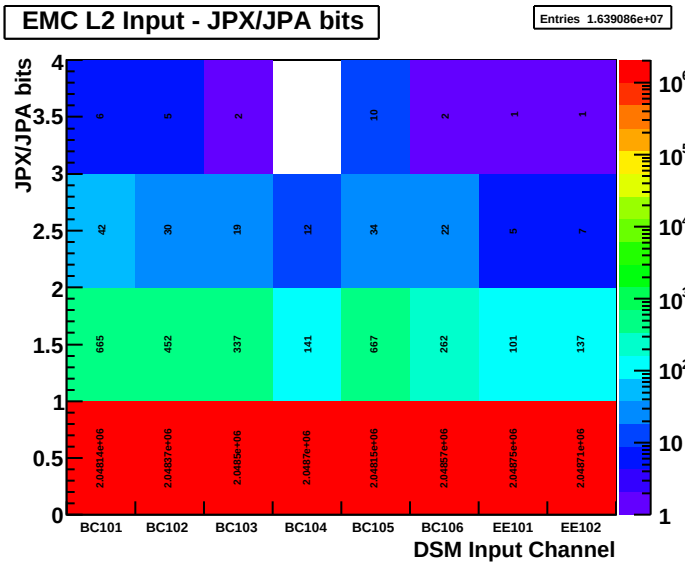

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

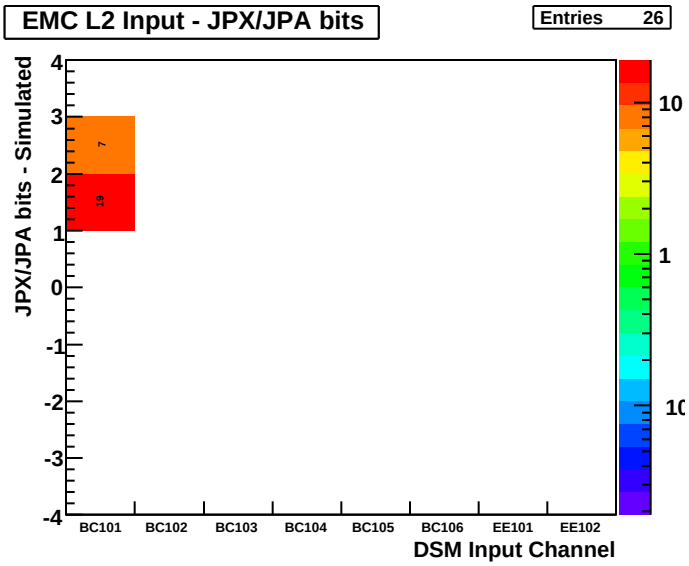

Endcap EMC L1 Input - High Eta Sum
Entries 2.458628e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

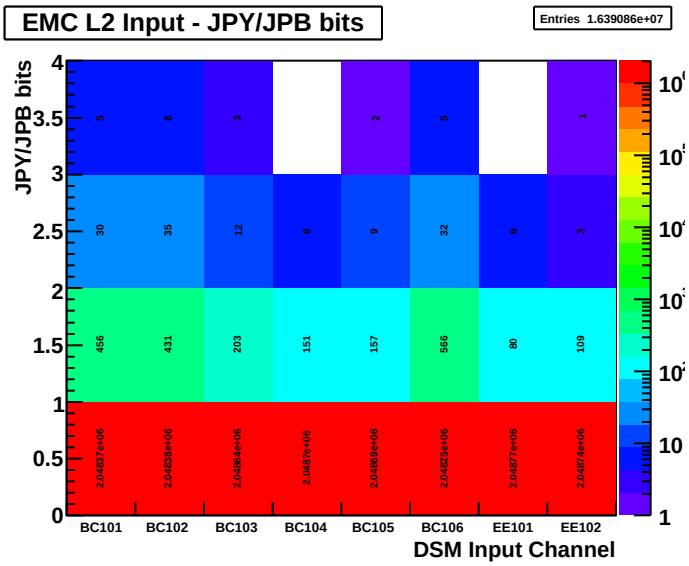

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

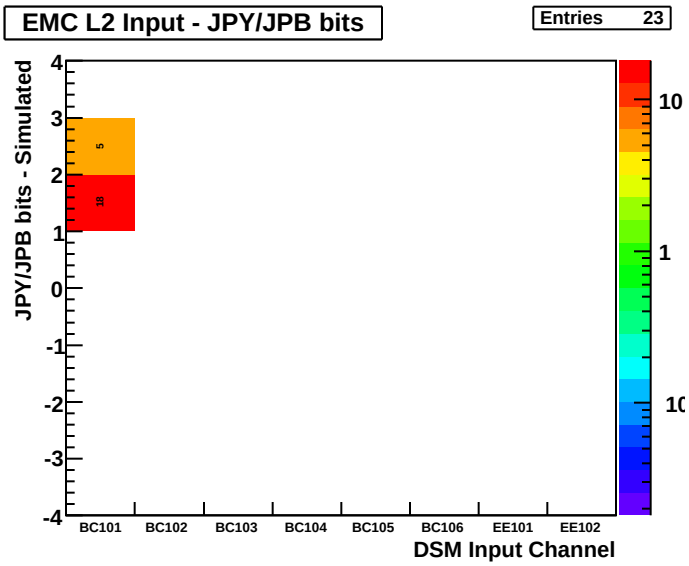

Endcap EMC L1 Input - High Tower Bits
Entries 2.458628e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

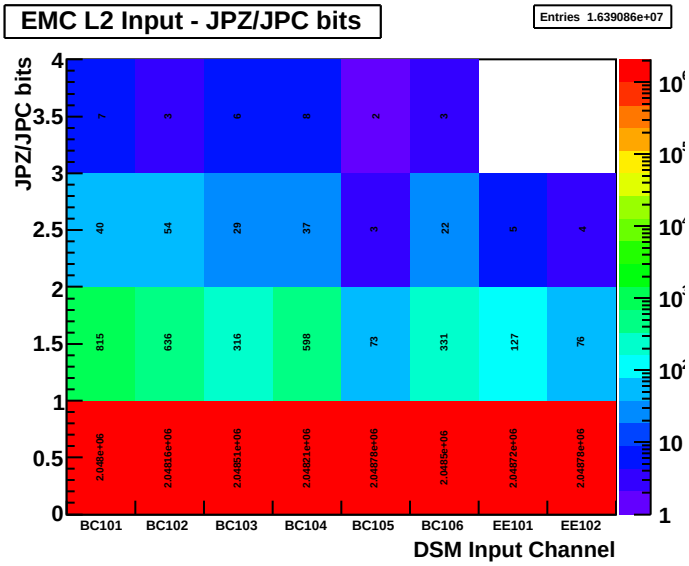

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

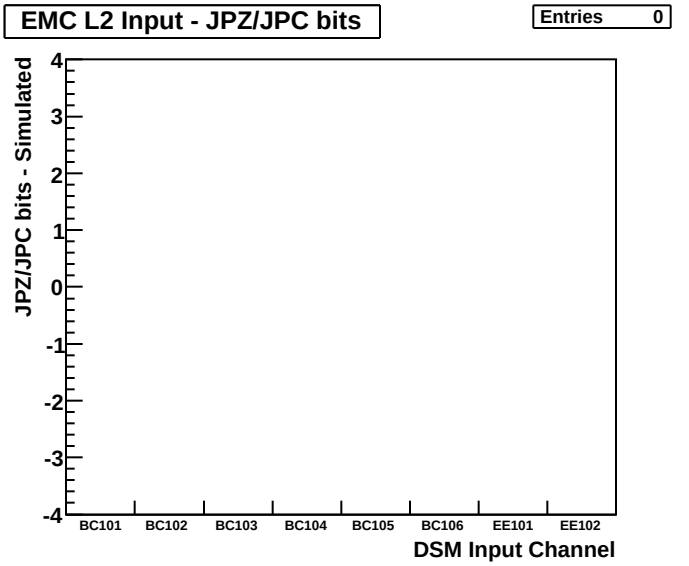

EMC L2 Input - JPX/JPA bits
Entries 1.639086e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6 5 2 10 2 1 1
42 30 19 12 34 22 5 7
665 452 337 141 667 262 101 137
2.04814e+06 2.04837e+06 2.0485e+06 2.0487e+06 2.04815e+06 2.04857e+06 2.04875e+06 2.04871e+06

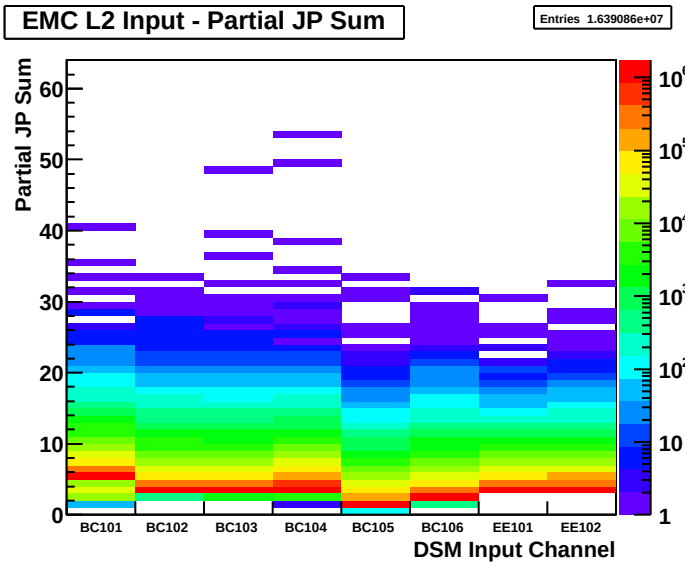

EMC L2 Input - JPX/JPA bits
Entries 26
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
7
19

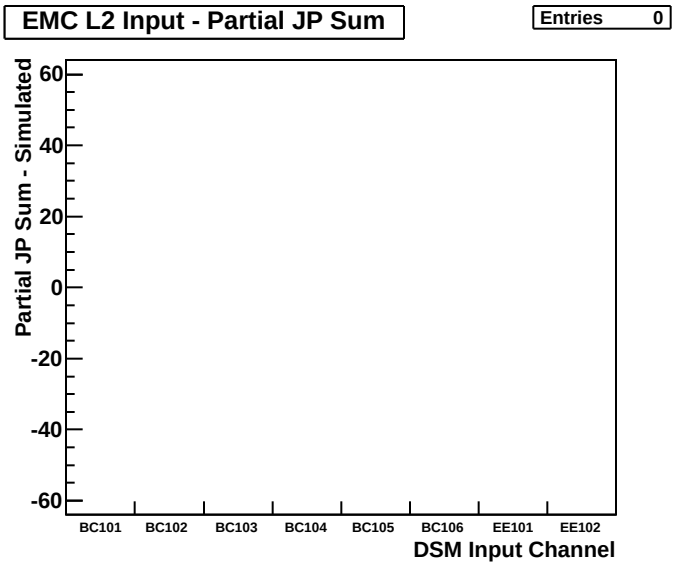

EMC L2 Input - JPY/JPB bits
Entries 1.639086e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5 8 3 2 5 1
30 35 12 8 9 32 6 3
456 431 203 151 157 566 80 109
2.04837e+06 2.04828e+06 2.04864e+06 2.04876e+06 2.04869e+06 2.04825e+06 2.04877e+06 2.04874e+06

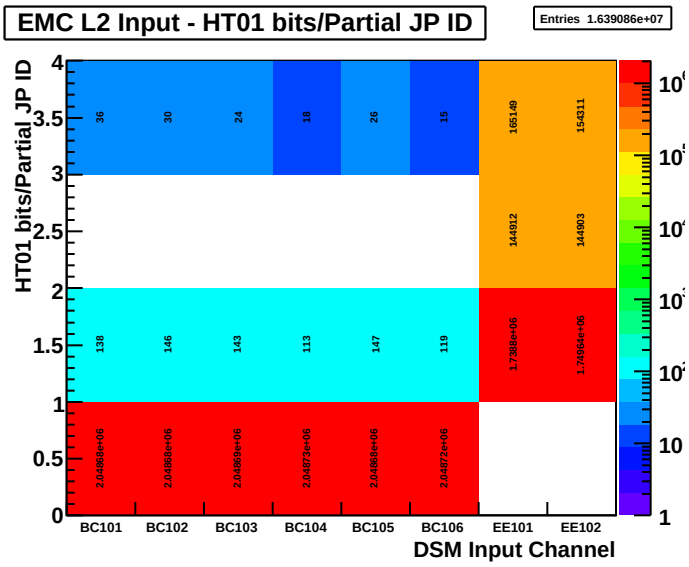

EMC L2 Input - JPY/JPB bits
Entries 23
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5
18

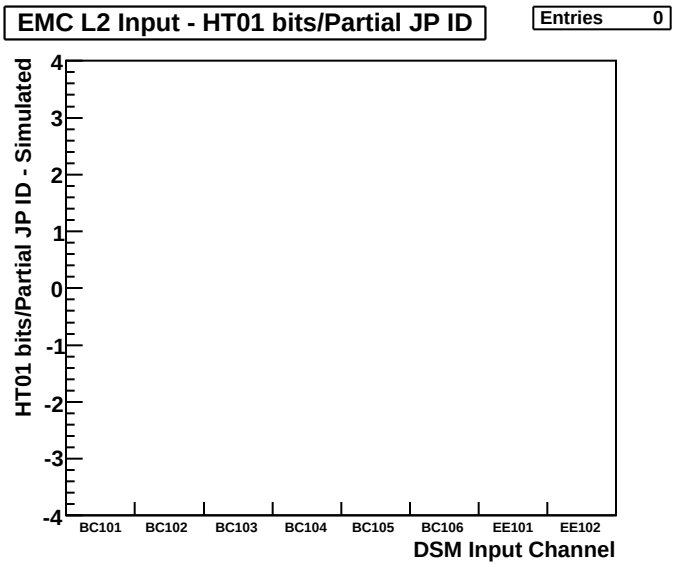

EMC L2 Input - JPZ/JPC bits
Entries 1.639086e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
7 3 6 8 2 3
40 54 29 37 3 22 5 4
815 636 316 598 73 331 127 76
2.048e+06 2.04816e+06 2.04851e+06 2.04821e+06 2.04878e+06 2.0485e+06 2.04872e+06 2.04878e+06

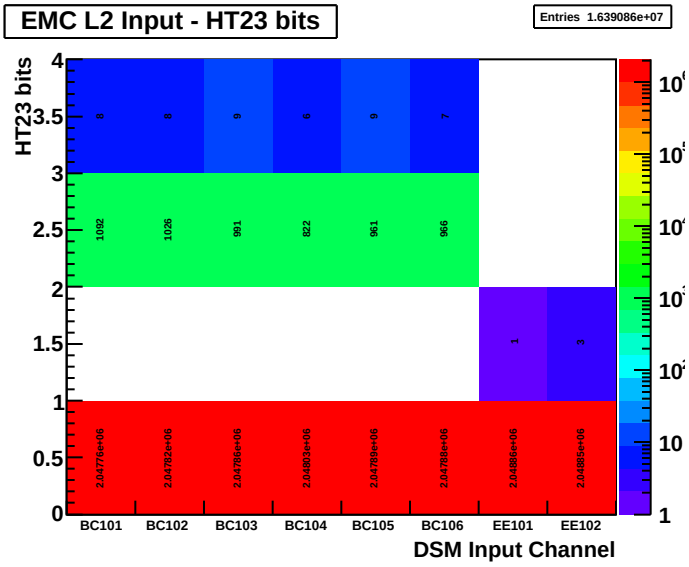

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

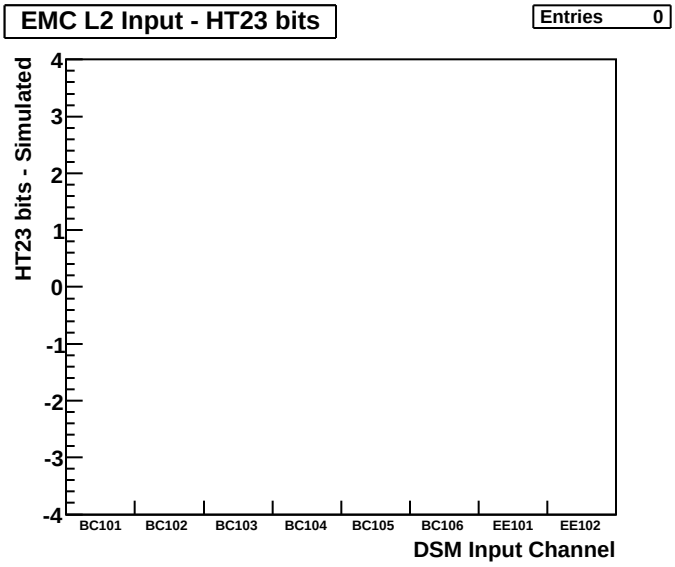

EMC L2 Input - Partial JP Sum
Entries 1.639086e+07
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

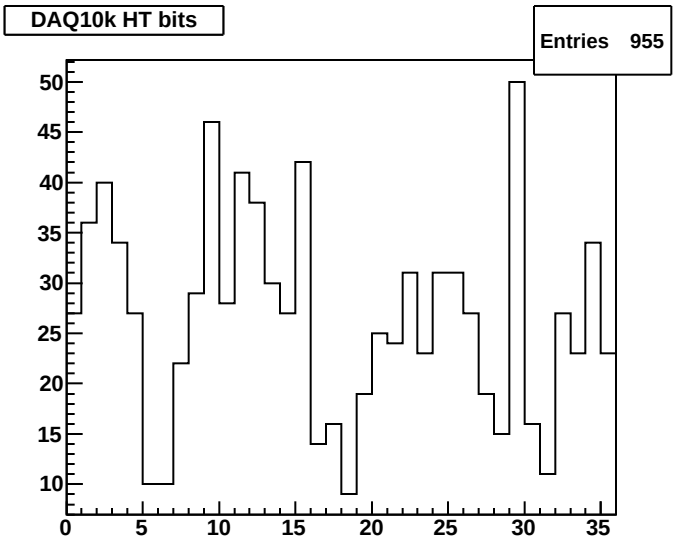

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

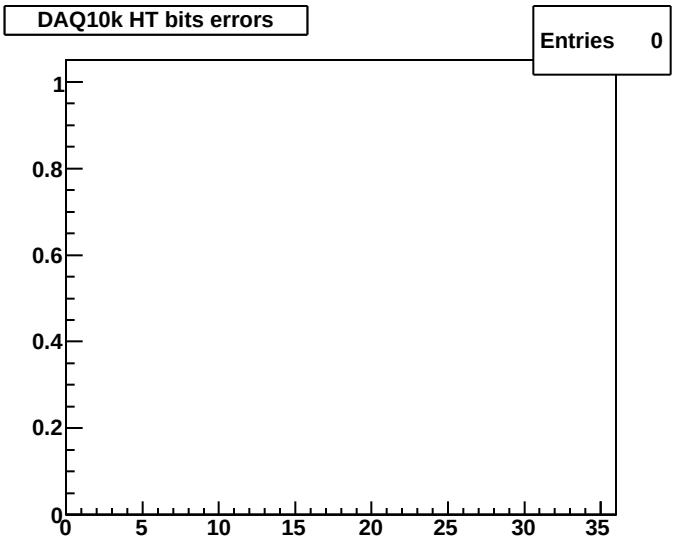

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.639086e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

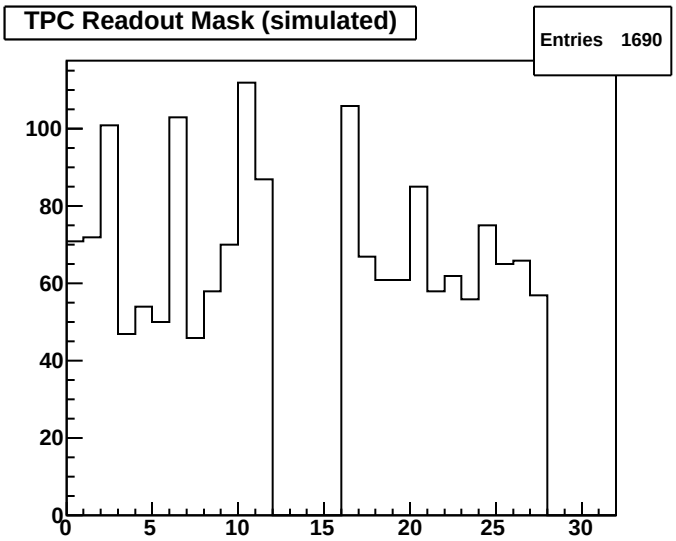

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

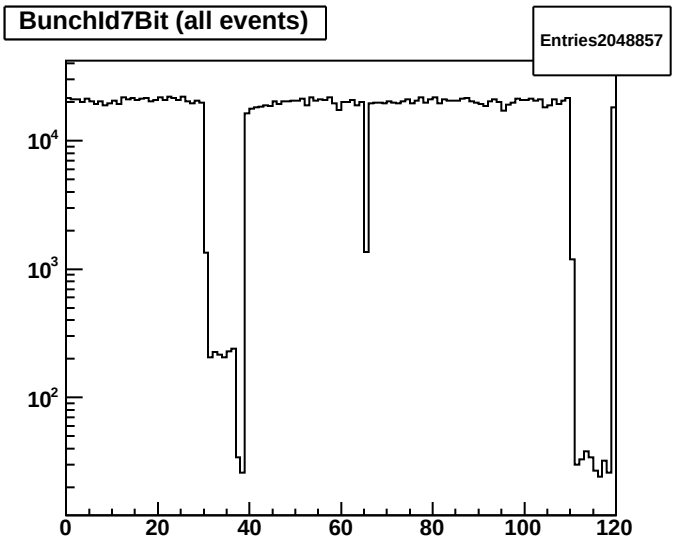

EMC L2 Input - HT23 bits
Entries 1.639086e+07
HT23 bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

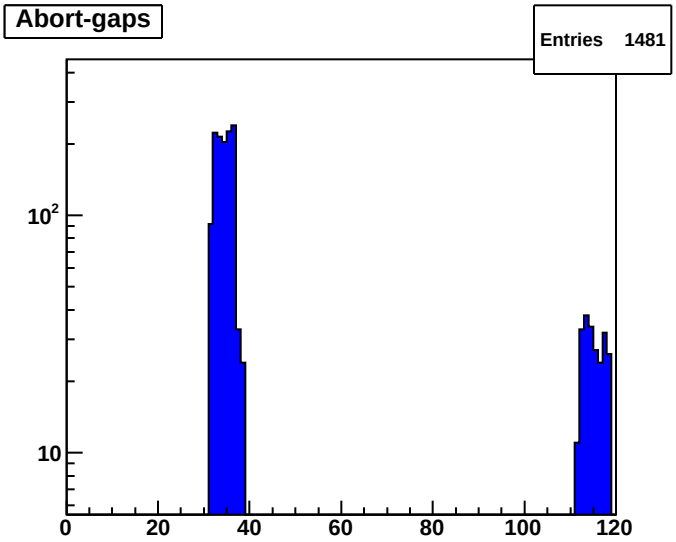

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

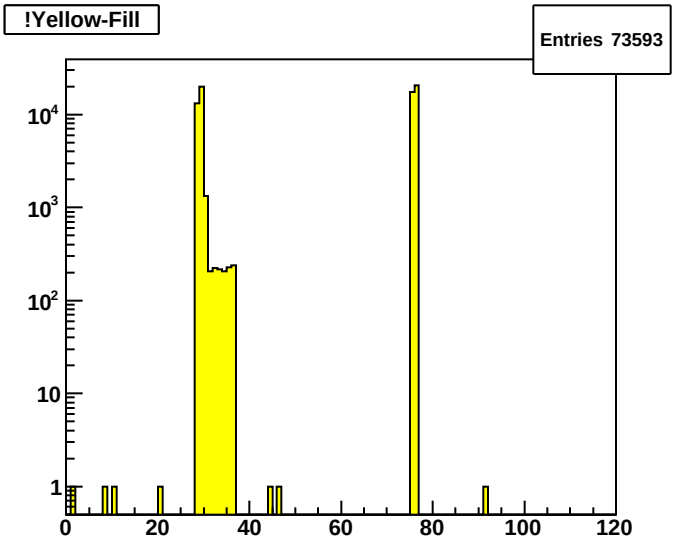

DAQ10k HT bits
Entries 955

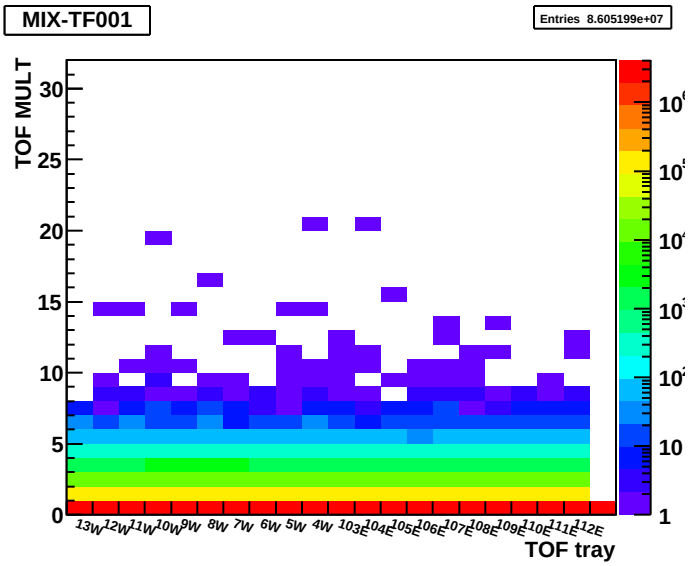

DAQ10k HT bits errors
Entries 0

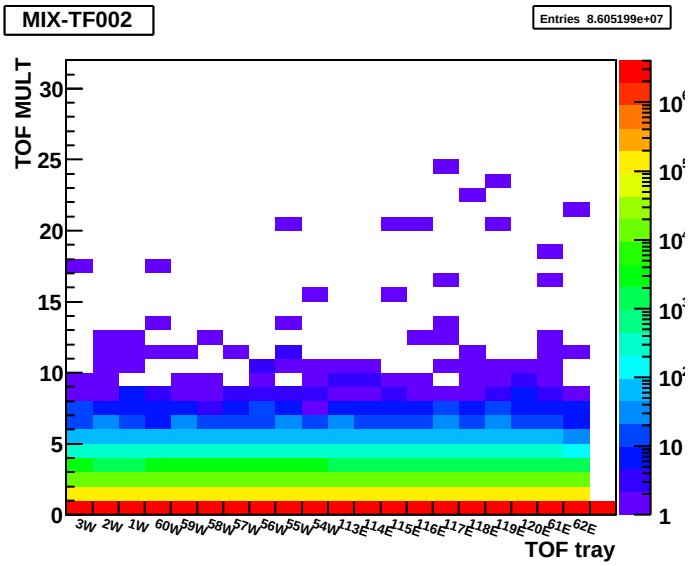

TPC Readout Mask (simulated)
Entries 1690

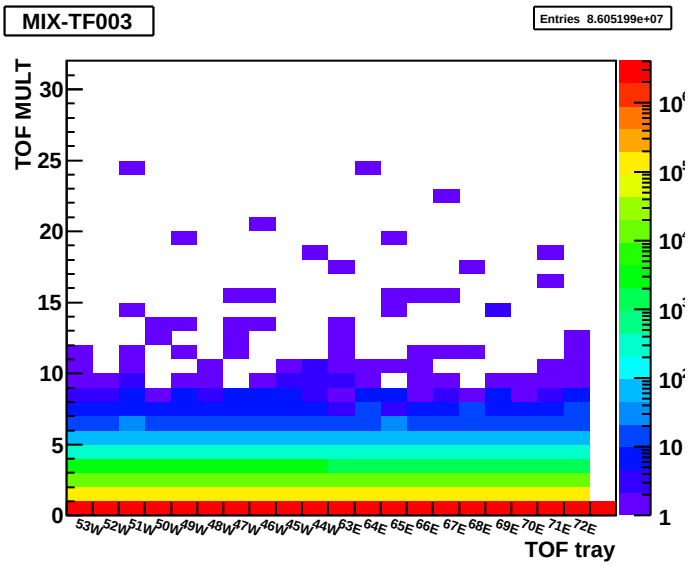

BunchId7Bit (all events)
Entries2048857

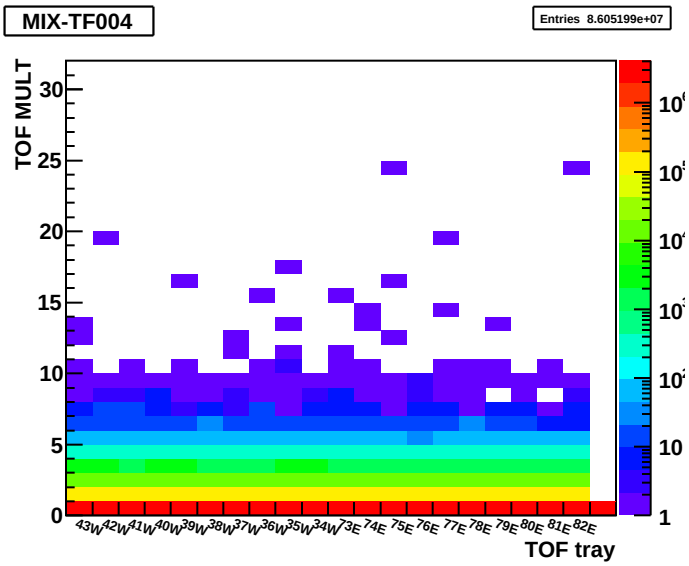

Abort-gaps
Entries 1481

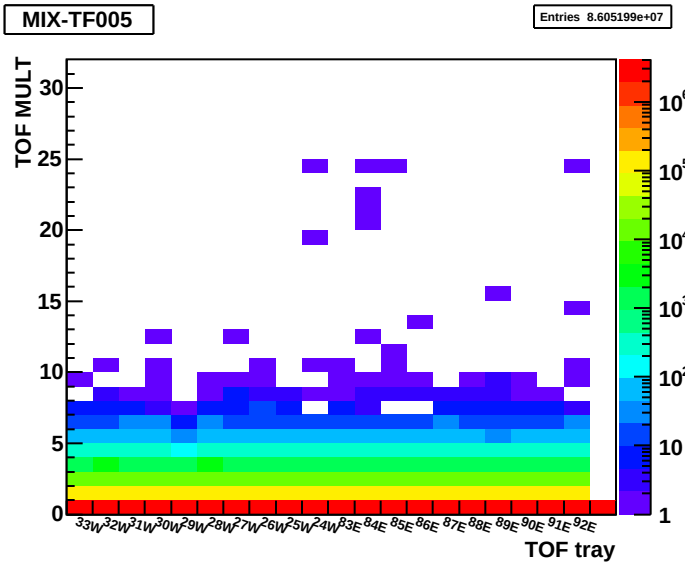

!Yellow-Fill
Entries 73593

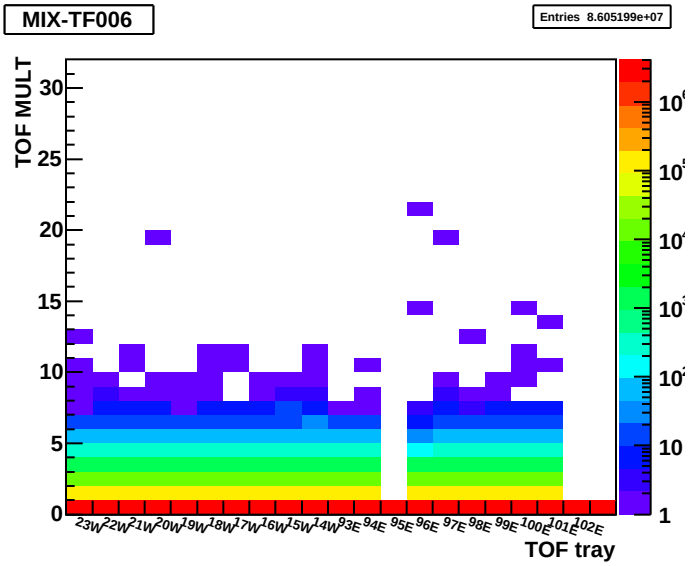

MIX-TF001
Entries 8.605199e+07
TOF MULT
TOF tray

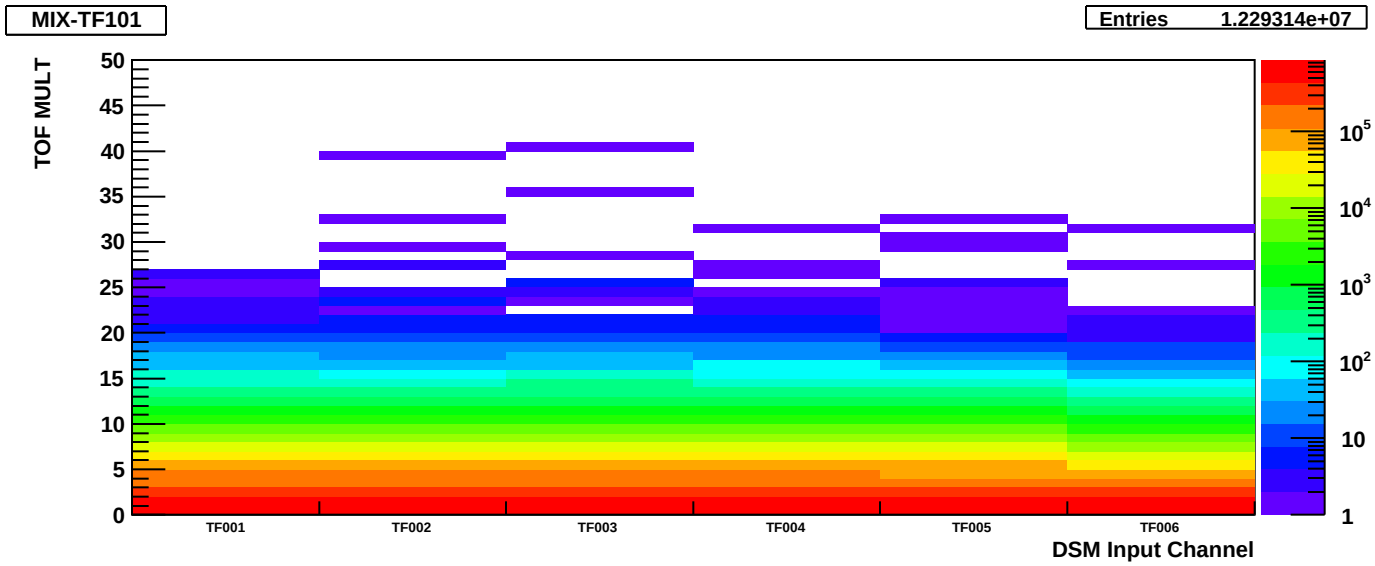

MIX-TF002
Entries 8.605199e+07
TOF MULT
TOF tray

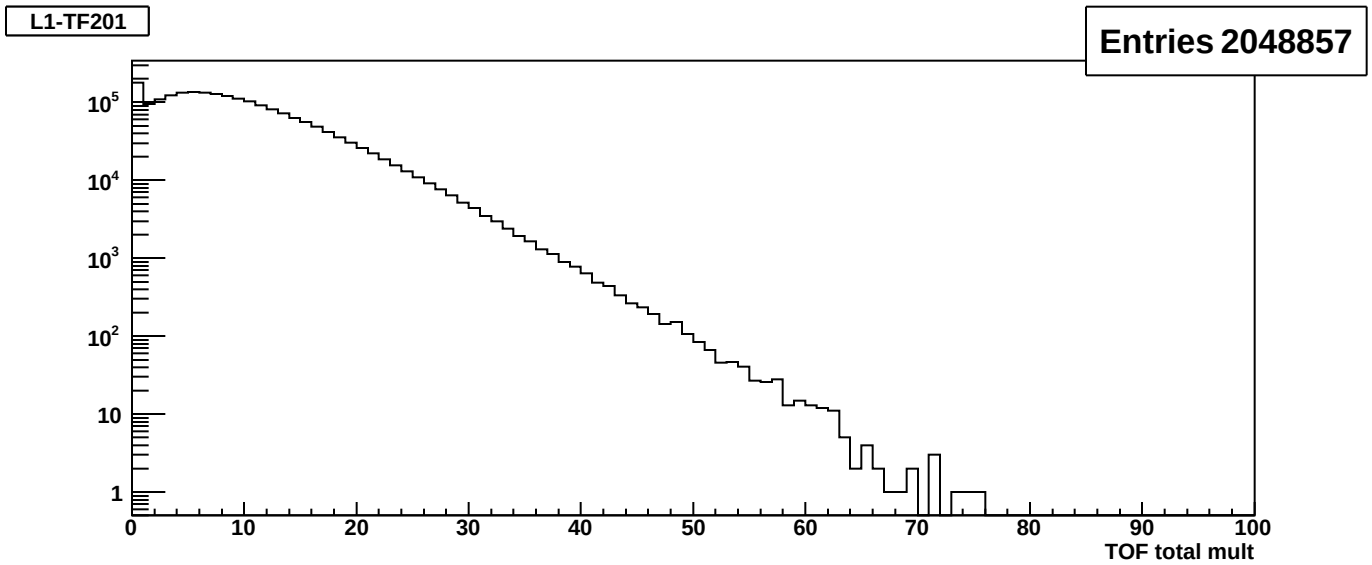

MIX-TF003
Entries 8.605199e+07
TOF MULT
TOF tray

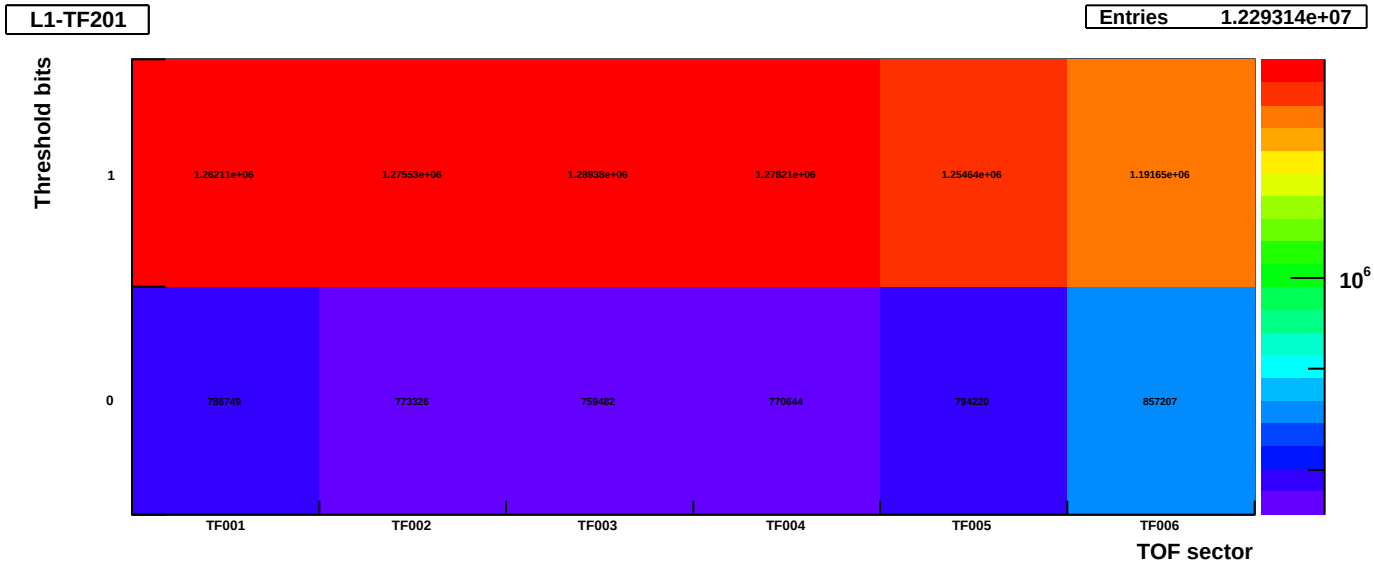

MIX-TF004
Entries 8.605199e+07
TOF MULT
TOF tray

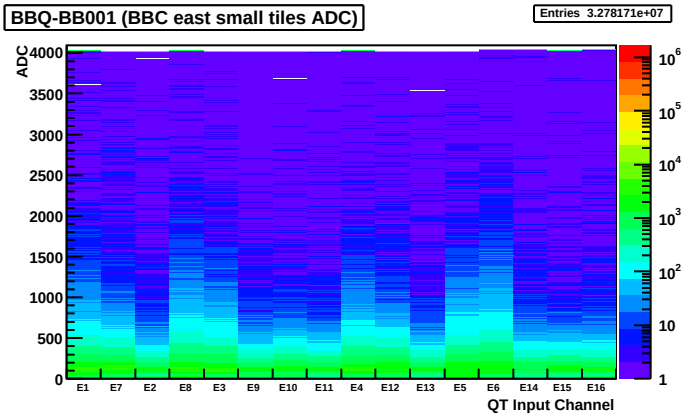

MIX-TF005
Entries 8.605199e+07
TOF MULT
TOF tray

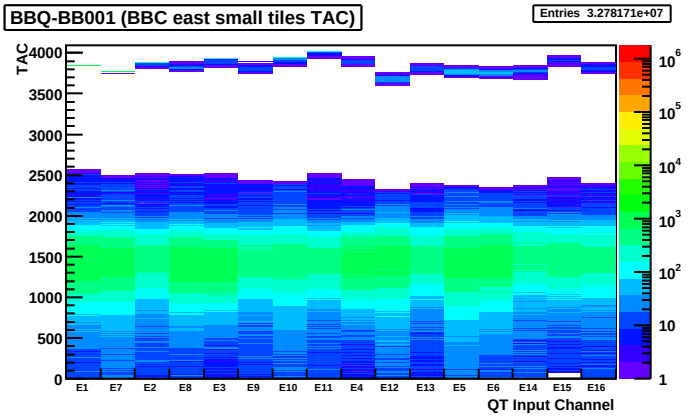

MIX-TF006
Entries 8.605199e+07
TOF MULT
TOF tray

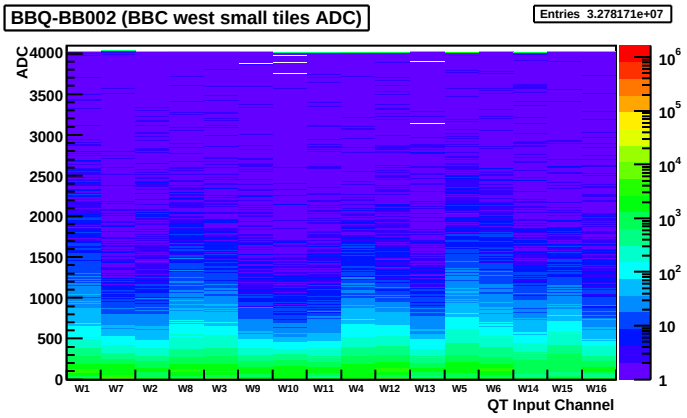

MIX-TF101
Entries 1.229314e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

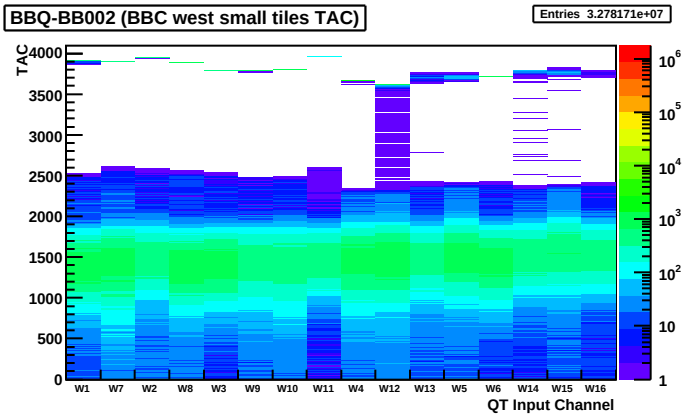

L1-TF201
Entries 2048857
TOF total mult

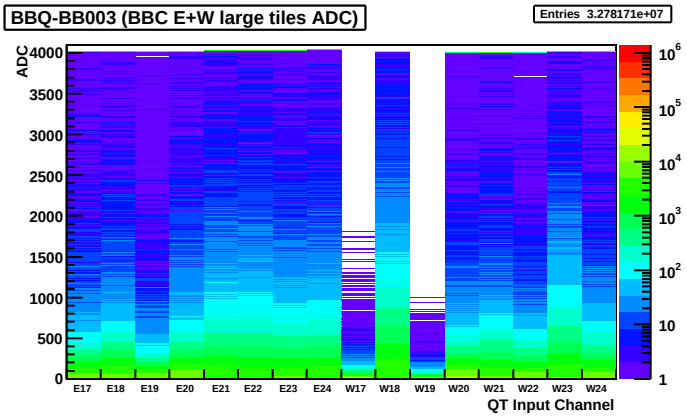

L1-TF201
Entries 1.229314e+07
Threshold bits
1.26211e+06
1.27553e+06
1.28938e+06
1.27821e+06
1.25464e+06
1.19165e+06
786749
773326
759482
770644
794220
857207
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

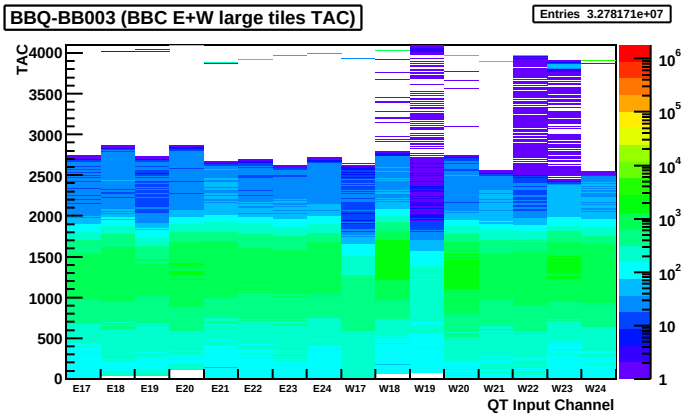

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.278171e+07
ADC
QT Input Channel

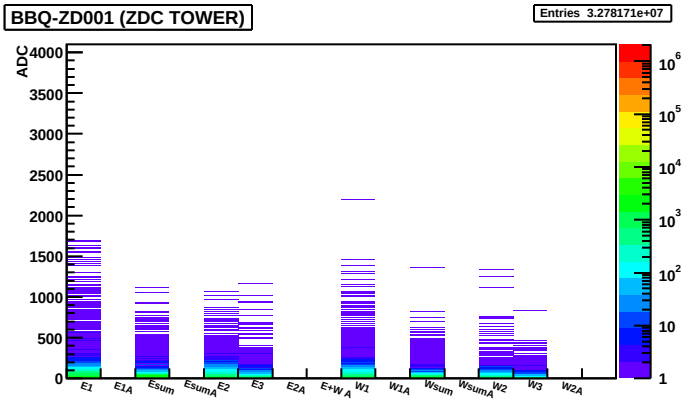

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.278171e+07
TAC
QT Input Channel

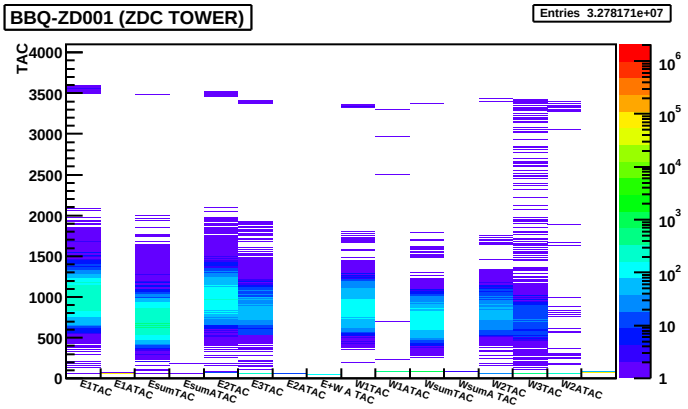

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.278171e+07
ADC
QT Input Channel

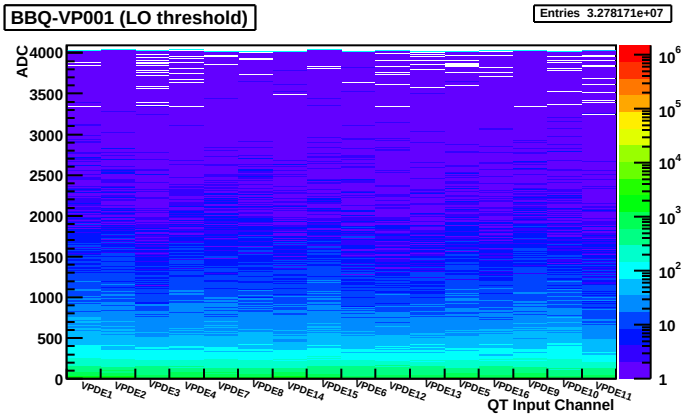

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.278171e+07
TAC
QT Input Channel

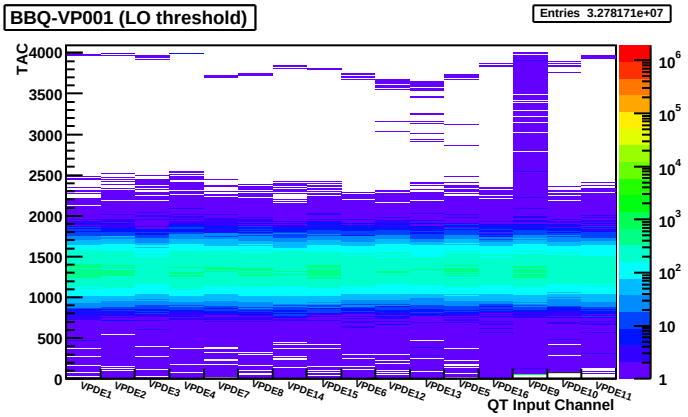

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.278171e+07
ADC
QT Input Channel

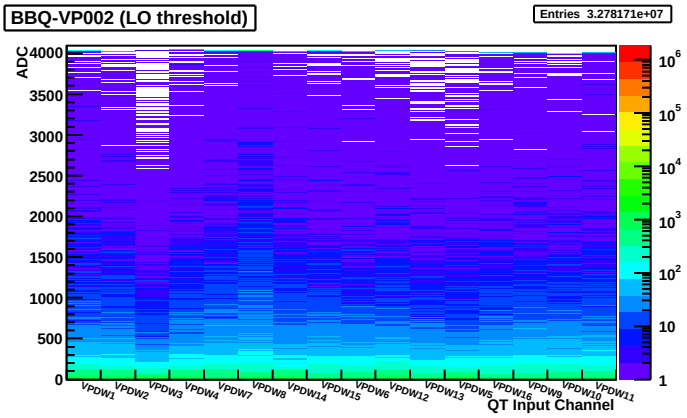

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.278171e+07
TAC
QT Input Channel

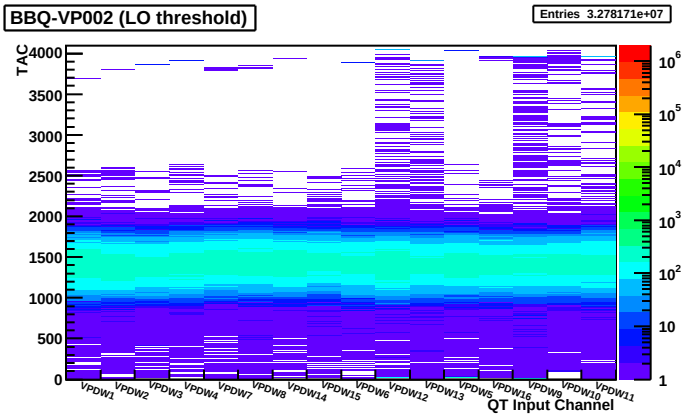

BBQ-ZD001 (ZDC TOWER)
Entries 3.278171e+07
ADC

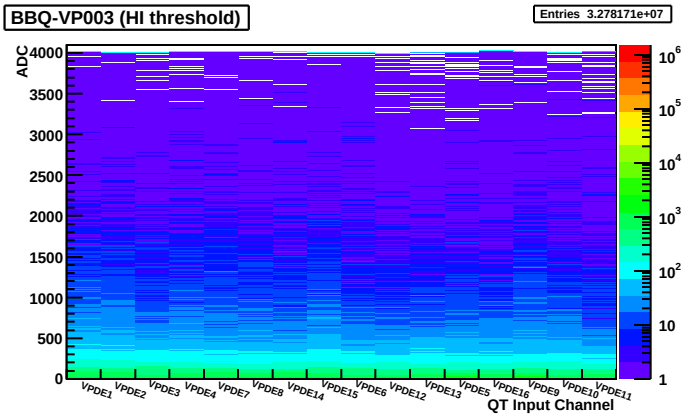

BBQ-ZD001 (ZDC TOWER)
Entries 3.278171e+07
TAC

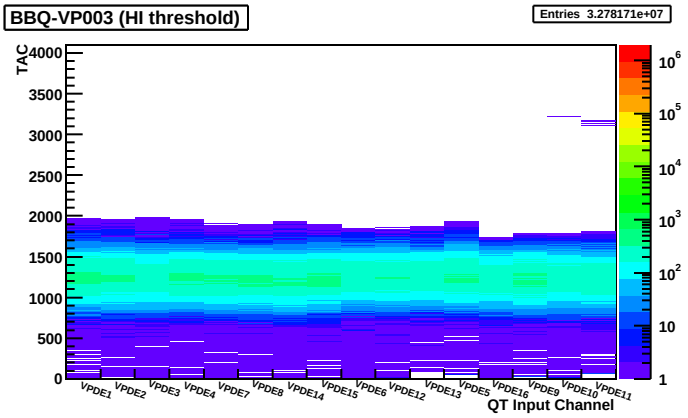

BBQ-VP001 (LO threshold)
Entries 3.278171e+07
ADC
QT Input Channel

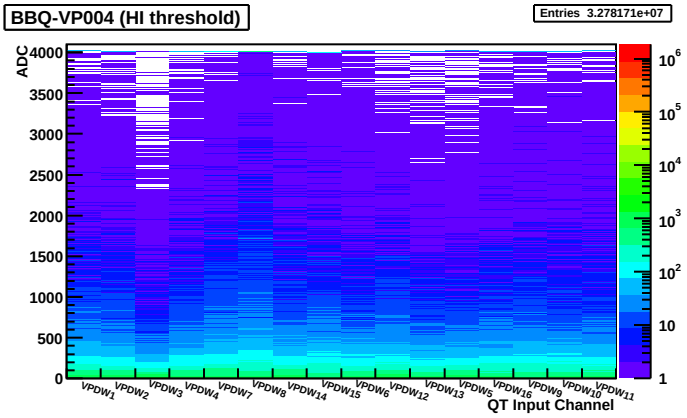

BBQ-VP001 (LO threshold)
Entries 3.278171e+07
TAC
QT Input Channel

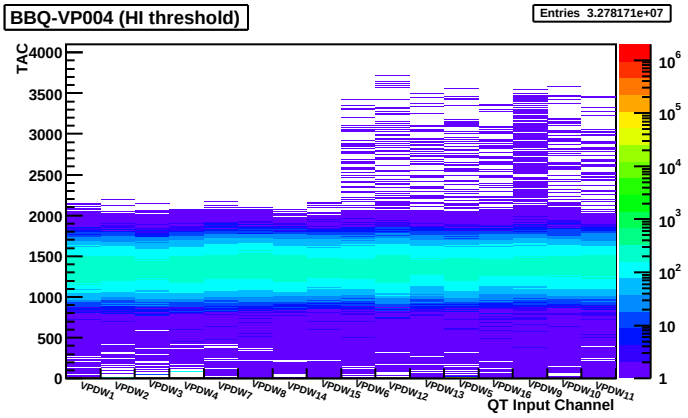

BBQ-VP002 (LO threshold)
Entries 3.278171e+07
ADC
QT Input Channel

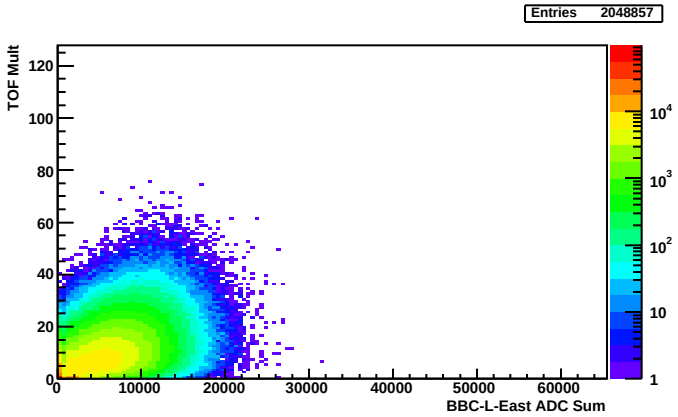

BBQ-VP002 (LO threshold)
Entries 3.278171e+07
TAC
QT Input Channel

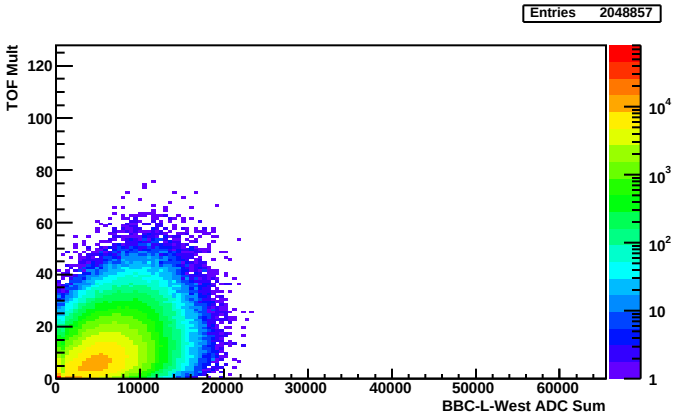

BBQ-VP003 (HI threshold)
Entries 3.278171e+07
ADC
QT Input Channel

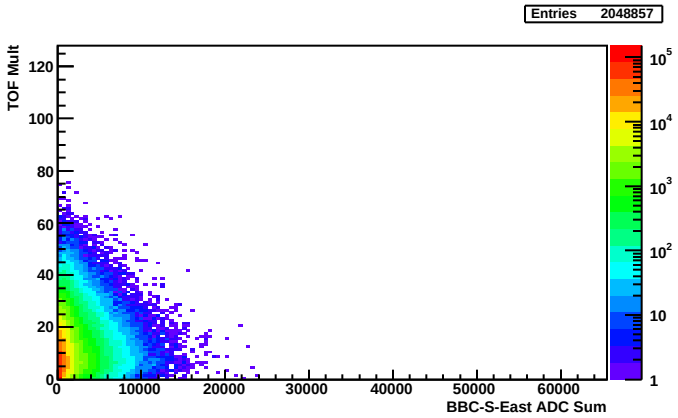

BBQ-VP003 (HI threshold)
Entries 3.278171e+07
TAC
QT Input Channel

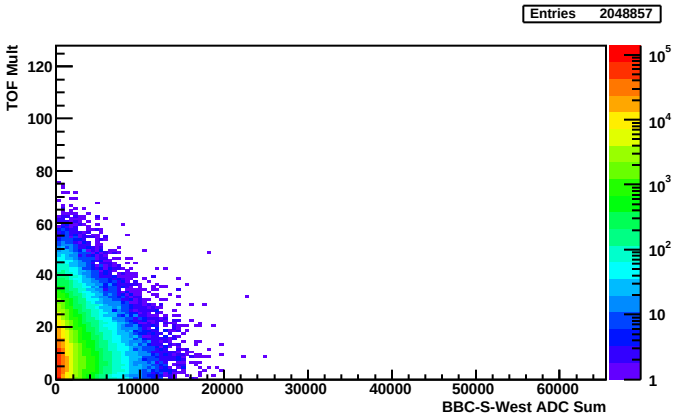

BBQ-VP004 (HI threshold)
Entries 3.278171e+07
ADC
QT Input Channel

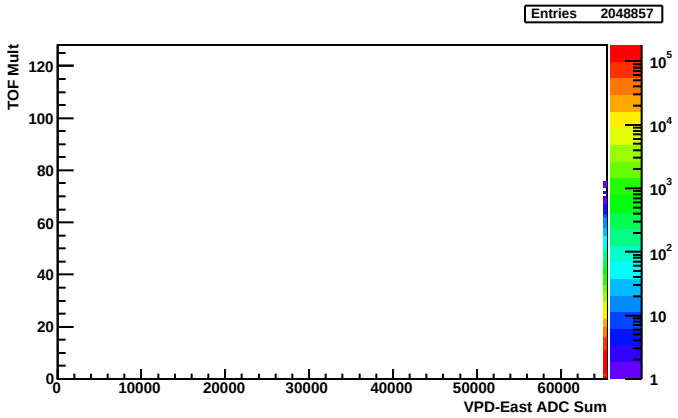

BBQ-VP004 (HI threshold)
Entries 3.278171e+07
TAC
QT Input Channel

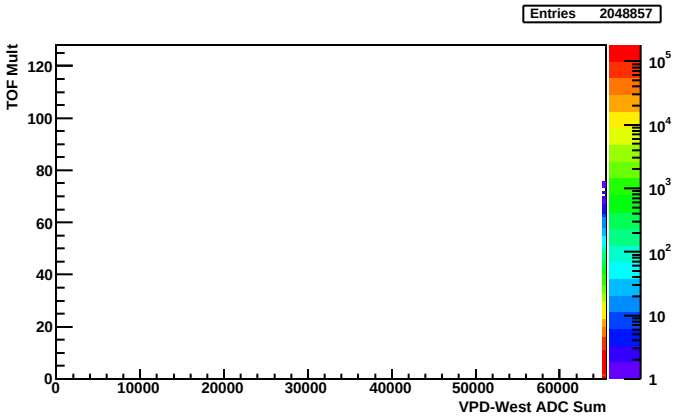

Entries 2048857
TOF Mult
BBC-L-East ADC Sum

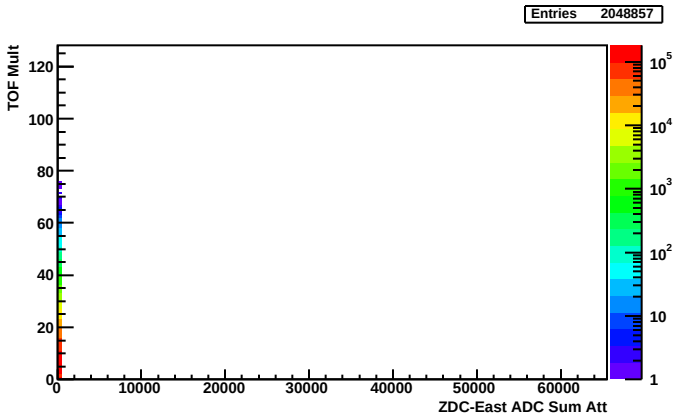

Entries 2048857
TOF Mult
BBC-L-West ADC Sum

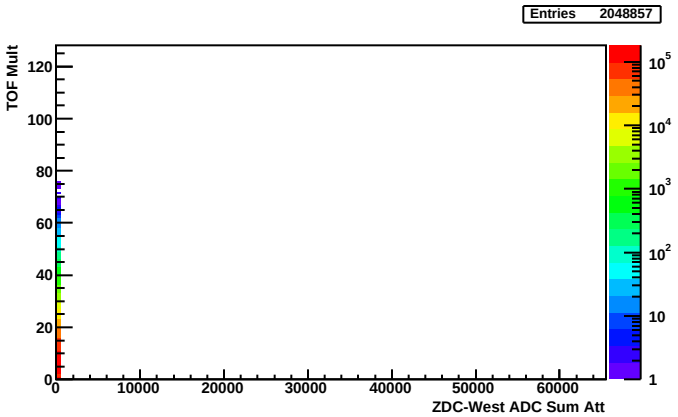

Entries 2048857
TOF Mult
BBC-S-East ADC Sum

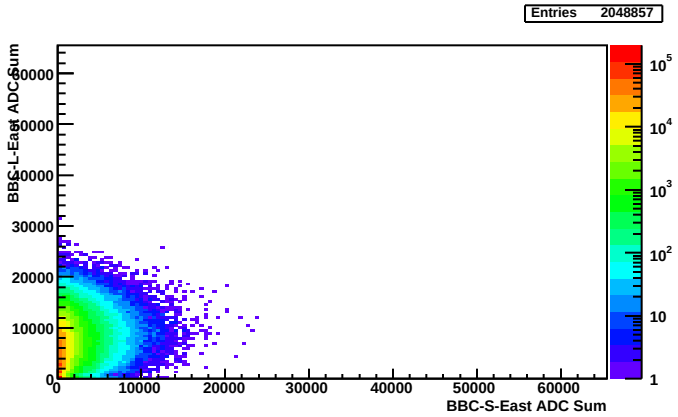

Entries 2048857
TOF Mult
BBC-S-West ADC Sum

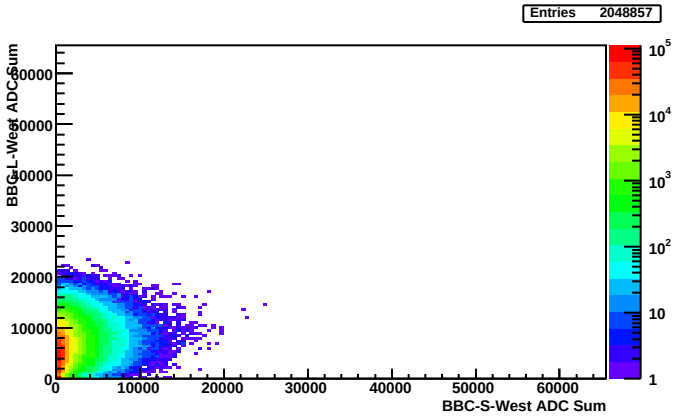

Entries 2048857
TOF Mult
VPD-East ADC Sum

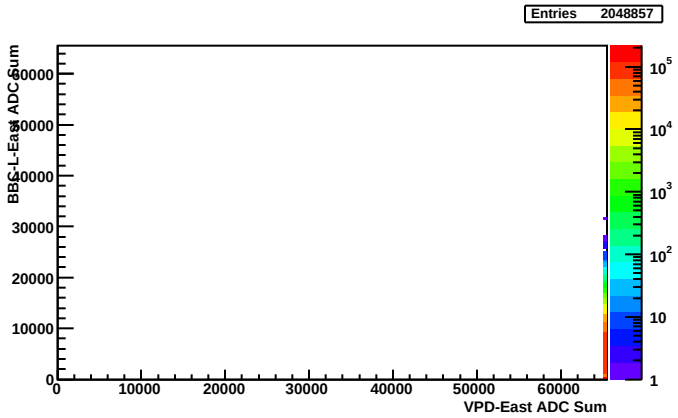

Entries 2048857
TOF Mult
VPD-West ADC Sum

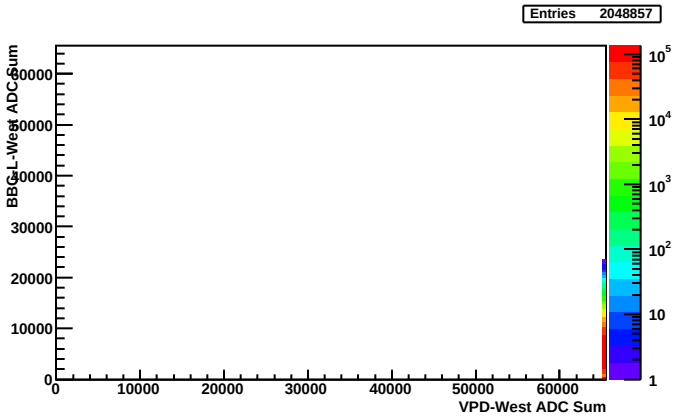

Entries 2048857
TOF Mult
ZDC-East ADC Sum Att

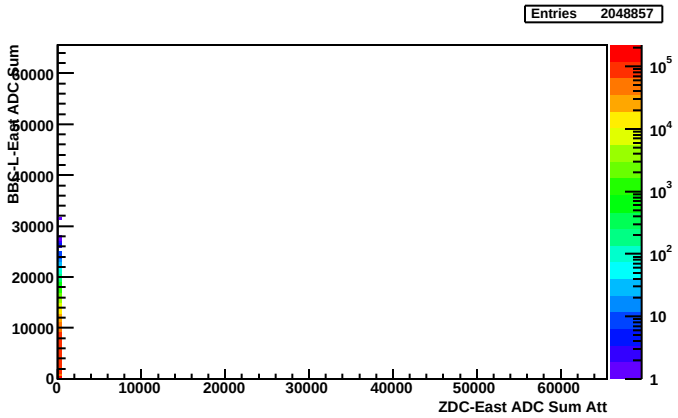

Entries 2048857
TOF Mult
ZDC-West ADC Sum Att

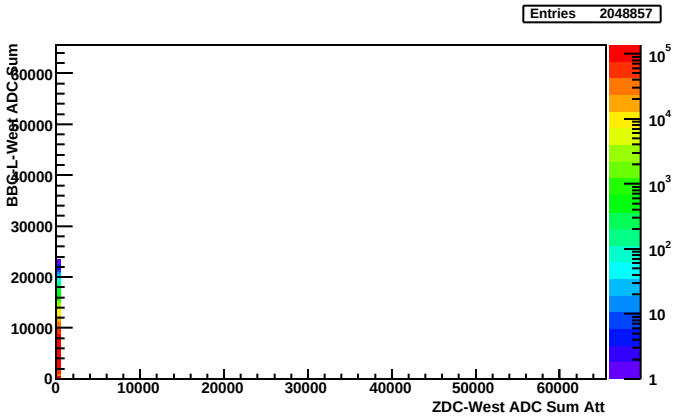

Entries 2048857
BBC-L-East ADC Sum
BBC-S-East ADC Sum

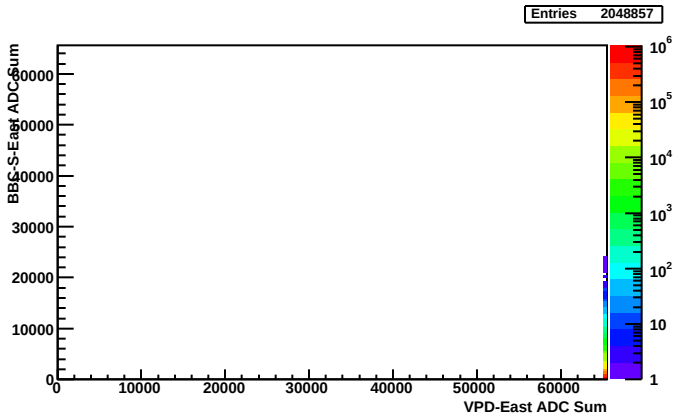

Entries 2048857
BBC-L-West ADC Sum
BBC-S-West ADC Sum

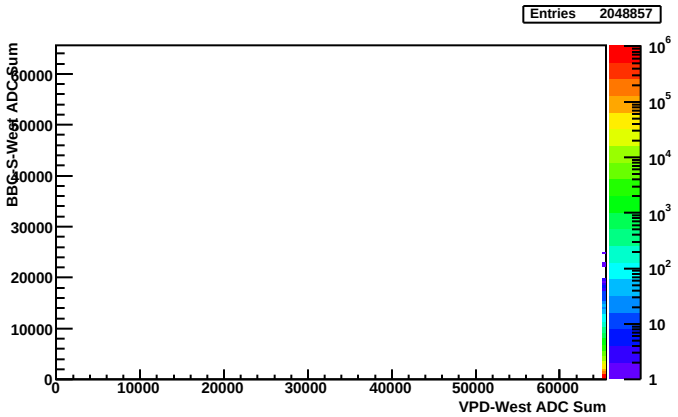

Entries 2048857
BBC-L-East ADC Sum
VPD-East ADC Sum

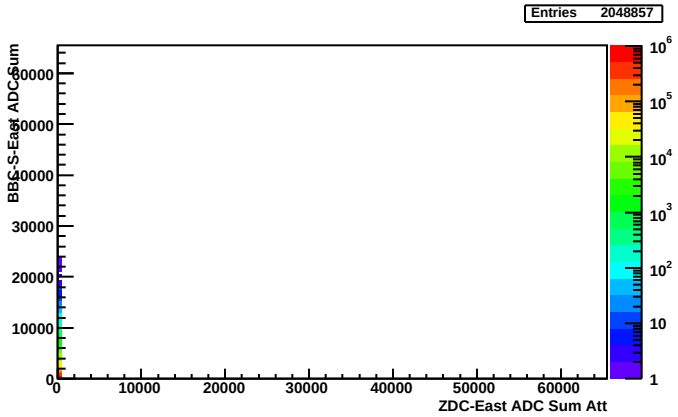

Entries 2048857
BBC-L-West ADC Sum
VPD-West ADC Sum

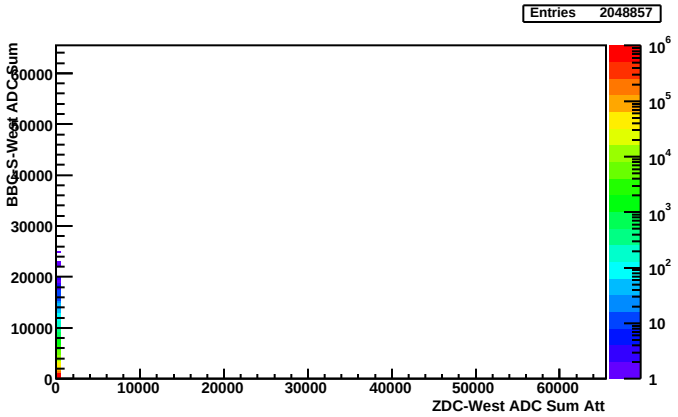

Entries 2048857
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

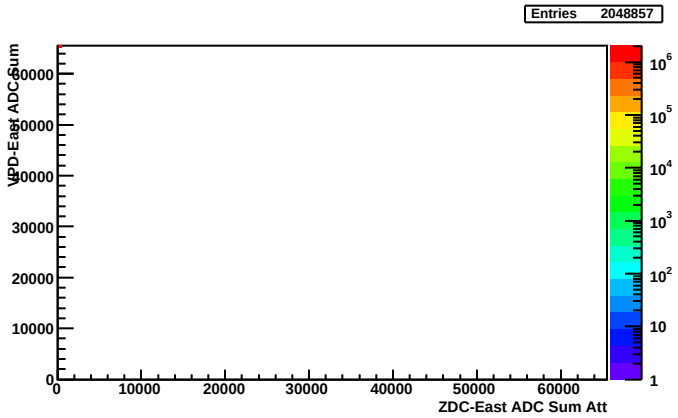

Entries 2048857
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

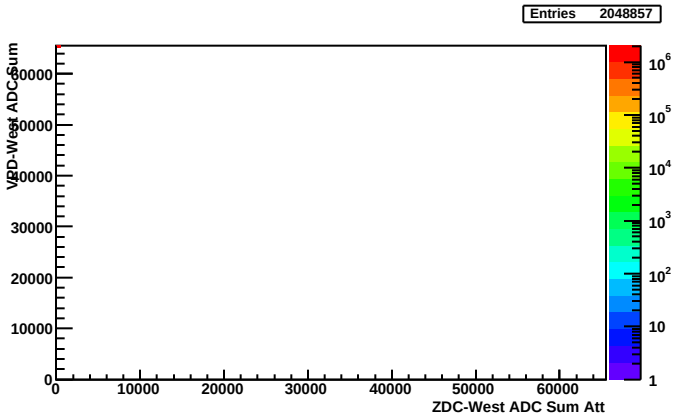

Entries 2048857
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 2048857
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 2048857
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 2048857
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 2048857
VPD-East ADC Sum
ZDC-East ADC Sum Att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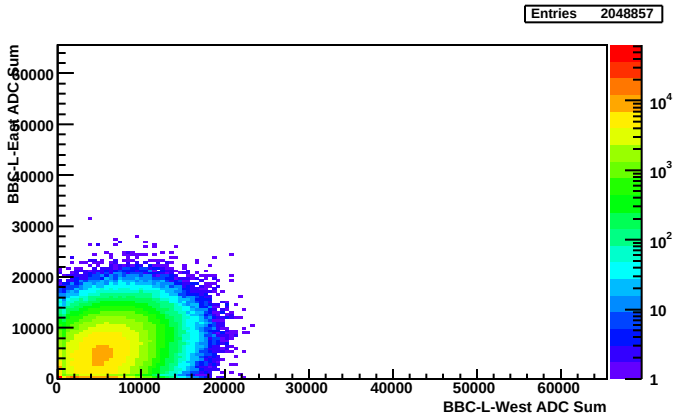
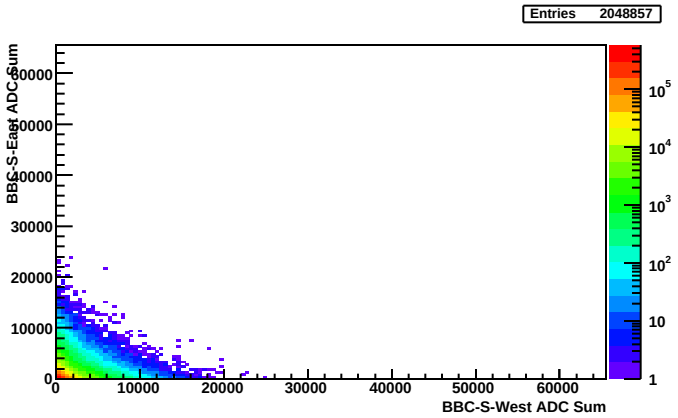
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 2048857
BBC-L-East ADC Sum
BBC-L-West ADC Sum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BBC-S-East ADC Sum
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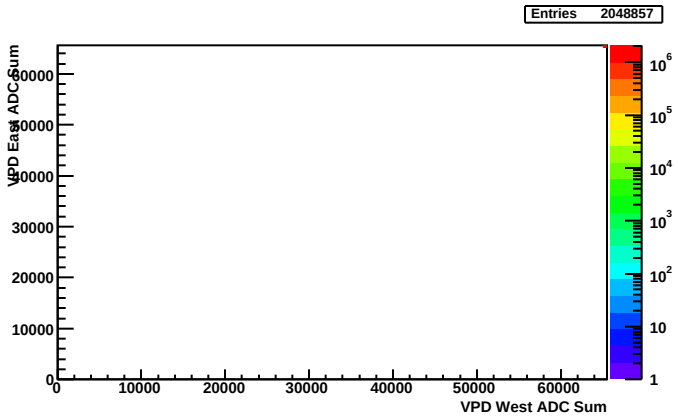
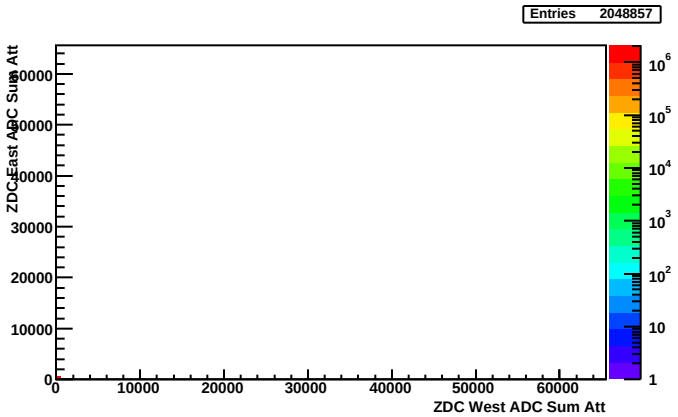
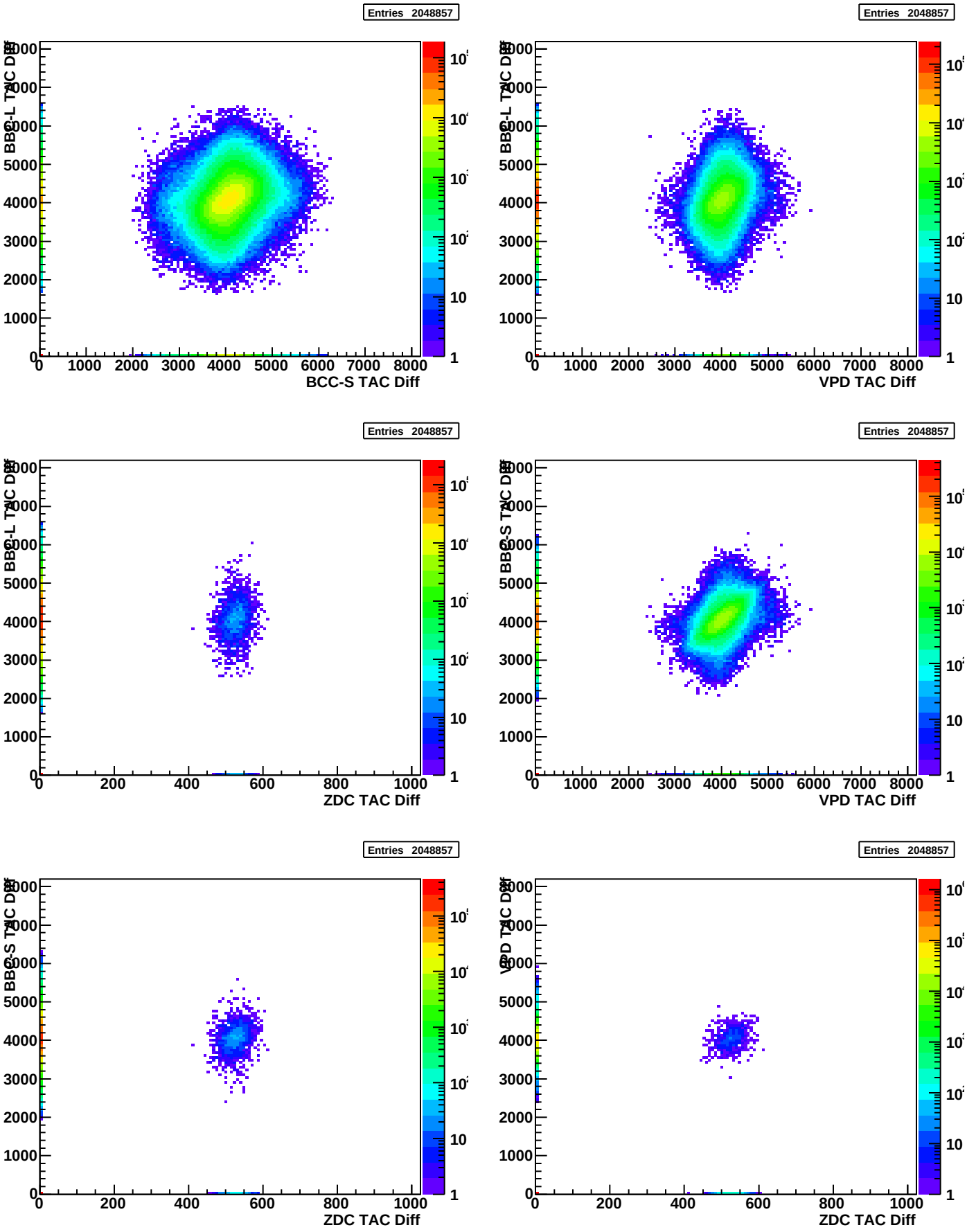

Entries 2048857
VPD East ADC Sum
VPD West ADC Sum


Entries 2048857
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 2048857
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2048857
BBC-L TAC Diff
VPD TAC Diff
Entries 2048857
BBC-L TAC Diff
ZDC TAC Diff
Entries 2048857
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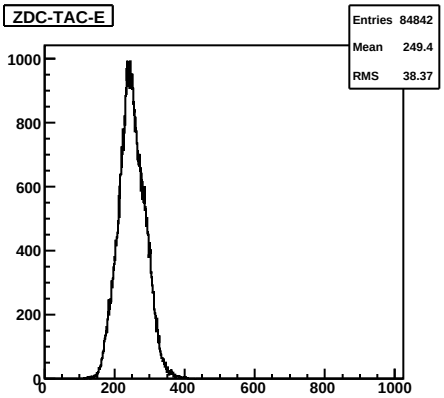
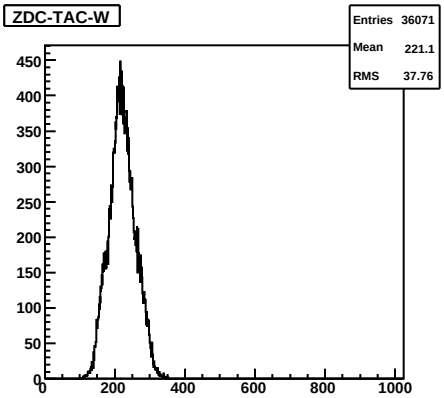
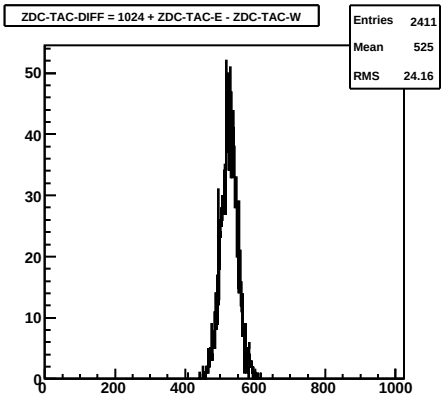
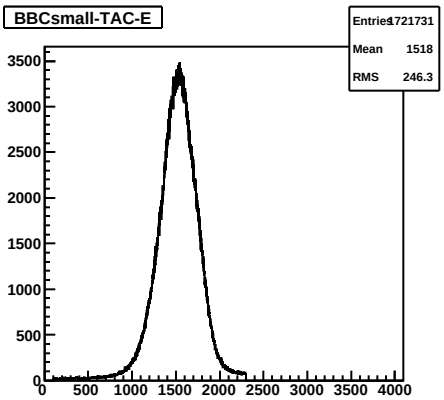
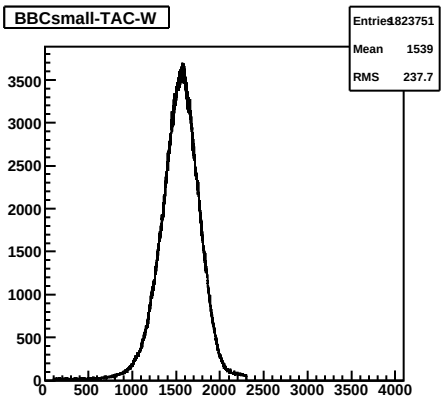
Entries 2048857
BBC-S TAC Diff
ZDC TAC Diff
Entries 2048857
VPD TAC Diff
ZDC TAC Diff

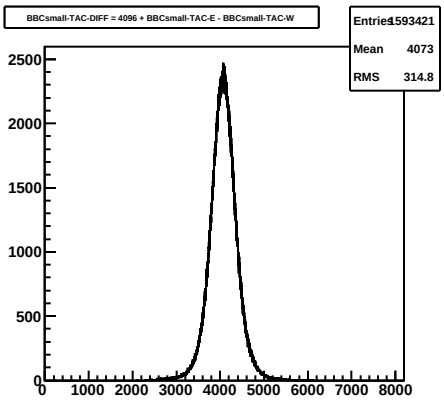

ZDC-TAC-E
Entries 84842
Mean 249.4
RMS 38.37

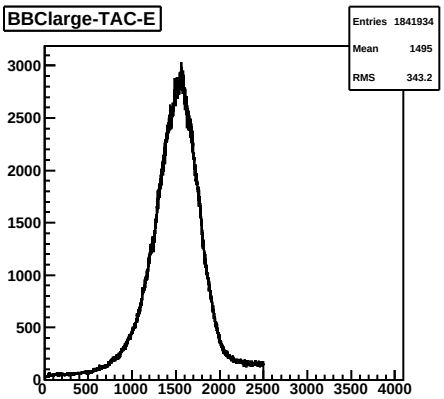

ZDC-TAC-W
Entries 36071
Mean 221.1
RMS 37.76

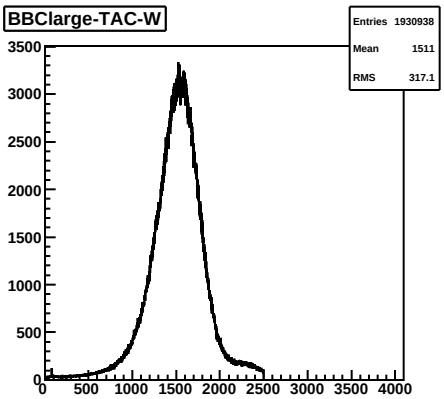

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2411
Mean 525
RMS 24.16

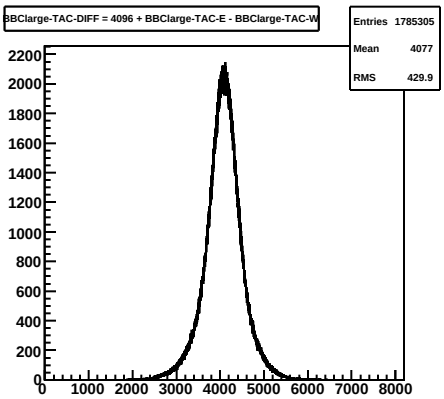

BBCsmall-TAC-E
Entries 1721731
Mean 1518
RMS 246.3

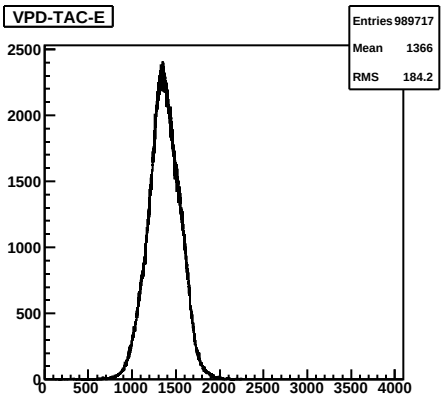

BBCsmall-TAC-W
Entries 1823751
Mean 1539
RMS 237.7

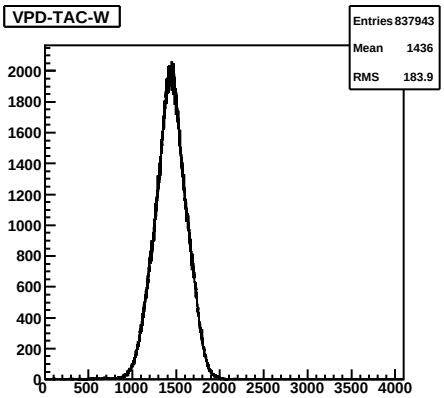

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1593421
Mean 4073
RMS 314.8

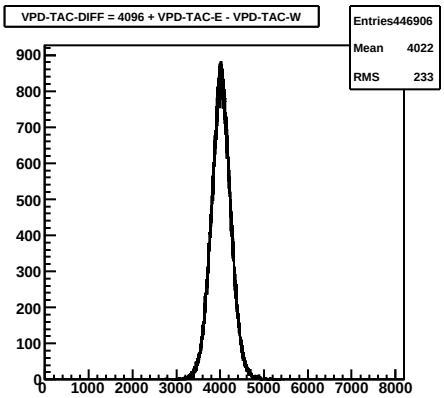

BBClarge-TAC-E
Entries 1841934
Mean 1495
RMS 343.2

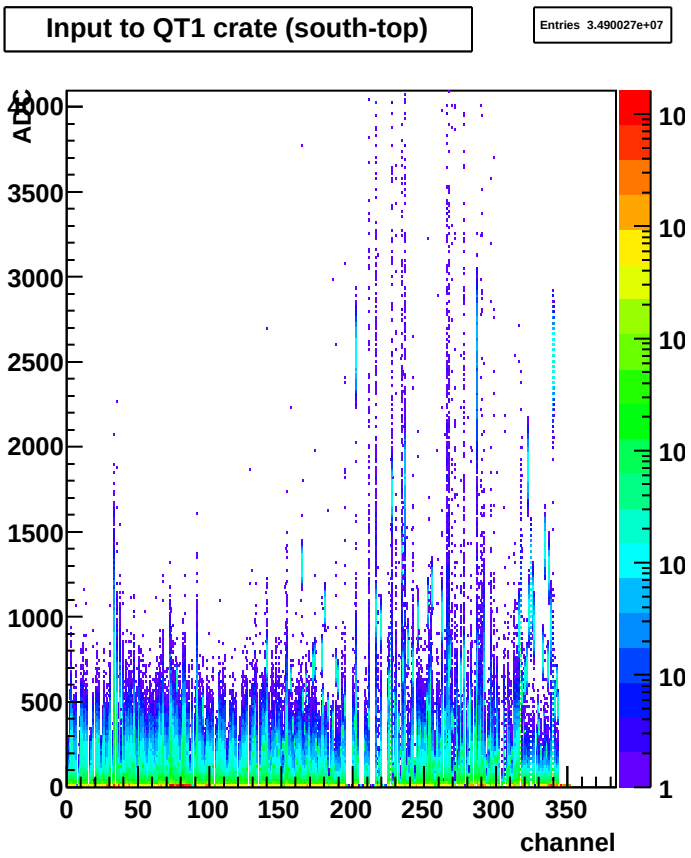

BBClarge-TAC-W
Entries 1930938
Mean 1511
RMS 317.1

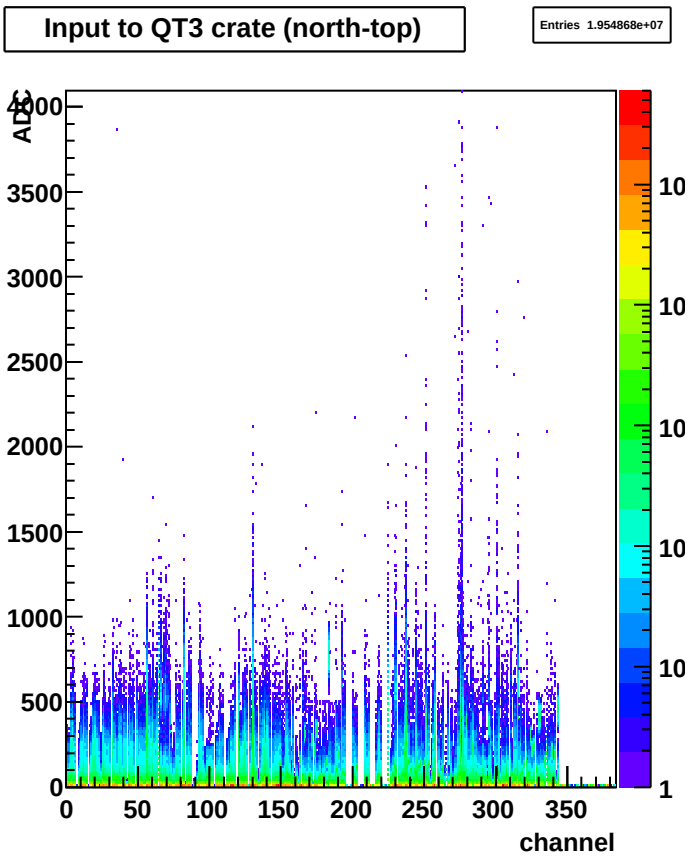

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1785305
Mean 4077
RMS 429.9

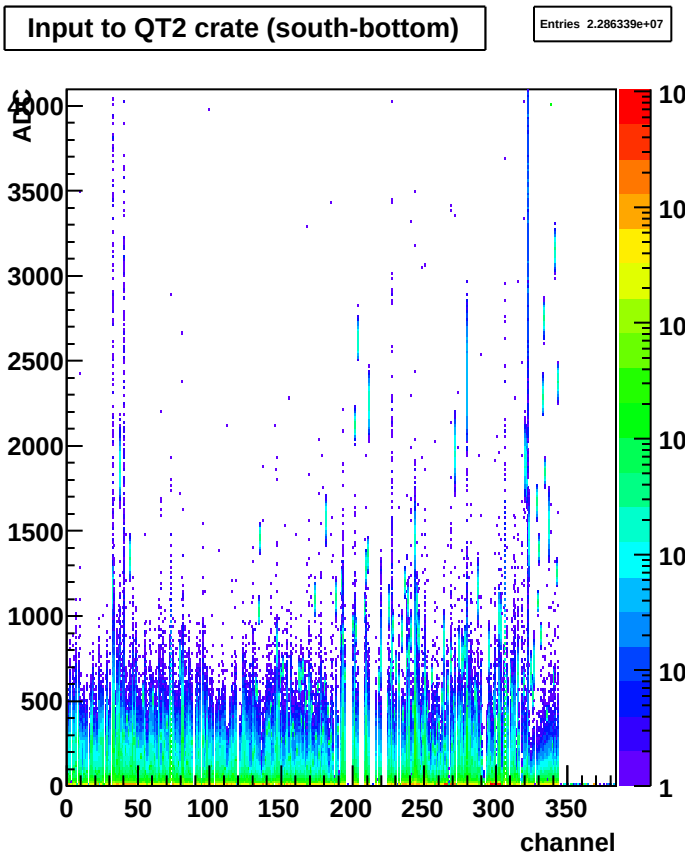

VPD-TAC-E
Entries 989717
Mean 1366
RMS 184.2

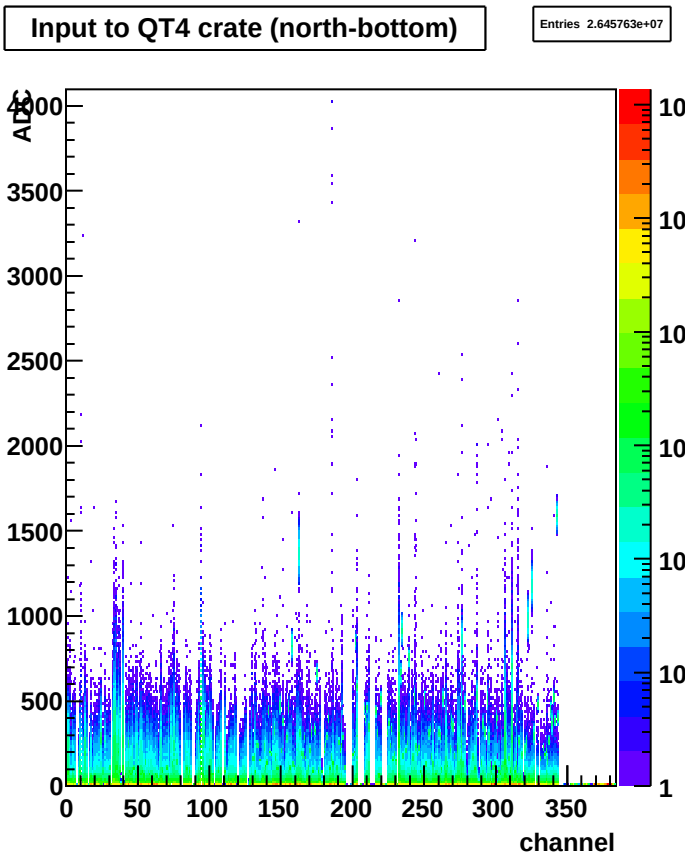

VPD-TAC-W
Entries 837943
Mean 1436
RMS 183.9

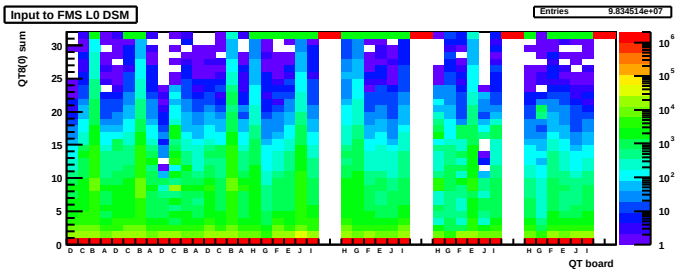

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 446906
Mean 4022
RMS 233

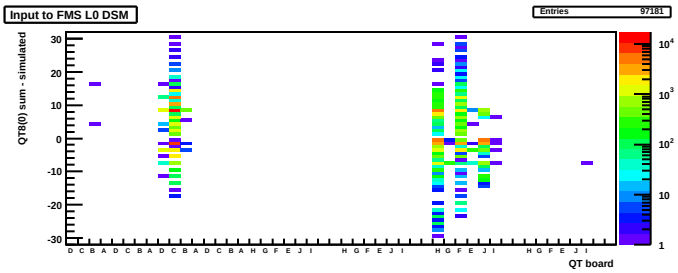

Input to QT1 crate (south-top)
Entries 3.490027e+07
ADC
channel

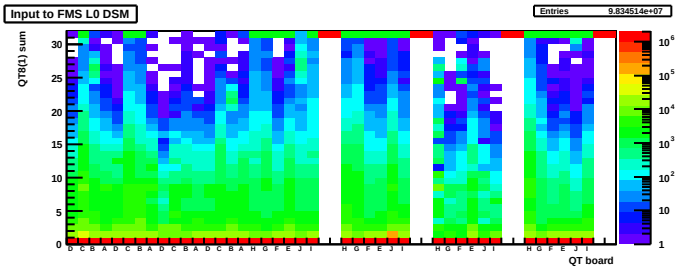

Input to QT3 crate (north-top)
Entries 1.954868e+07
ADC
channel

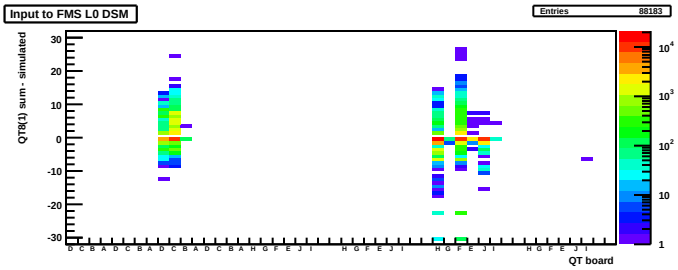

Input to QT2 crate (south-bottom)
Entries 2.286339e+07
ADC
channel

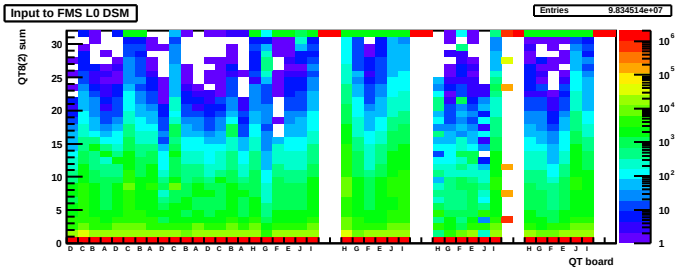

Input to QT4 crate (north-bottom)
Entries 2.645763e+07
ADC
channel

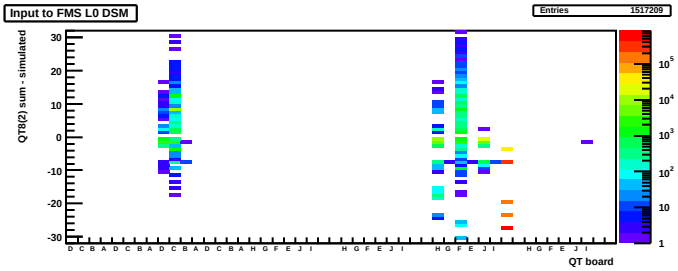

Input to FMS L0 DSM
Entries 9.834514e+07
QT8(0) sum
QT board

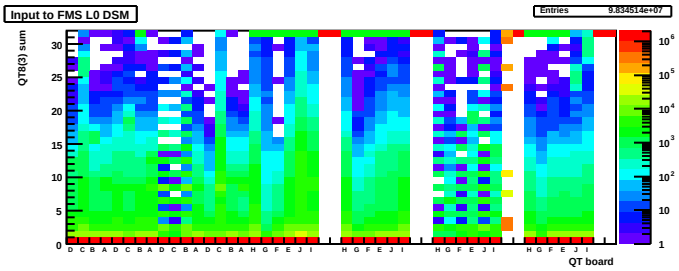

Input to FMS L0 DSM
Entries 97181
QT8(0) sum - simulated
QT board

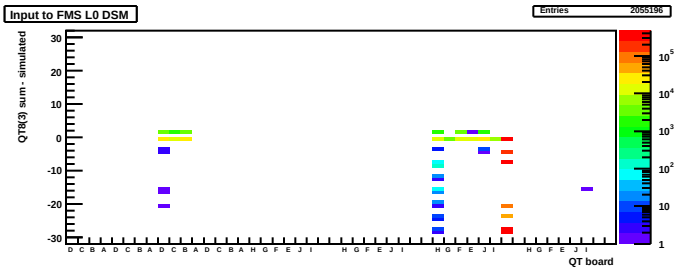

Input to FMS L0 DSM
Entries 9.834514e+07
QT8(1) sum
QT board

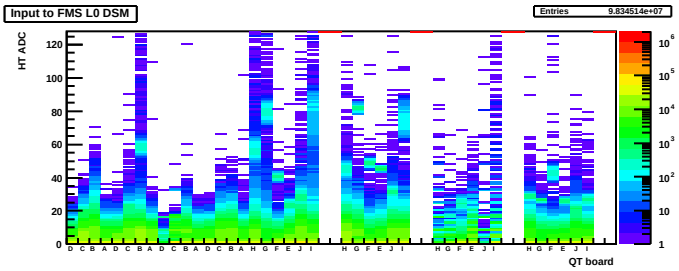

Input to FMS L0 DSM
Entries 88183
QT8(1) sum - simulated
QT board

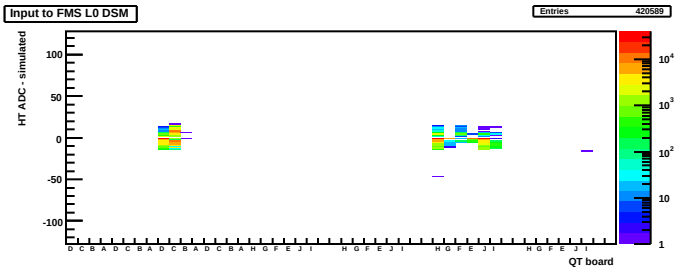

Input to FMS L0 DSM
Entries 9.834514e+07
QT8(2) sum
QT board

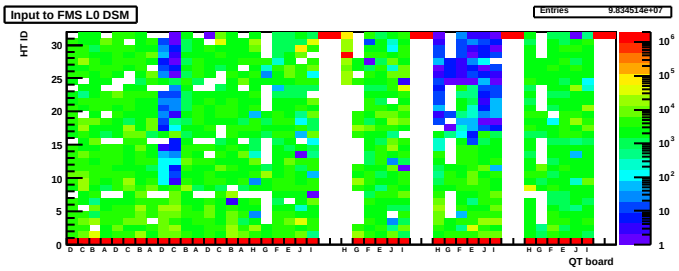

Input to FMS L0 DSM
Entries 1517209
QT8(2) sum - simulated
QT board

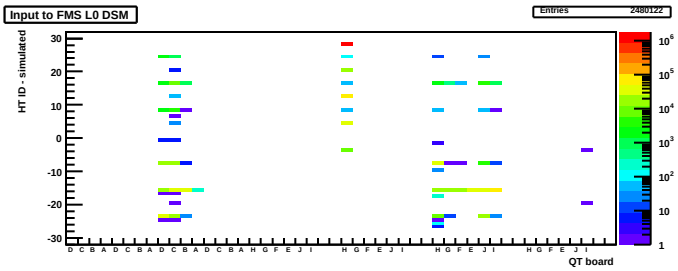

Input to FMS L0 DSM
Entries 9.834514e+07
QT8(3) sum
QT board

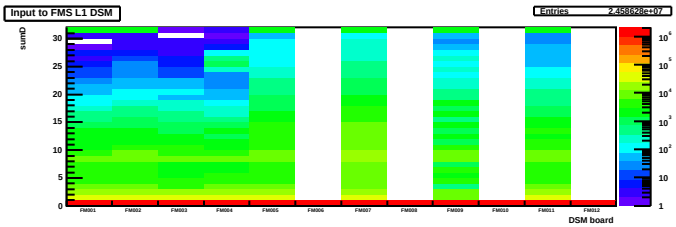

Input to FMS L0 DSM
Entries 2055196
QT8(3) sum - simulated
QT board

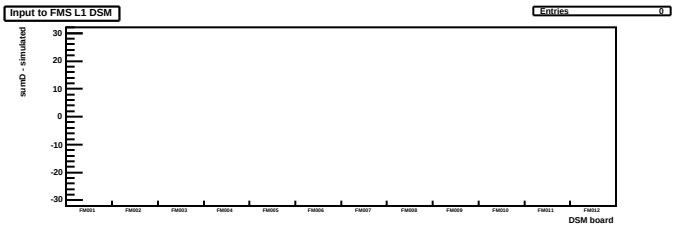

Input to FMS L0 DSM
Entries 9.834514e+07
HT ADC
QT board

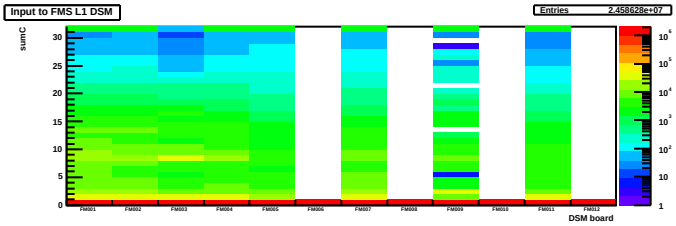

Input to FMS L0 DSM
Entries 420589
HT ADC - simulated
QT board

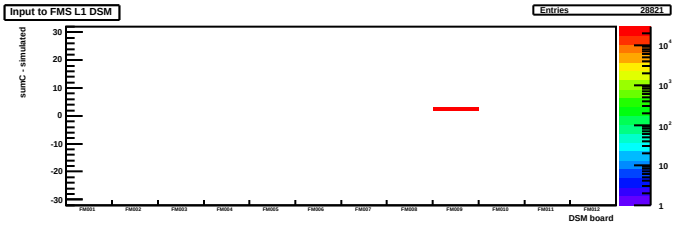

Input to FMS L0 DSM
Entries 9.834514e+07
HT ID
QT board

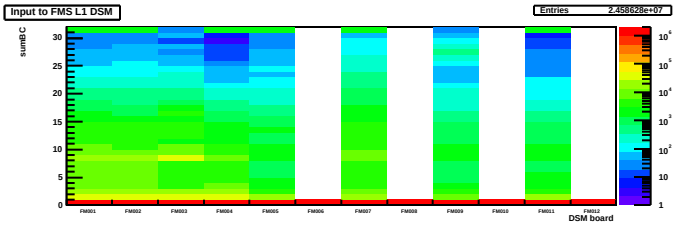

Input to FMS L0 DSM
Entries 2480122
HT ID - simulated
QT board

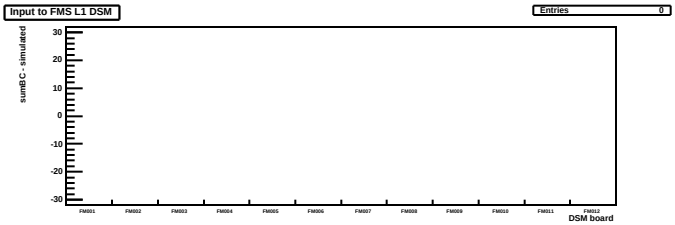

Input to FMS L1 DSM
Entries 2.458628e+07
sumD
DSM board

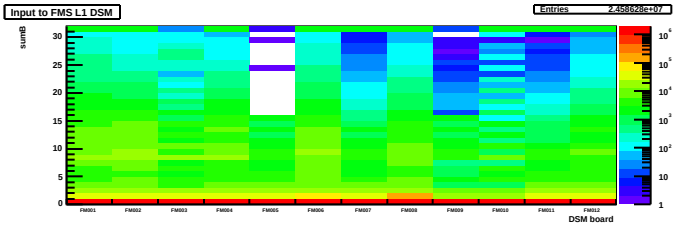

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

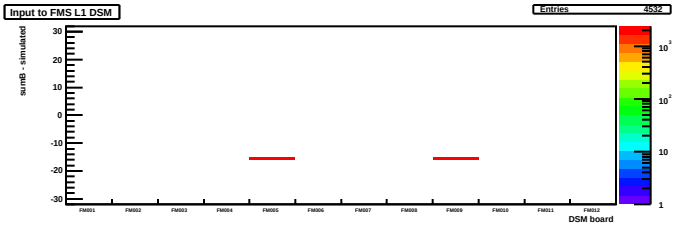

Input to FMS L1 DSM
Entries 2.458628e+07
sumC
DSM board

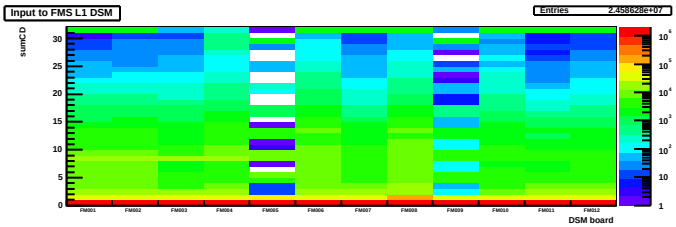

Input to FMS L1 DSM
Entries 28821
sumC - simulated
DSM board

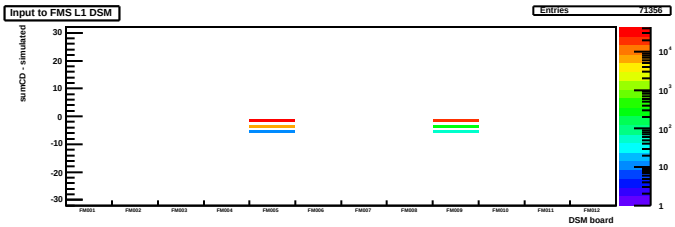

Input to FMS L1 DSM
Entries 2.458628e+07
sumBC
DSM board

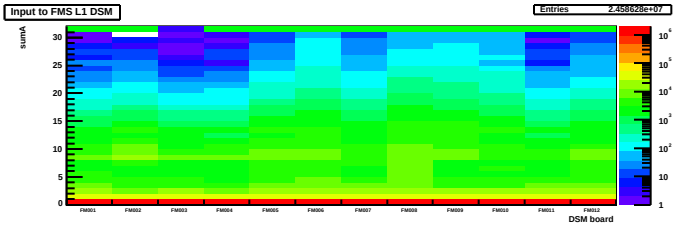

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

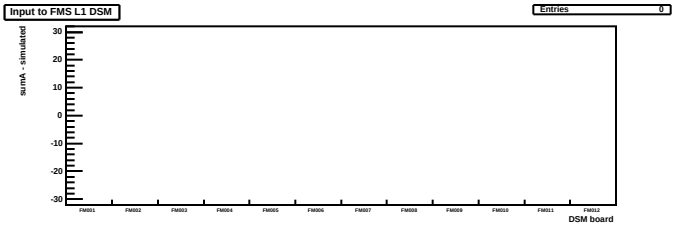

Input to FMS L1 DSM
Entries 2.458628e+07
sumB
DSM board

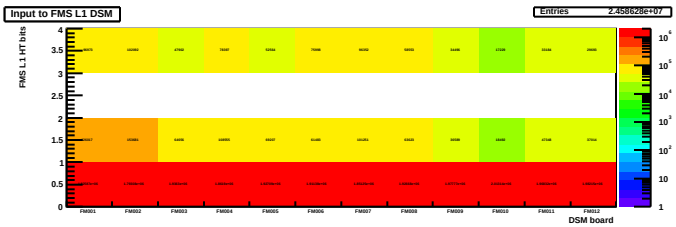

Input to FMS L1 DSM
Entries 4532
sumB - simulated
DSM board

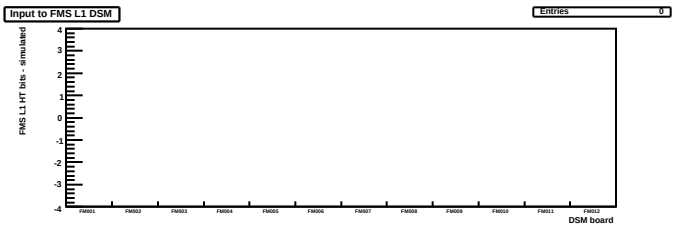

Input to FMS L1 DSM
Entries 2.458628e+07
sumCD
DSM board

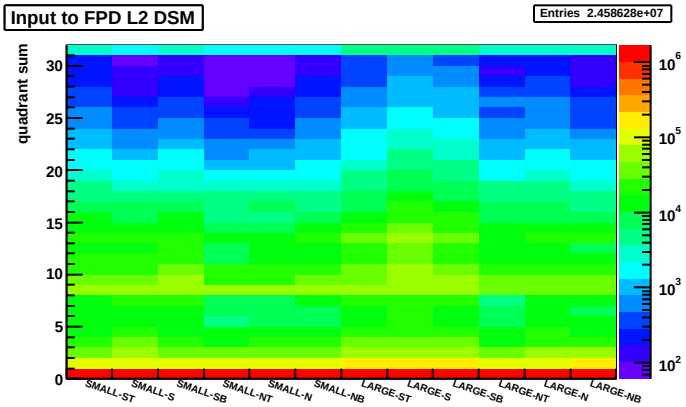

Input to FMS L1 DSM
Entries 71356
sumCD - simulated
DSM board

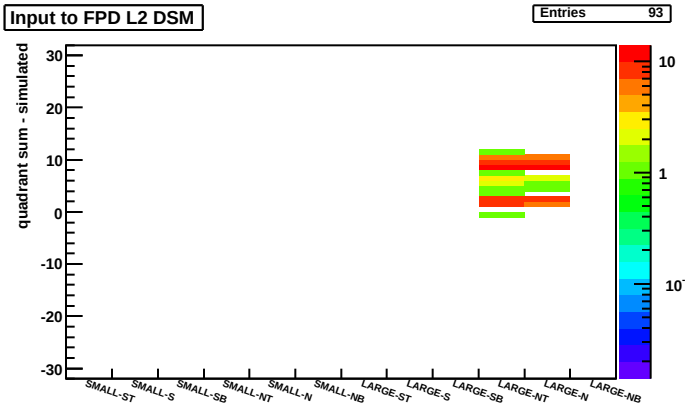

Input to FMS L1 DSM
Entries 2.458628e+07
sumA
DSM board

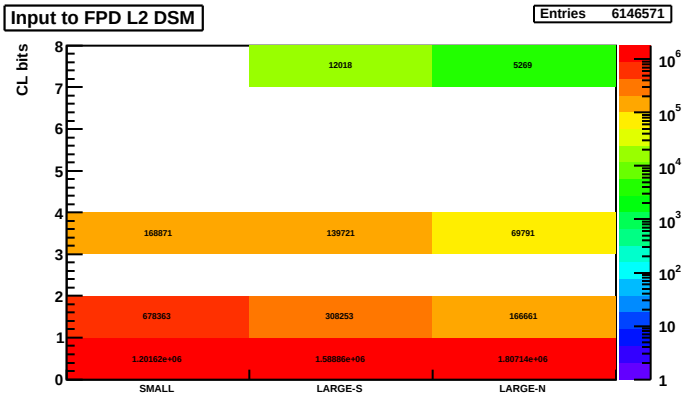

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

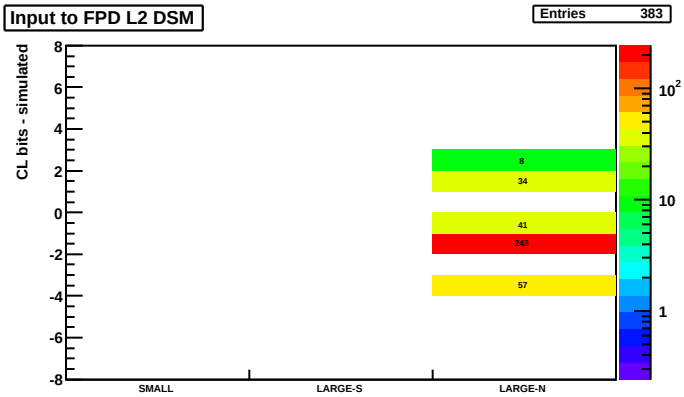

Input to FMS L1 DSM
Entries 2.458628e+07
FMS L1 HT bits
DSM board

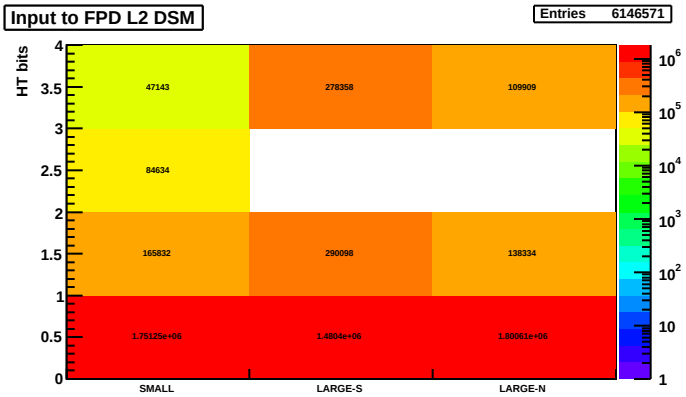

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

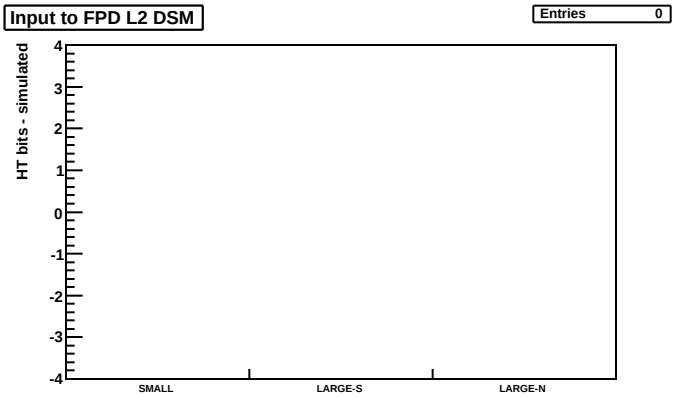

Input to FPD L2 DSM
Entries 2.458628e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

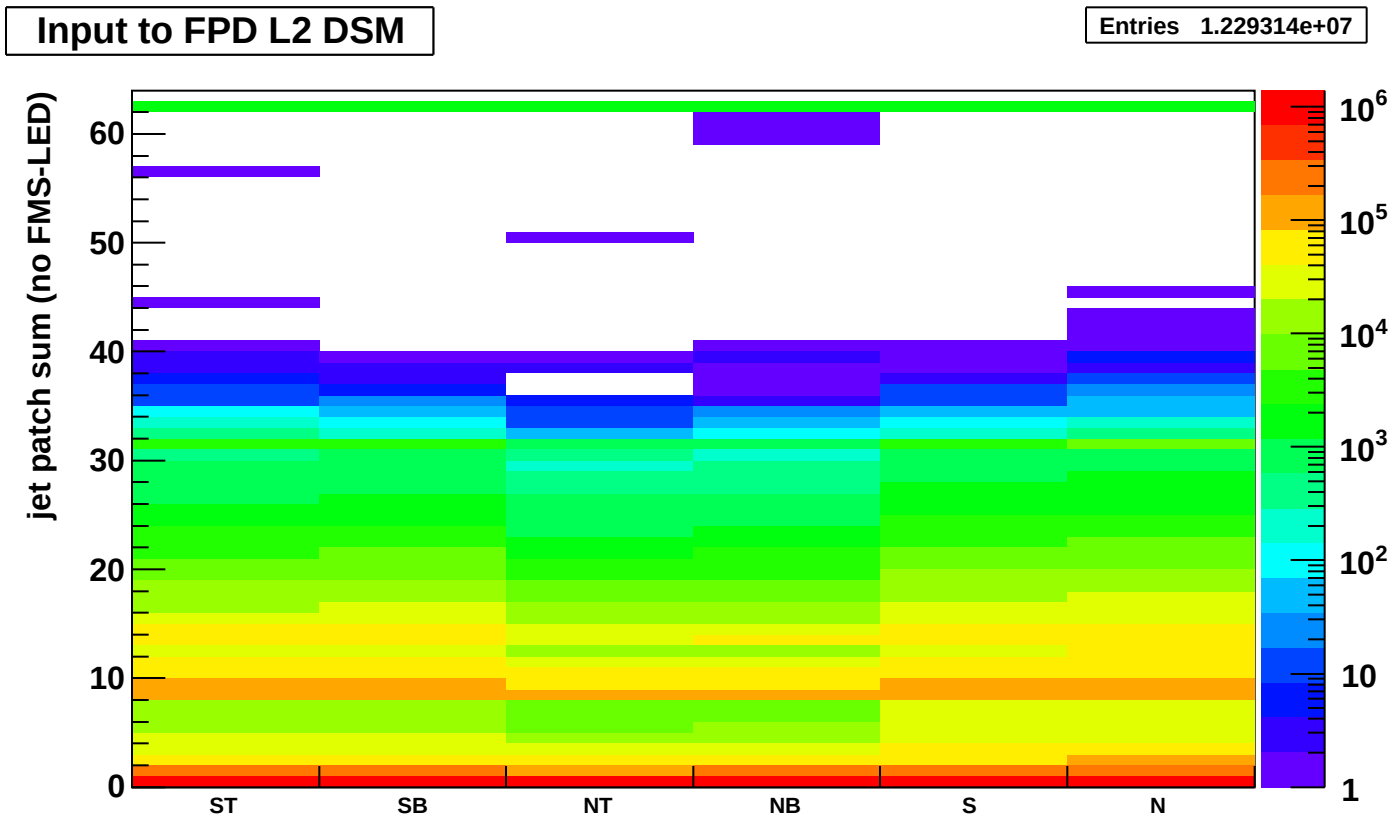

Input to FPD L2 DSM
Entries 93
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

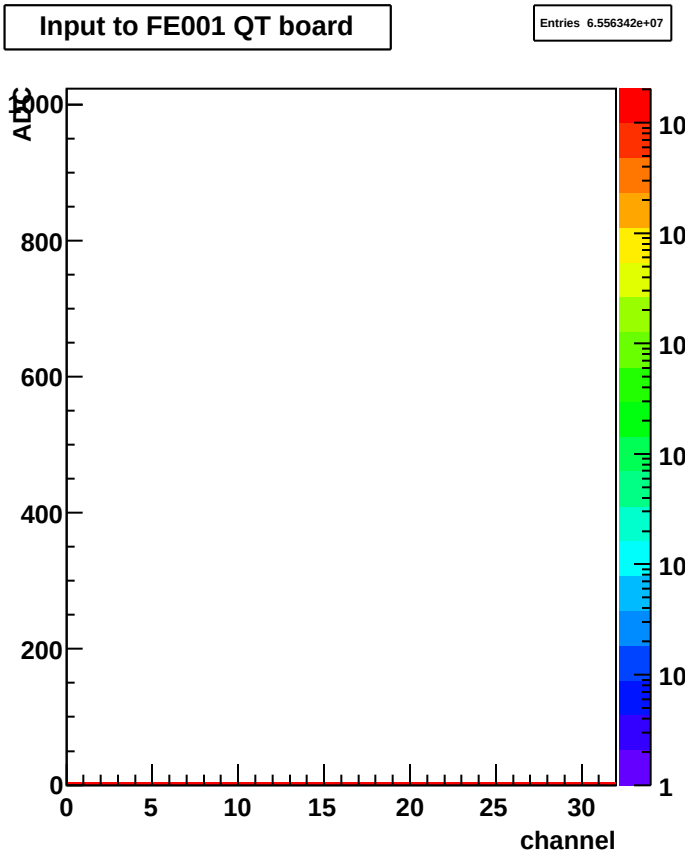

Input to FPD L2 DSM
Entries 6146571
CL bits
12018
5269
168871
139721
69791
678363
308253
166661
1.20162e+06
1.58886e+06
1.80714e+06
SMALL
LARGE-S
LARGE-N

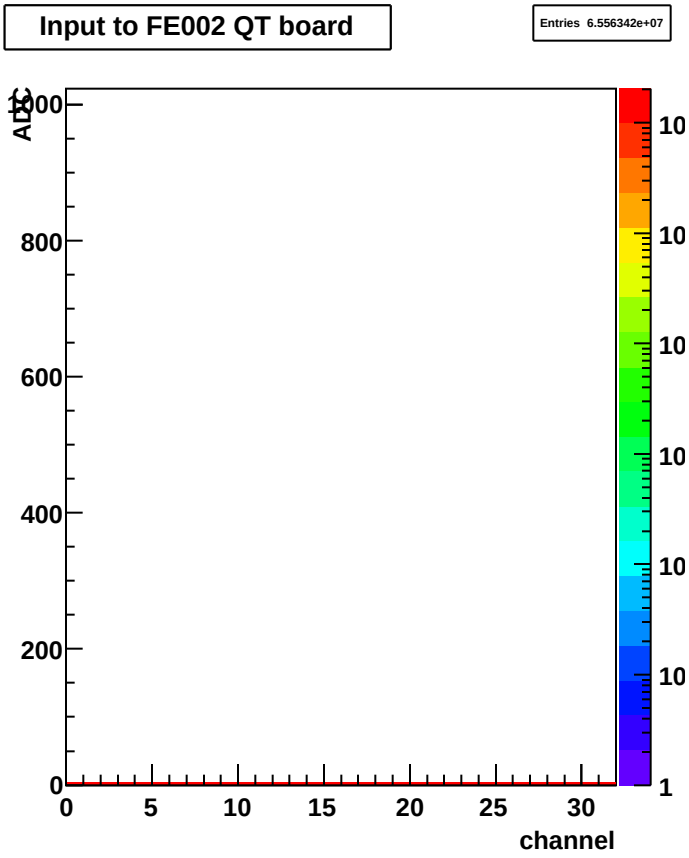

Input to FPD L2 DSM
Entries 383
CL bits - simulated
8
34
41
243
57
SMALL
LARGE-S
LARGE-N

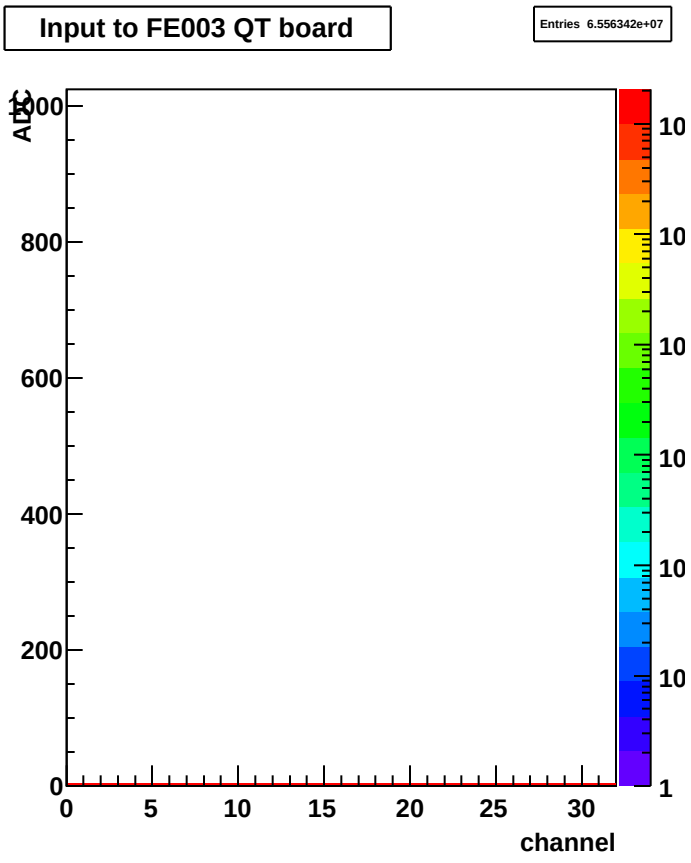

Input to FPD L2 DSM
Entries 6146571
HT bits
47143
278358
109909
84634
165832
290098
138334
1.75125e+06
1.4804e+06
1.80061e+06
SMALL
LARGE-S
LARGE-N

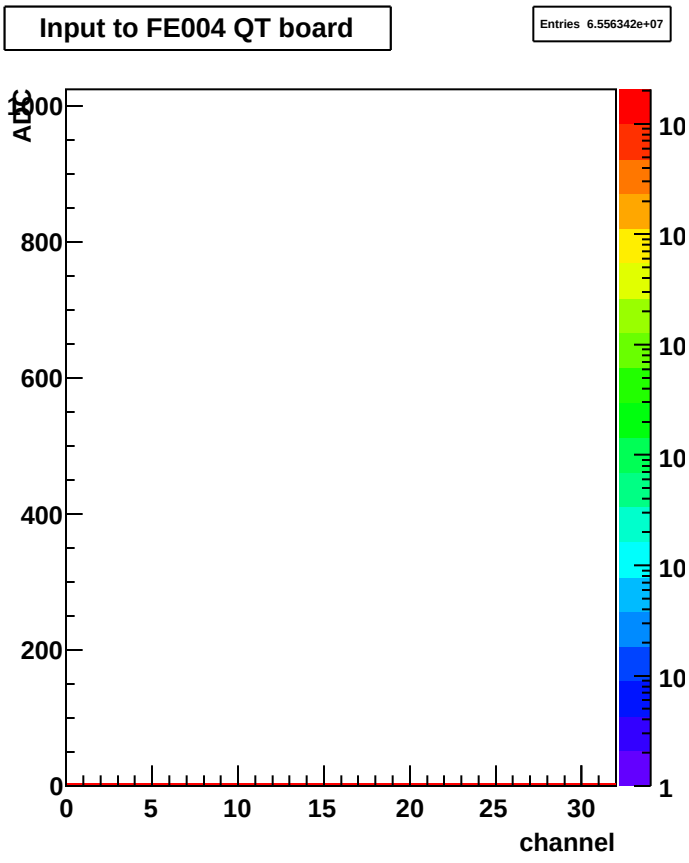

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

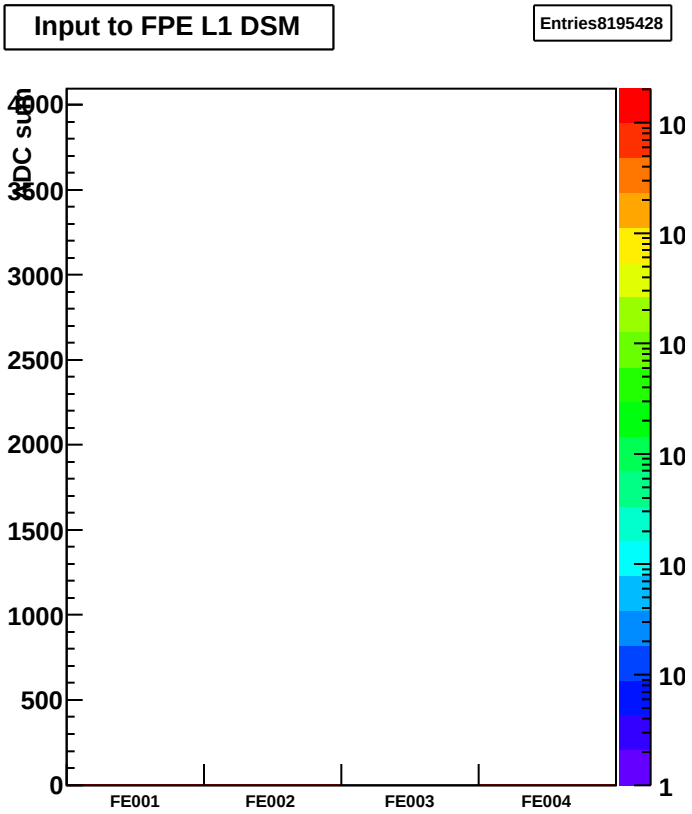

Input to FPD L2 DSM
Entries 1.229314e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

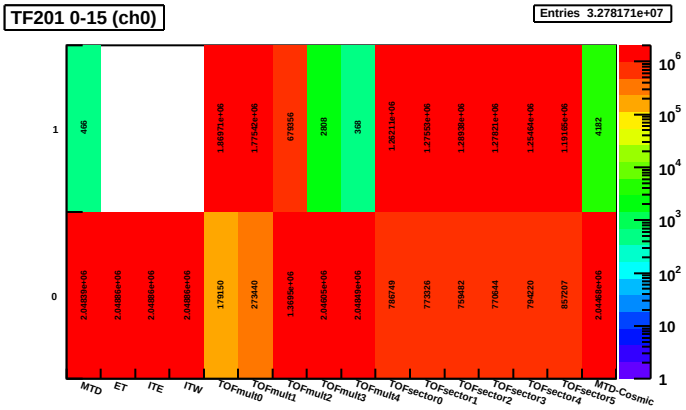

Input to FE001 QT board
Entries 6.556342e+07
ADC
channel

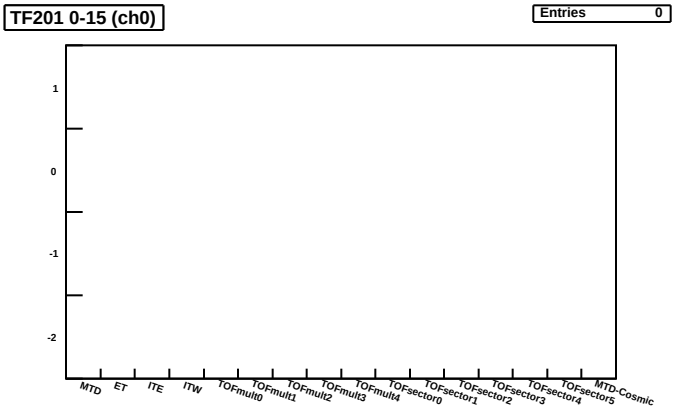

Input to FE002 QT board
Entries 6.556342e+07
ADC
channel

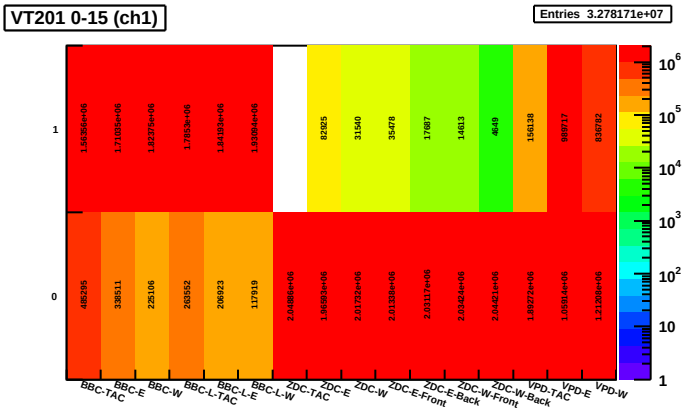

Input to FE003 QT board
Entries 6.556342e+07
ADC
channel


Input to FE004 QT board
Entries 6.556342e+07
ADC
channel


Input to FPE L1 DSM
Entries8195428
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 3.278171e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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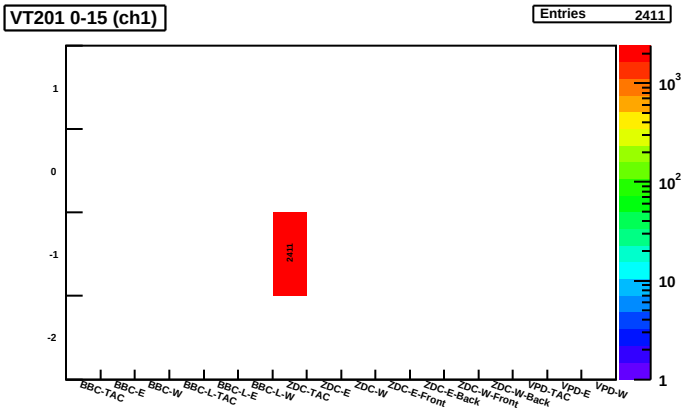
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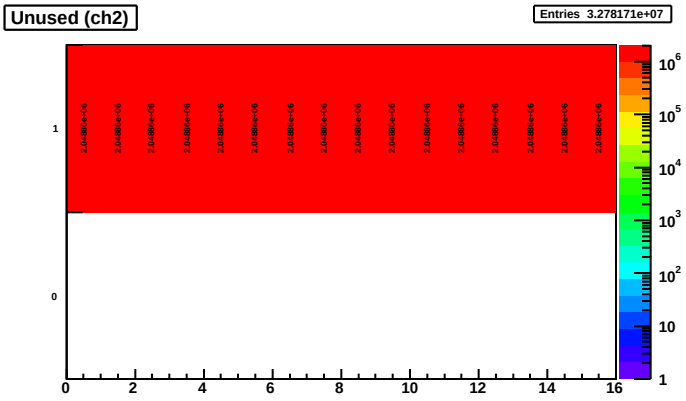
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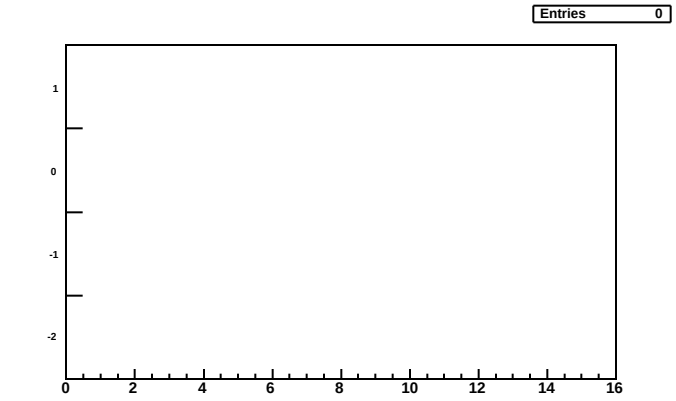
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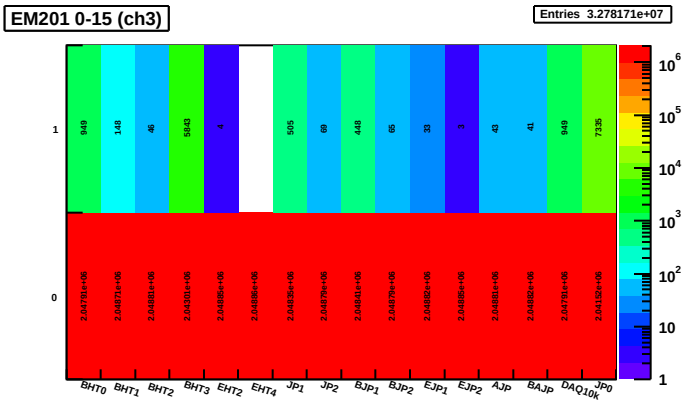
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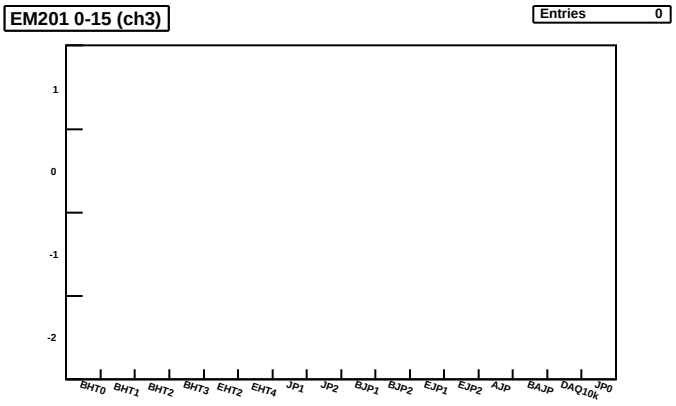
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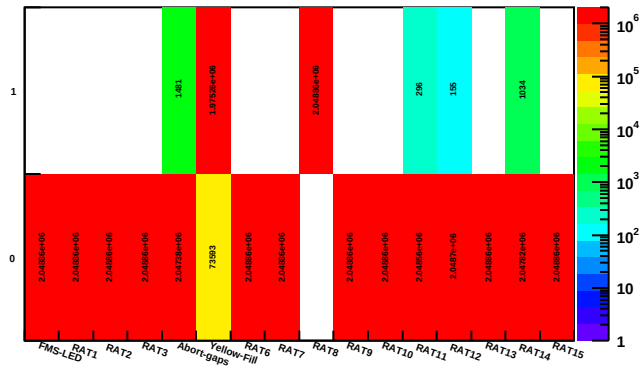
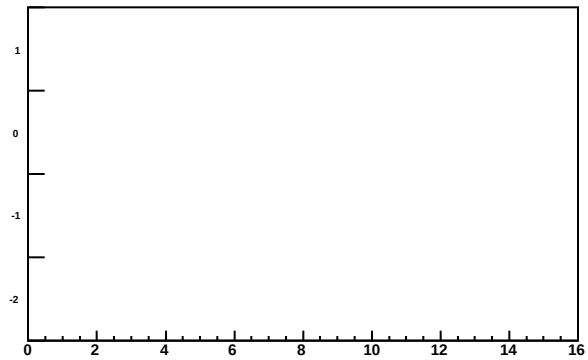
RAT board (ch4)





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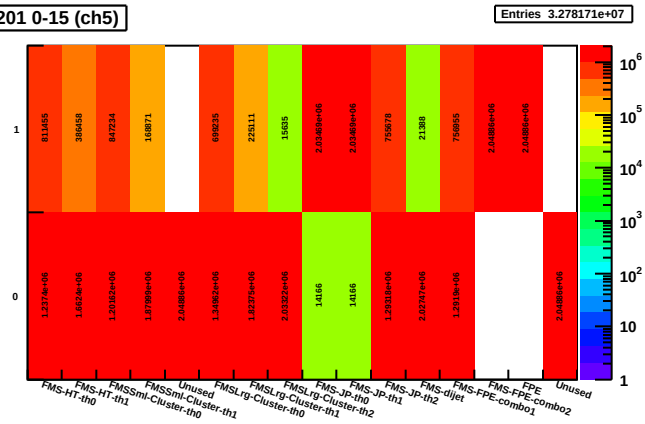

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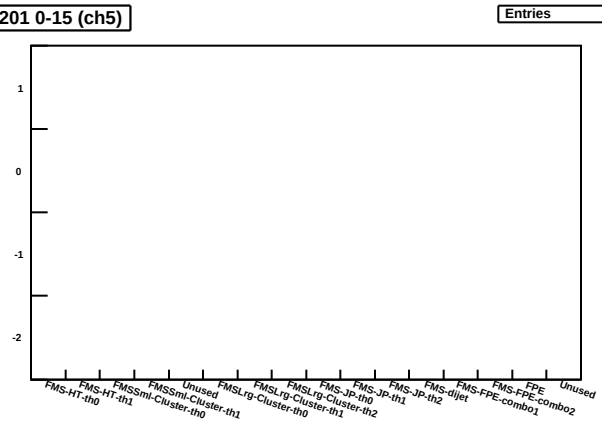

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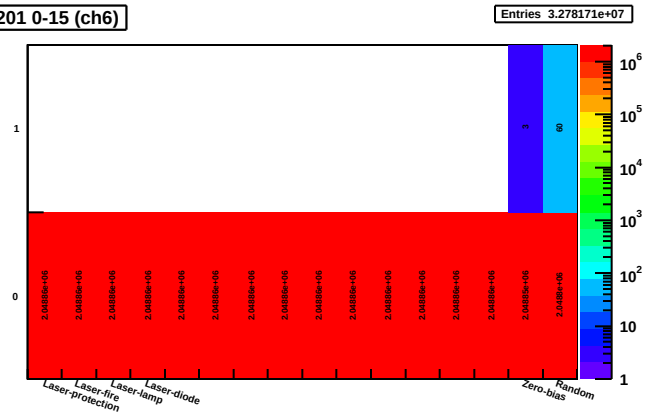

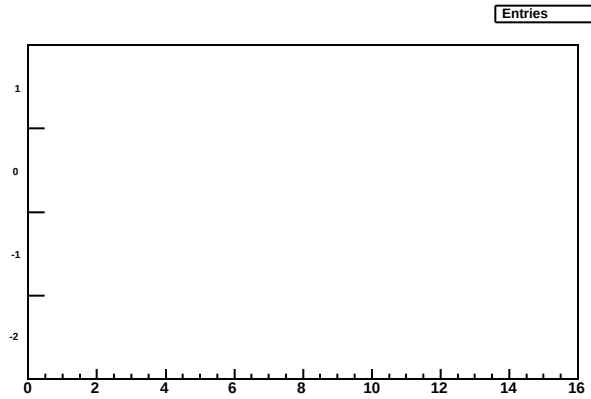
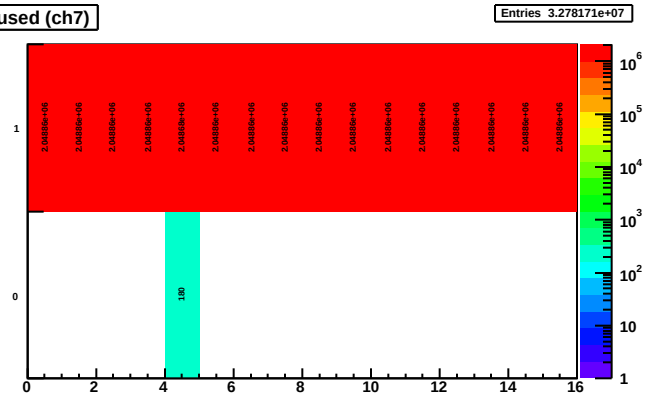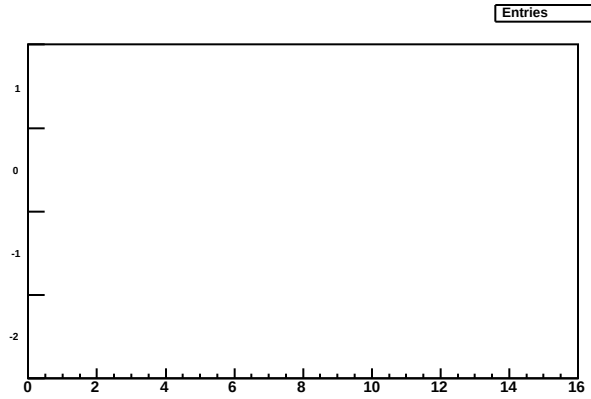
Unused (ch7)