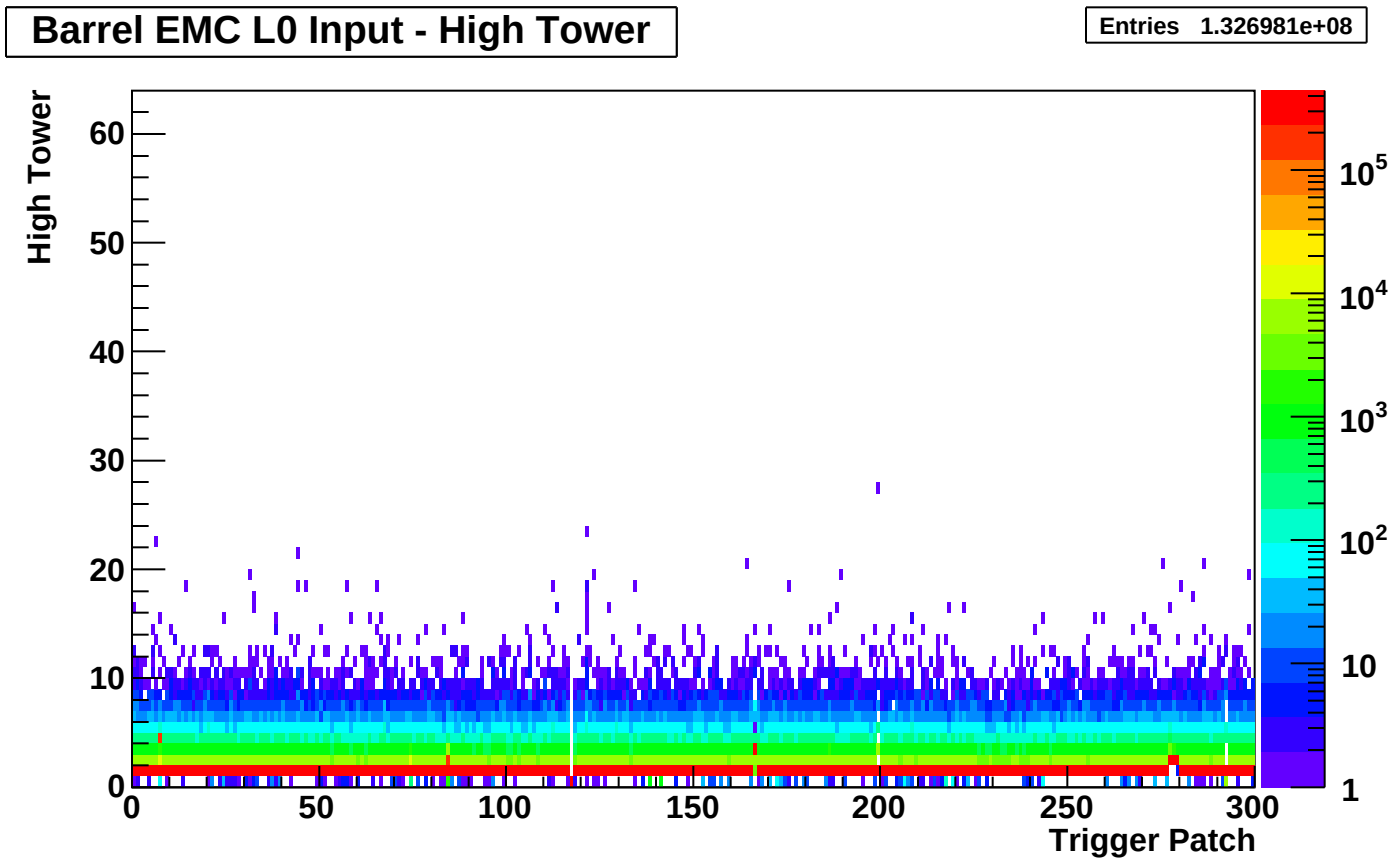
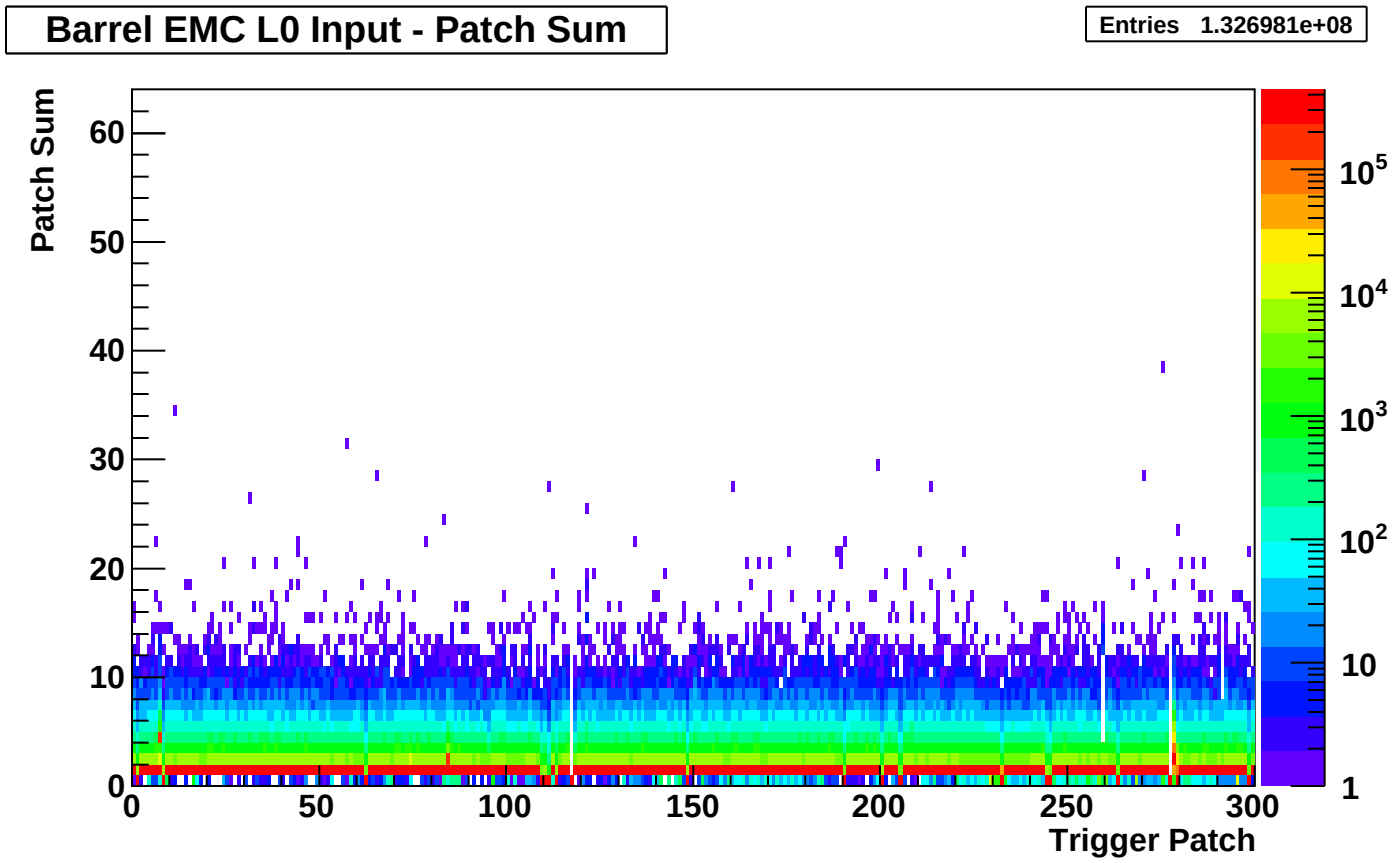
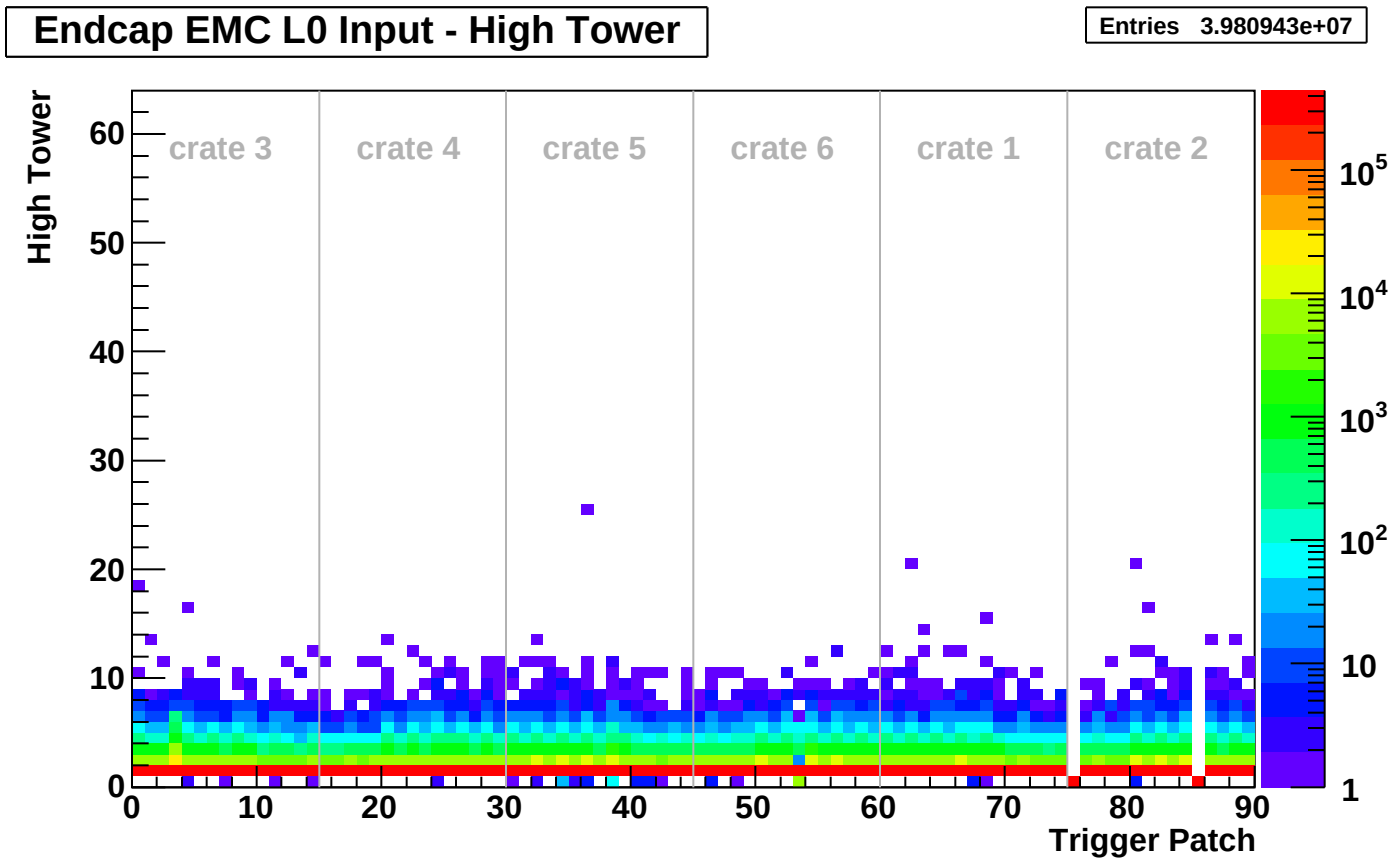
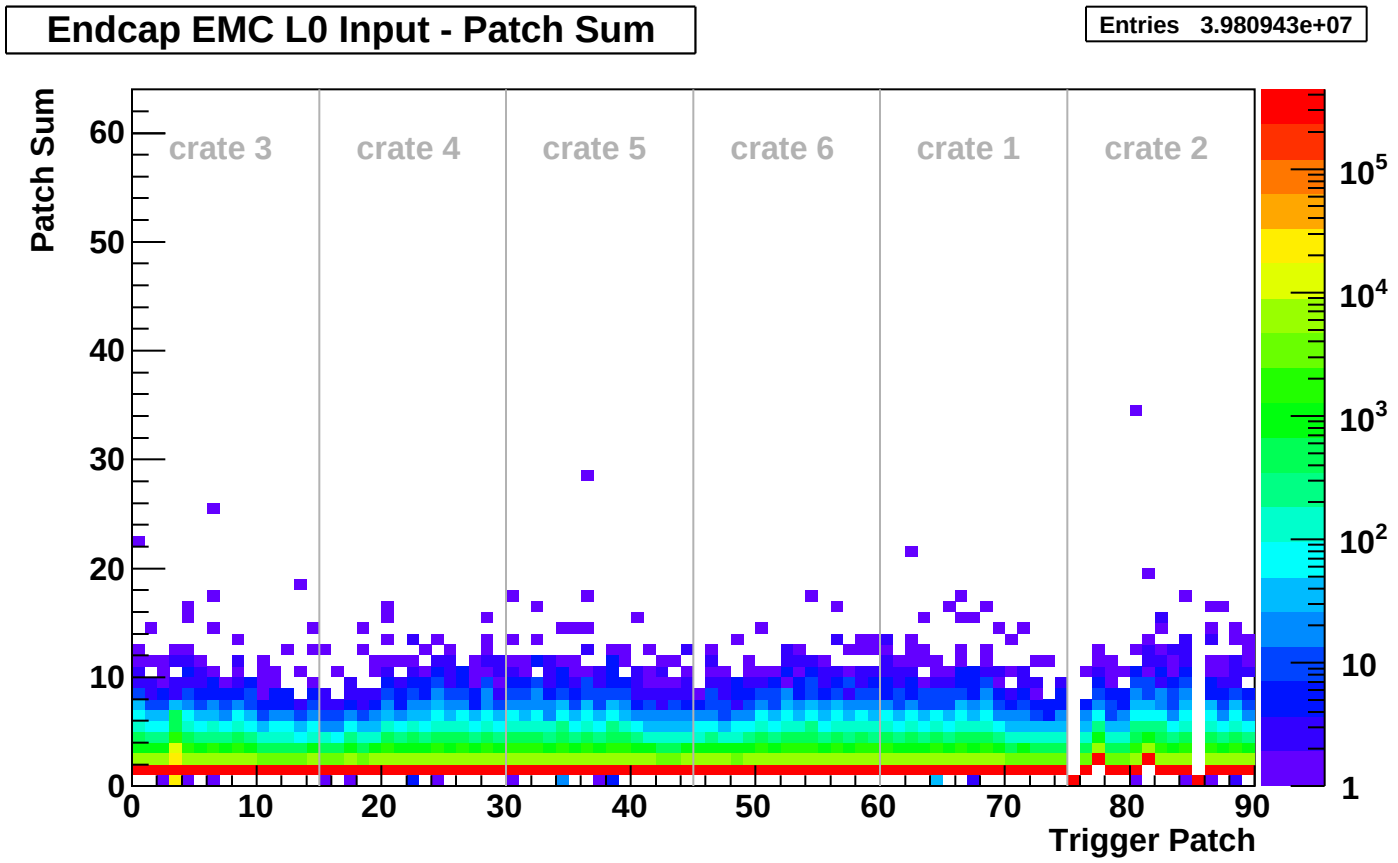
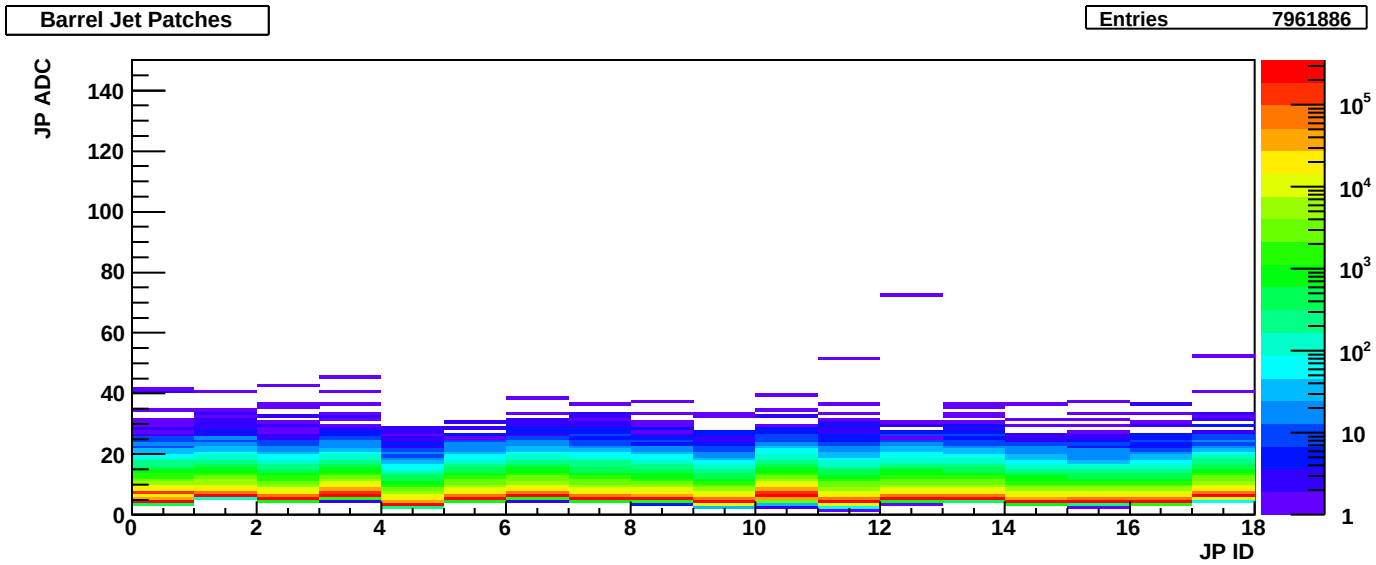
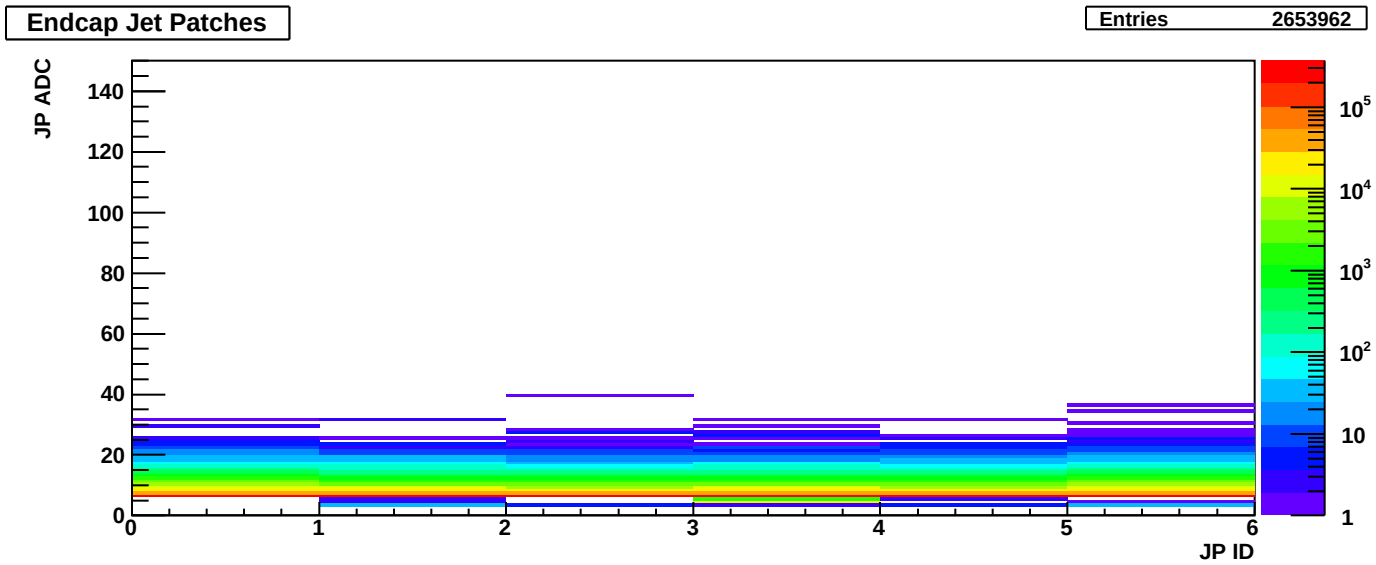

Barrel EMC L0 Input - High Tower
Entries 1.326981e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

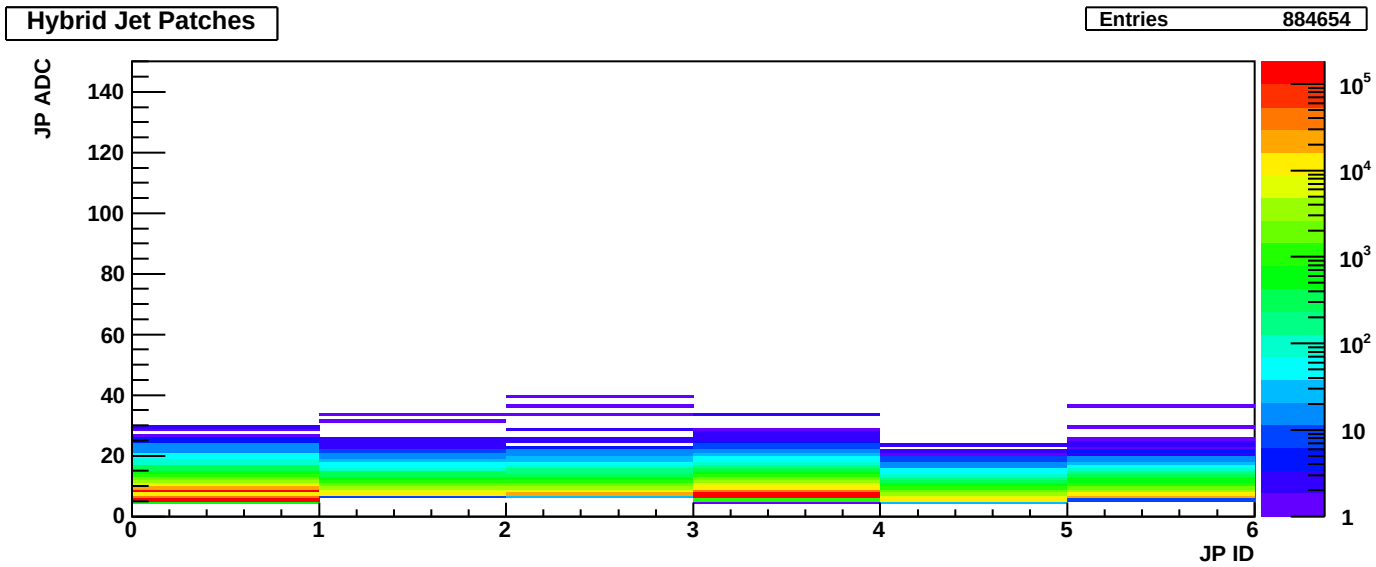

Barrel EMC L0 Input - Patch Sum
Entries 1.326981e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

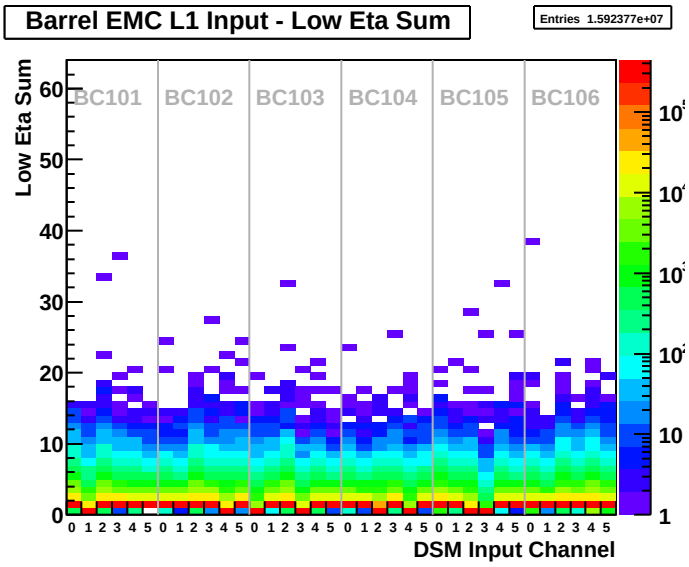

Endcap EMC L0 Input - High Tower
Entries 3.980943e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

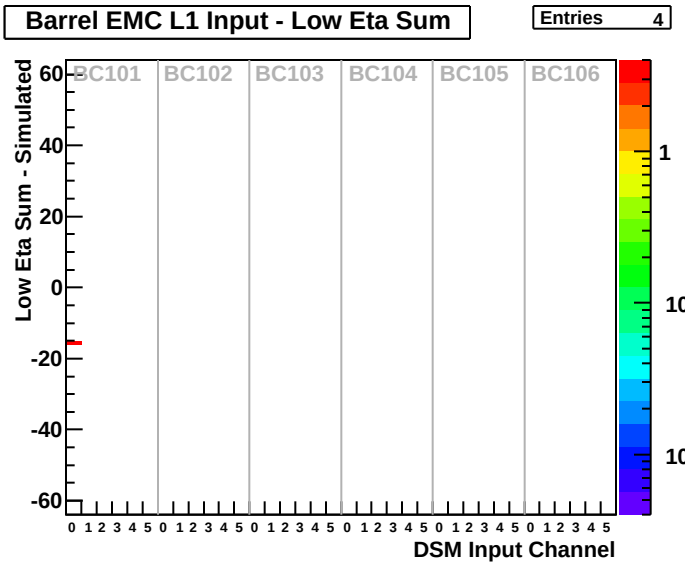

Endcap EMC L0 Input - Patch Sum
Entries 3.980943e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

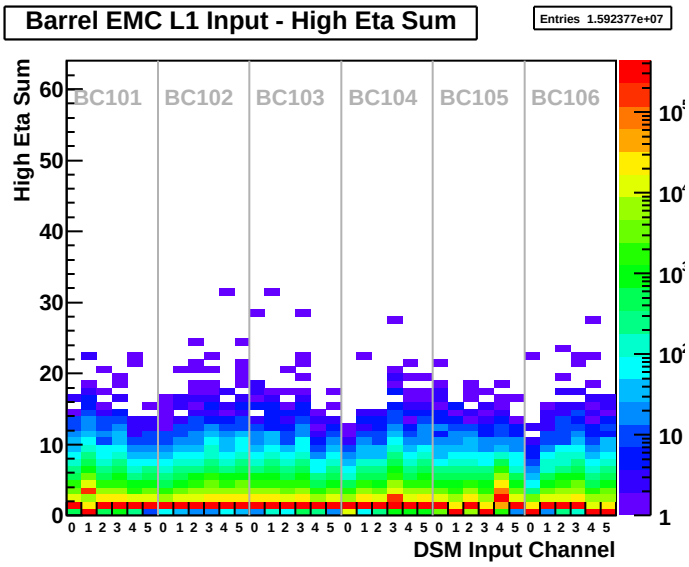

Barrel Jet Patches
Entries 7961886
JP ADC
JP ID

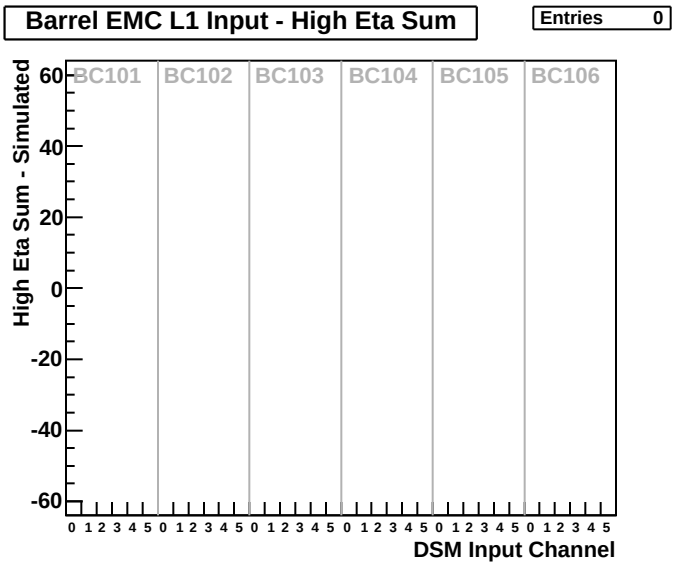

Endcap Jet Patches
Entries 2653962
JP ADC
JP ID

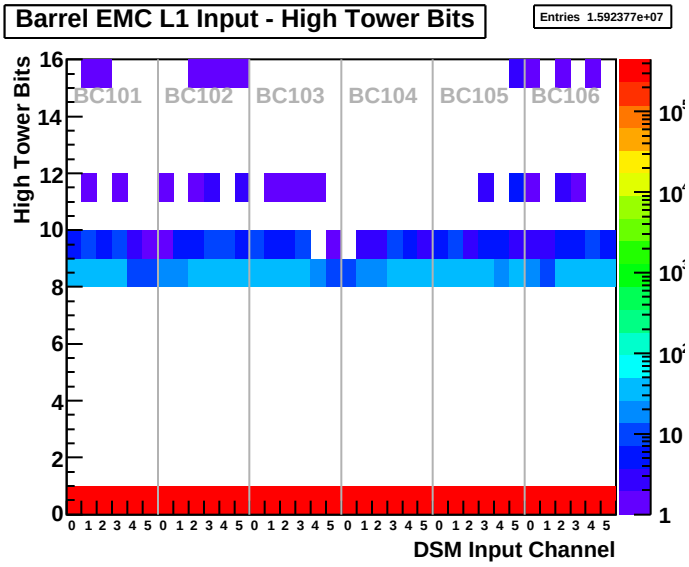

Hybrid Jet Patches
Entries 884654
JP ADC
JP ID

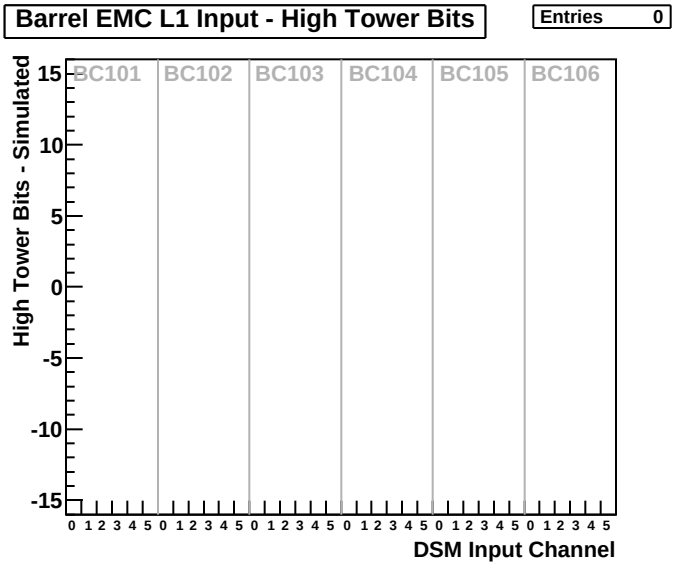

Barrel EMC L1 Input - Low Eta Sum
Entries 1.592377e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

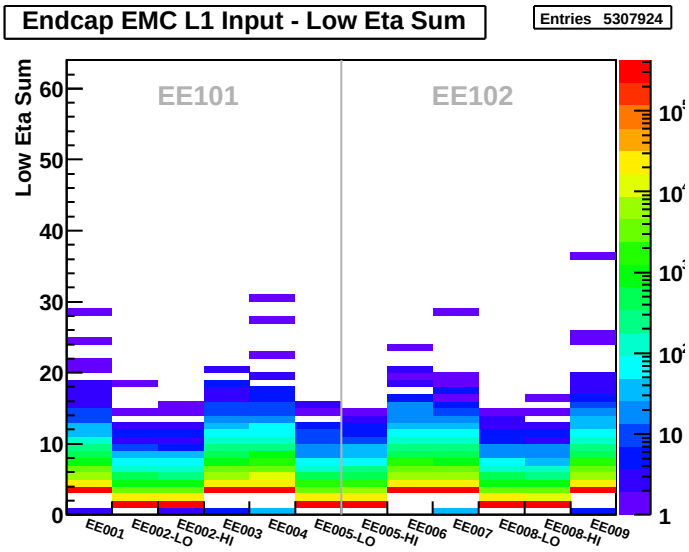

Barrel EMC L1 Input - Low Eta Sum
Entries 4
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

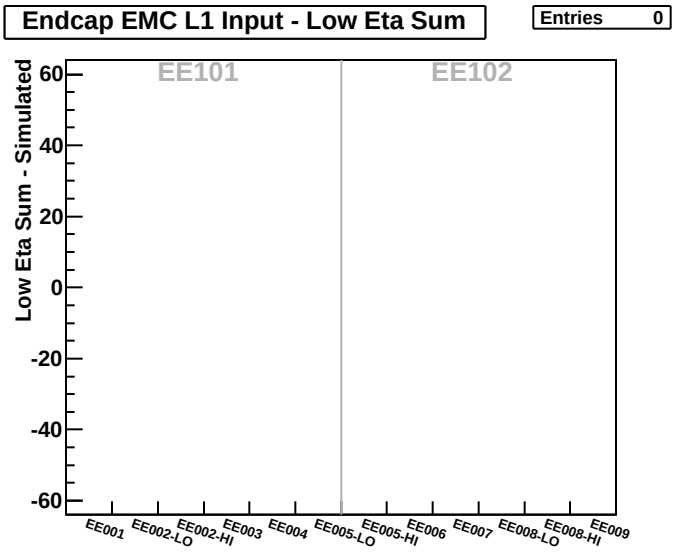

Barrel EMC L1 Input - High Eta Sum
Entries 1.592377e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

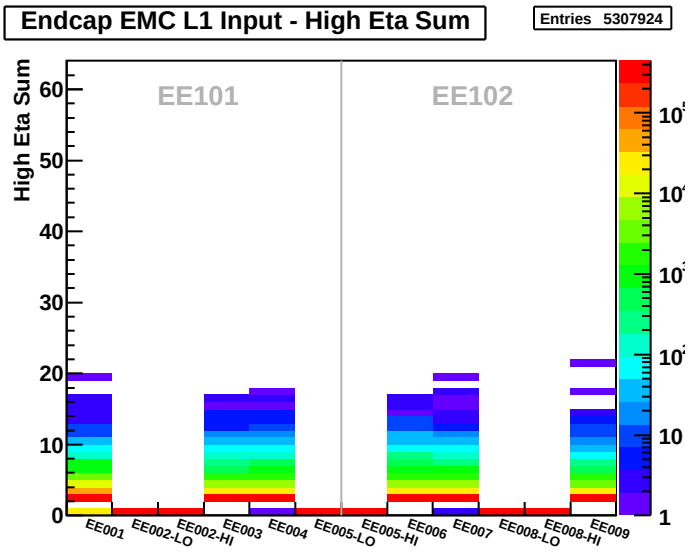

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

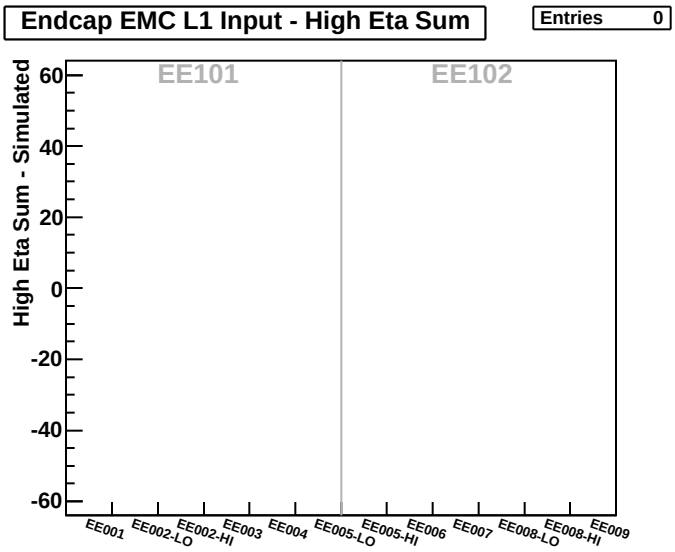

Barrel EMC L1 Input - High Tower Bits
Entries 1.592377e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

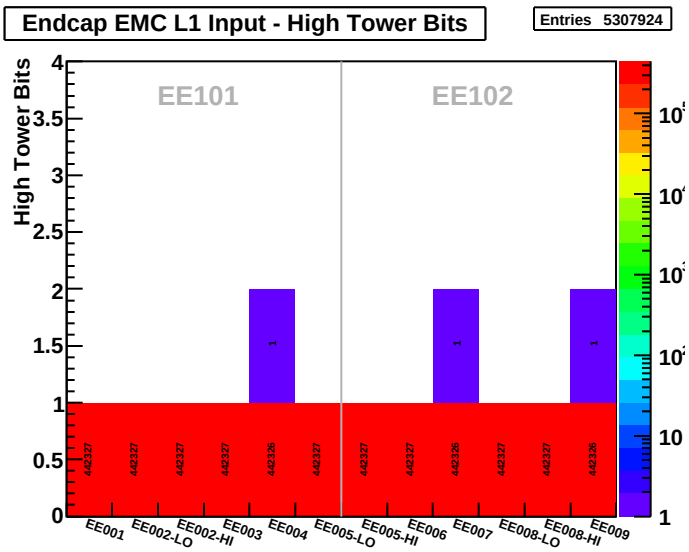

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

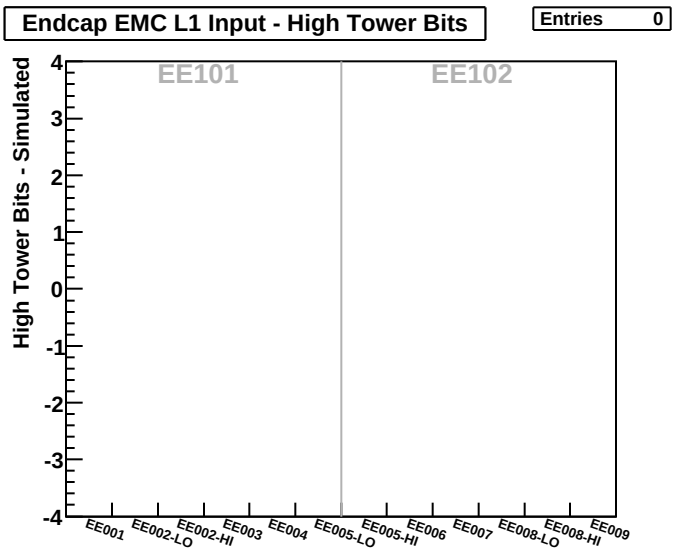

Endcap EMC L1 Input - Low Eta Sum
Entries 5307924
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

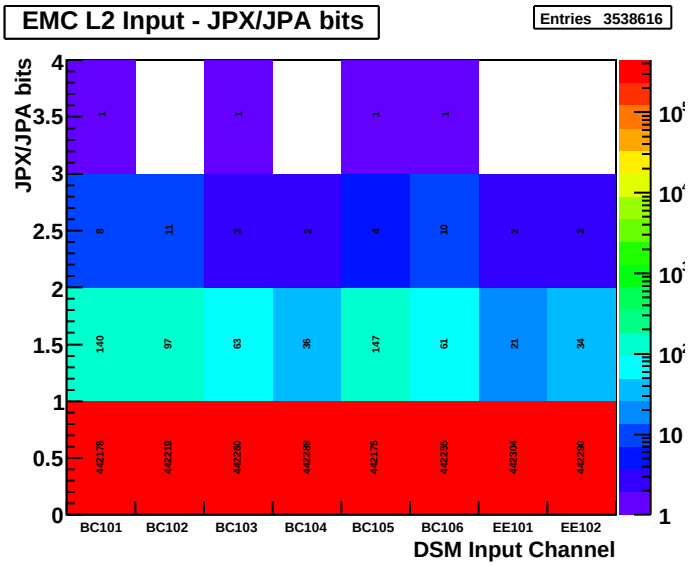

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

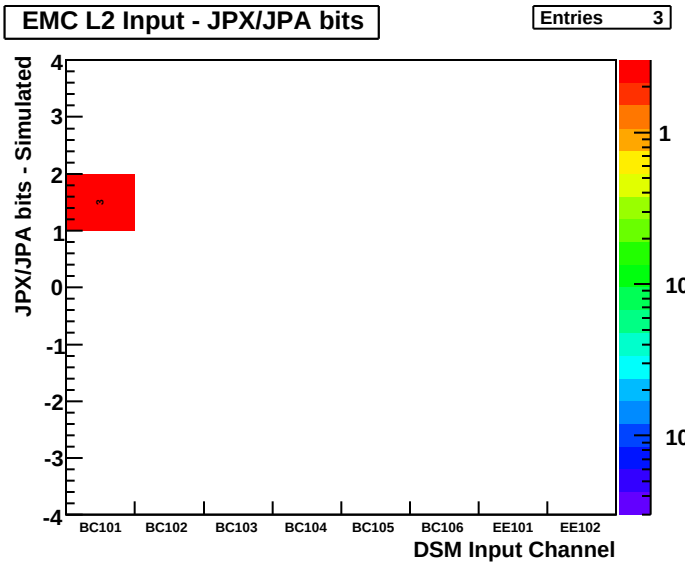

Endcap EMC L1 Input - High Eta Sum
Entries 5307924
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

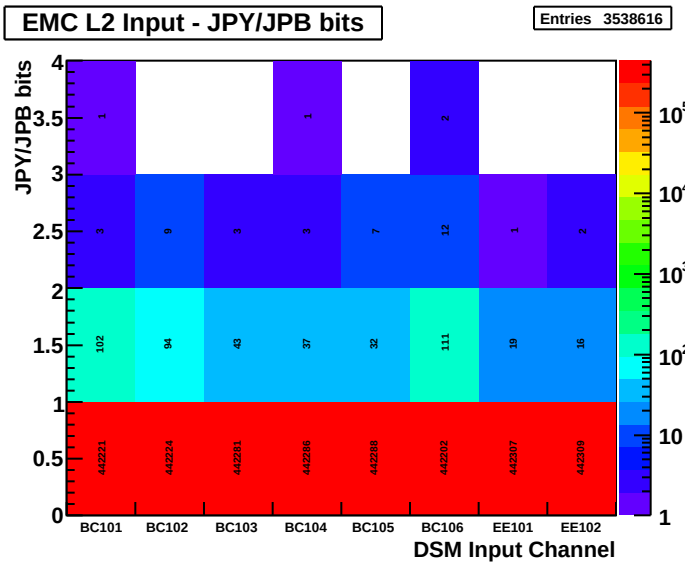

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

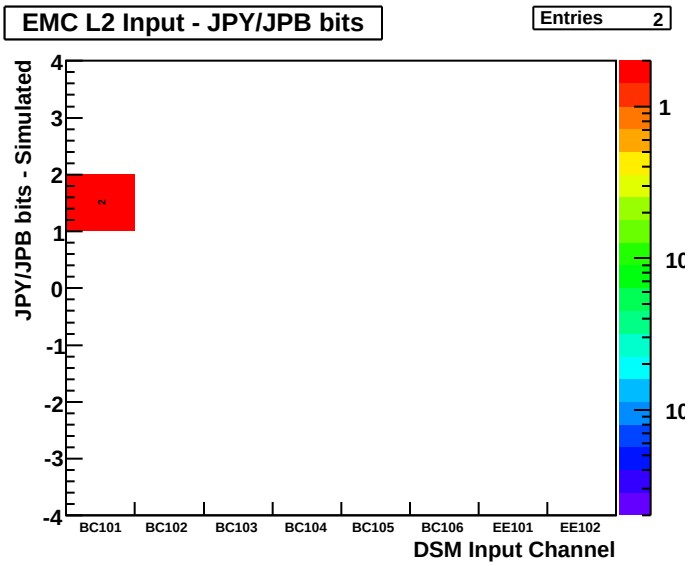

Endcap EMC L1 Input - High Tower Bits
Entries 5307924
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

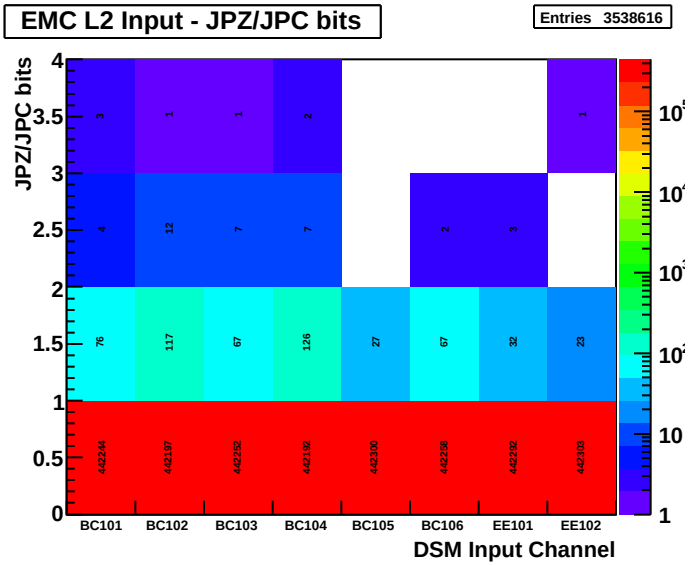

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

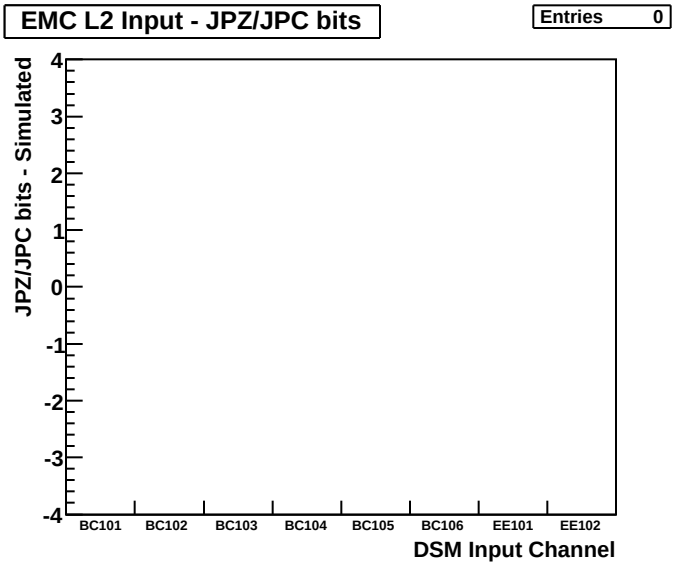

EMC L2 Input - JPX/JPA bits
Entries 3538616
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

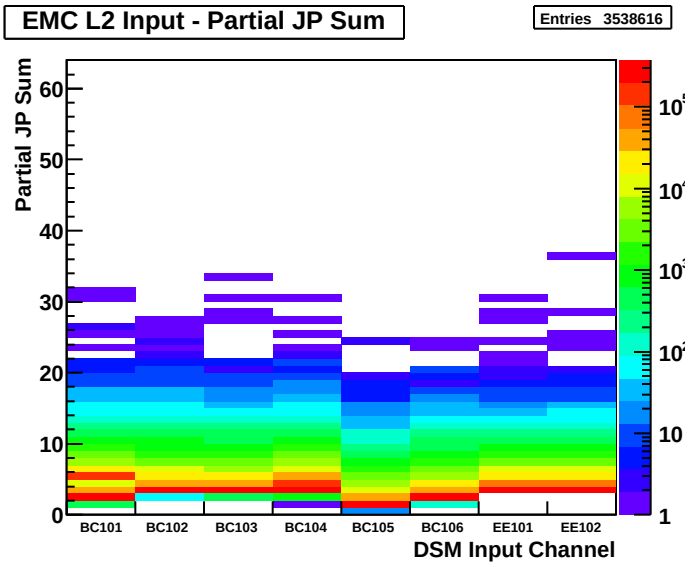

EMC L2 Input - JPX/JPA bits
Entries 3
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

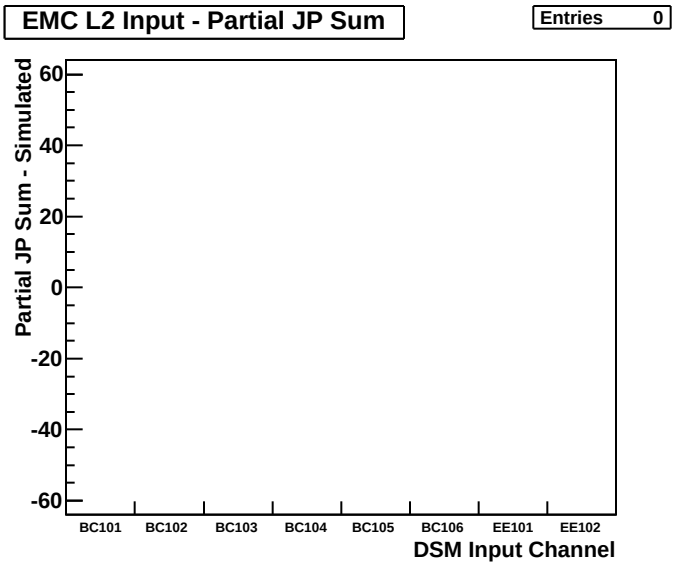

EMC L2 Input - JPY/JPB bits
Entries 3538616
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

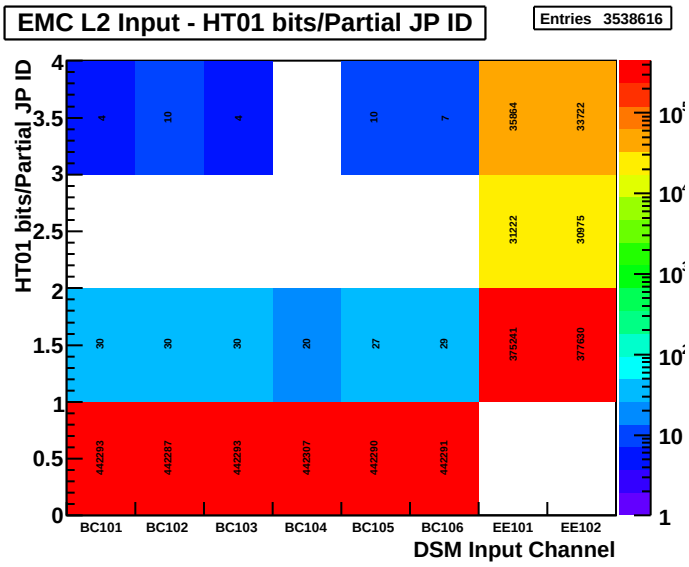

EMC L2 Input - JPY/JPB bits
Entries 2
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

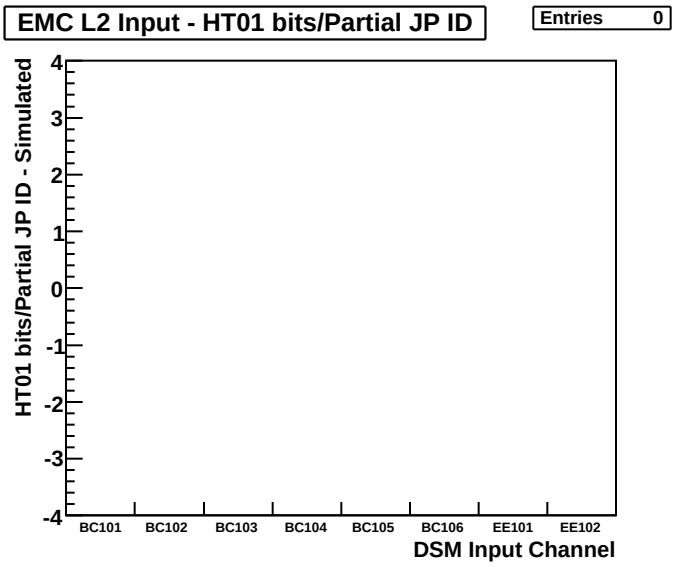

EMC L2 Input - JPZ/JPC bits
Entries 3538616
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

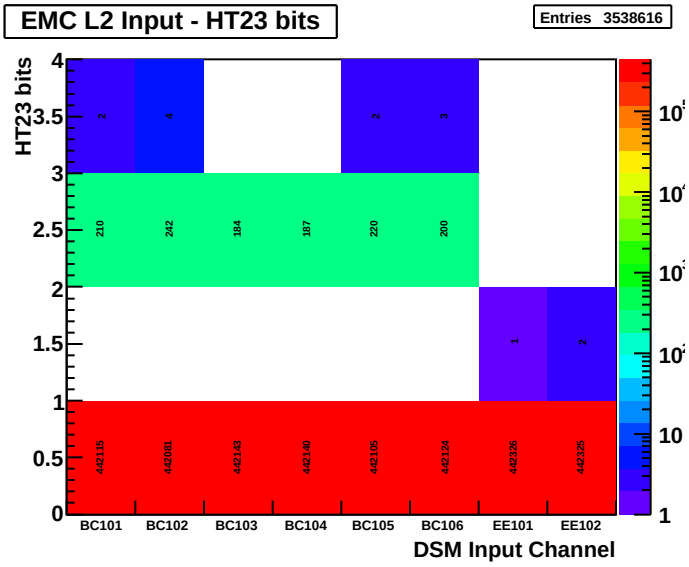

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

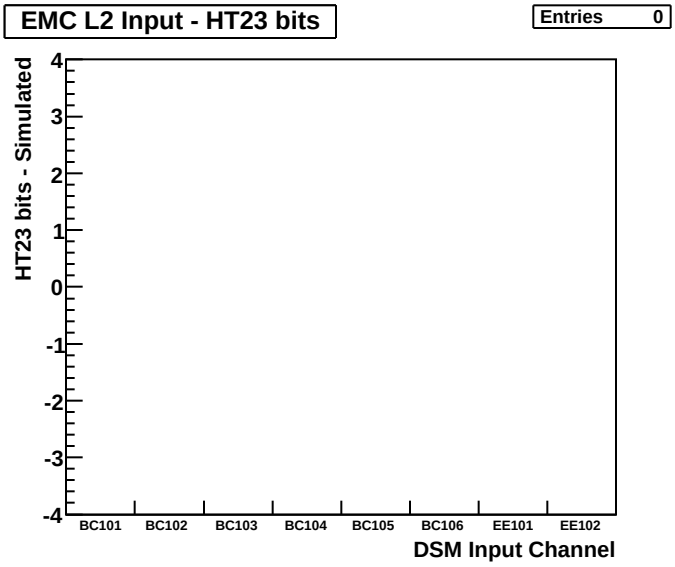

EMC L2 Input - Partial JP Sum
Entries 3538616
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

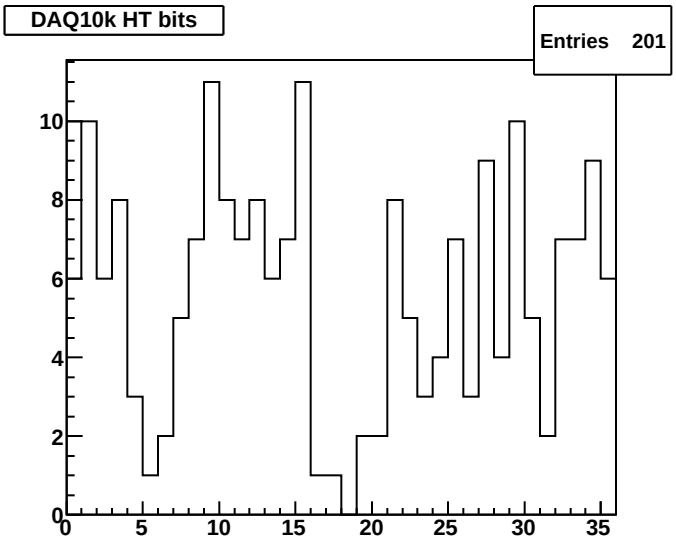

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

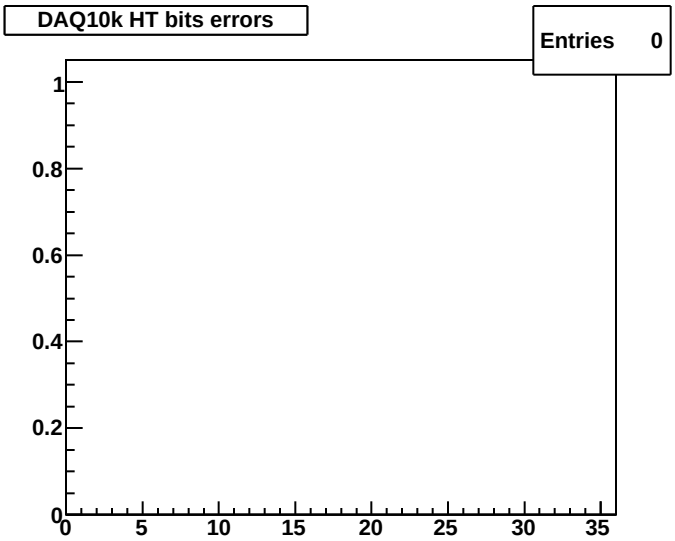

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3538616
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

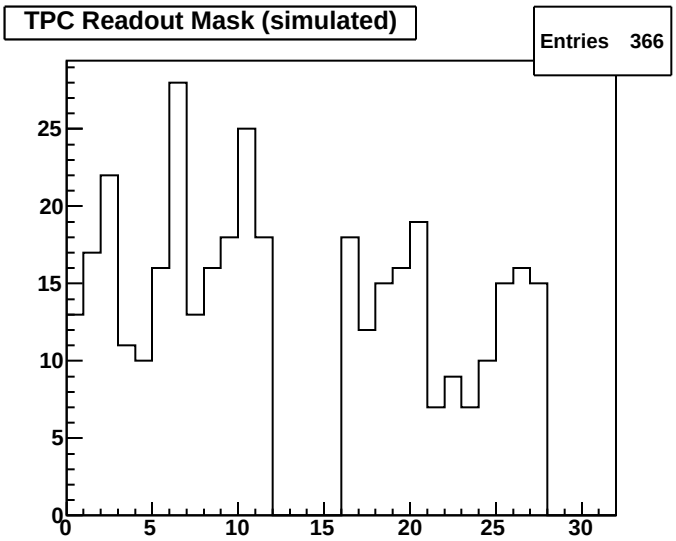

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

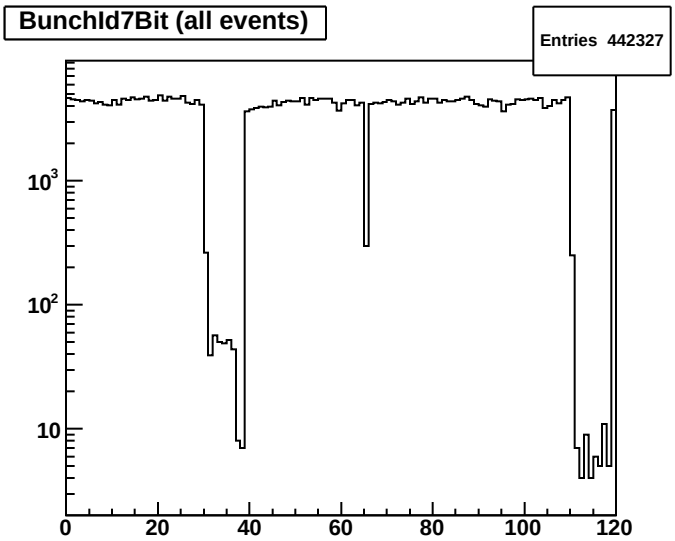

EMC L2 Input - HT23 bits
Entries 3538616
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

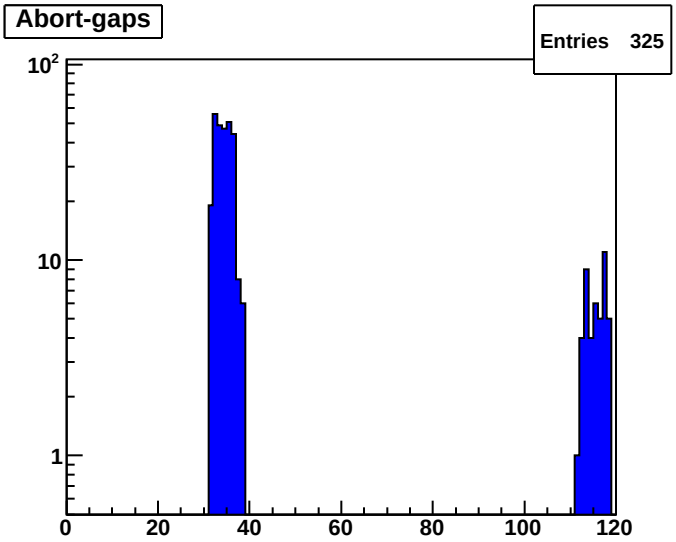

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

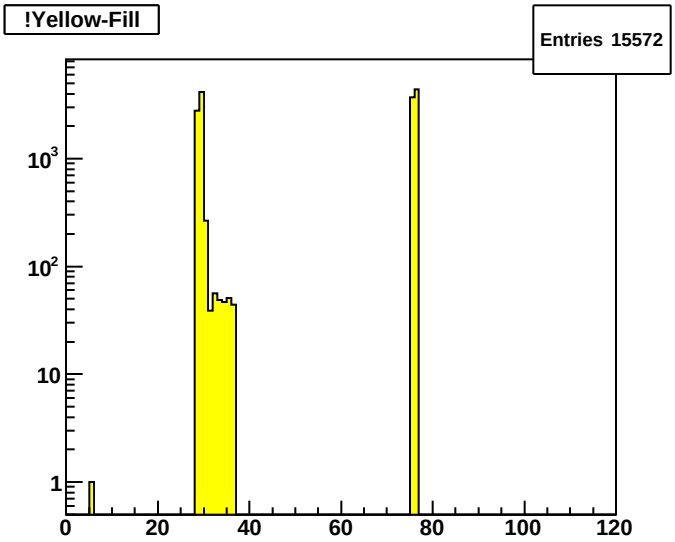

DAQ10k HT bits
Entries 201

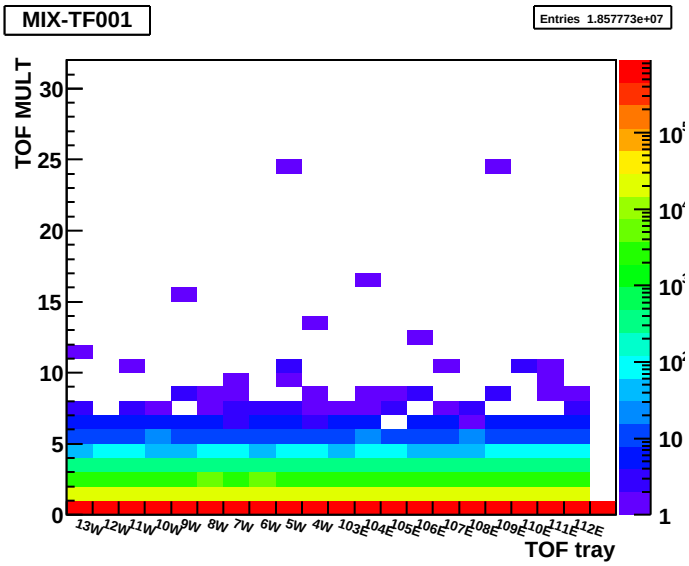

DAQ10k HT bits errors
Entries 0

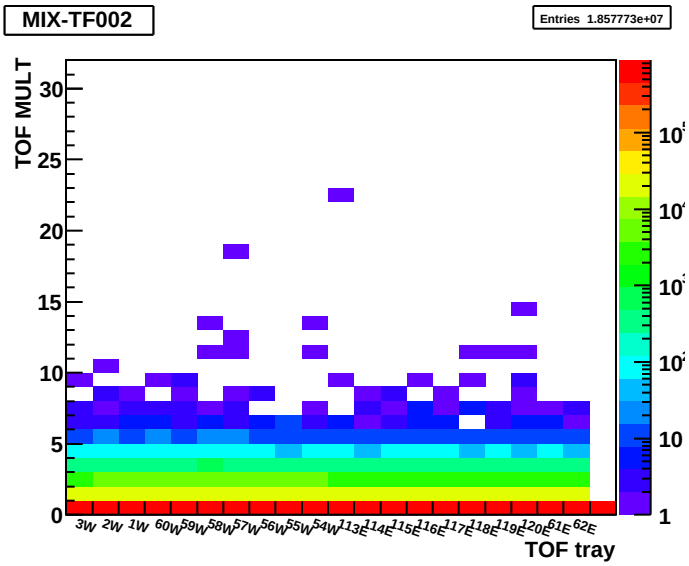

TPC Readout Mask (simulated)
Entries 366

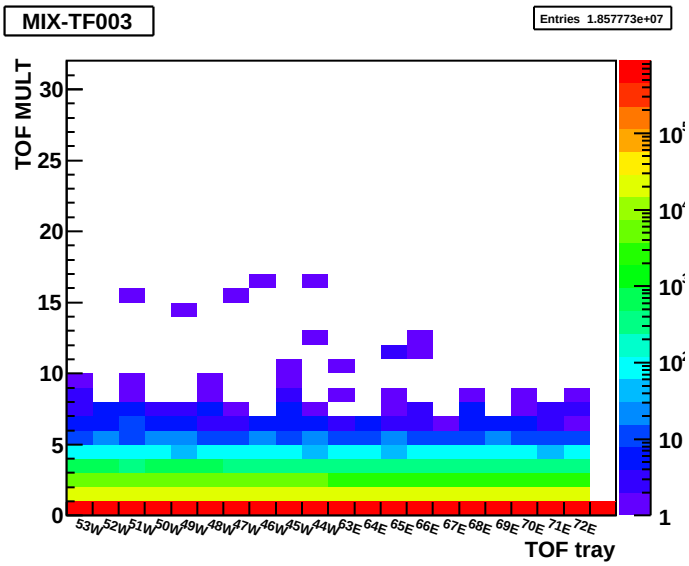

BunchId7Bit (all events)
Entries 442327

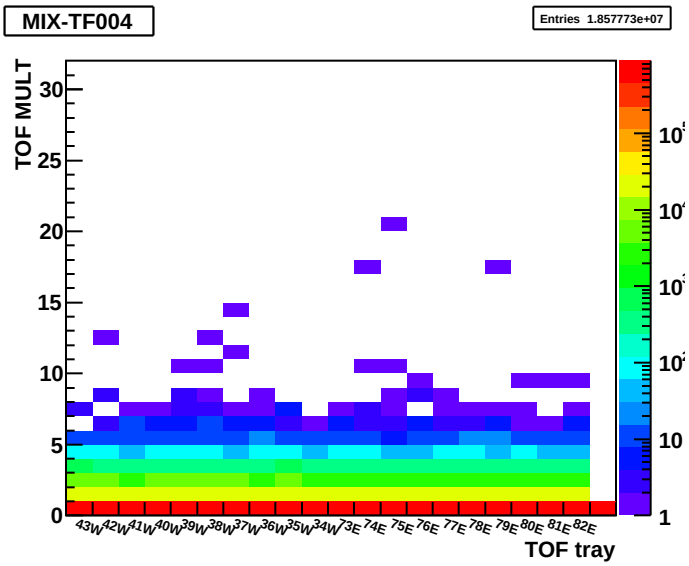

Abort-gaps
Entries 325

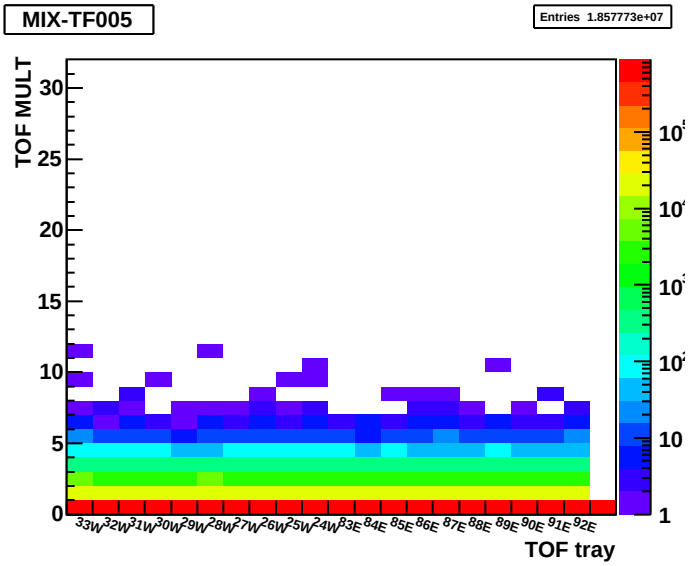

!Yellow-Fill
Entries 15572

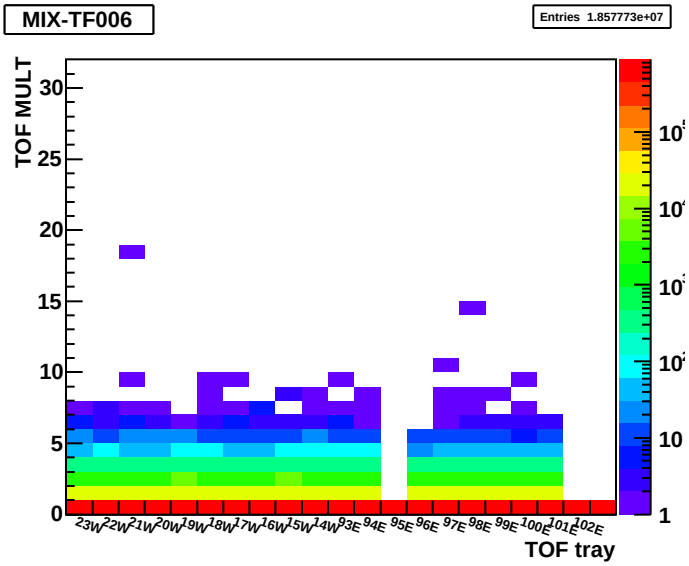

MIX-TF001
Entries 1.857773e+07
TOF MULT
TOF tray

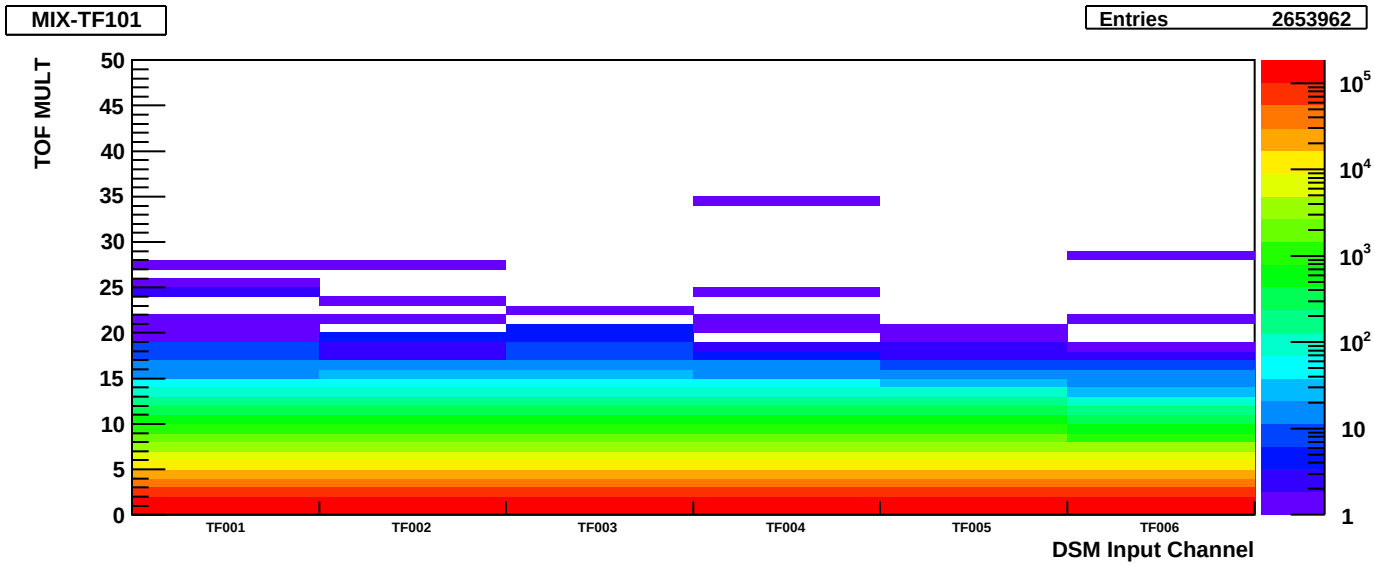

MIX-TF002
Entries 1.857773e+07
TOF MULT
TOF tray

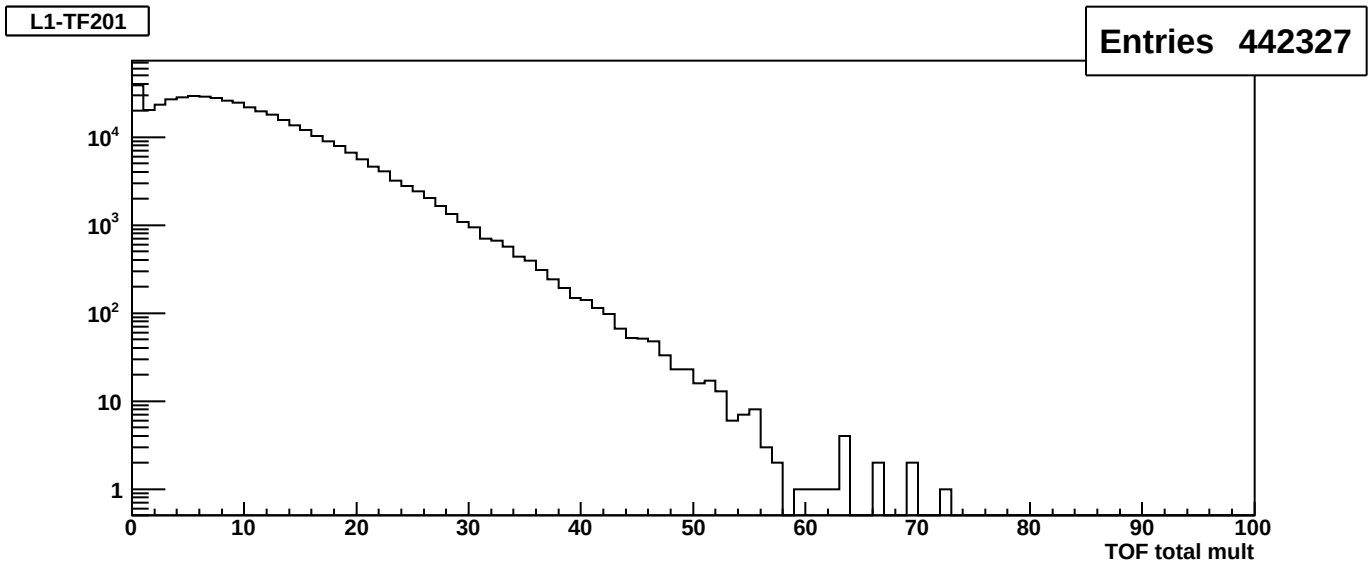

MIX-TF003
Entries 1.857773e+07
TOF MULT
TOF tray

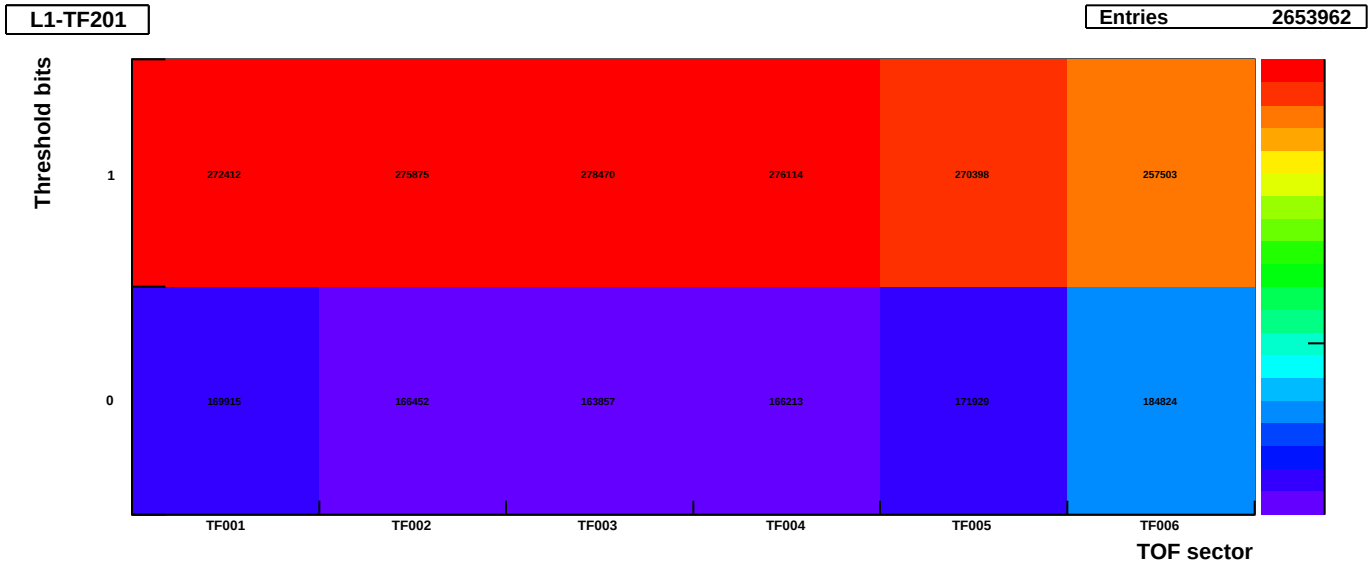

MIX-TF004
Entries 1.857773e+07
TOF MULT
TOF tray

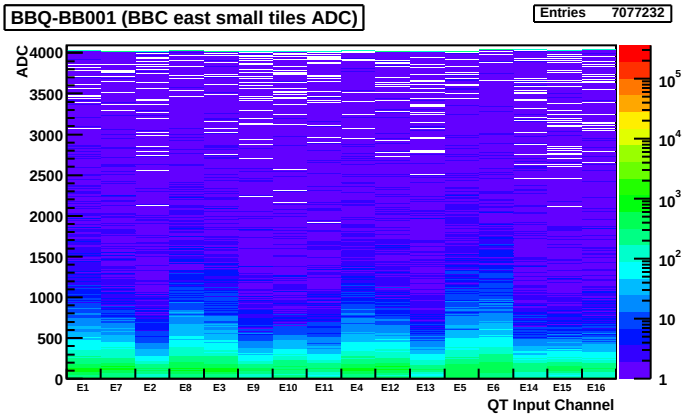

MIX-TF005
Entries 1.857773e+07
TOF MULT
TOF tray

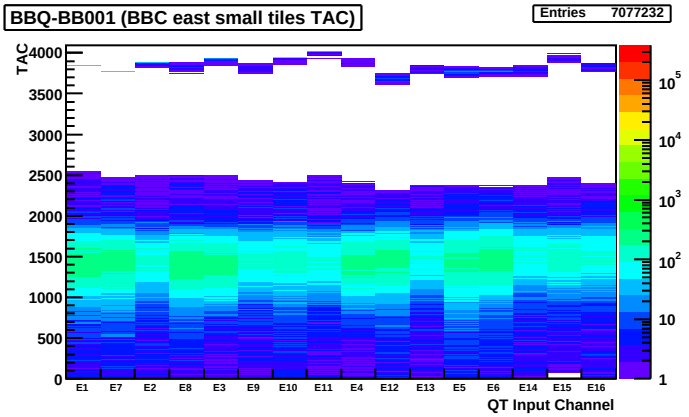

MIX-TF006
Entries 1.857773e+07
TOF MULT
TOF tray

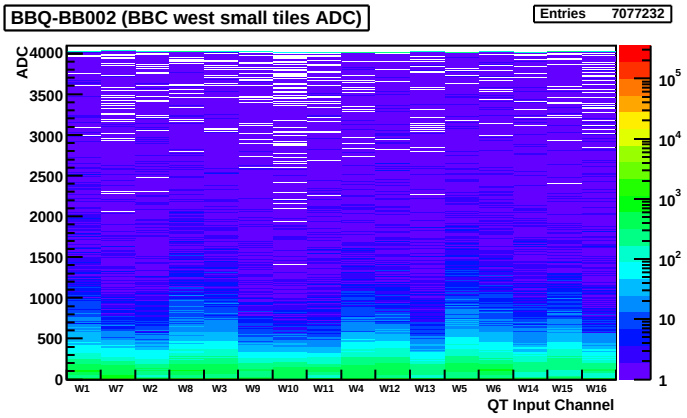

MIX-TF101
Entries 2653962
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

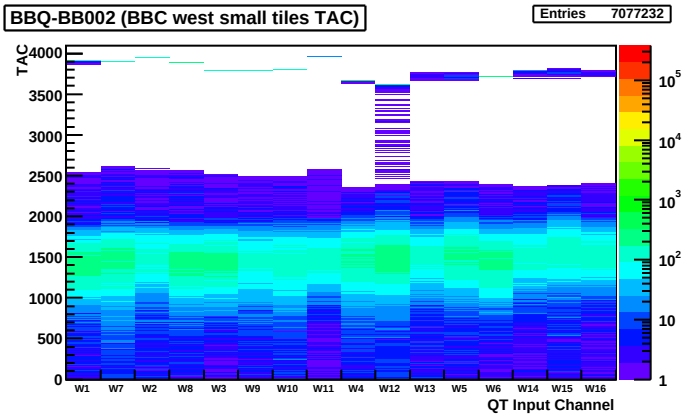

L1-TF201
Entries 442327
TOF total mult

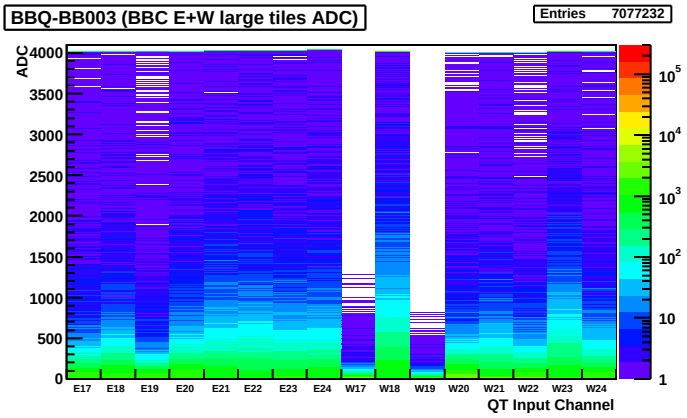

L1-TF201
Entries 2653962
Threshold bits
272412
275875
278470
276114
270398
257503
169915
166452
163857
166213
171929
184824
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

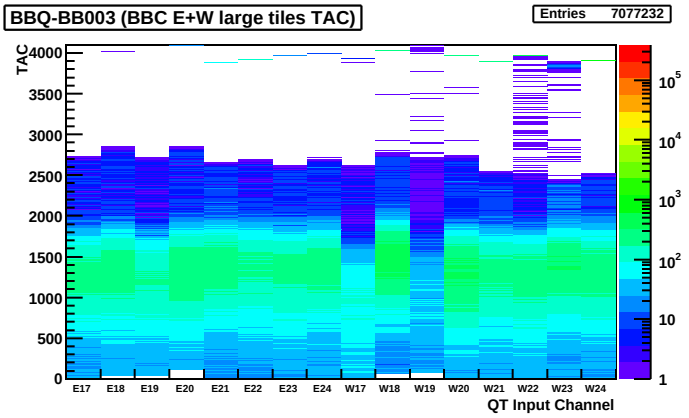

BBQ-BB001 (BBC east small tiles ADC)
Entries 7077232
ADC
QT Input Channel

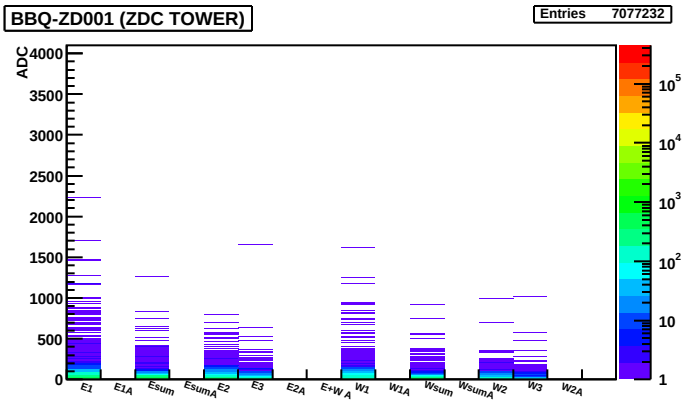

BBQ-BB001 (BBC east small tiles TAC)
Entries 7077232
TAC
QT Input Channel

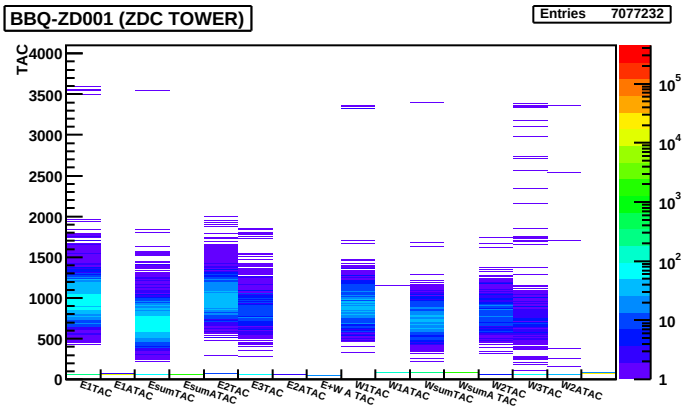

BBQ-BB002 (BBC west small tiles ADC)
Entries 7077232
ADC
QT Input Channel

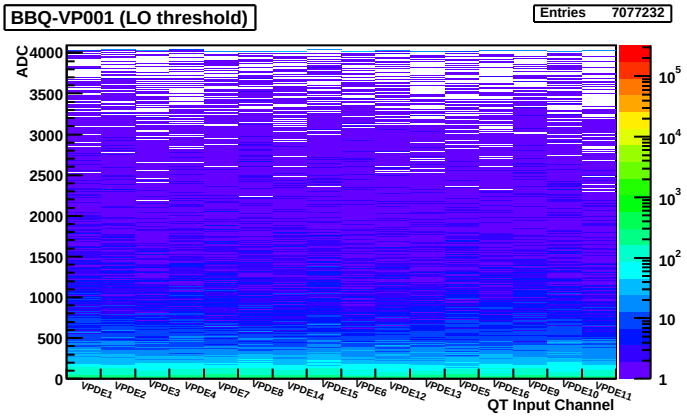

BBQ-BB002 (BBC west small tiles TAC)
Entries 7077232
TAC
QT Input Channel

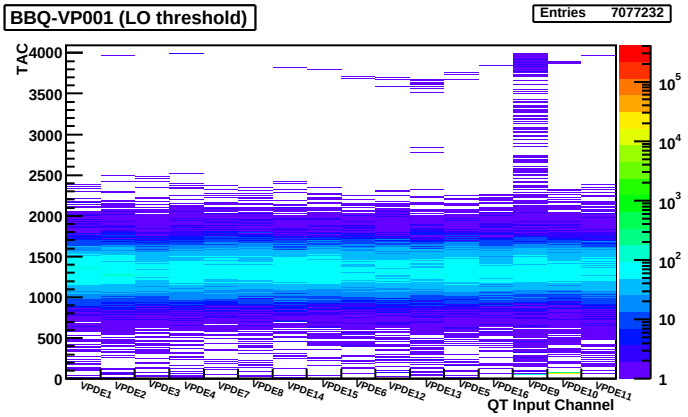

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7077232
ADC
QT Input Channel

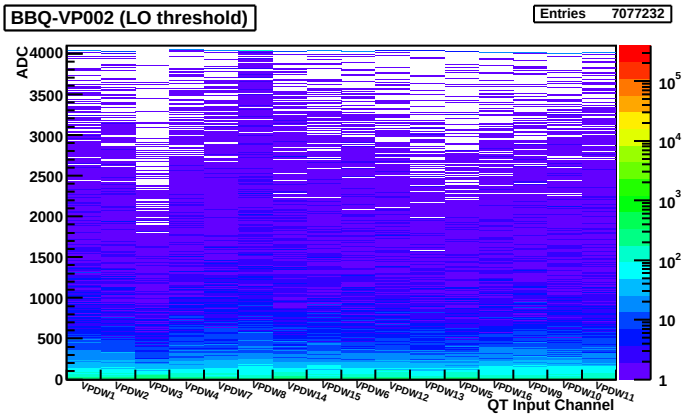

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7077232
TAC
QT Input Channel

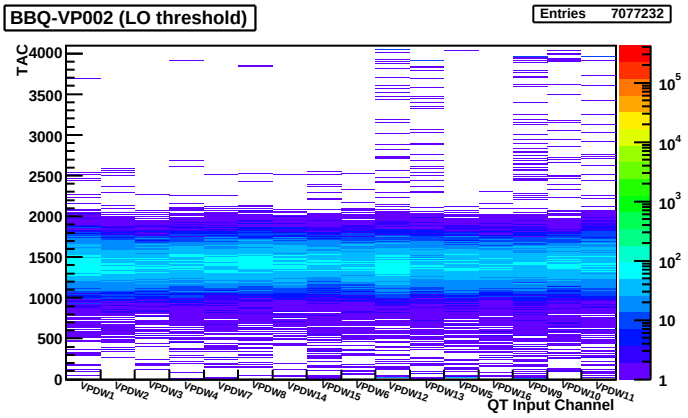

BBQ-ZD001 (ZDC TOWER)
Entries 7077232
ADC

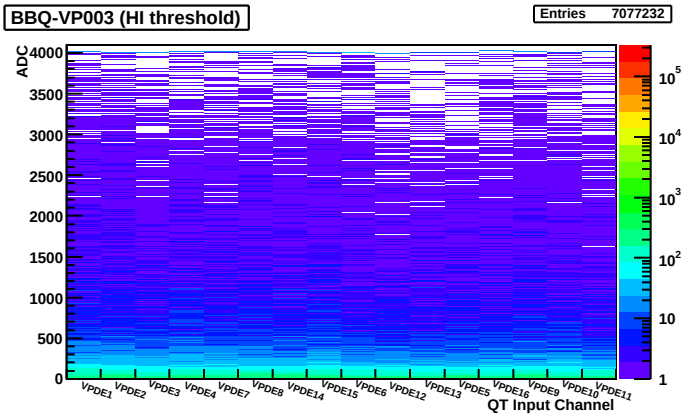

BBQ-ZD001 (ZDC TOWER)
Entries 7077232
TAC

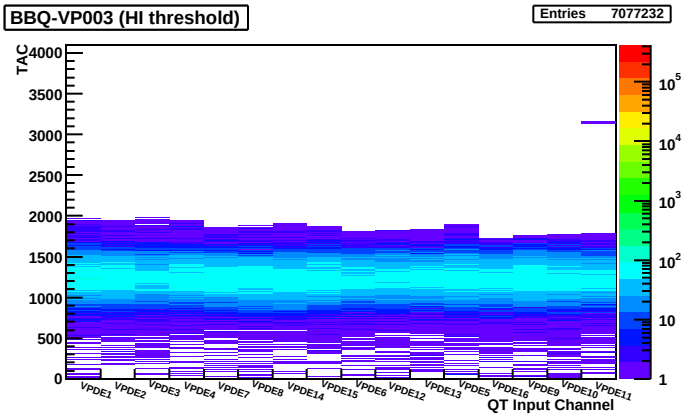

BBQ-VP001 (LO threshold)
Entries 7077232
ADC
QT Input Channel

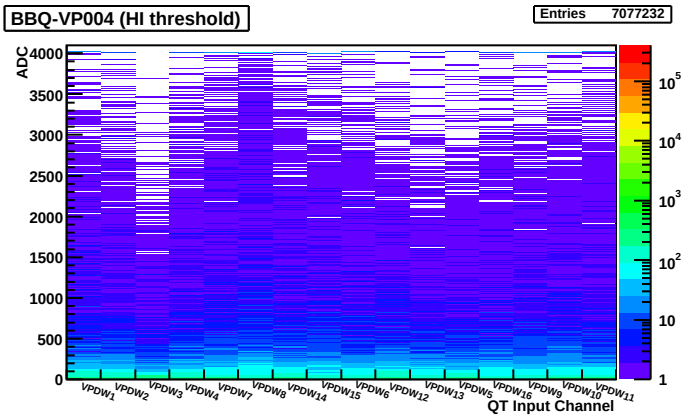

BBQ-VP001 (LO threshold)
Entries 7077232
TAC
QT Input Channel

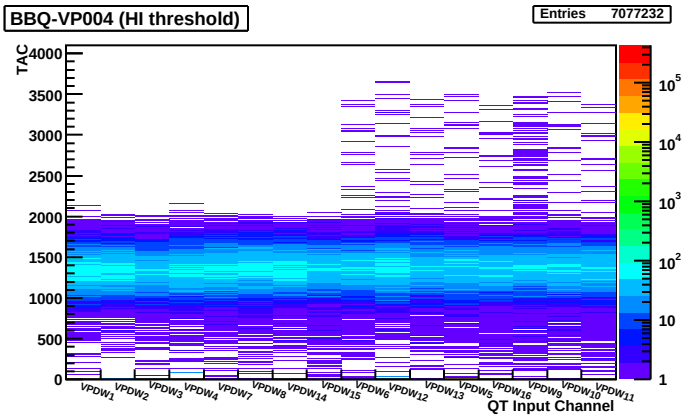

BBQ-VP002 (LO threshold)
Entries 7077232
ADC
QT Input Channel

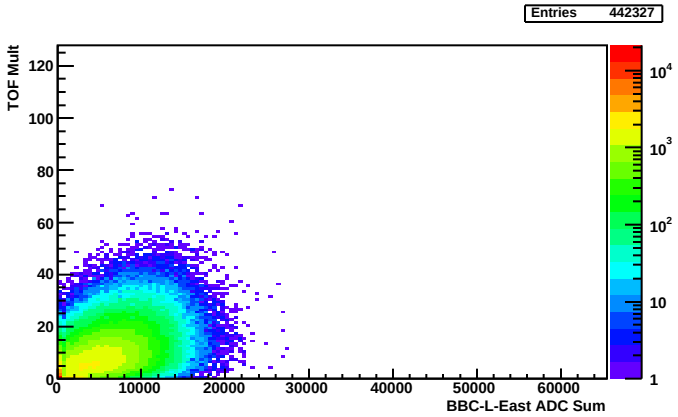

BBQ-VP002 (LO threshold)
Entries 7077232
TAC
QT Input Channel

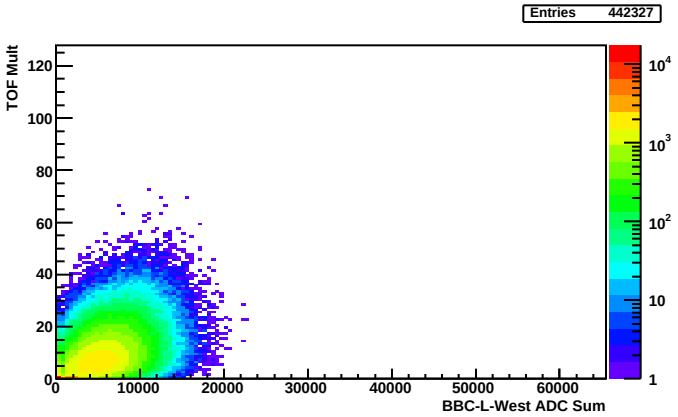

BBQ-VP003 (HI threshold)
Entries 7077232
ADC
QT Input Channel

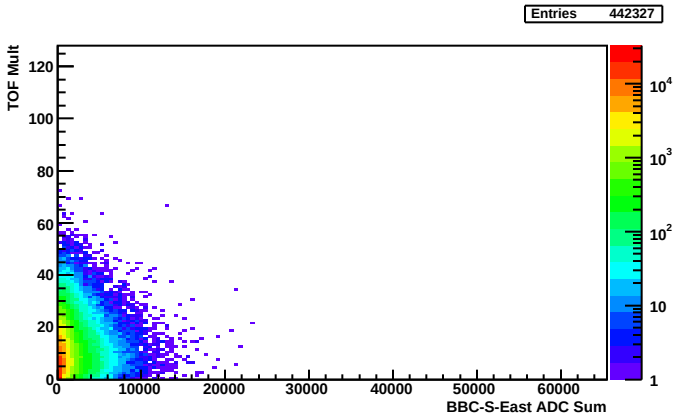

BBQ-VP003 (HI threshold)
Entries 7077232
TAC
QT Input Channel

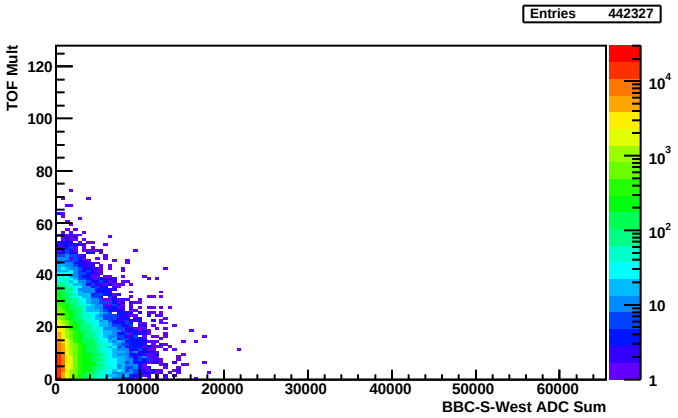

BBQ-VP004 (HI threshold)
Entries 7077232
ADC
QT Input Channel

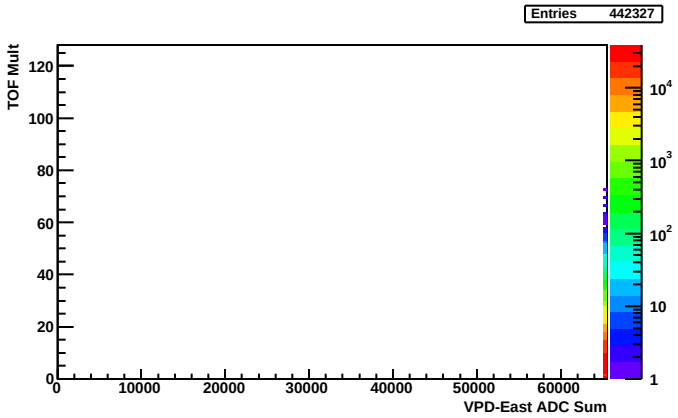

BBQ-VP004 (HI threshold)
Entries 7077232
TAC
QT Input Channel

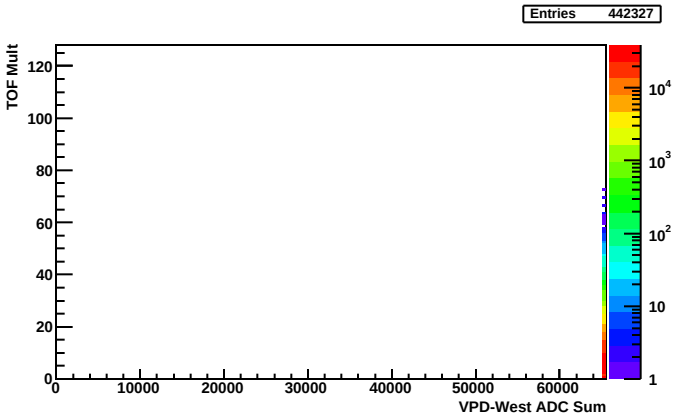

Entries 442327
TOF Mult
BBC-L-East ADC Sum

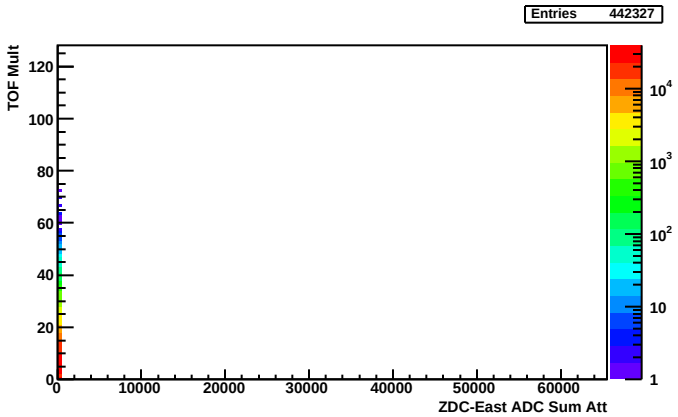

Entries 442327
TOF Mult
BBC-L-West ADC Sum

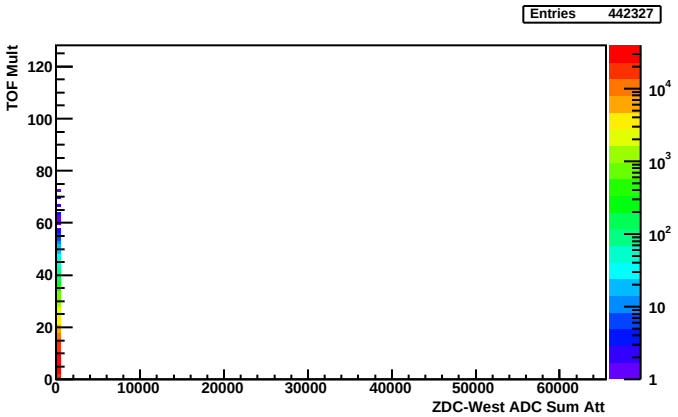

Entries 442327
TOF Mult
BBC-S-East ADC Sum

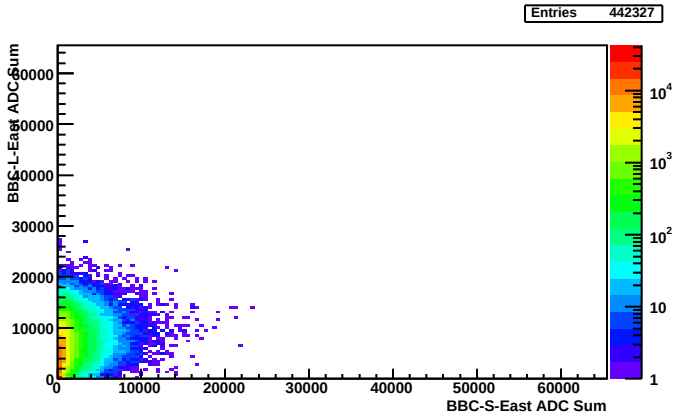

Entries 442327
TOF Mult
BBC-S-West ADC Sum

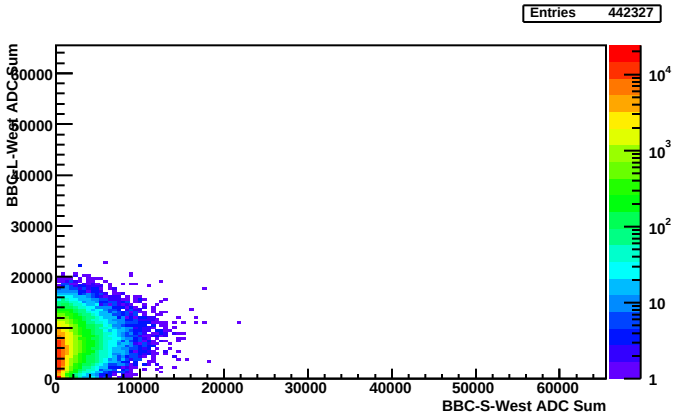

Entries 442327
TOF Mult
VPD-East ADC Sum

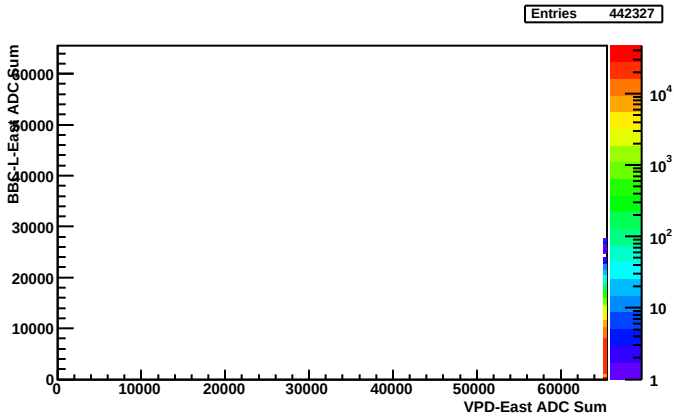

Entries 442327
TOF Mult
VPD-West ADC Sum

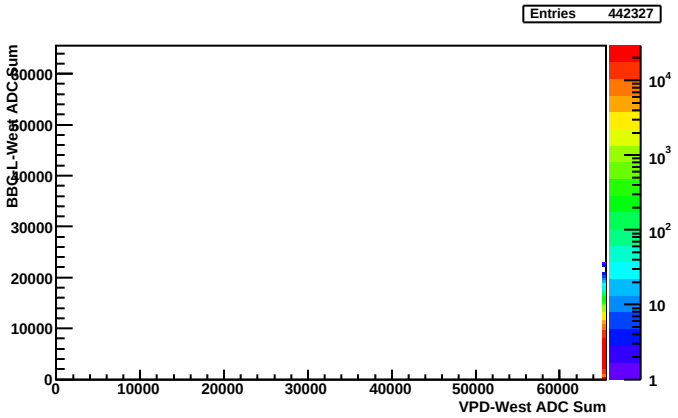

Entries 442327
TOF Mult
ZDC-East ADC Sum Att


Entries 442327
TOF Mult
ZDC-West ADC Sum Att


Entries 442327
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 442327
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 442327
BBC-L-East ADC Sum
VPD-East ADC Sum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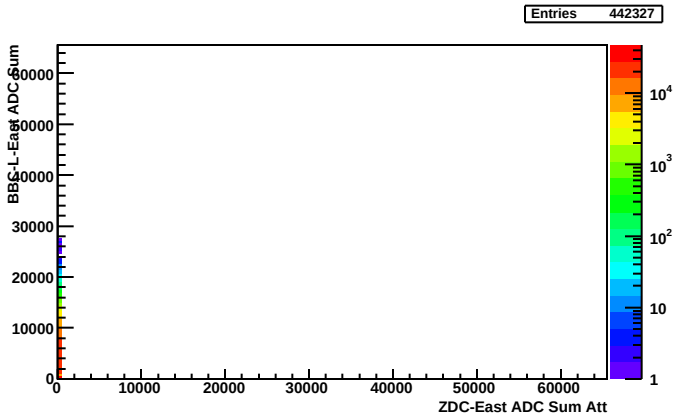
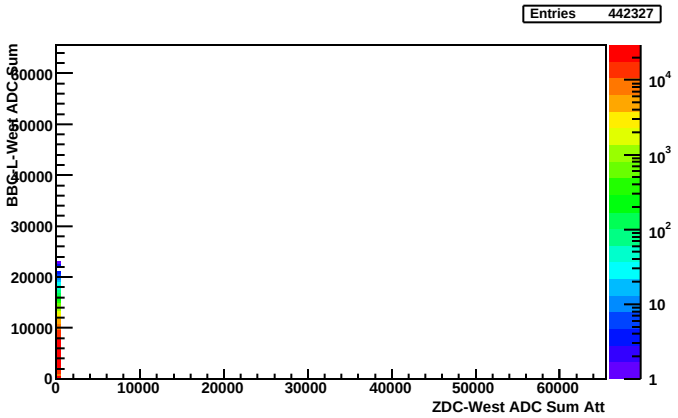
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 442327
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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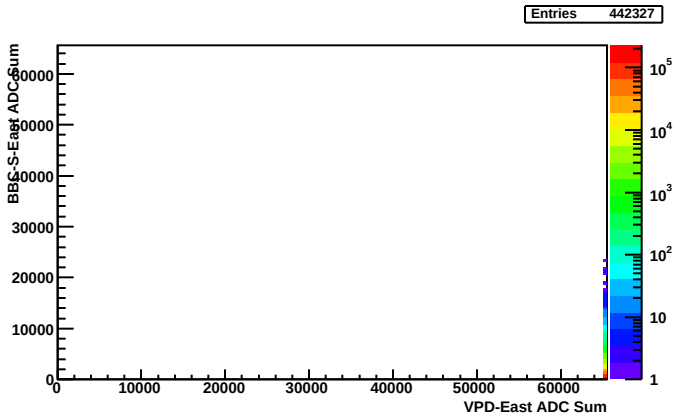
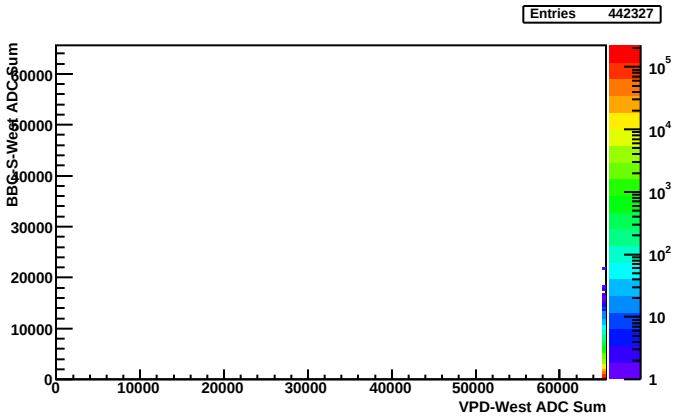
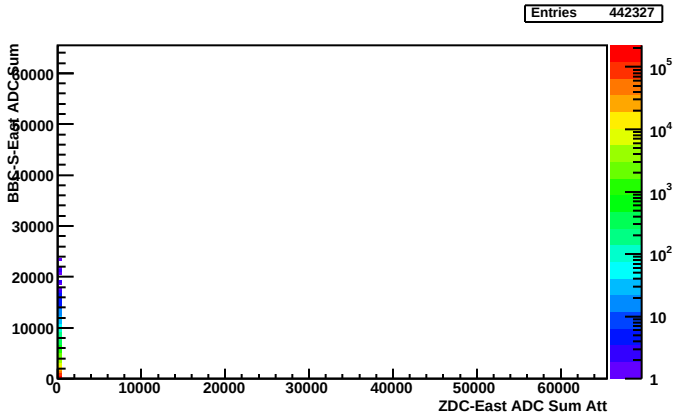
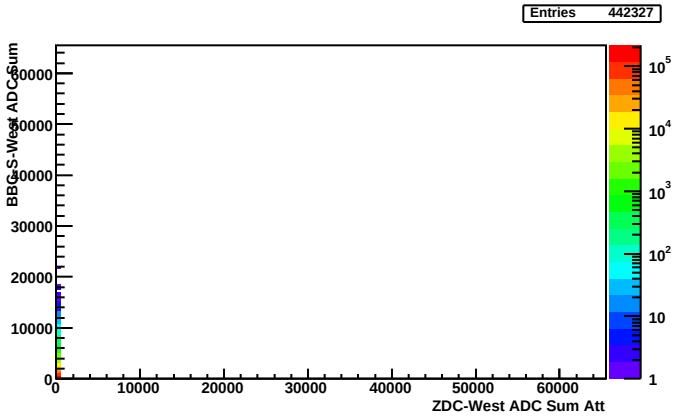
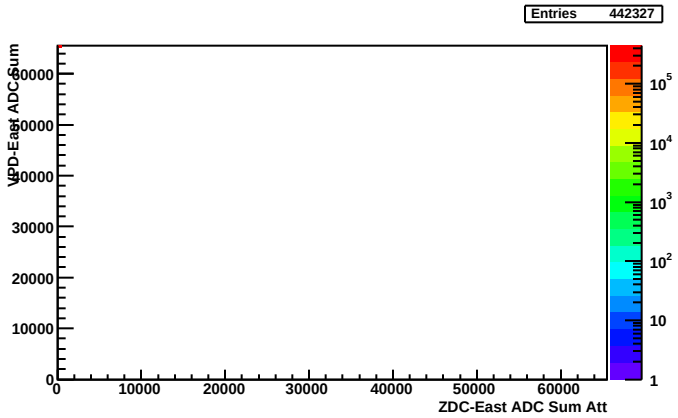
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

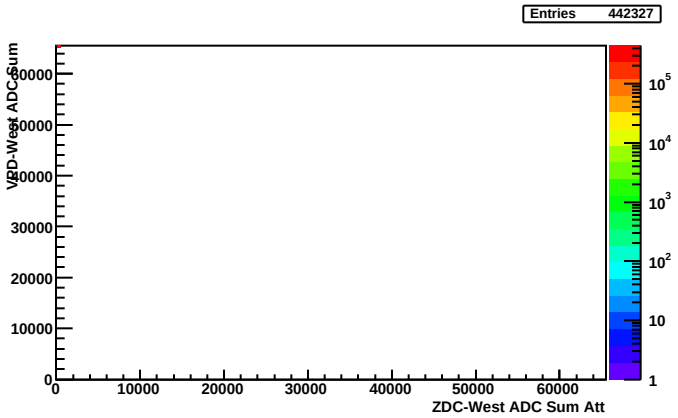

Entries 442327
BBC-S-East ADC Sum
VPD-East ADC Sum

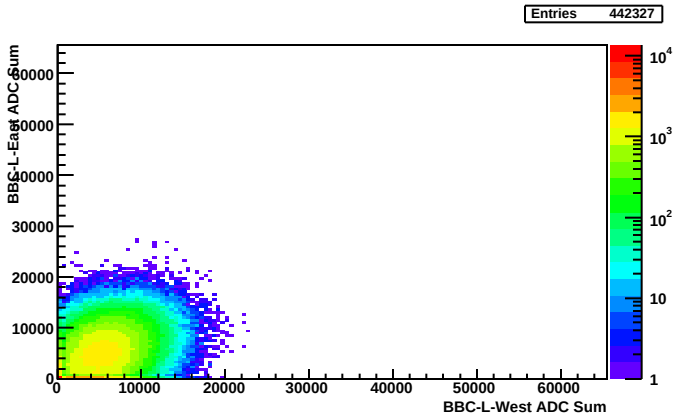

Entries 442327
BBC-S-West ADC Sum
VPD-West ADC Sum

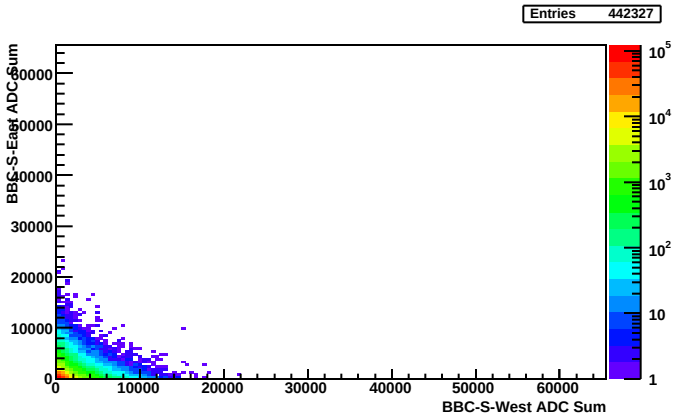

Entries 442327
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

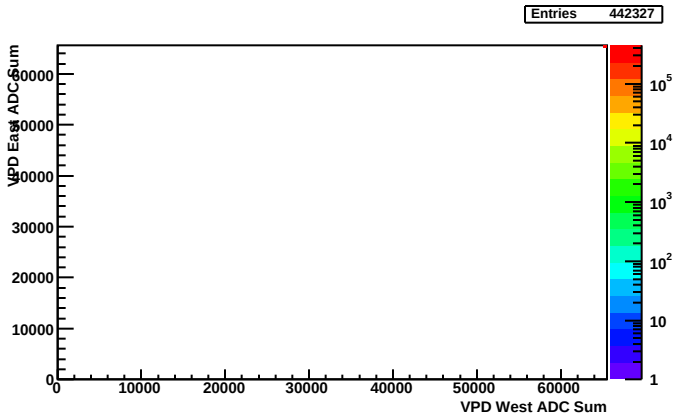

Entries 442327
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

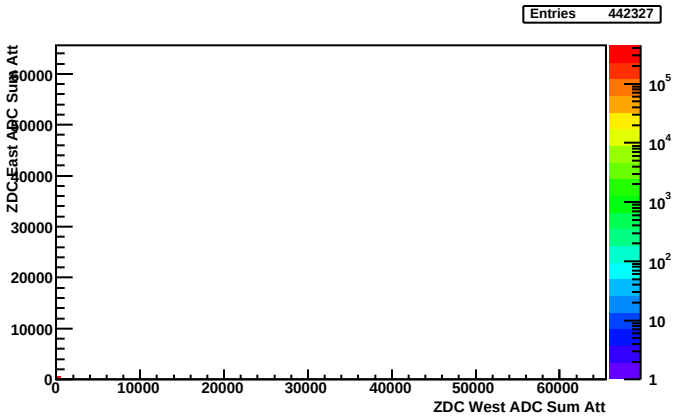

Entries 442327
VPD-East ADC Sum
ZDC-East ADC Sum Att

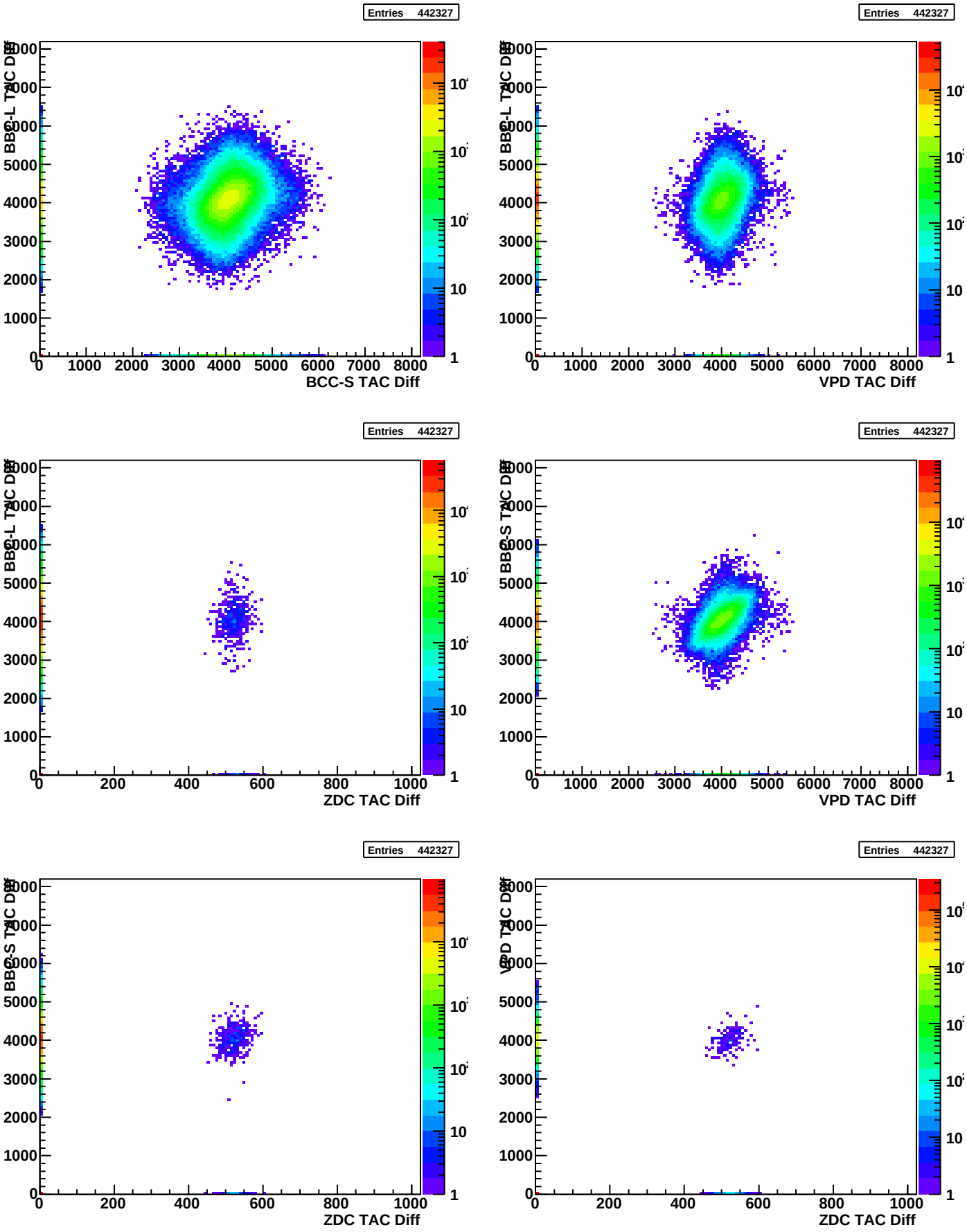

Entries 442327
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 442327
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 442327
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 442327
VPD East ADC Sum
VPD West ADC Sum


Entries 442327
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 442327
BBC-L TAC Diff
BCC-S TAC Diff
Entries 442327
BBC-L TAC Diff
VPD TAC Diff
Entries 442327
BBC-L TAC Diff
ZDC TAC Diff
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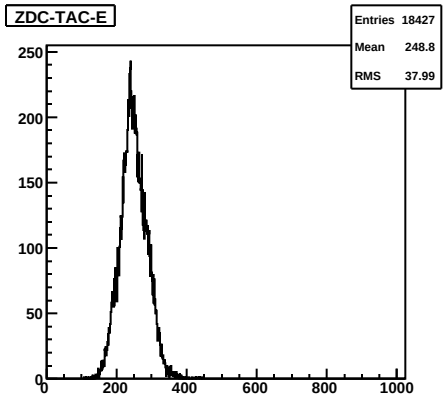
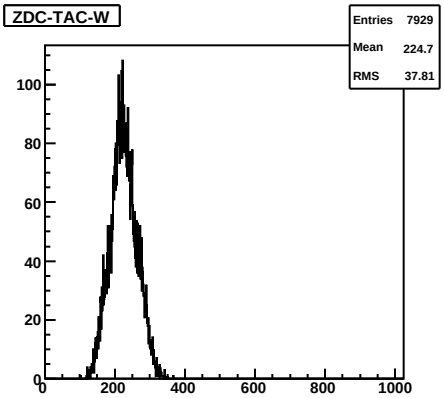
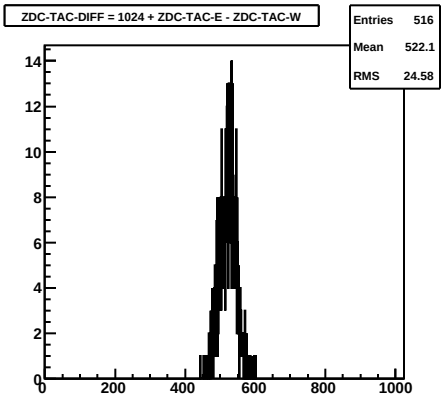
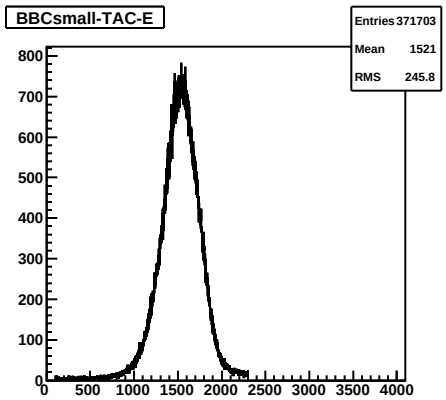
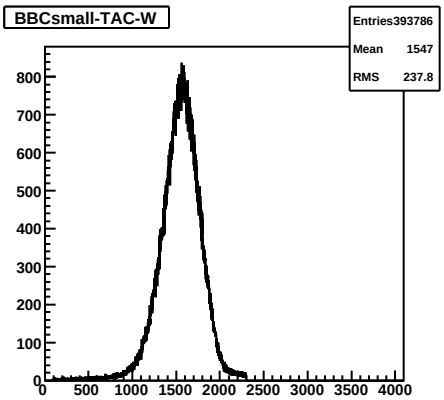
BBC-S TAC Diff
VPD TAC Diff
Entries 442327
BBC-S TAC Diff
ZDC TAC Diff
Entries 442327
VPD TAC Diff
ZDC TAC Diff

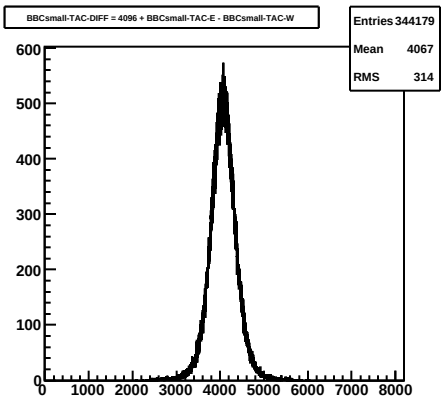

ZDC-TAC-E
Entries 18427
Mean 248.8
RMS 37.99

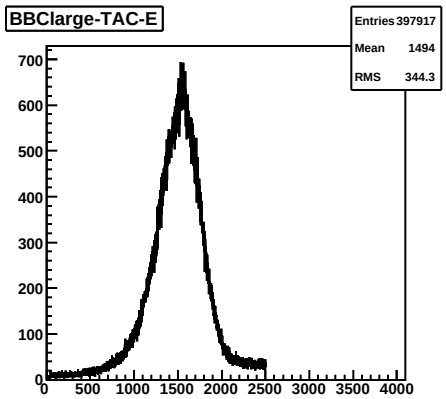

ZDC-TAC-W
Entries 7929
Mean 224.7
RMS 37.81

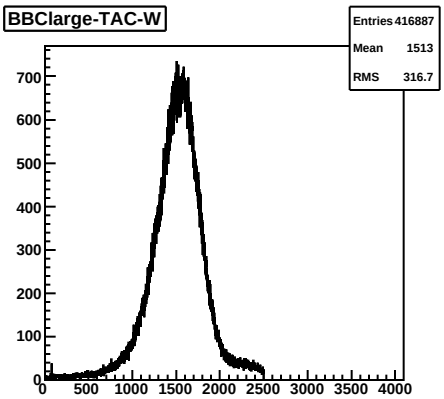

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 516
Mean 522.1
RMS 24.58

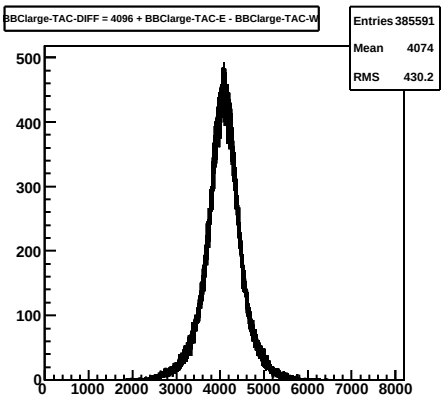

BBCsmall-TAC-E
Entries 371703
Mean 1521
RMS 245.8

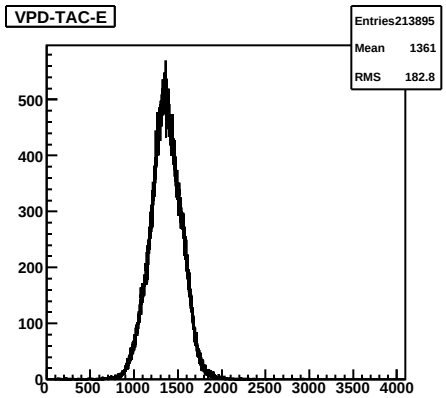

BBCsmall-TAC-W
Entries 393786
Mean 1547
RMS 237.8

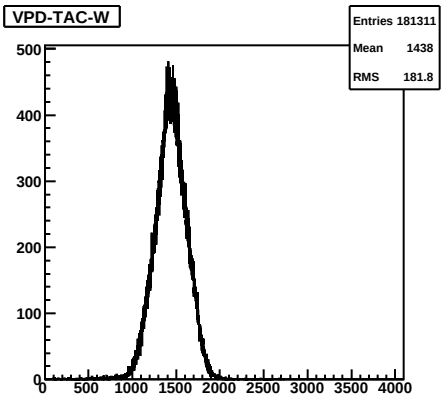

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 344179
Mean 4067
RMS 314

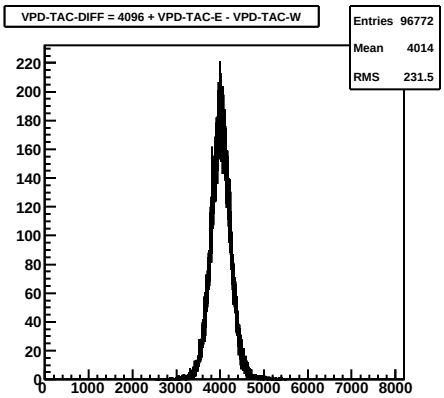

BBClarge-TAC-E
Entries 397917
Mean 1494
RMS 344.3

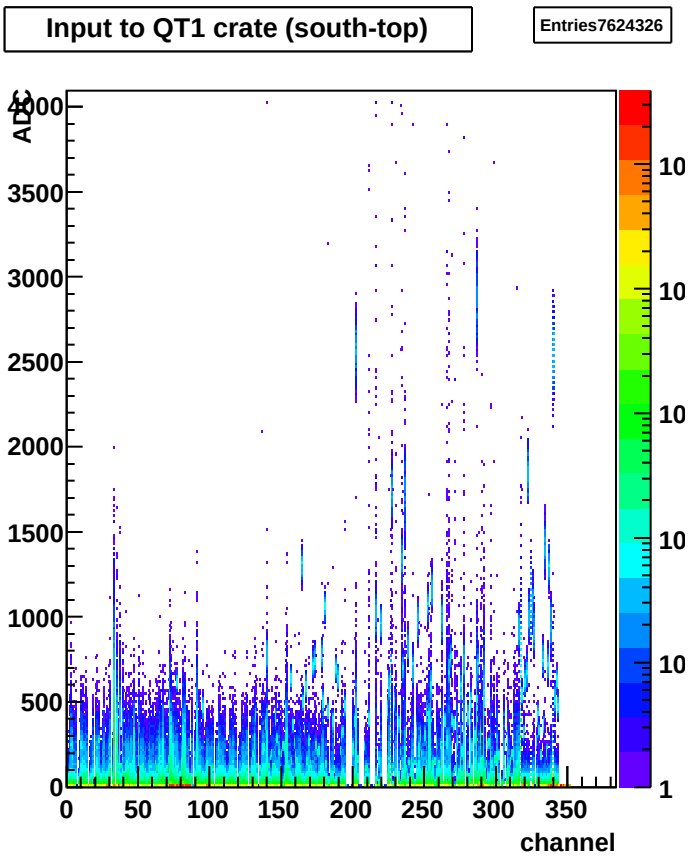

BBClarge-TAC-W
Entries 416887
Mean 1513
RMS 316.7

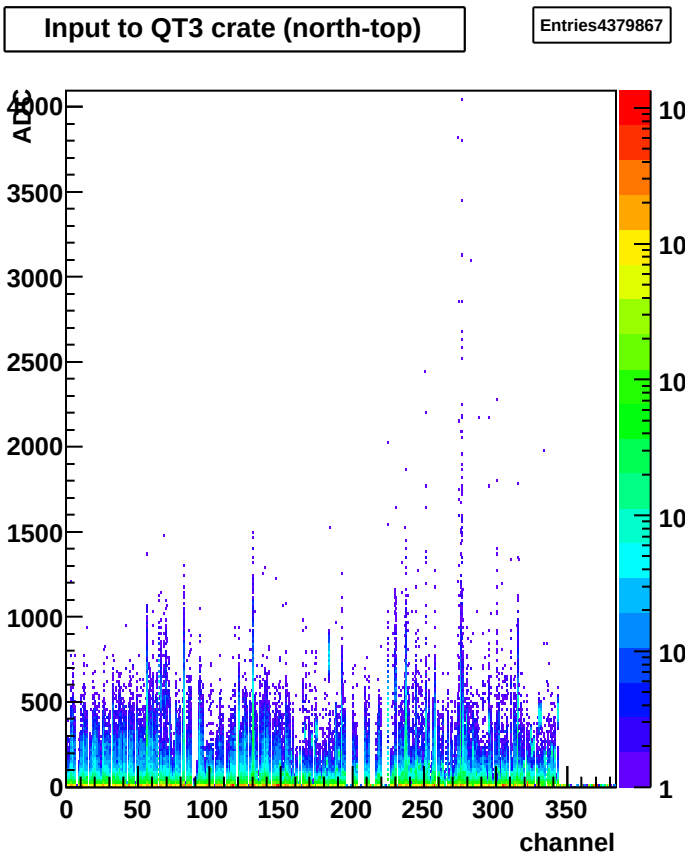

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 385591
Mean 4074
RMS 430.2

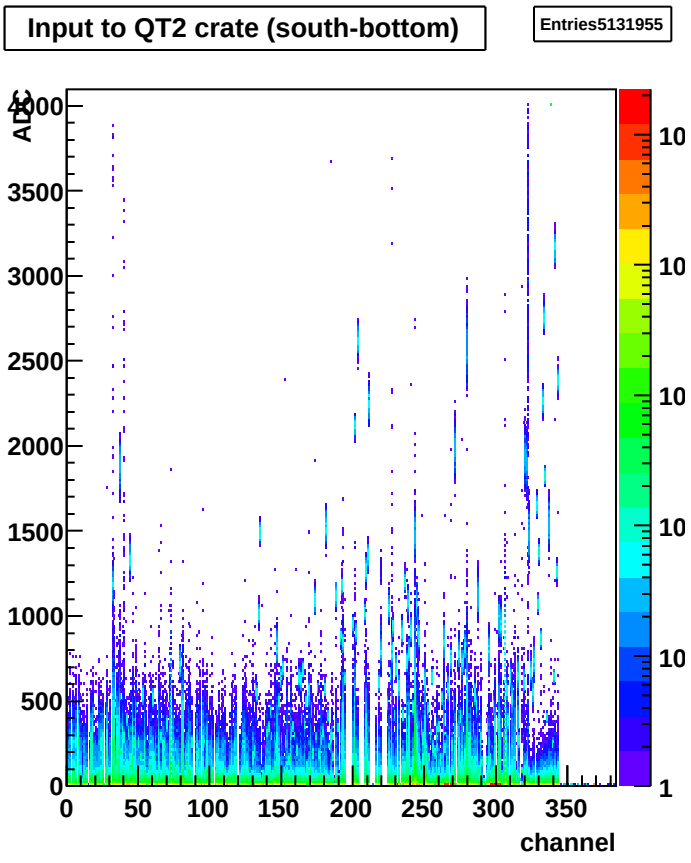

VPD-TAC-E
Entries 213895
Mean 1361
RMS 182.8

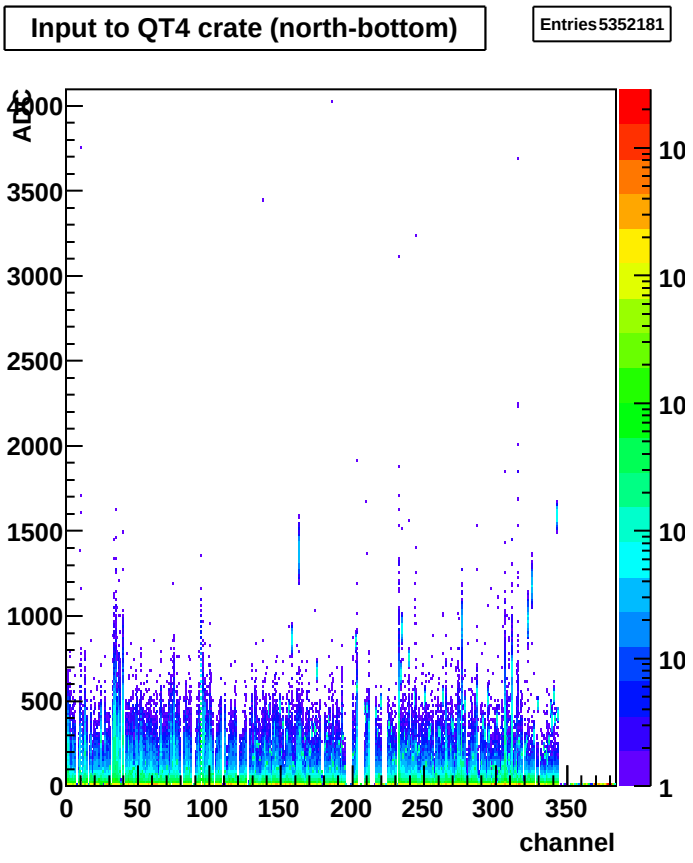

VPD-TAC-W
Entries 181311
Mean 1438
RMS 181.8

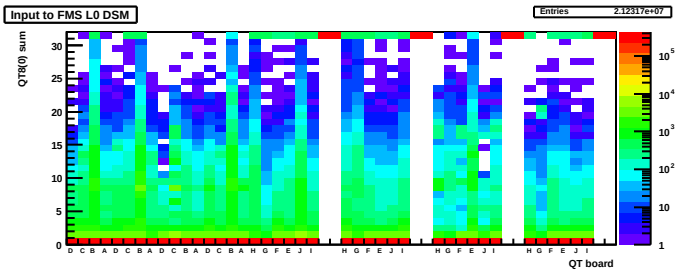

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 96772
Mean 4014
RMS 231.5

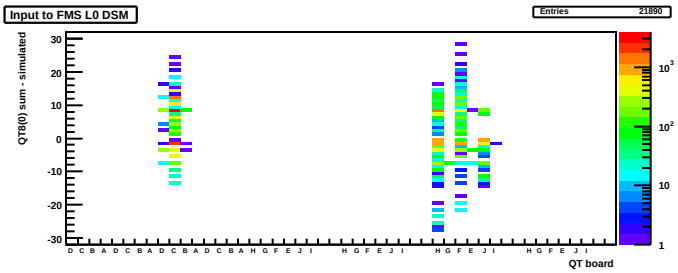

Input to QT1 crate (south-top)
Entries7624326
ADC
channel

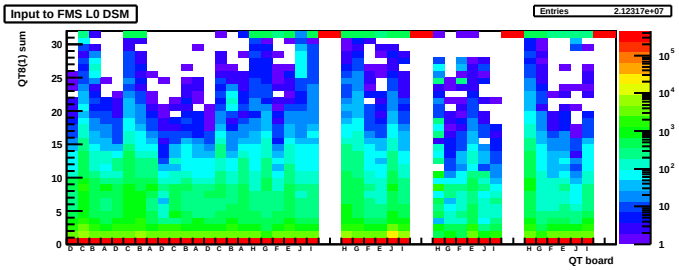

Input to QT3 crate (north-top)
Entries4379867
ADC
channel

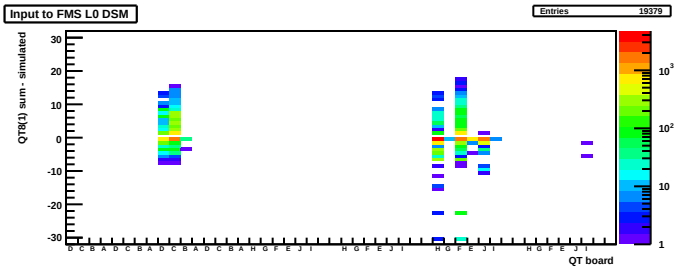

Input to QT2 crate (south-bottom)
Entries5131955
ADC
channel

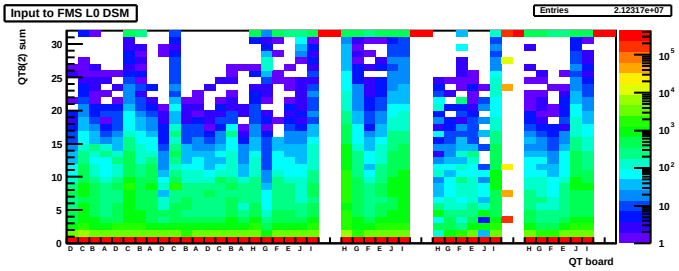

Input to QT4 crate (north-bottom)
Entries5352181
ADC
channel

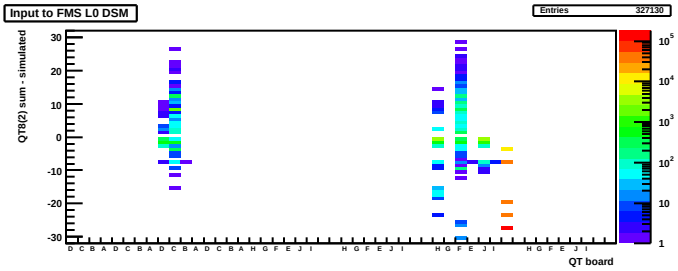

Input to FMS L0 DSM
Entries 2.12317e+07
QT8(0) sum
QT board

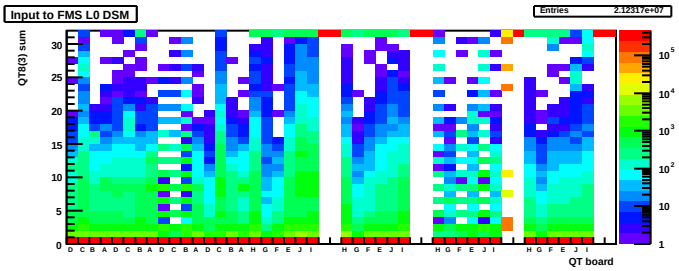

Input to FMS L0 DSM
Entries 21890
QT8(0) sum - simulated
QT board

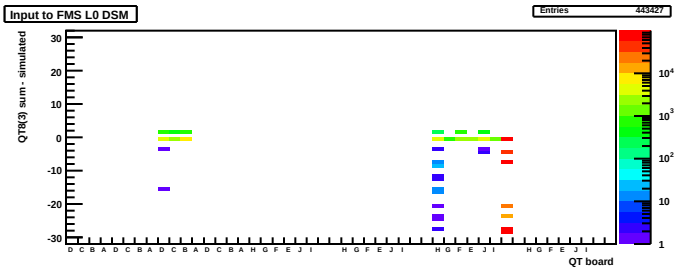

Input to FMS L0 DSM
Entries 2.12317e+07
QT8(1) sum
QT board

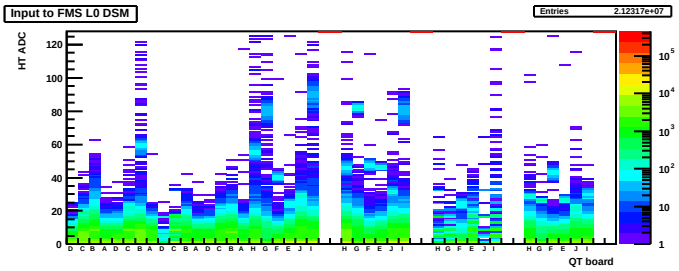

Input to FMS L0 DSM
Entries 19379
QT8(1) sum - simulated
QT board

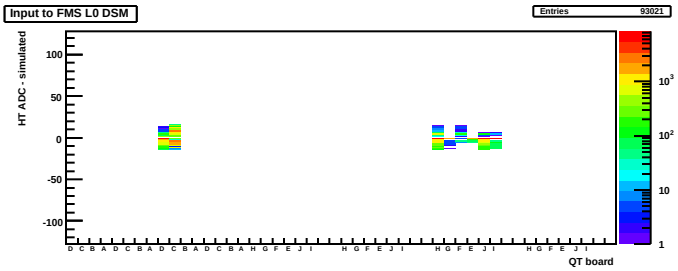

Input to FMS L0 DSM
Entries 2.12317e+07
QT8(2) sum
QT board

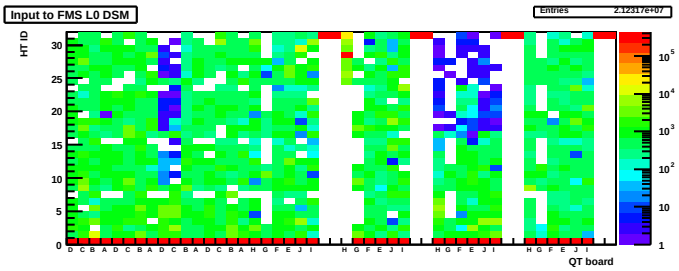

Input to FMS L0 DSM
Entries 327130
QT8(2) sum - simulated
QT board

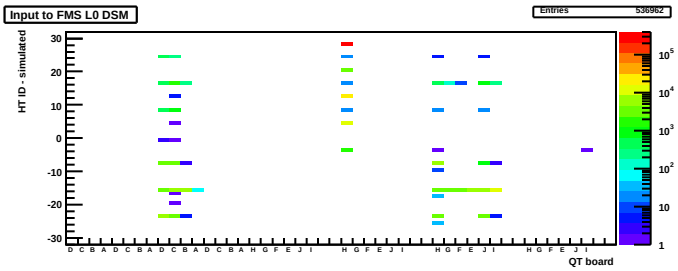

Input to FMS L0 DSM
Entries 2.12317e+07
QT8(3) sum
QT board

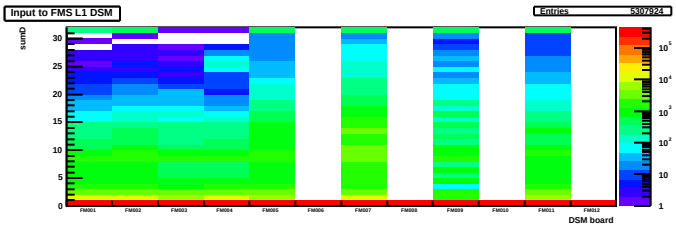

Input to FMS L0 DSM
Entries 443427
QT8(3) sum - simulated
QT board

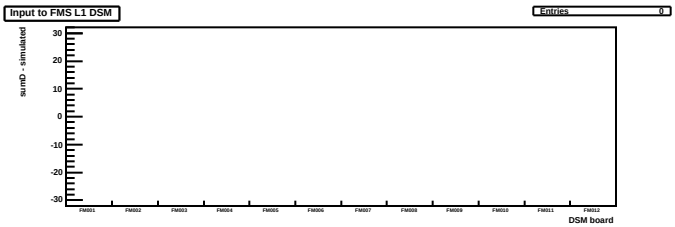

Input to FMS L0 DSM
Entries 2.12317e+07
HT ADC
QT board

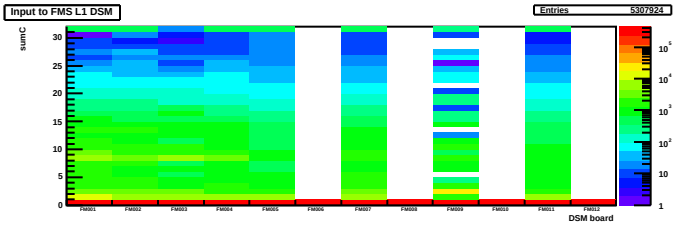

Input to FMS L0 DSM
Entries 93021
HT ADC - simulated
QT board

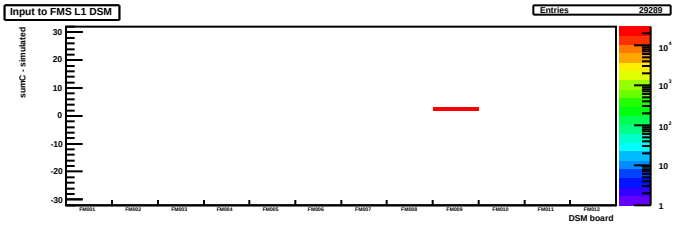

Input to FMS L0 DSM
Entries 2.12317e+07
HT ID
QT board

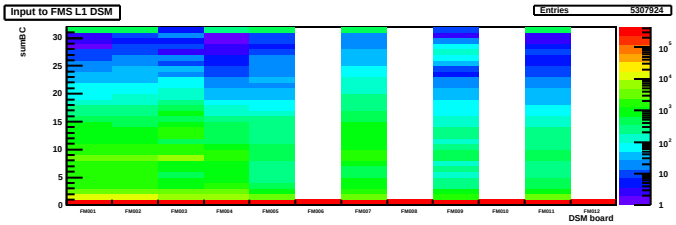

Input to FMS L0 DSM
Entries 536962
HT ID - simulated
QT board

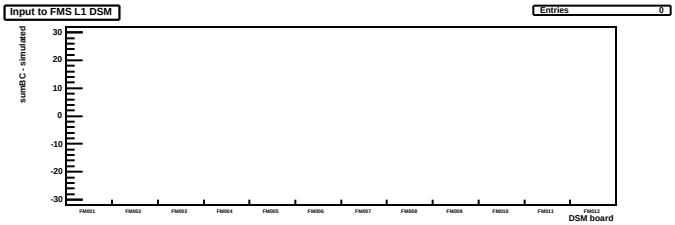

Input to FMS L1 DSM
Entries 5307924
sumD
DSM board

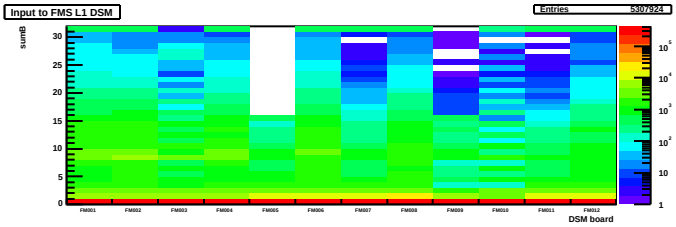

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

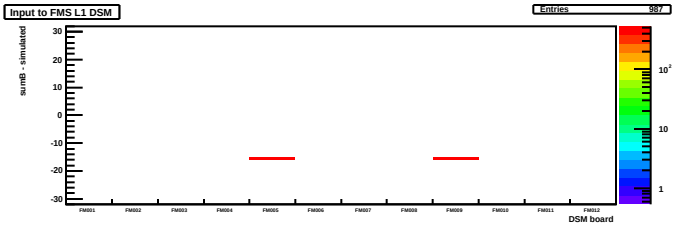

Input to FMS L1 DSM
Entries 5307924
sumC
DSM board

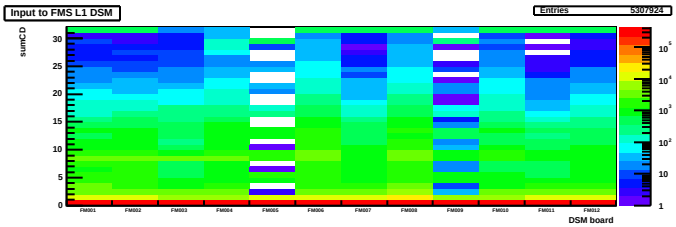

Input to FMS L1 DSM
Entries 29289
sumC - simulated
DSM board

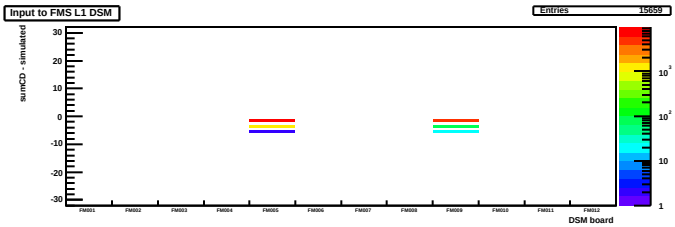

Input to FMS L1 DSM
Entries 5307924
sumBC
DSM board

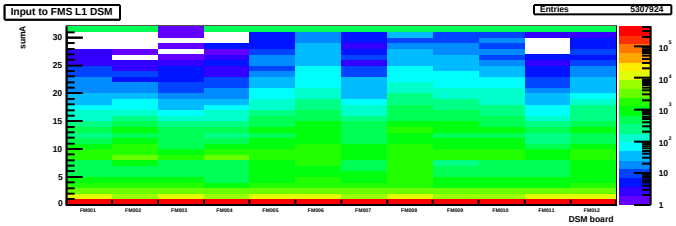

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

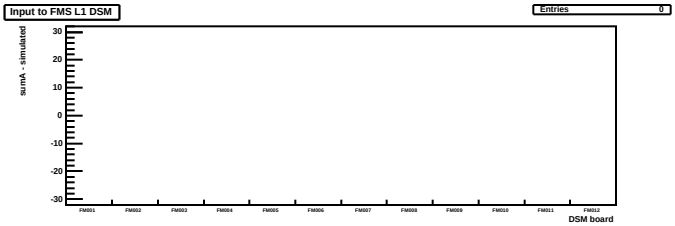

Input to FMS L1 DSM
Entries 5307924
sumB
DSM board

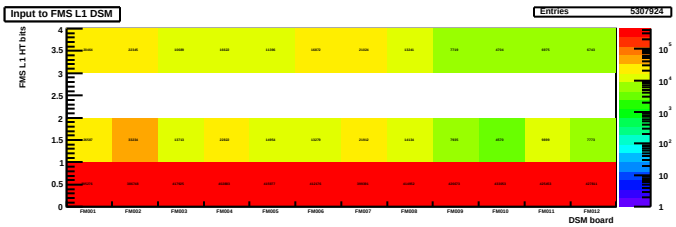

Input to FMS L1 DSM
Entries 987
sumB - simulated
DSM board

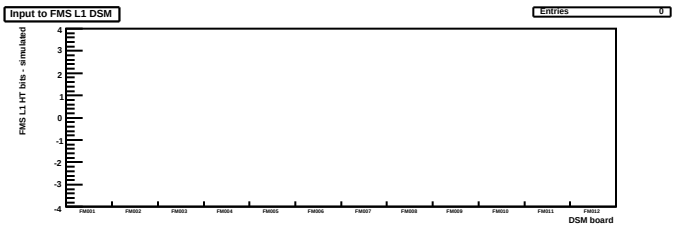

Input to FMS L1 DSM
Entries 5307924
sumCD
DSM board

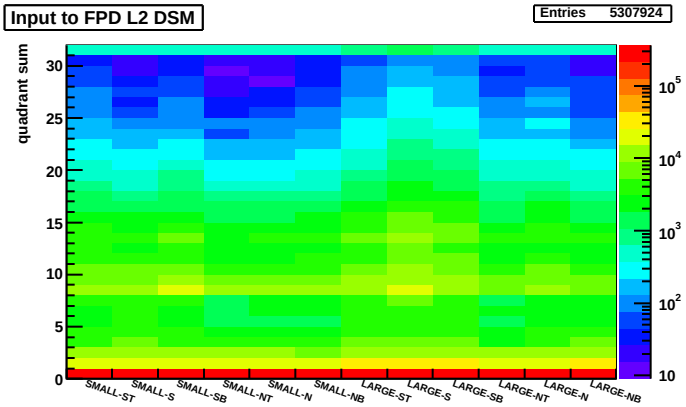

Input to FMS L1 DSM
Entries 15659
sumCD - simulated
DSM board

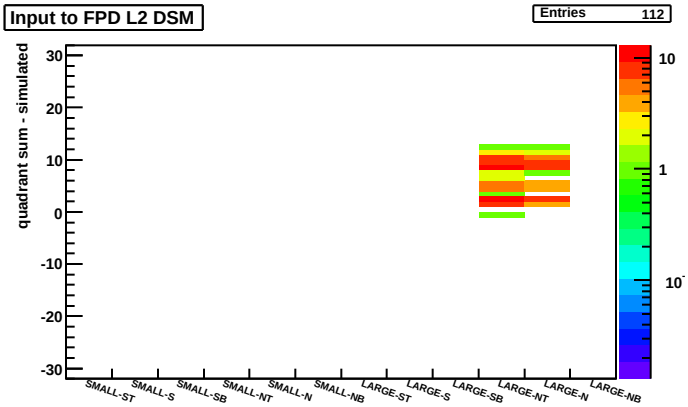

Input to FMS L1 DSM
Entries 5307924
sumA
DSM board

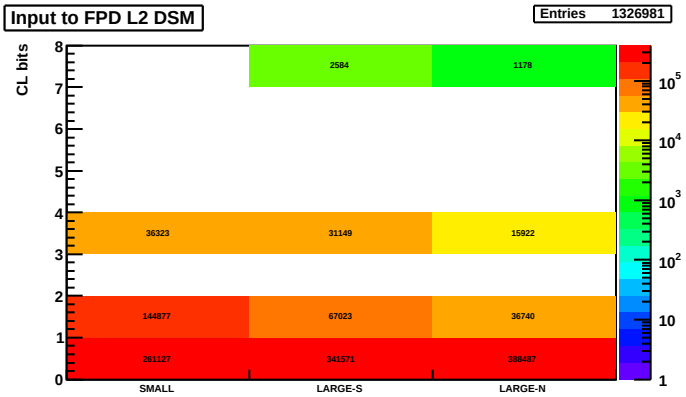

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

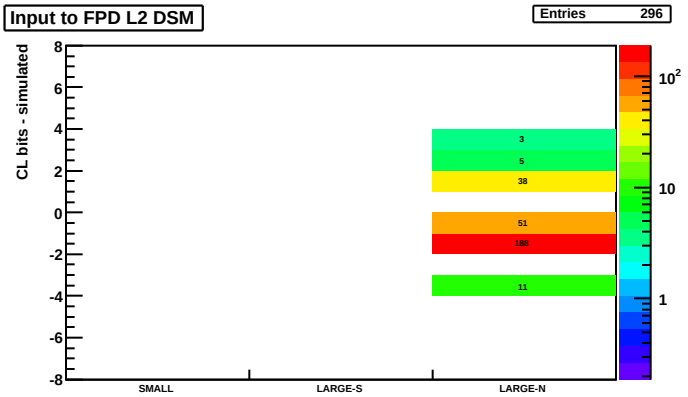

Input to FMS L1 DSM
Entries 5307924
FMS L1 HT bits
DSM board

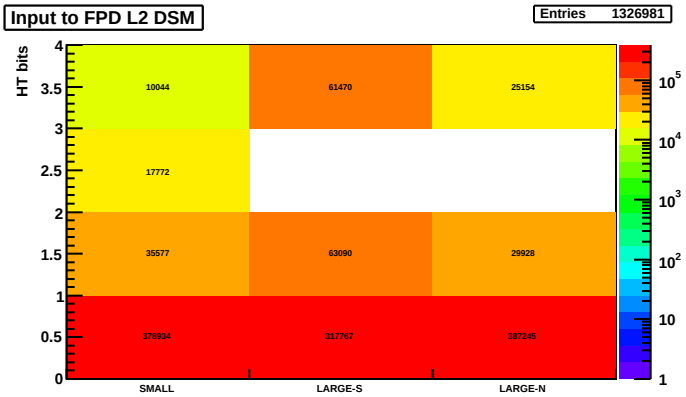

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

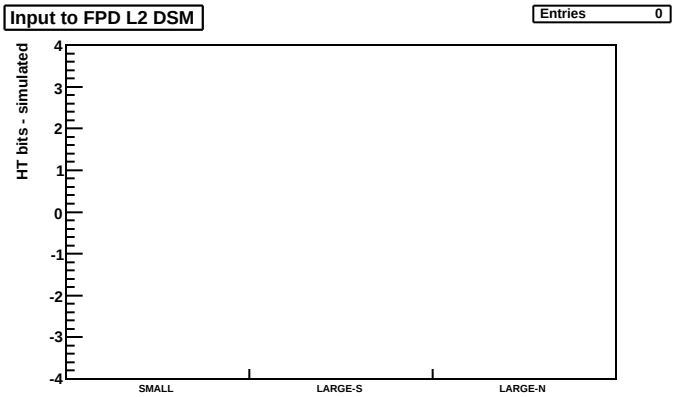

Input to FPD L2 DSM
Entries 5307924
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

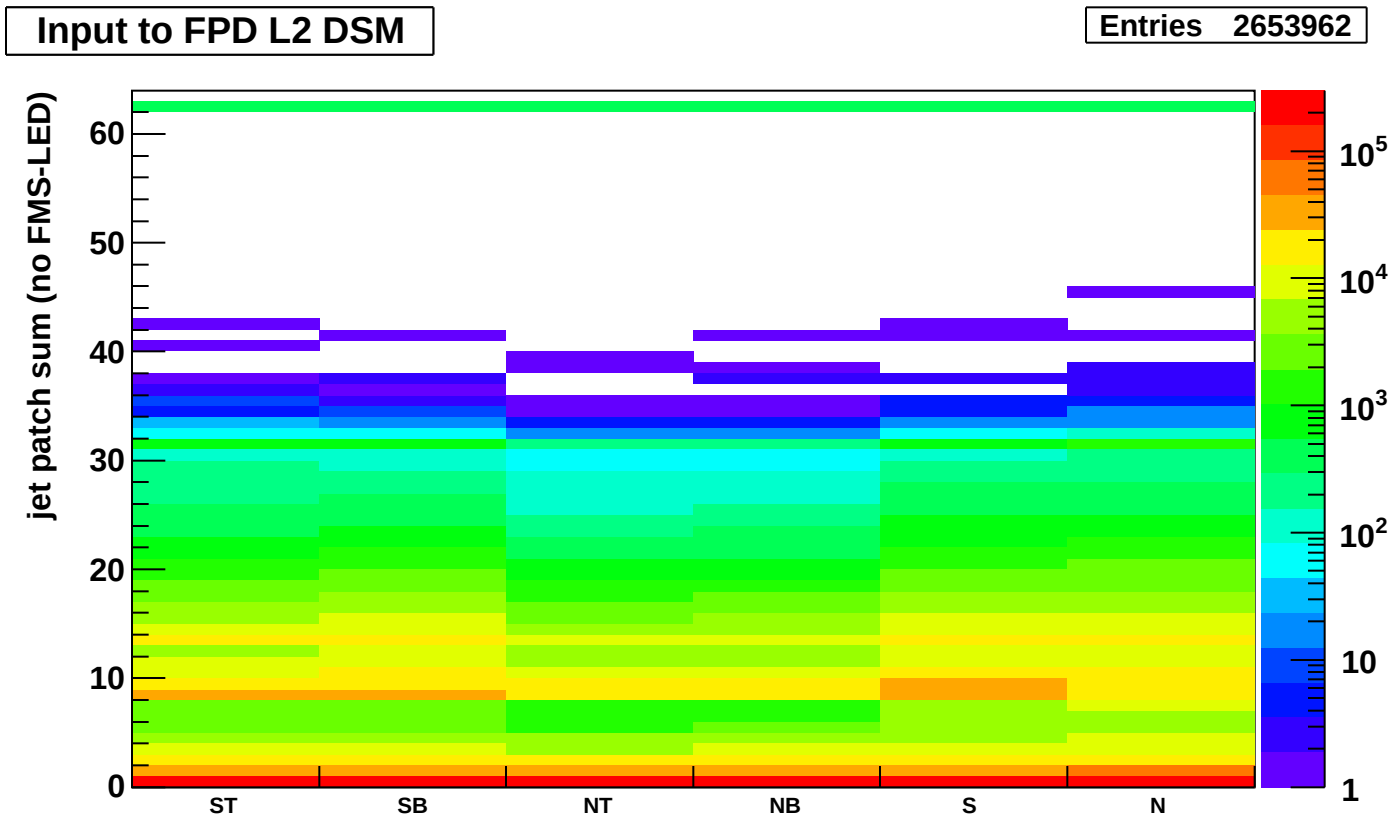

Input to FPD L2 DSM
Entries 112
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

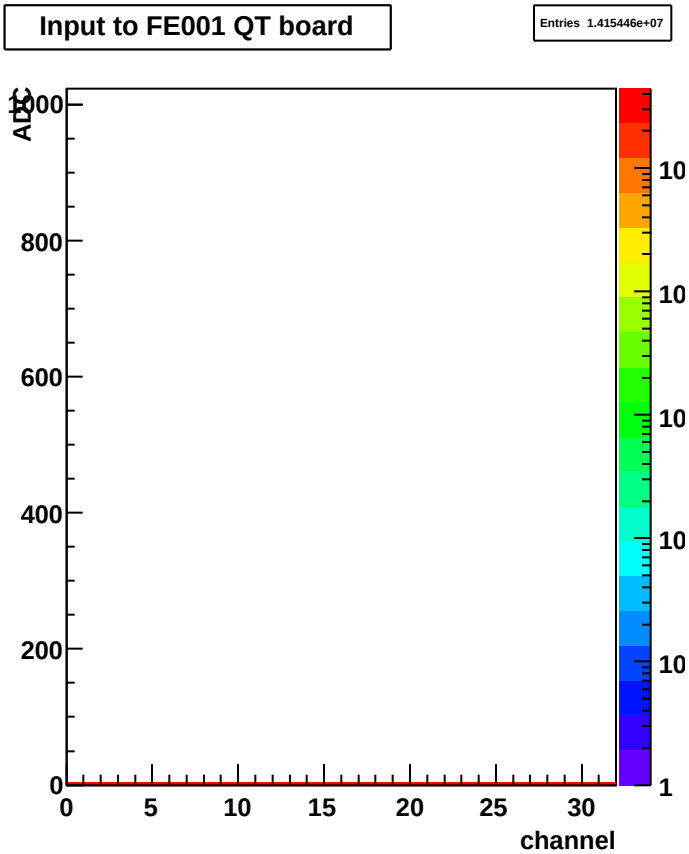

Input to FPD L2 DSM
Entries 1326981
CL bits
2584
1178
36323
31149
15922
144877
67023
36740
261127
341571
388487
SMALL
LARGE-S
LARGE-N

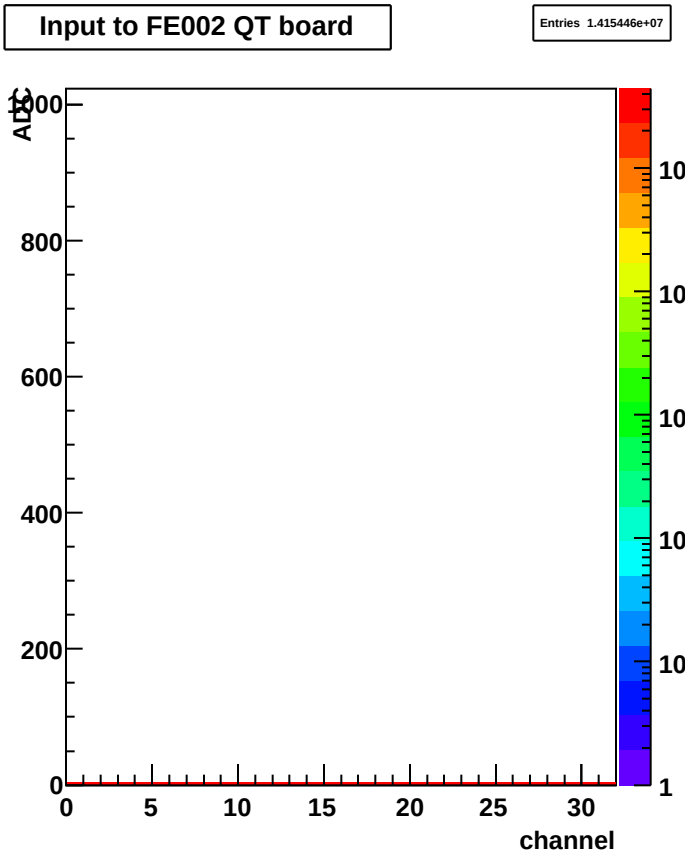

Input to FPD L2 DSM
Entries 296
CL bits - simulated
3
5
38
51
188
11
SMALL
LARGE-S
LARGE-N

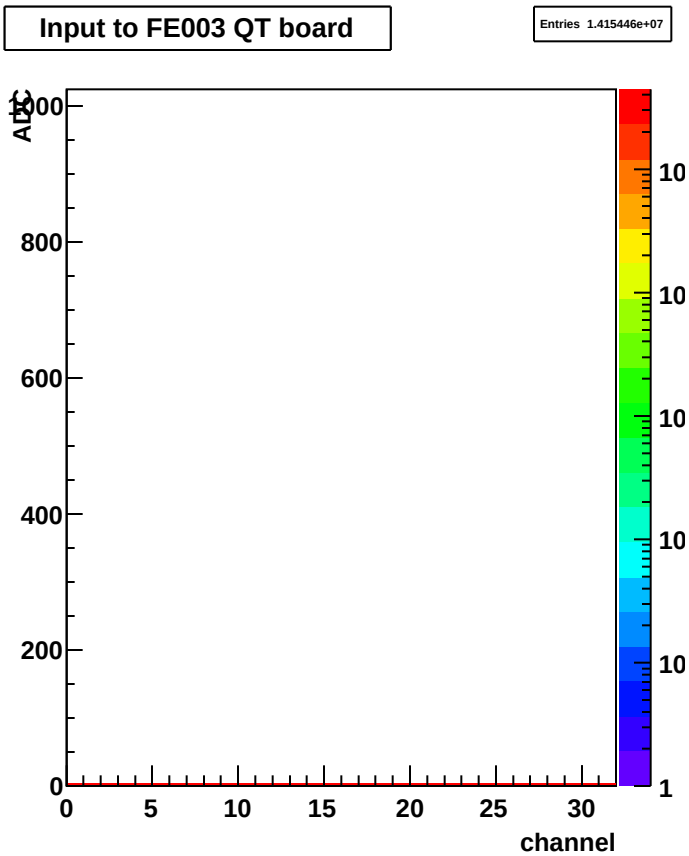

Input to FPD L2 DSM
Entries 1326981
HT bits
10044
61470
25154
17772
35577
63090
29928
378934
317767
387245
SMALL
LARGE-S
LARGE-N

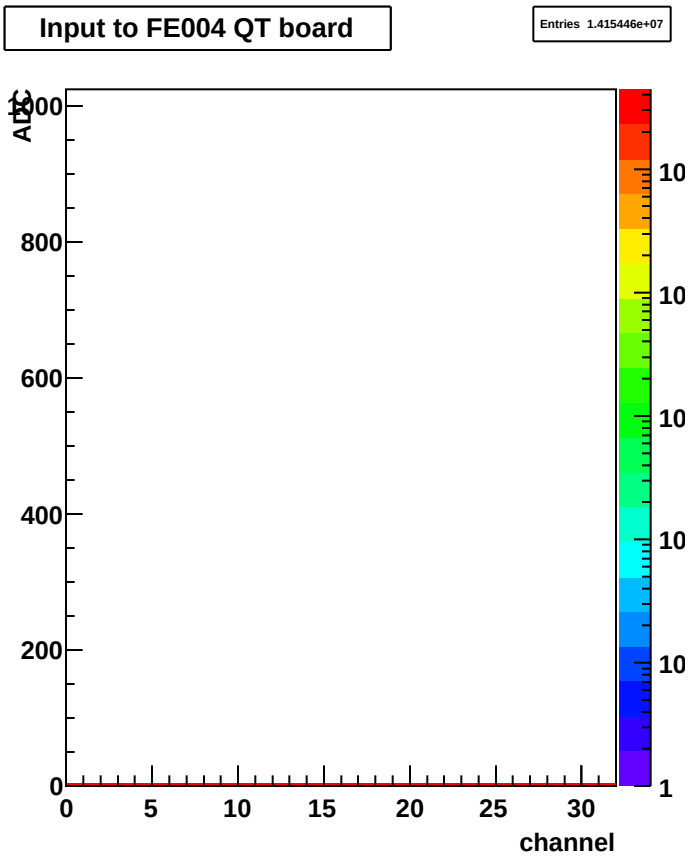

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

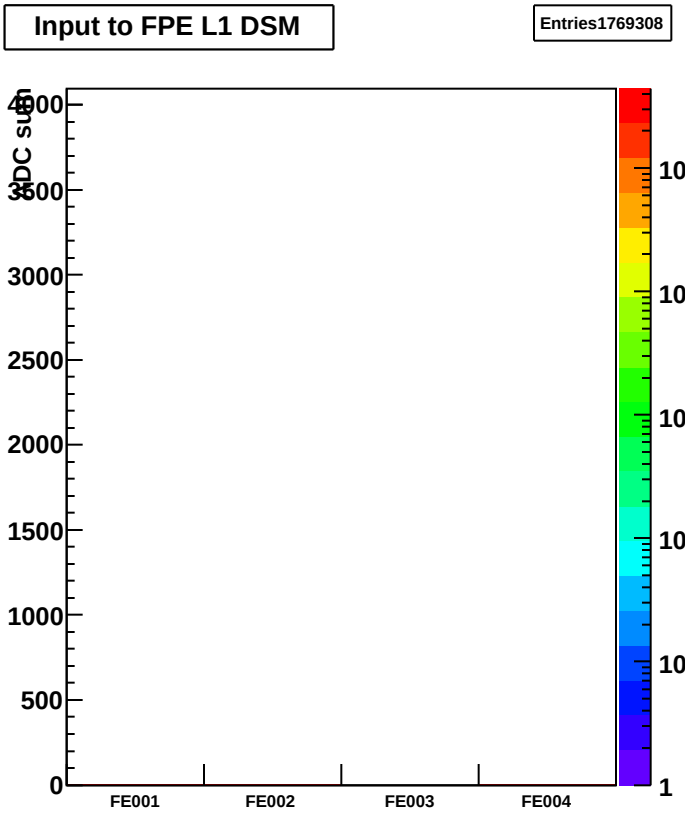

Input to FPD L2 DSM
Entries 2653962
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

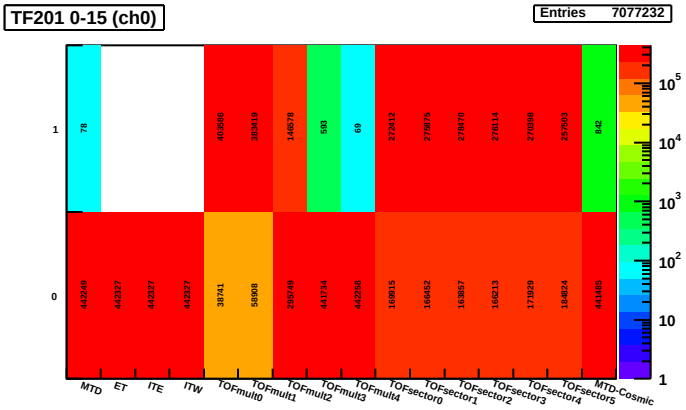

Input to FE001 QT board
Entries 1.415446e+07
ADC
channel

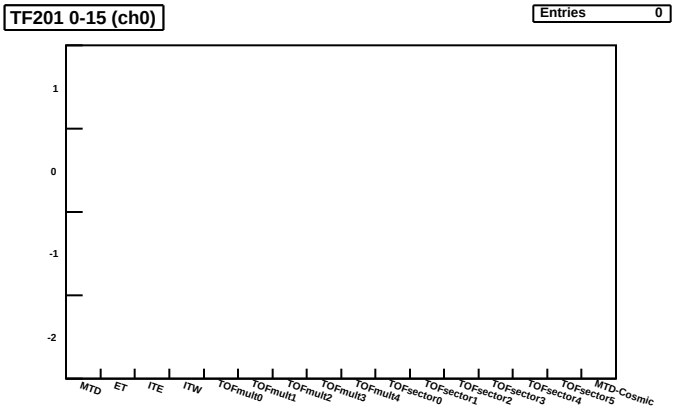

Input to FE002 QT board
Entries 1.415446e+07
ADC
channel

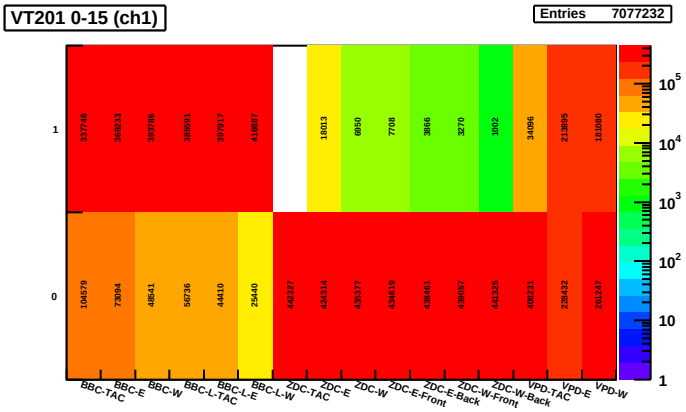

Input to FE003 QT board
Entries 1.415446e+07
ADC
channel

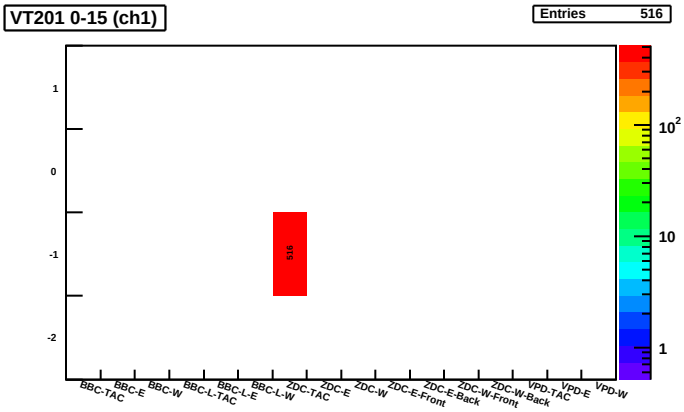

Input to FE004 QT board
Entries 1.415446e+07
ADC
channel

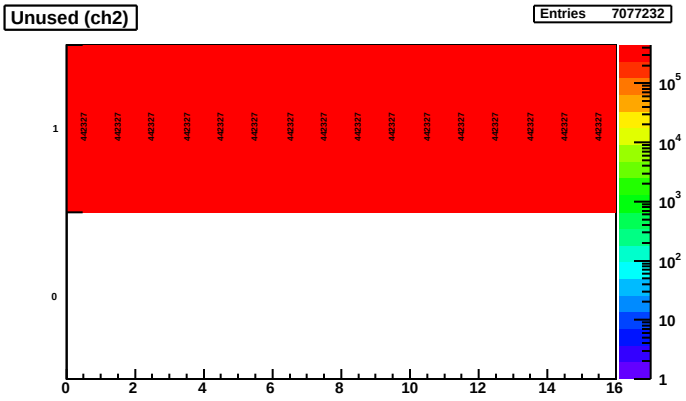

Input to FPE L1 DSM
Entries1769308
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

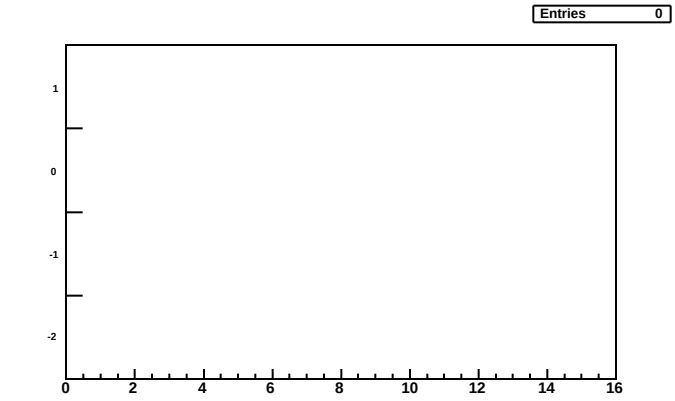

TF201 0-15 (ch0)
Entries 7077232
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 7077232
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 516
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 7077232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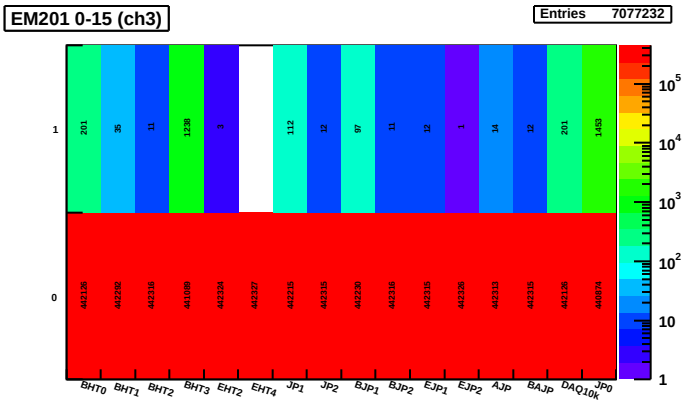
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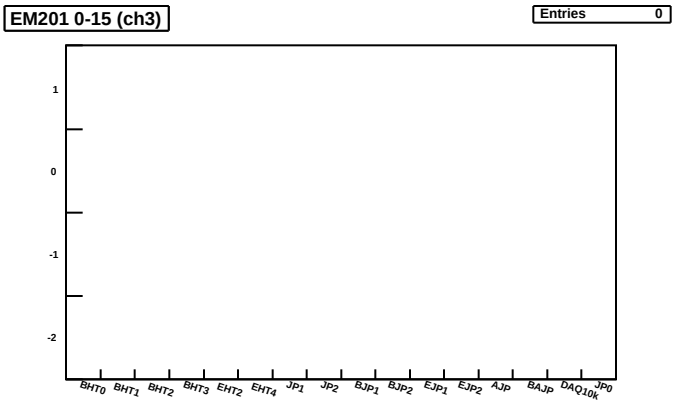
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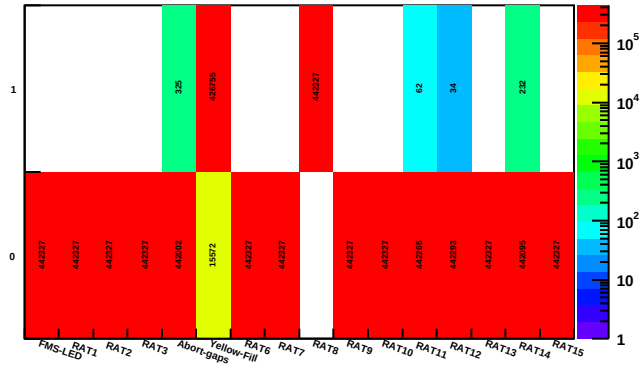
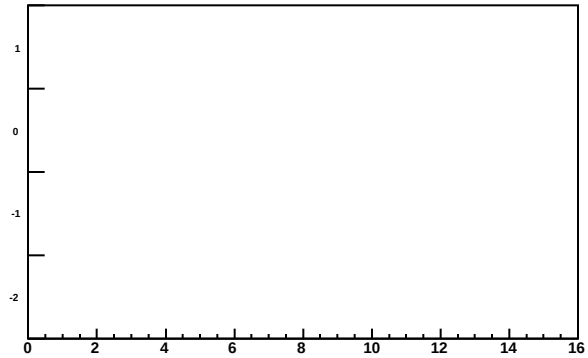
RAT board (ch4)





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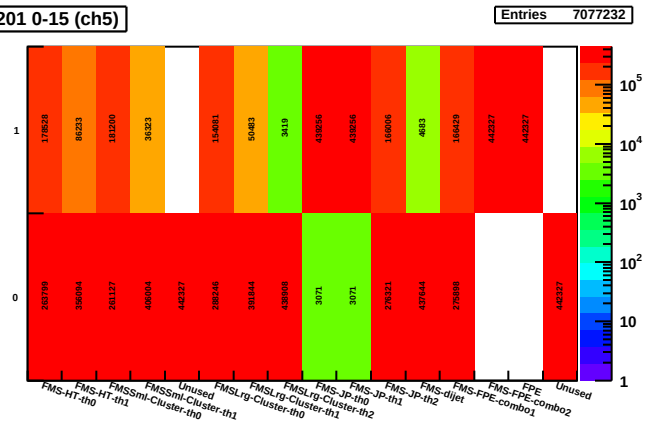

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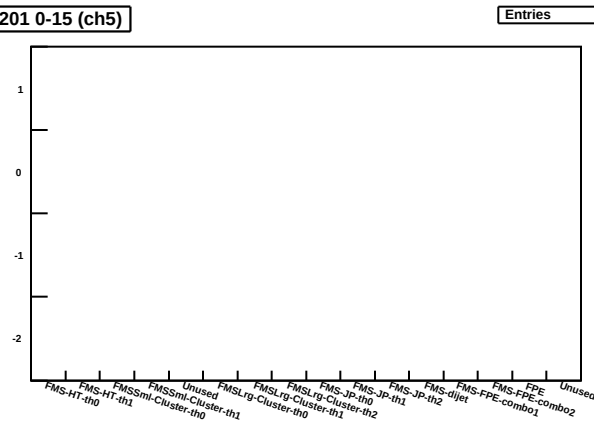
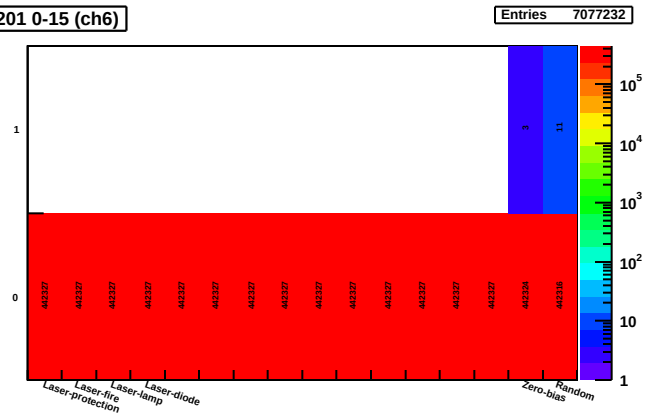
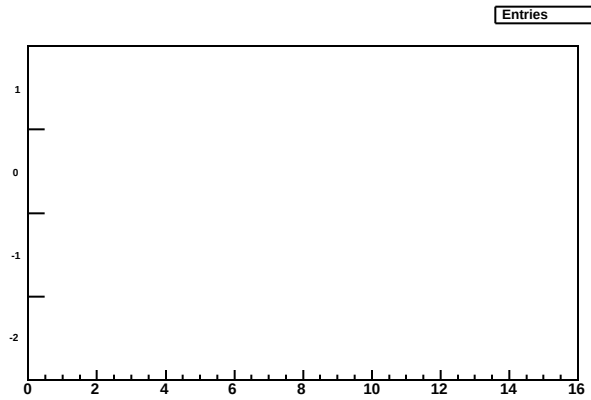
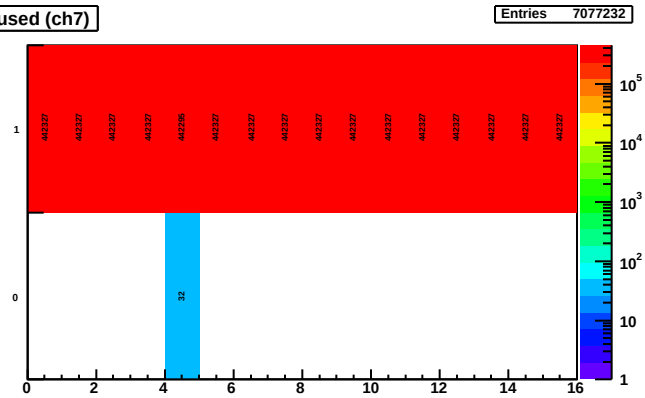
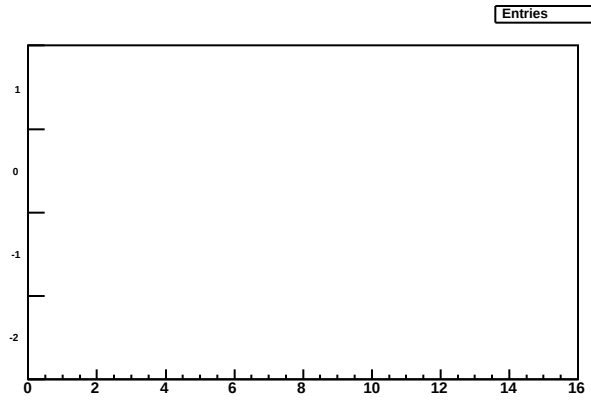


ST201 0-15 (ch6)





Unused (ch7)