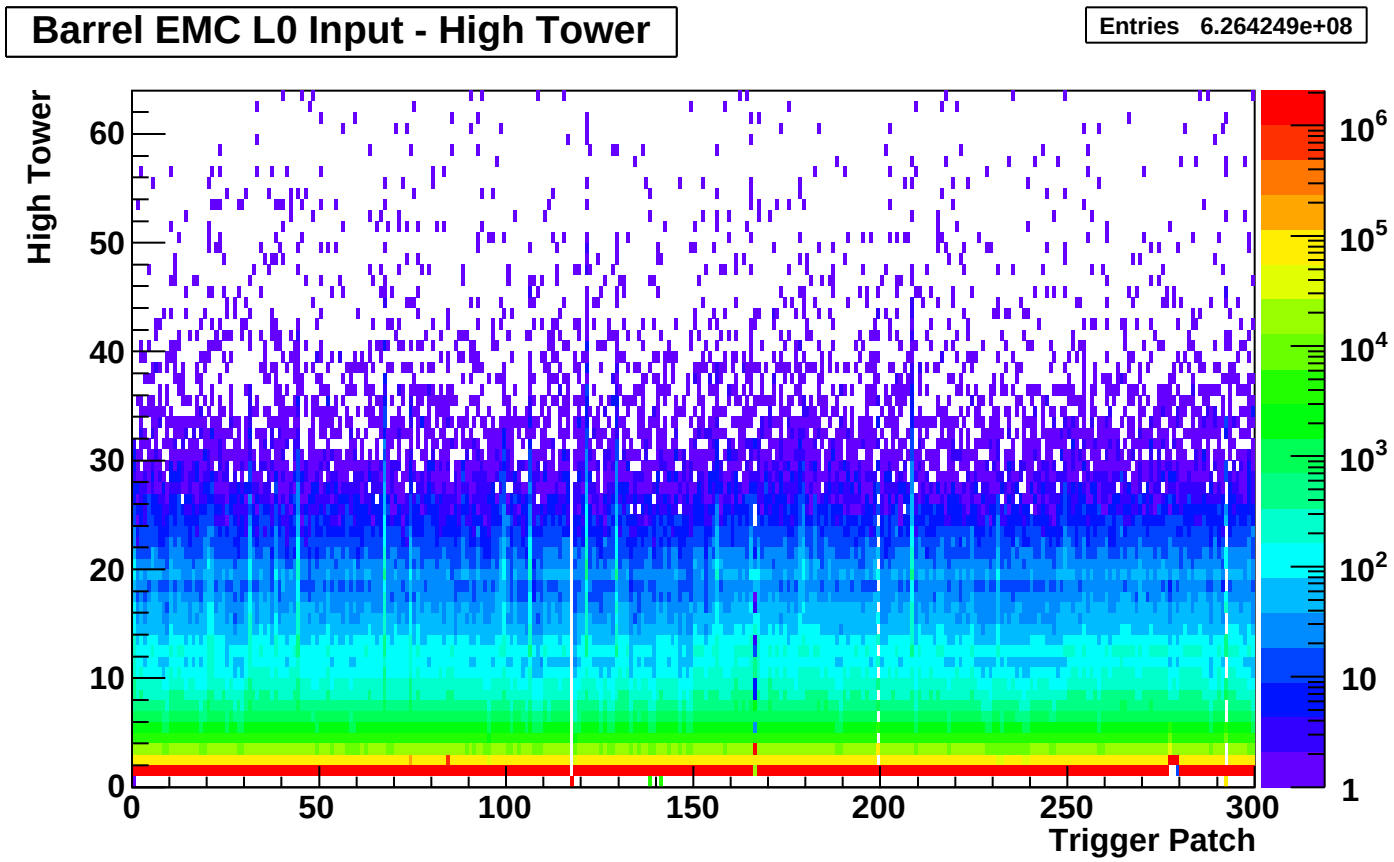
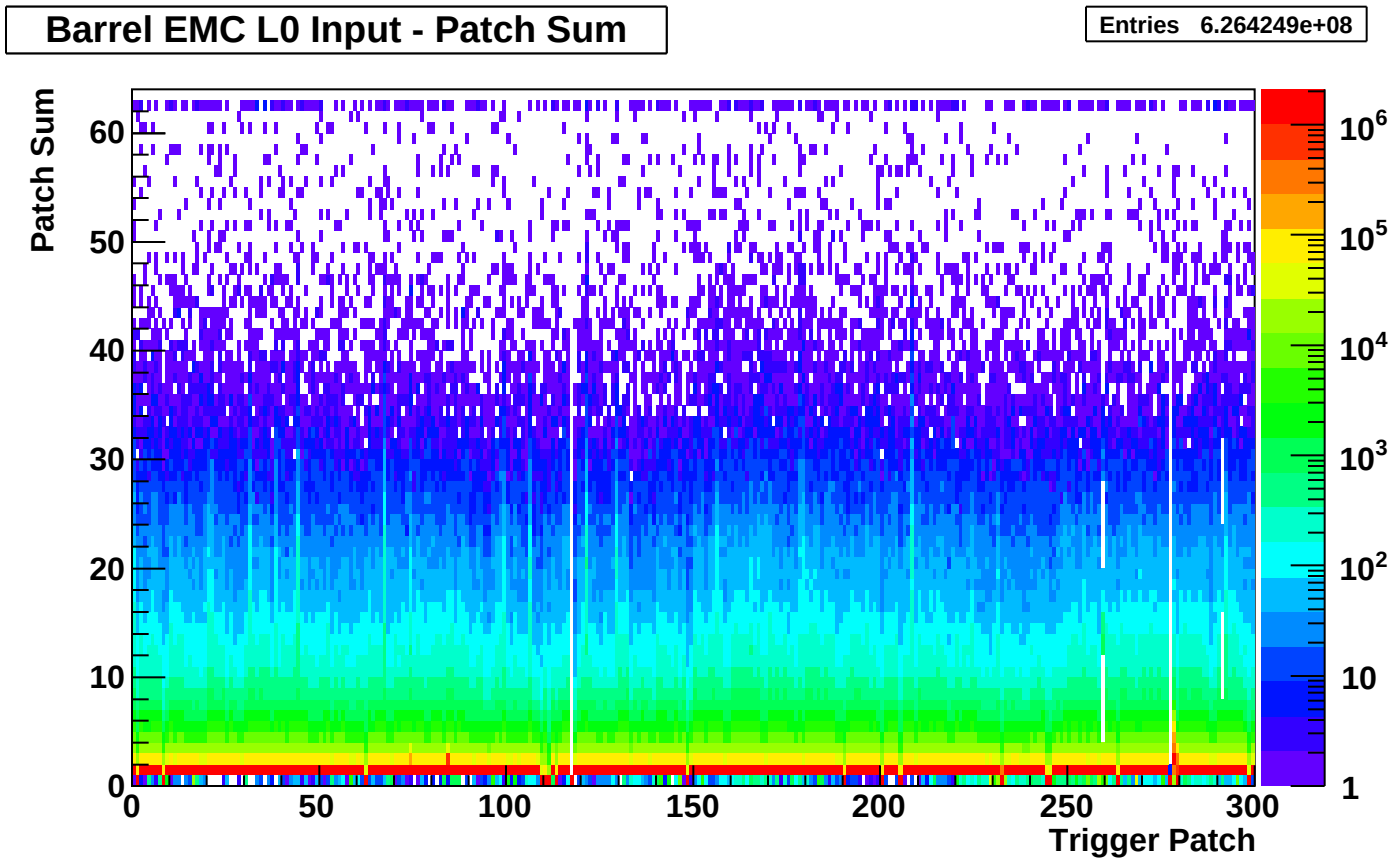
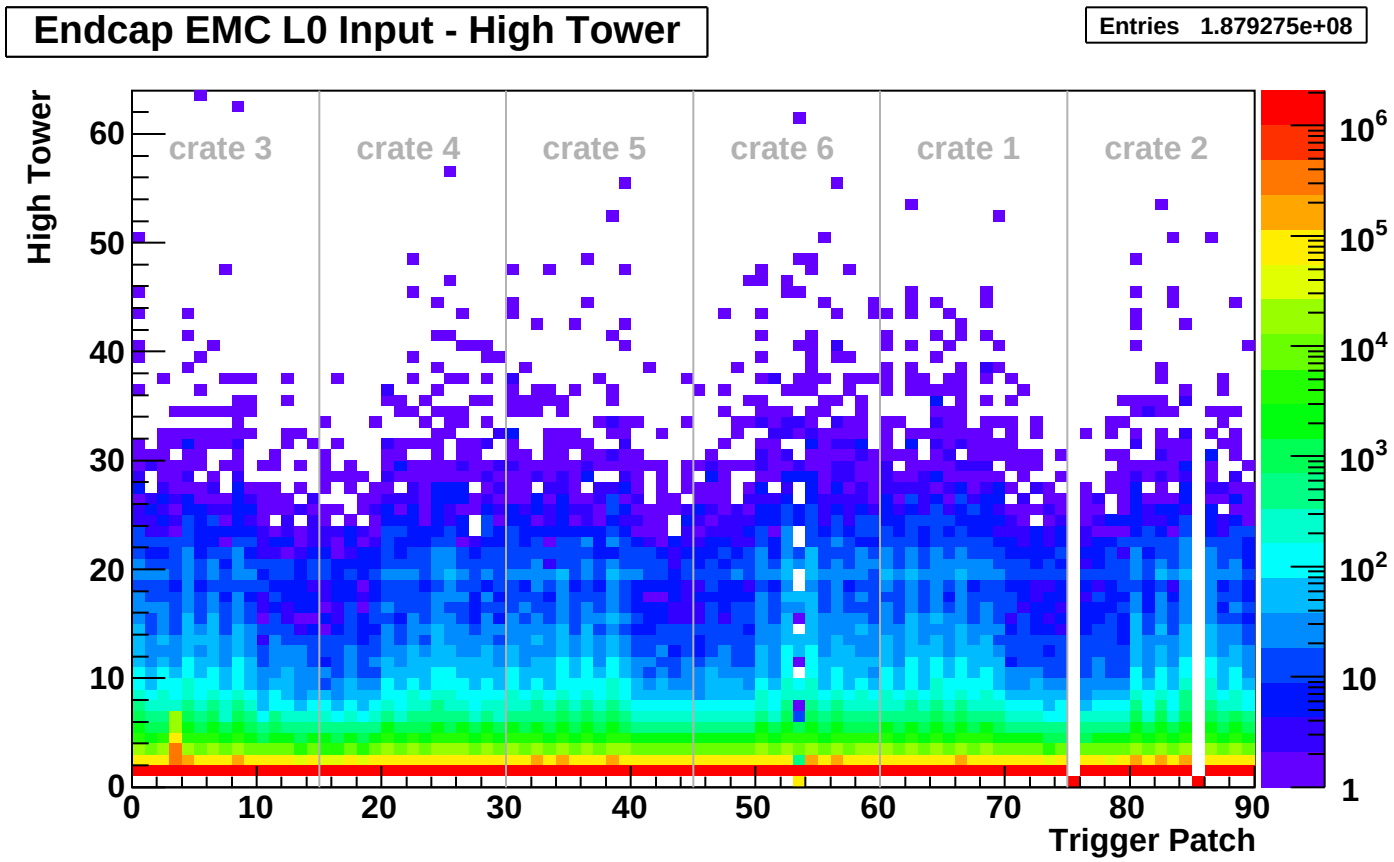
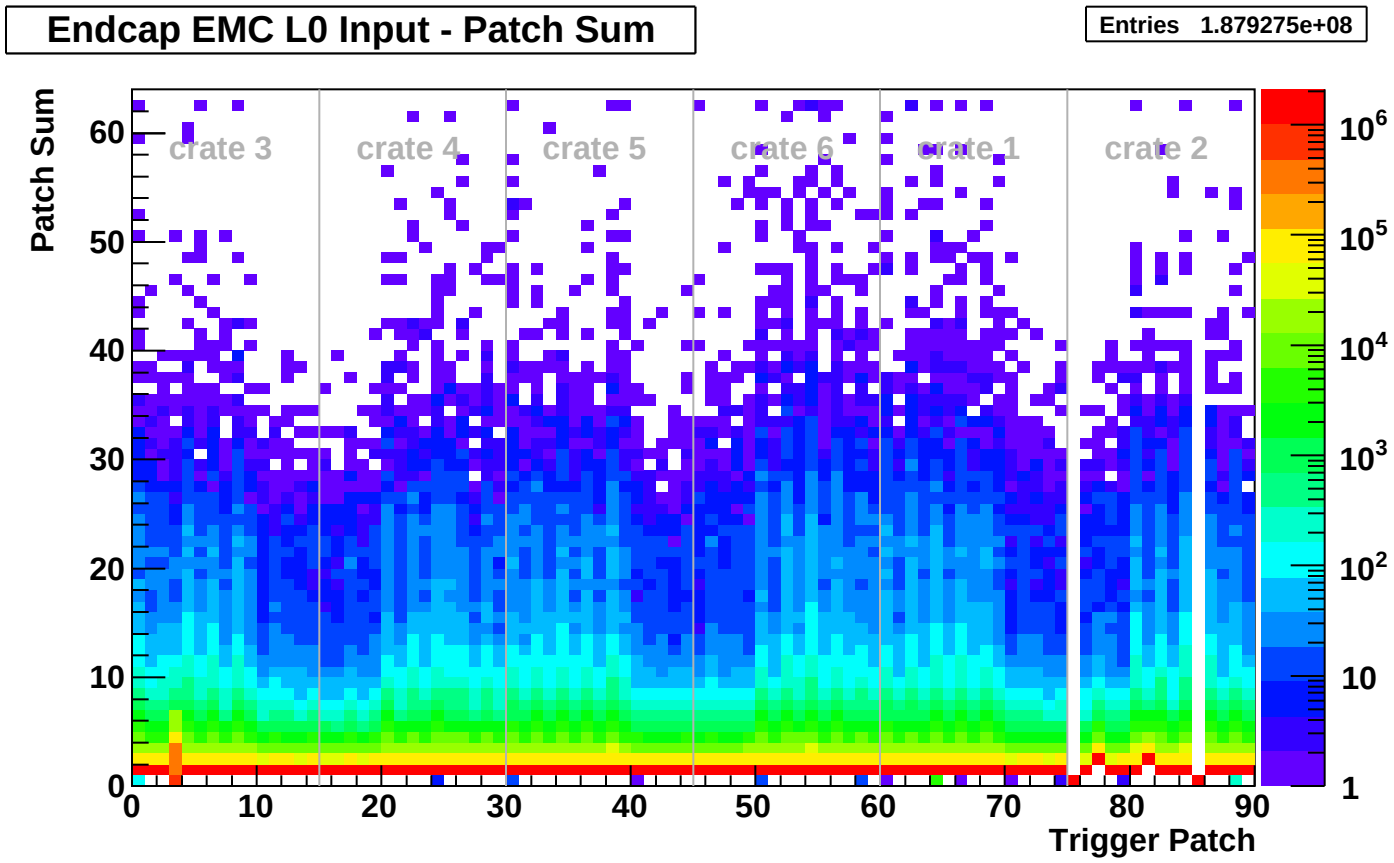
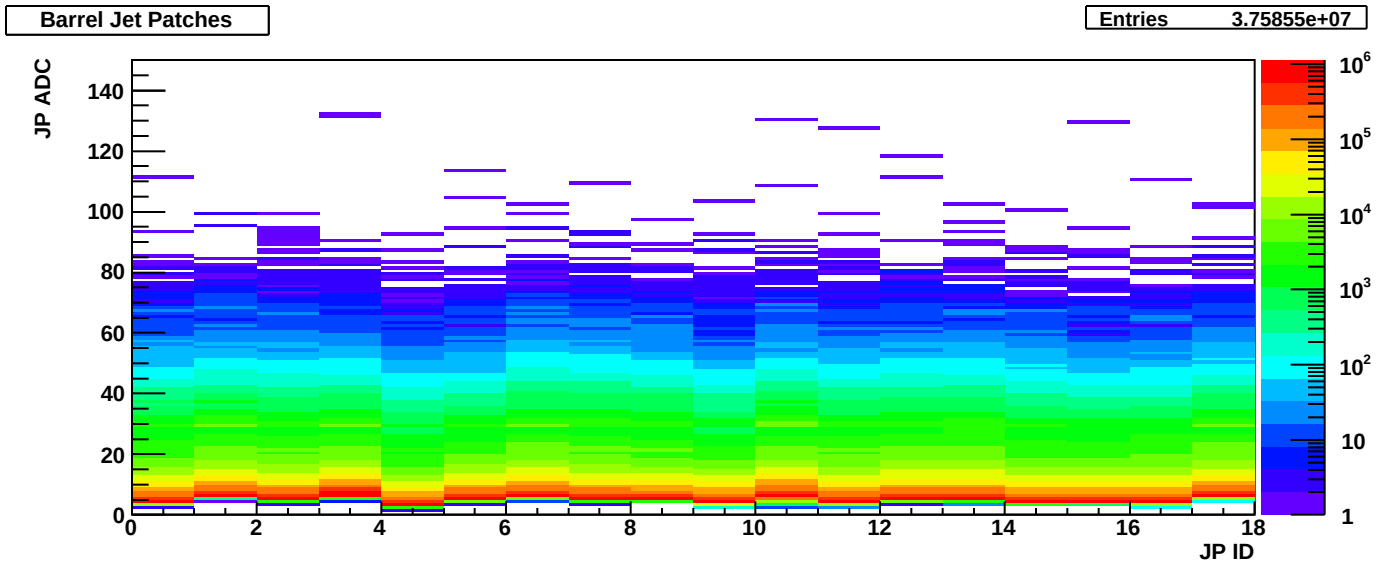
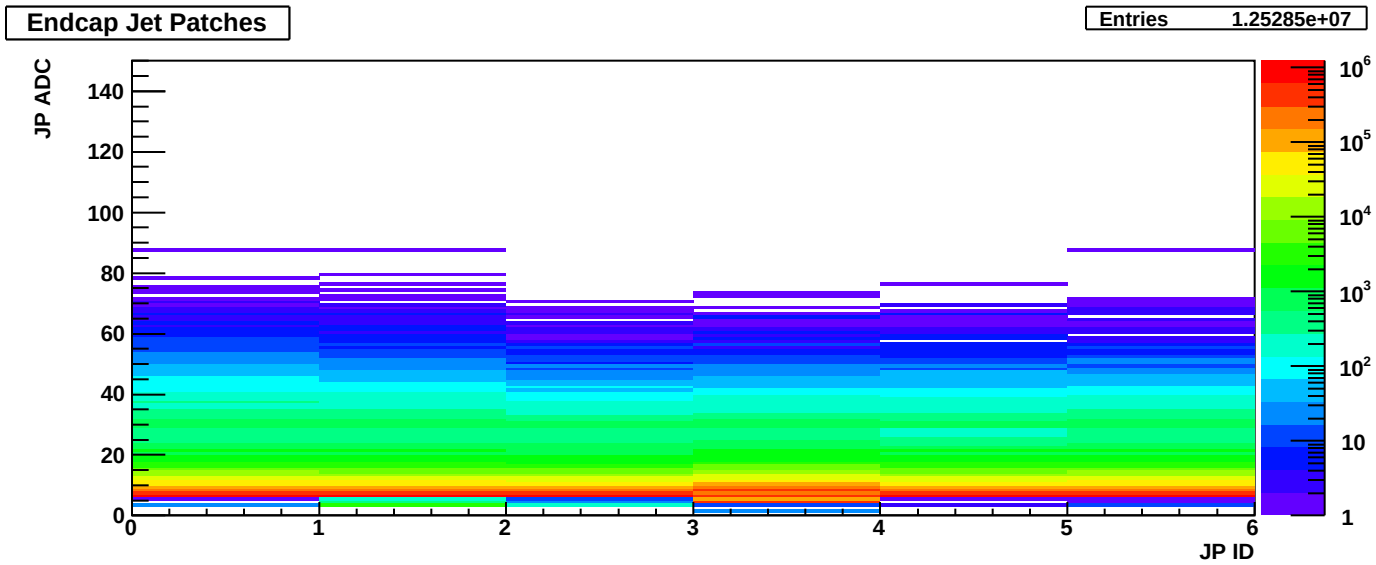

Barrel EMC L0 Input - High Tower
Entries 6.264249e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

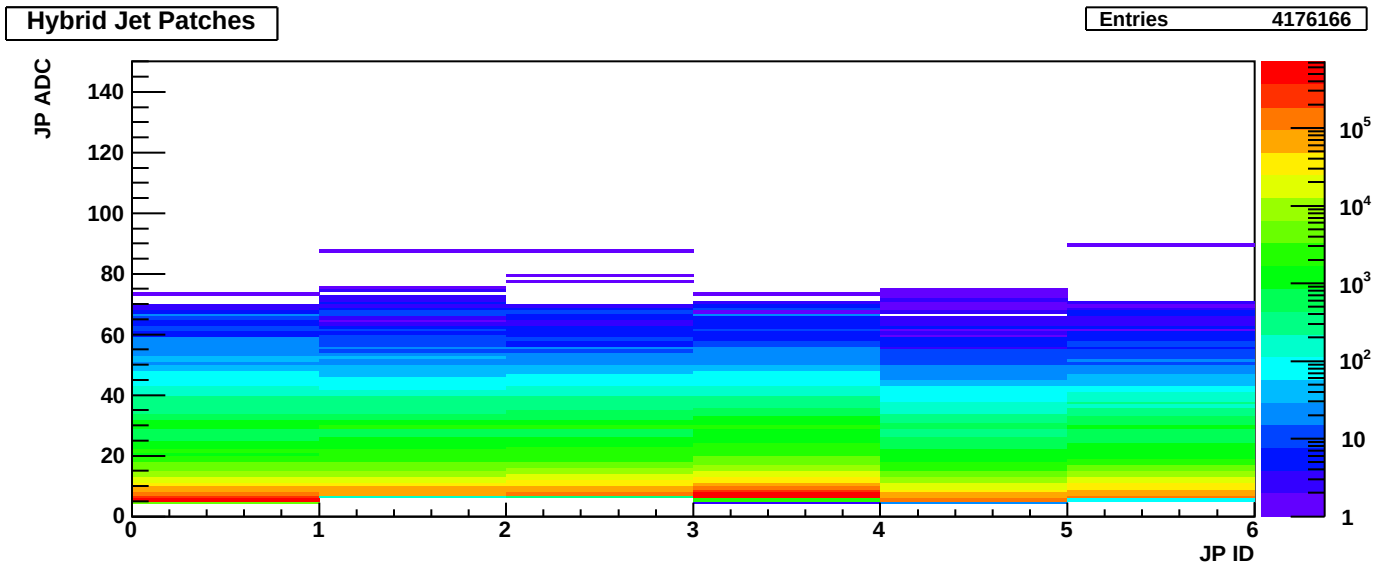

Barrel EMC L0 Input - Patch Sum
Entries 6.264249e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

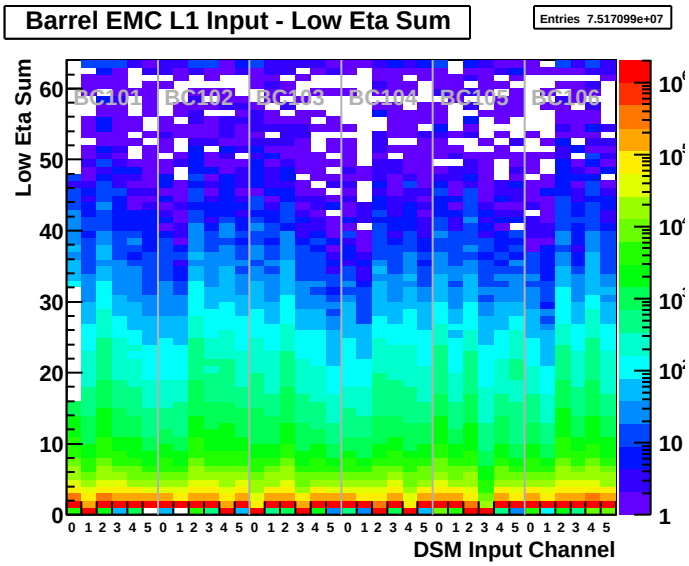

Endcap EMC L0 Input - High Tower
Entries 1.879275e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

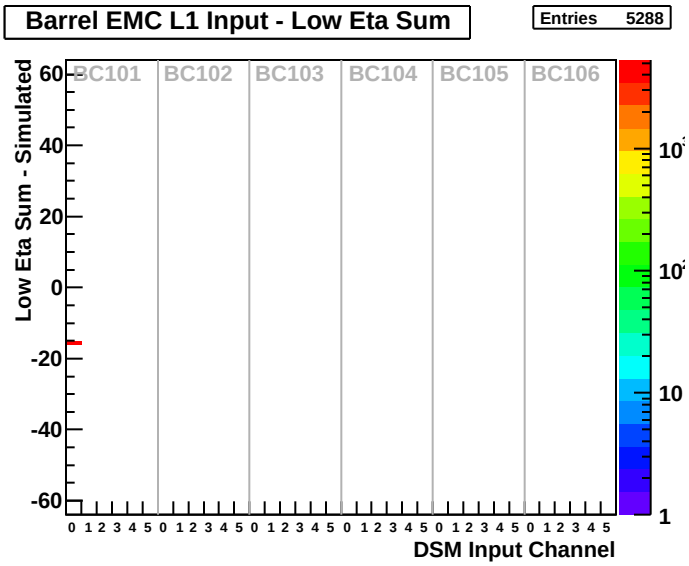

Endcap EMC L0 Input - Patch Sum
Entries 1.879275e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

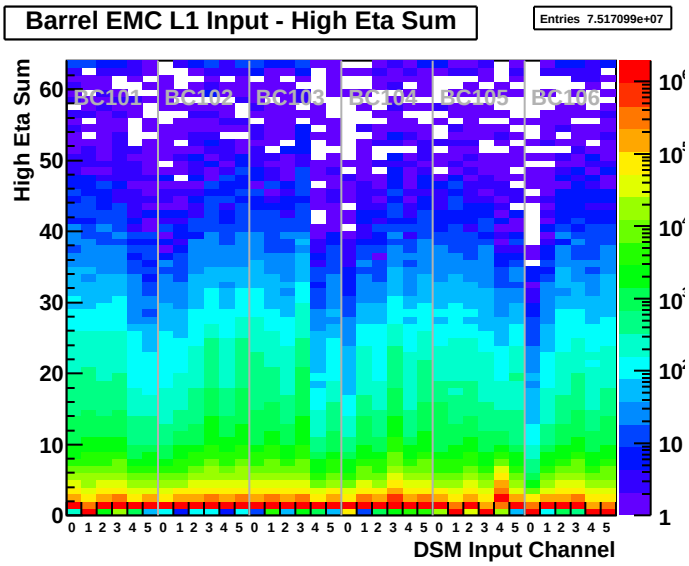

Barrel Jet Patches
Entries 3.75855e+07
JP ADC
JP ID

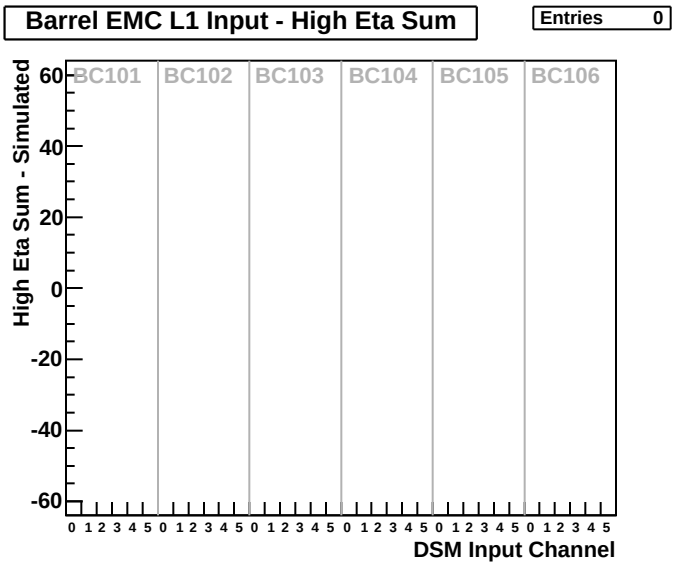

Endcap Jet Patches
Entries 1.25285e+07
JP ADC
JP ID

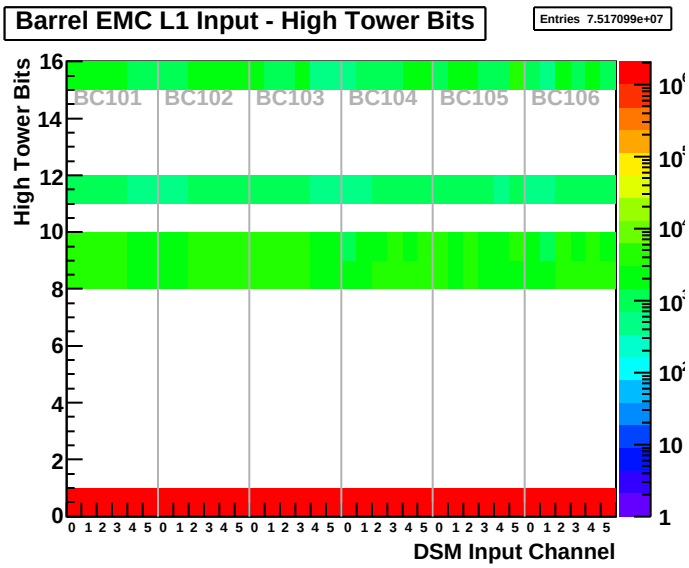

Hybrid Jet Patches
Entries 4176166
JP ADC
JP ID

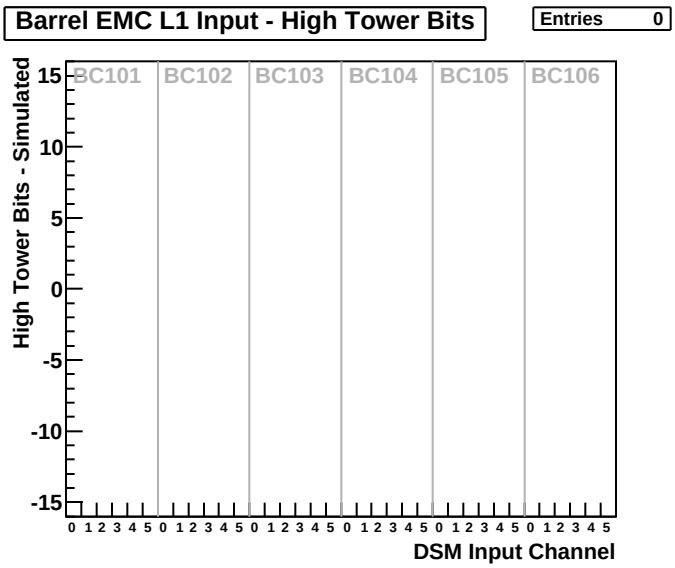

Barrel EMC L1 Input - Low Eta Sum
Entries 7.517099e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

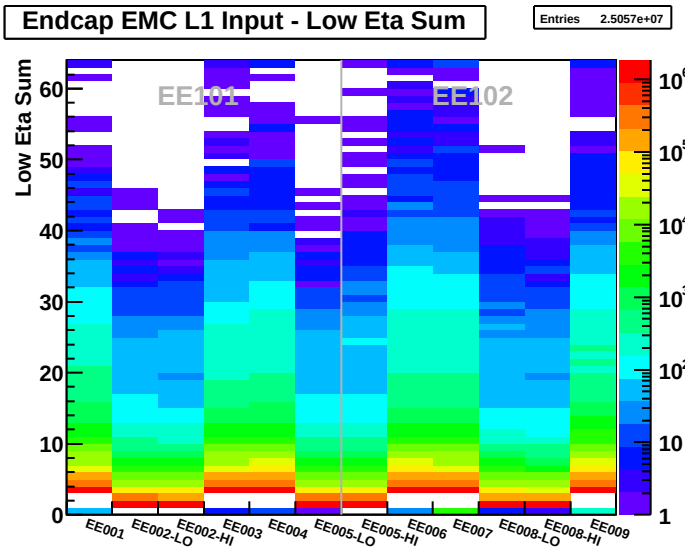

Barrel EMC L1 Input - Low Eta Sum
Entries 5288
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

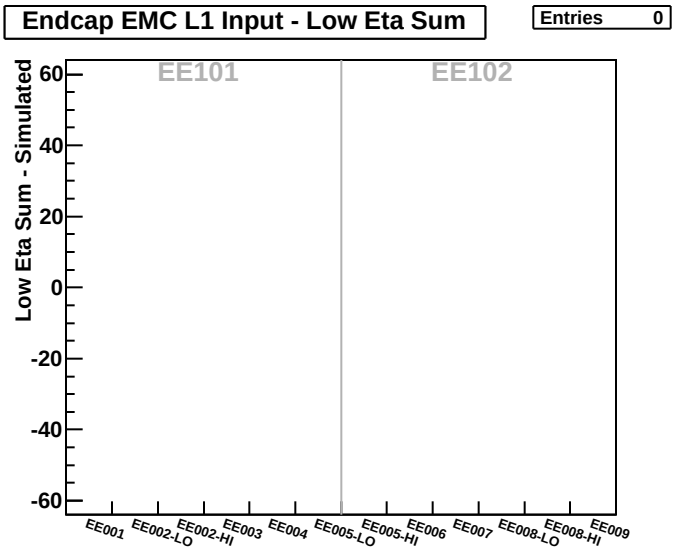

Barrel EMC L1 Input - High Eta Sum
Entries 7.517099e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

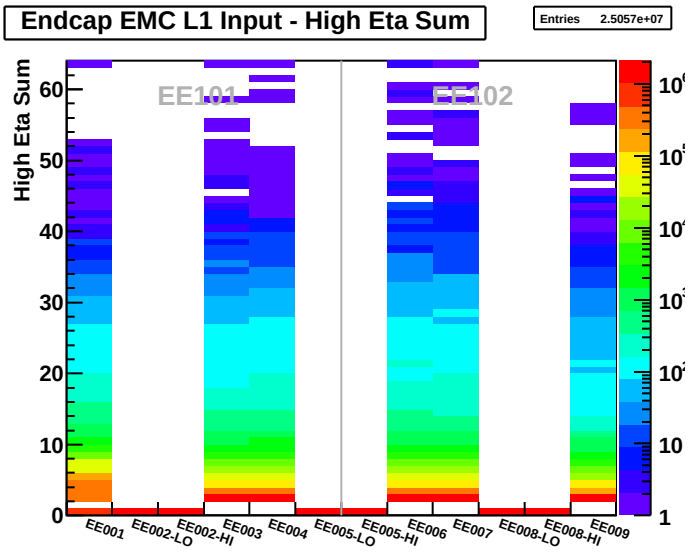

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

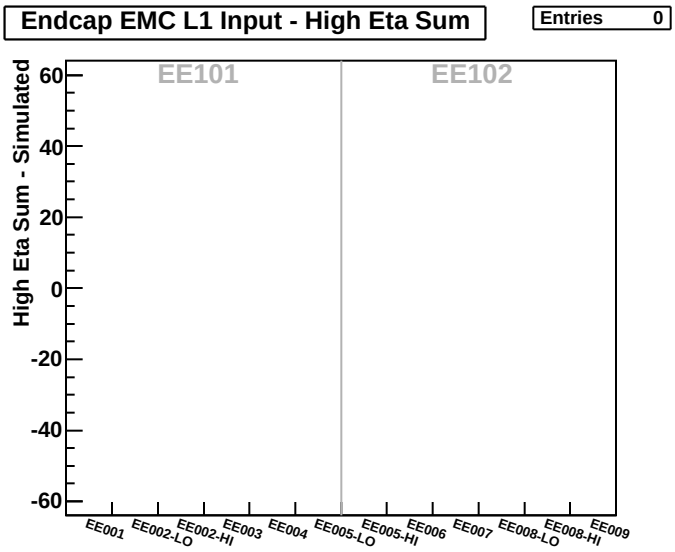

Barrel EMC L1 Input - High Tower Bits
Entries 7.517099e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

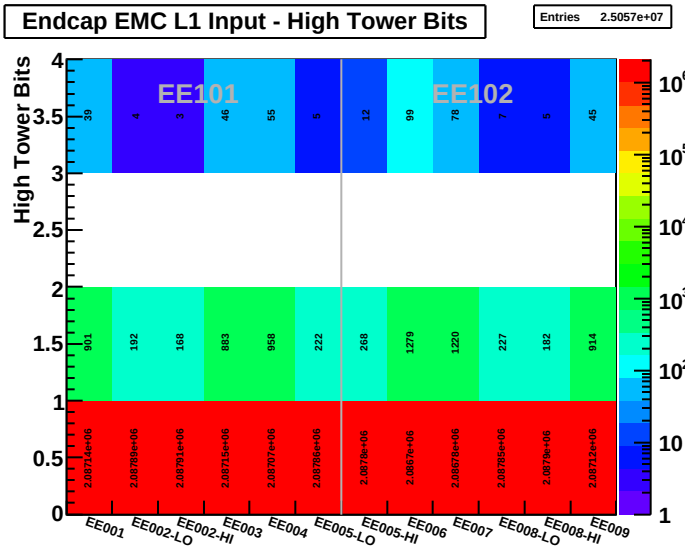

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

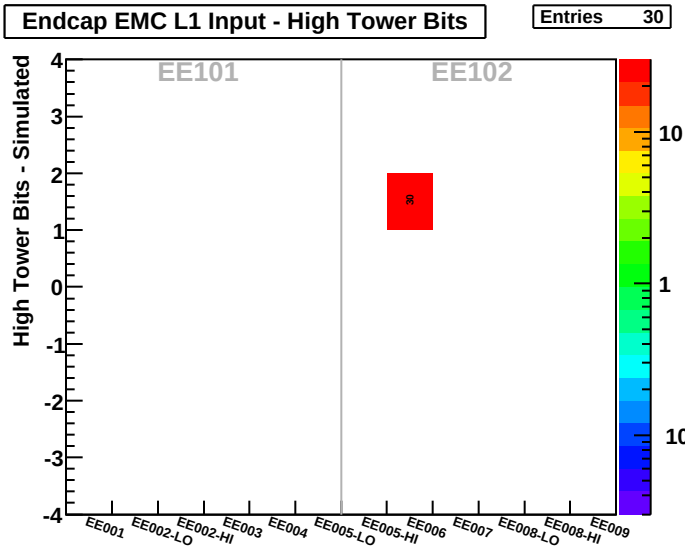

Endcap EMC L1 Input - Low Eta Sum
Entries 2.5057e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

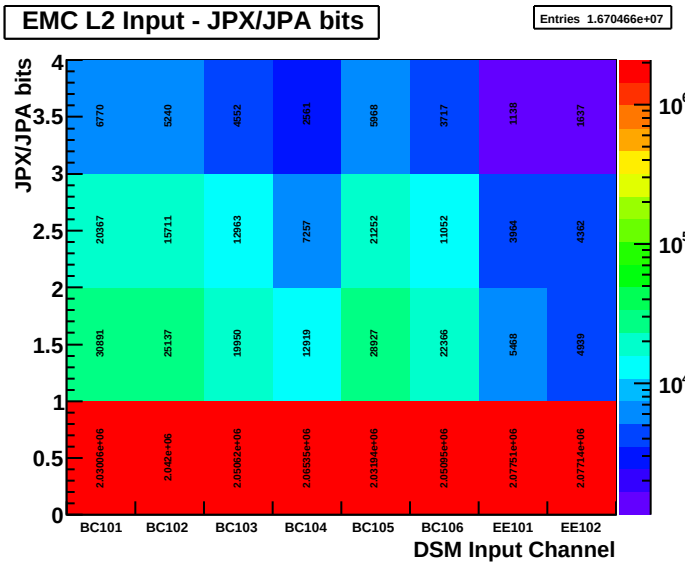

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

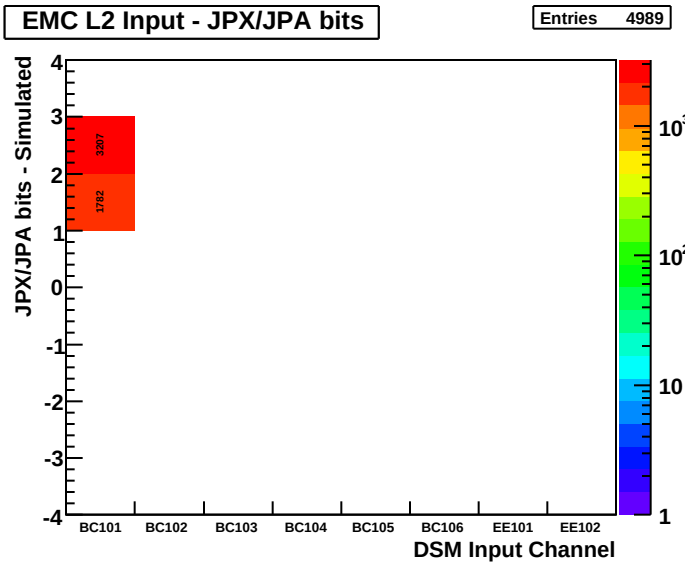

Endcap EMC L1 Input - High Eta Sum
Entries 2.5057e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

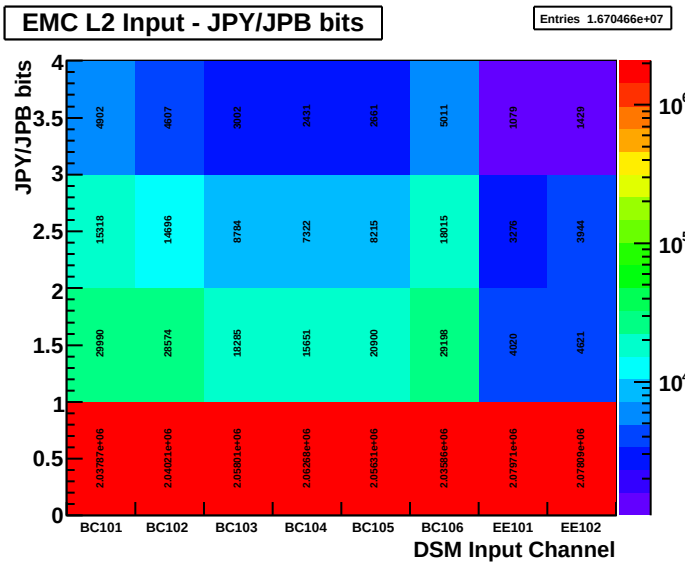

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

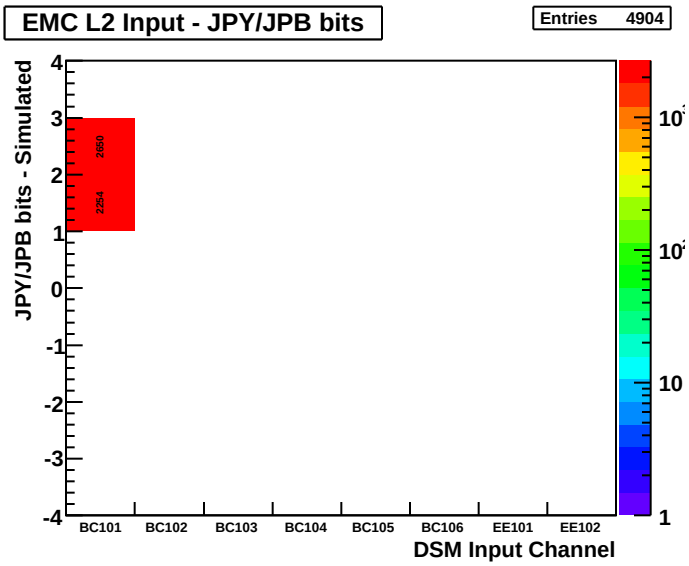

Endcap EMC L1 Input - High Tower Bits
Entries 2.5057e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

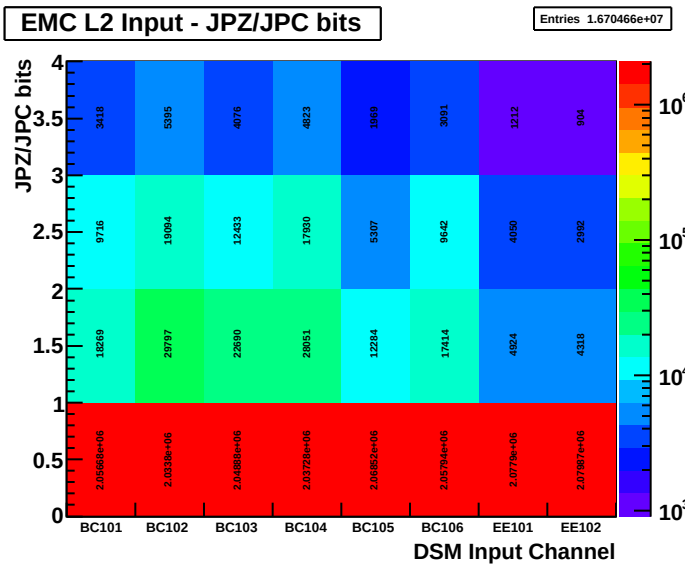

Endcap EMC L1 Input - High Tower Bits
Entries 30
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

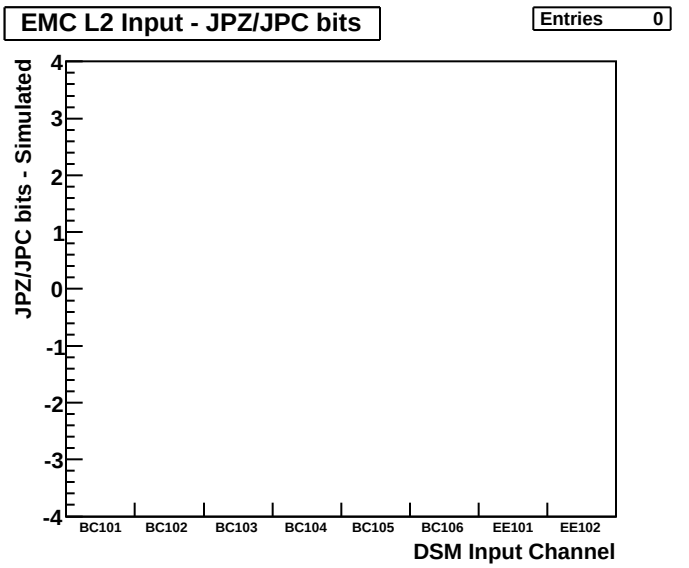

EMC L2 Input - JPX/JPA bits
Entries 1.670466e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6770 5240 4652 2561 5968 3717 1138 1637
20367 15711 12963 7257 21252 11052 3964 4362
30891 25137 19950 12919 28927 22366 5468 4399
2.03006e+06 2.042e+06 2.05682e+06 2.06535e+06 2.03194e+06 2.05095e+06 2.07751e+06 2.07714e+06

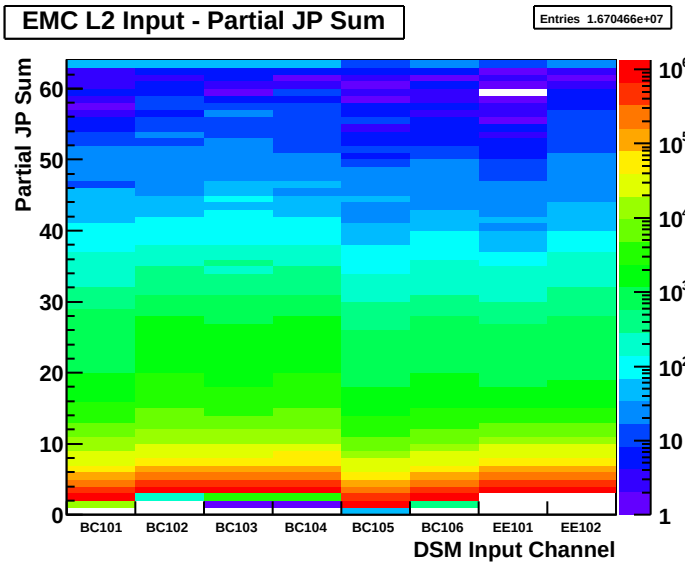

EMC L2 Input - JPX/JPA bits
Entries 4989
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3207
1782

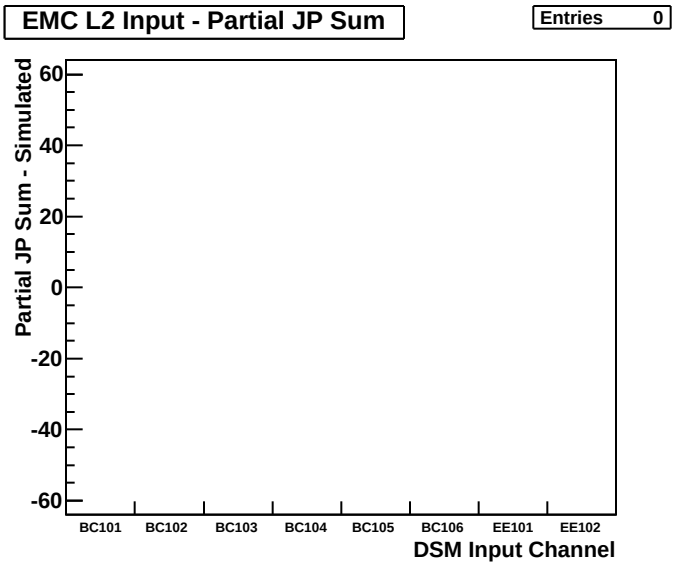

EMC L2 Input - JPY/JPB bits
Entries 1.670466e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4902 4607 3002 2431 2661 5011 1079 1429
13318 14696 8784 7322 8215 18015 3276 3944
29990 28574 18285 15651 20900 29198 4020 4621
2.03787e+06 2.04021e+06 2.05801e+06 2.06268e+06 2.05631e+06 2.03586e+06 2.07971e+06 2.07809e+06

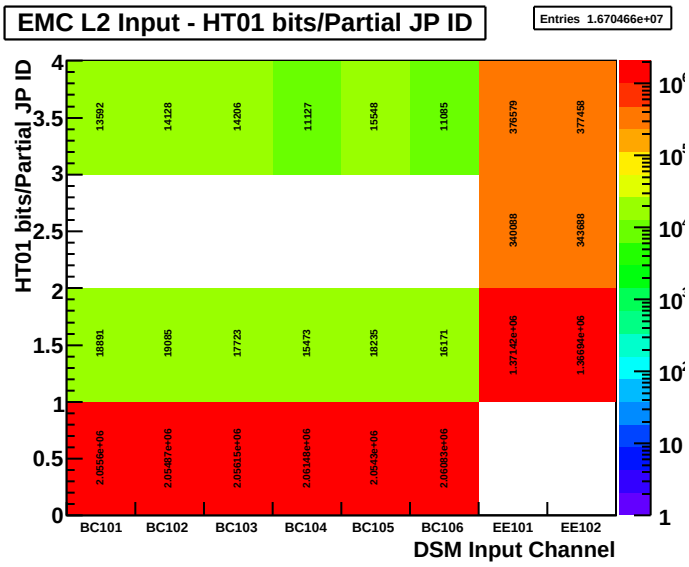

EMC L2 Input - JPY/JPB bits
Entries 4904
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2650
2254

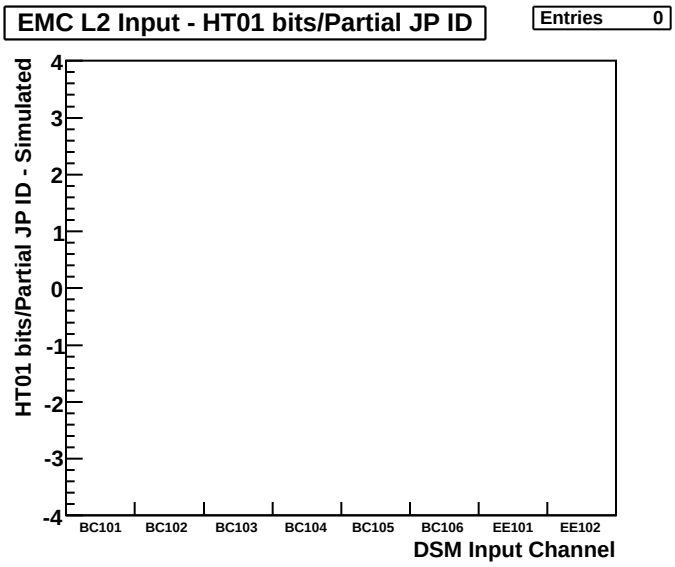

EMC L2 Input - JPZ/JPC bits
Entries 1.670466e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3418 5395 4076 4823 1969 3091 1212 904
9716 19094 12433 17930 5307 9642 4050 2932
18269 29797 22690 28051 12284 17414 4924 4318
2.05668e+06 2.0338e+06 2.04888e+06 2.03728e+06 2.06852e+06 2.05794e+06 2.0779e+06 2.07987e+06

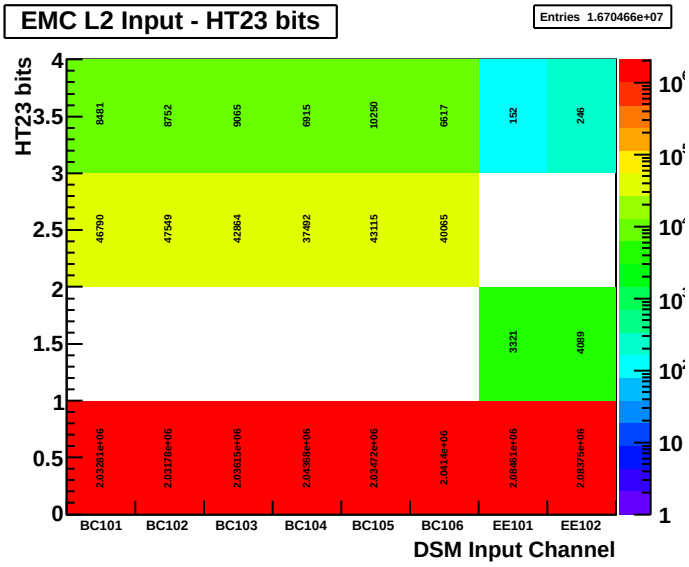

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

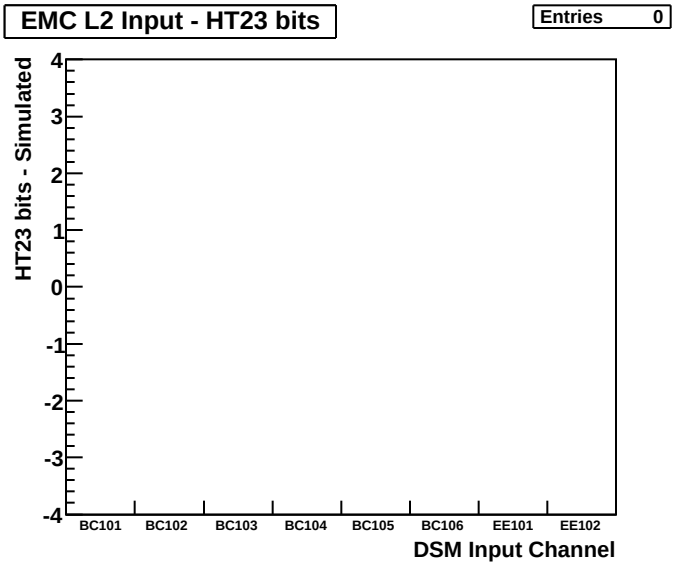

EMC L2 Input - Partial JP Sum
Entries 1.670466e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

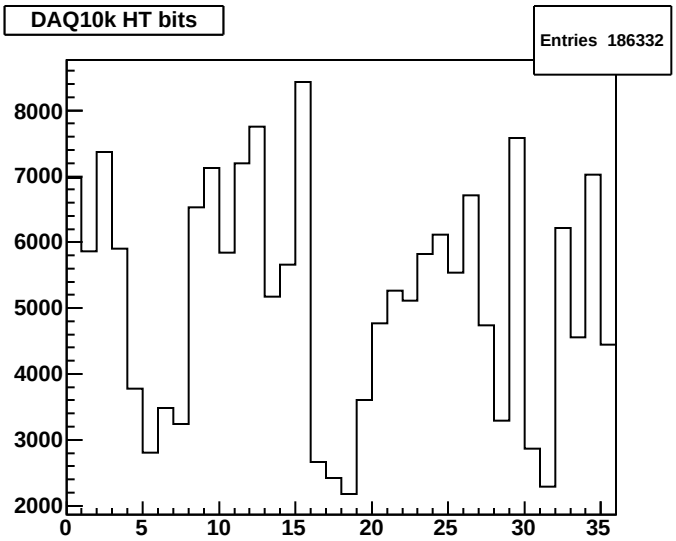

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

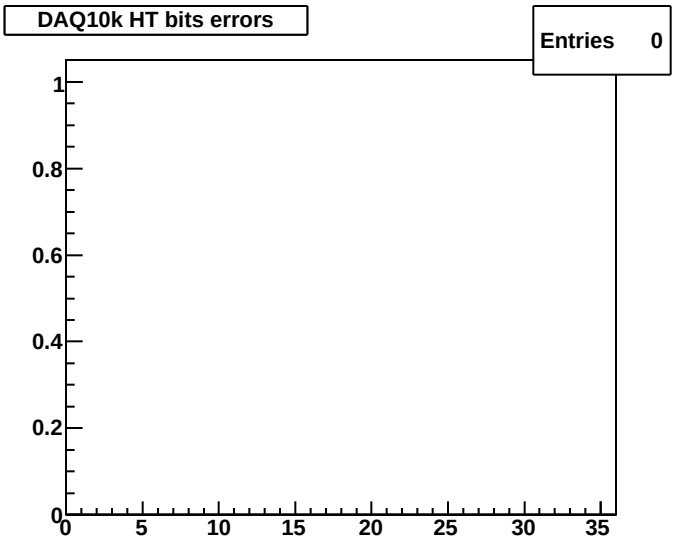

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.670466e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

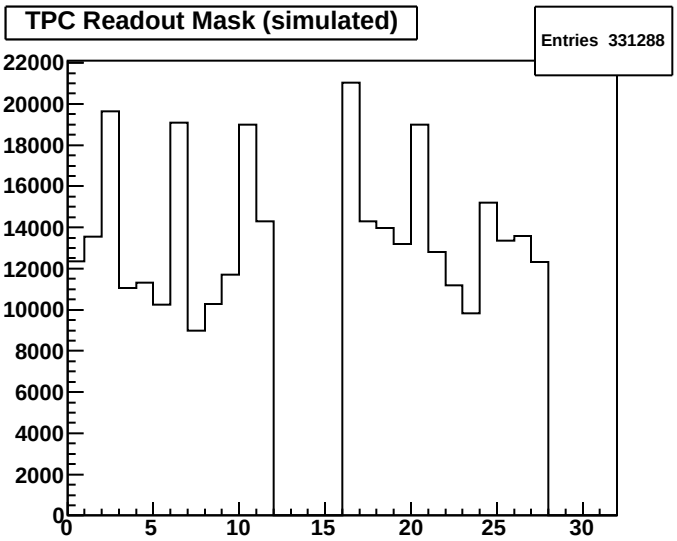

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

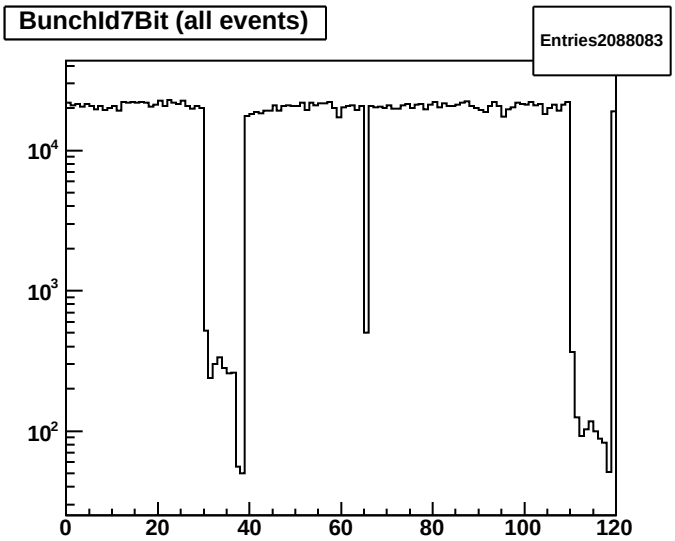

EMC L2 Input - HT23 bits
Entries 1.670466e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

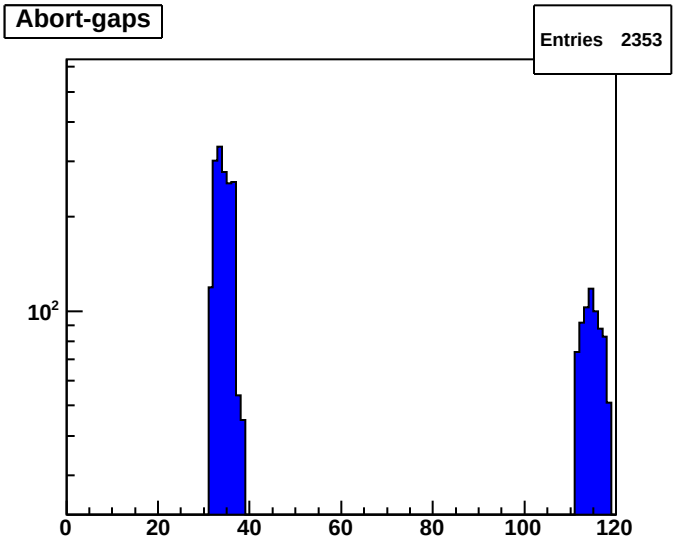

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

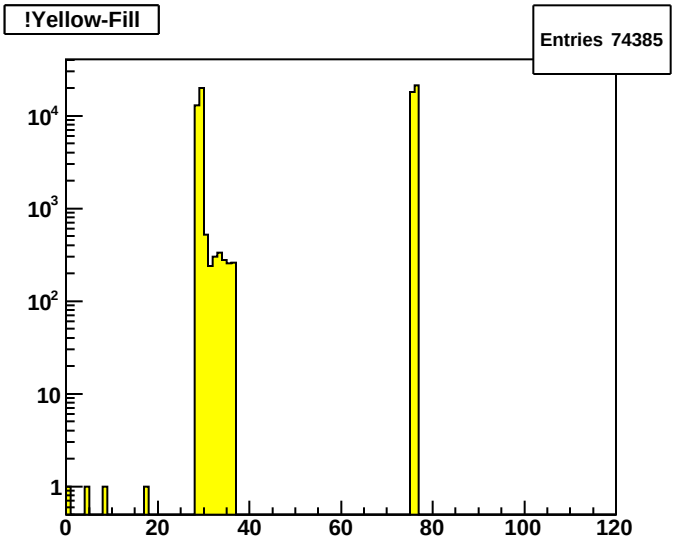

DAQ10k HT bits
Entries 186332

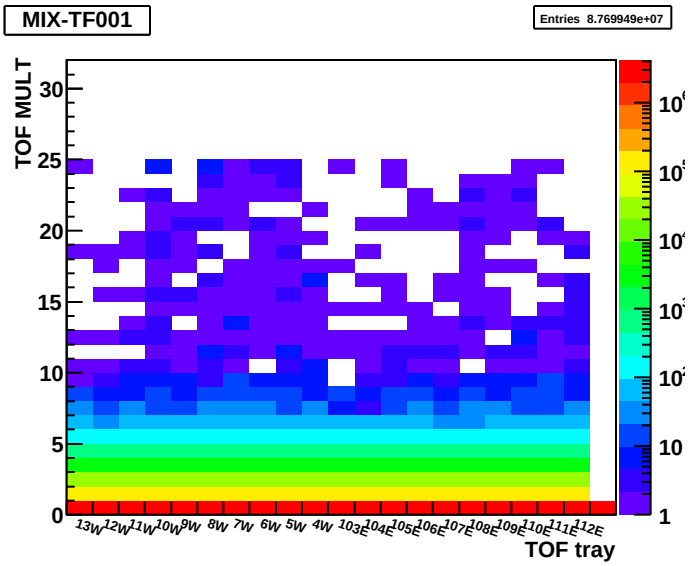

DAQ10k HT bits errors
Entries 0

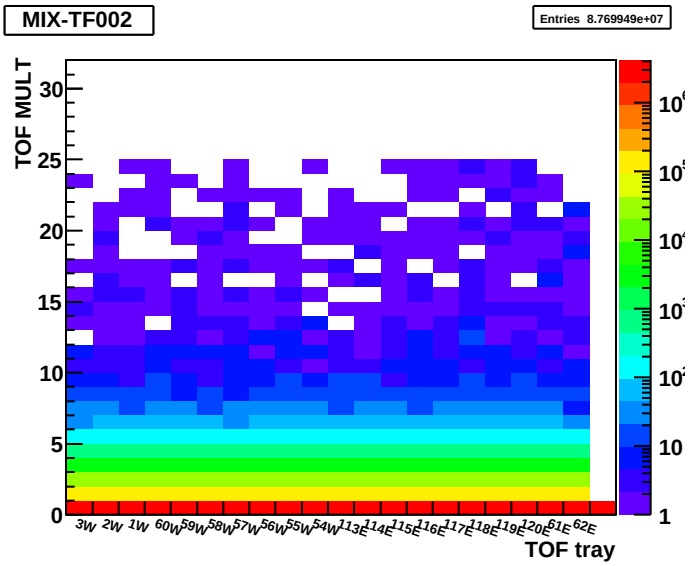

TPC Readout Mask (simulated)
Entries 331288

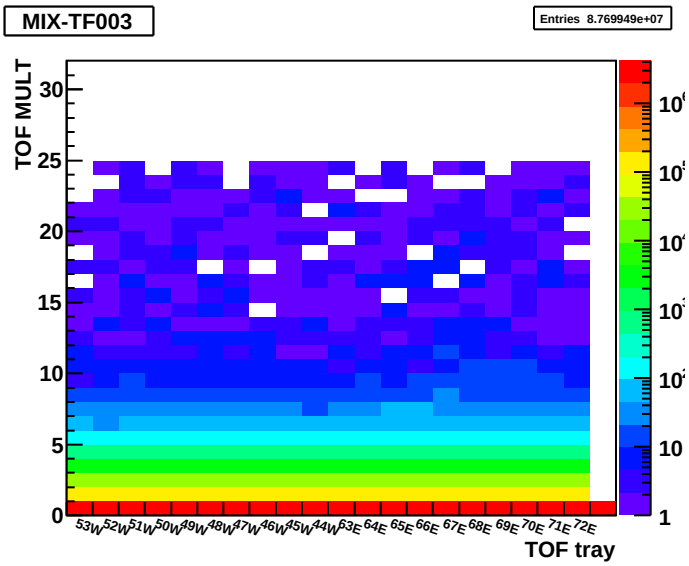

BunchId7Bit (all events)
Entries2088083

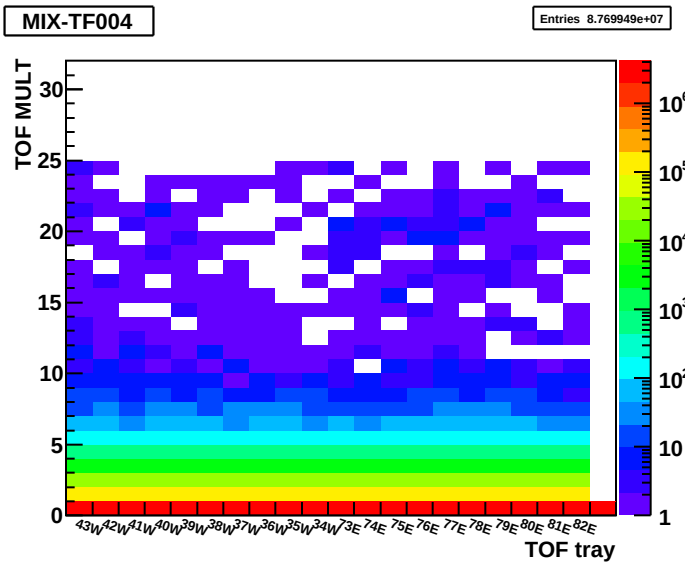

Abort-gaps
Entries 2353

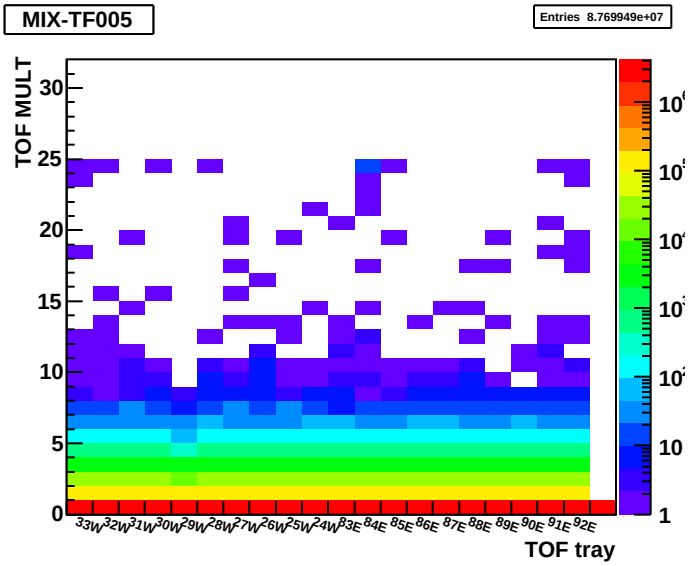

!Yellow-Fill
Entries 74385

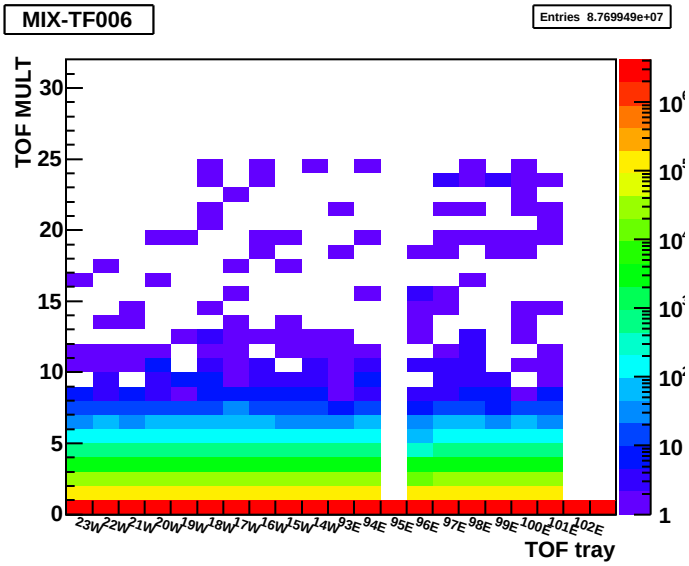

MIX-TF001
Entries 8.769949e+07
TOF MULT
TOF tray

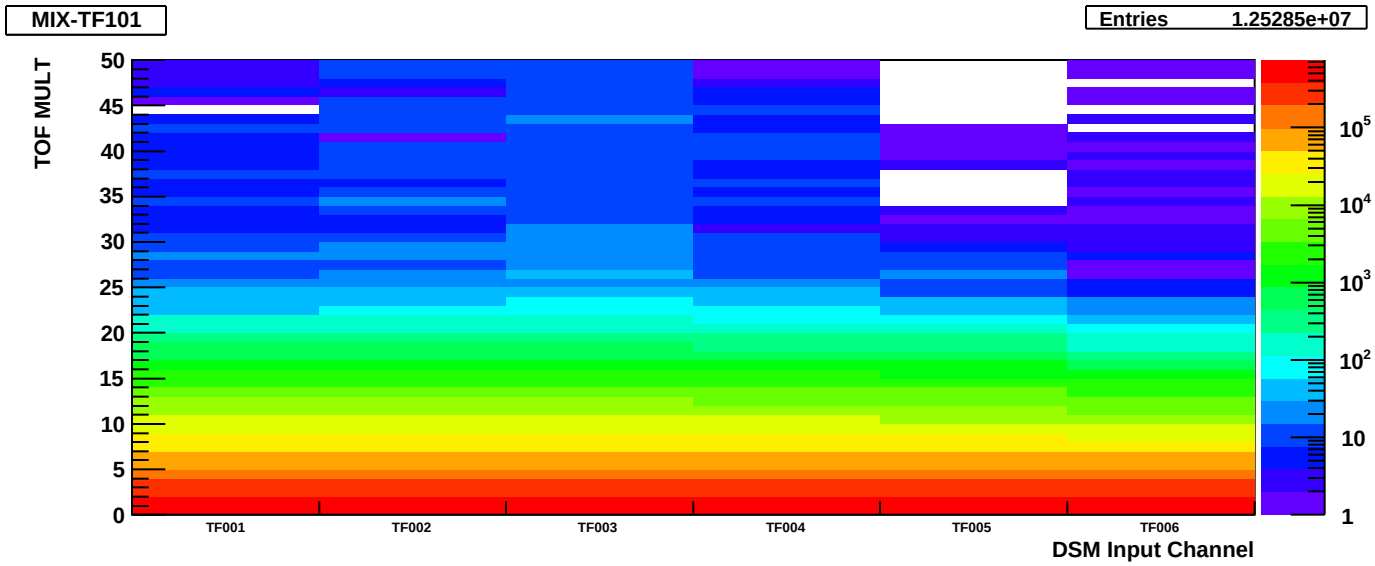

MIX-TF002
Entries 8.769949e+07
TOF MULT
TOF tray

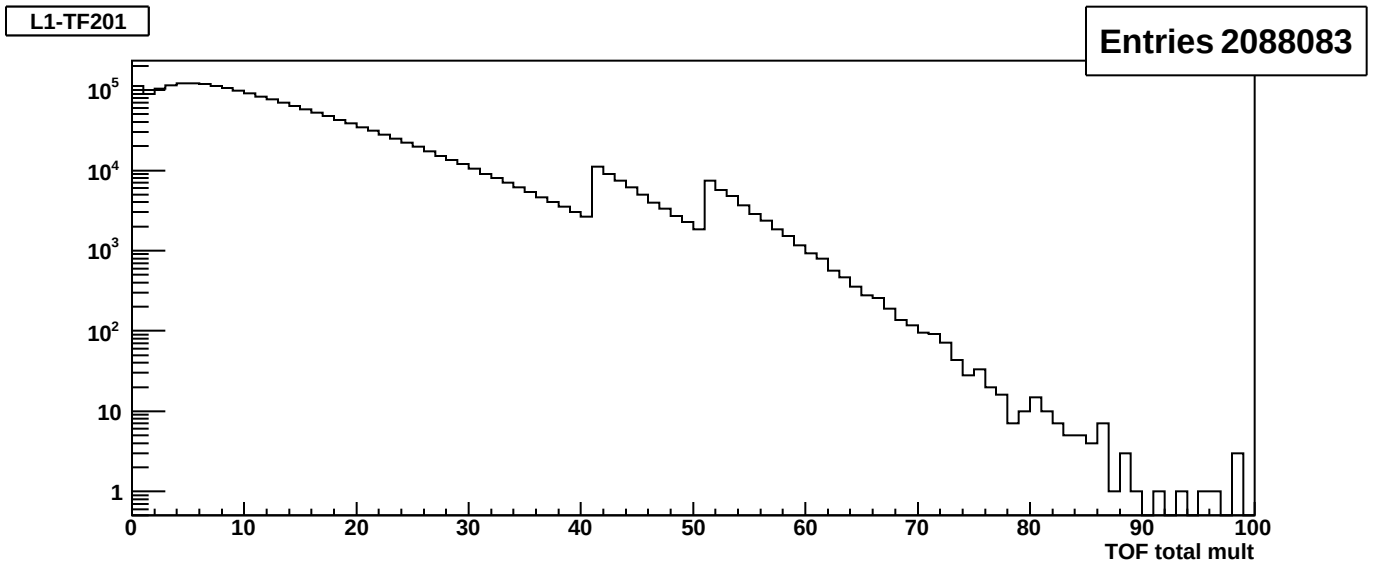

MIX-TF003
Entries 8.769949e+07
TOF MULT
TOF tray

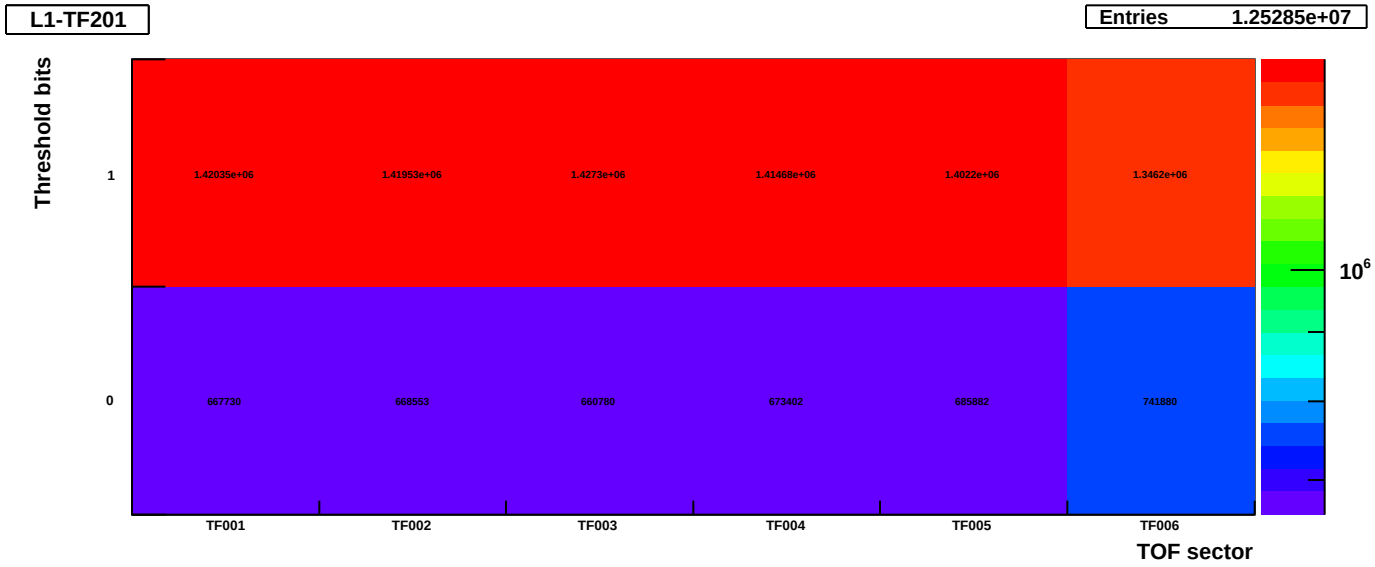

MIX-TF004
Entries 8.769949e+07
TOF MULT
TOF tray

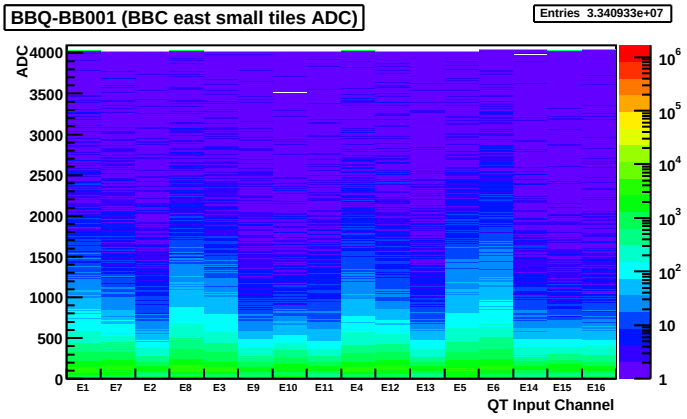

MIX-TF005
Entries 8.769949e+07
TOF MULT
TOF tray

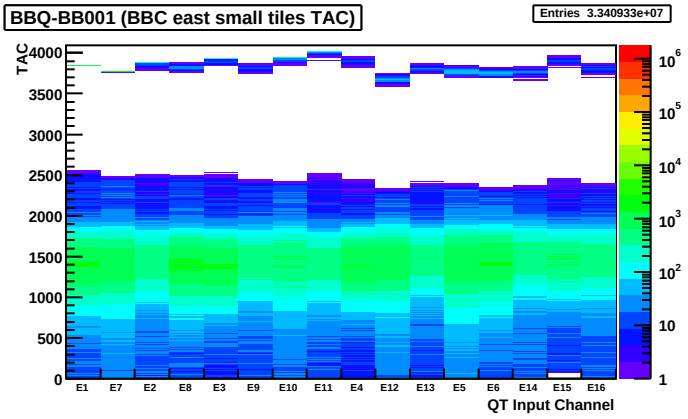

MIX-TF006
Entries 8.769949e+07
TOF MULT
TOF tray

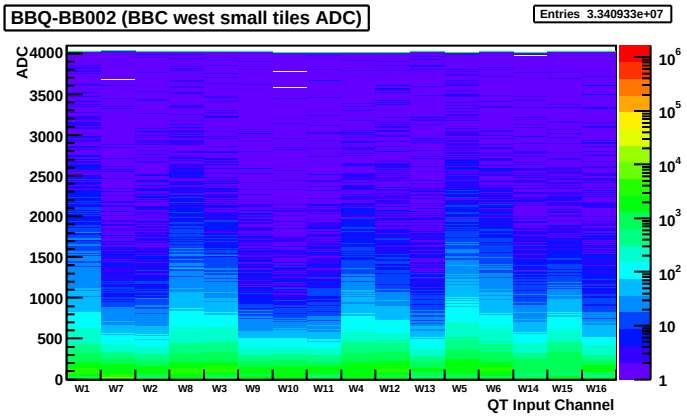

MIX-TF101
Entries 1.25285e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

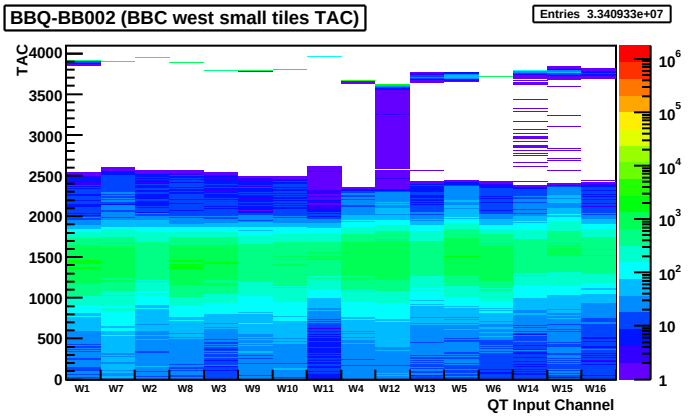

L1-TF201
Entries 2088083
TOF total mult

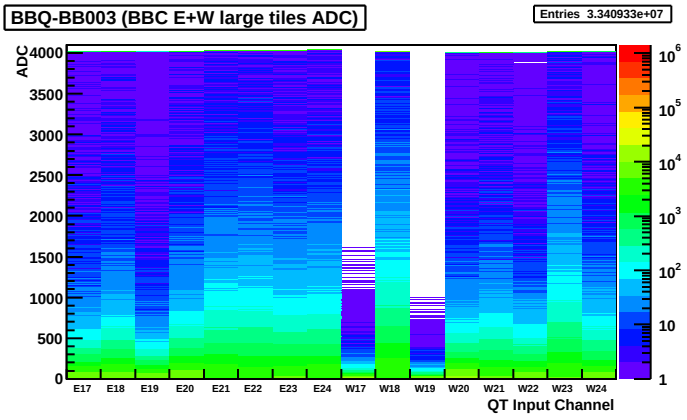

L1-TF201
Entries 1.25285e+07
Threshold bits
1.42035e+06
1.41953e+06
1.4273e+06
1.41468e+06
1.4022e+06
1.3462e+06
667730
668553
660780
673402
685882
741880
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

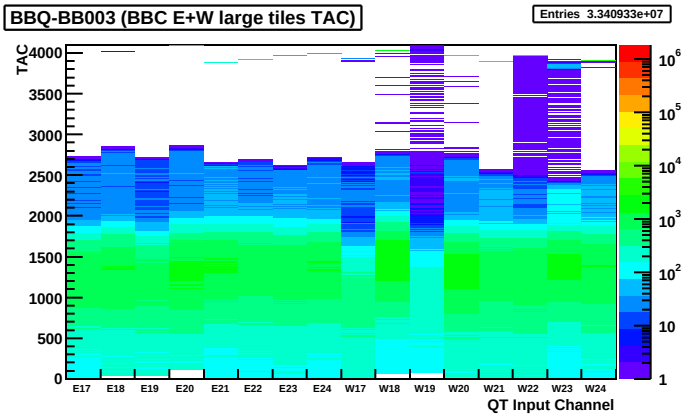

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.340933e+07
ADC
QT Input Channel

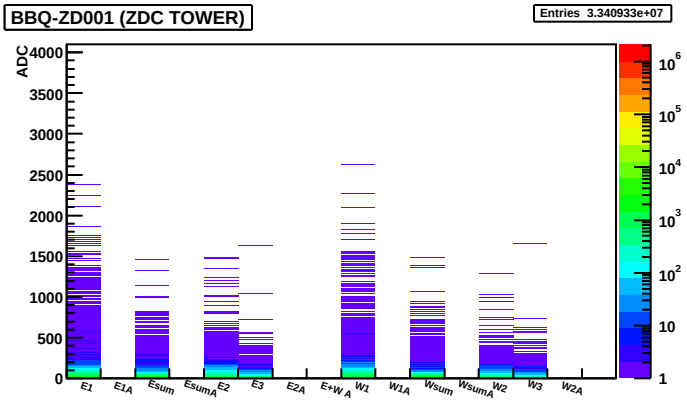

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.340933e+07
TAC
QT Input Channel

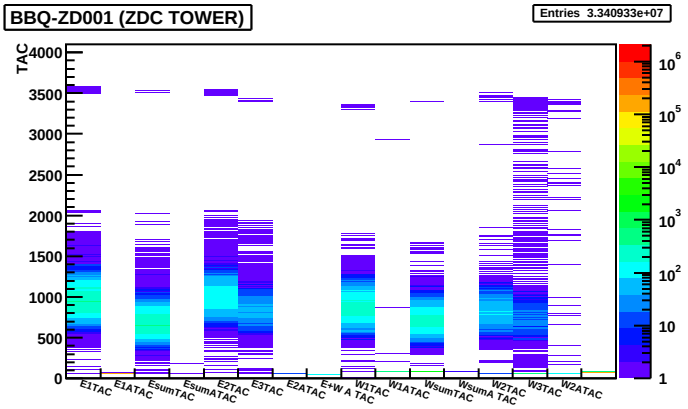

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.340933e+07
ADC
QT Input Channel

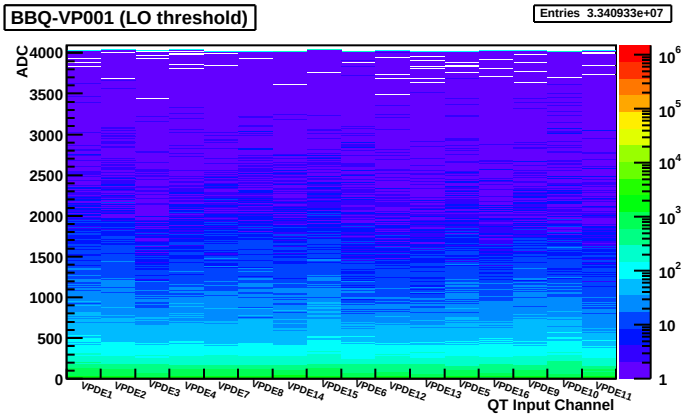

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.340933e+07
TAC
QT Input Channel

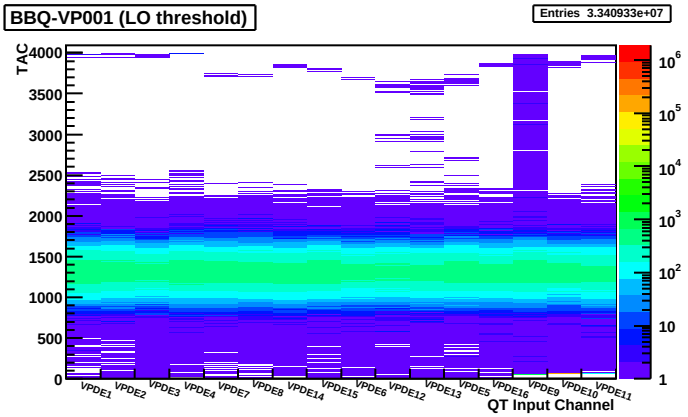

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.340933e+07
ADC
QT Input Channel

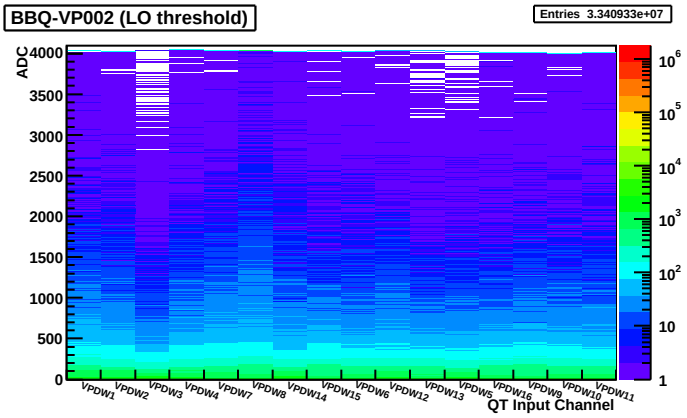

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.340933e+07
TAC
QT Input Channel

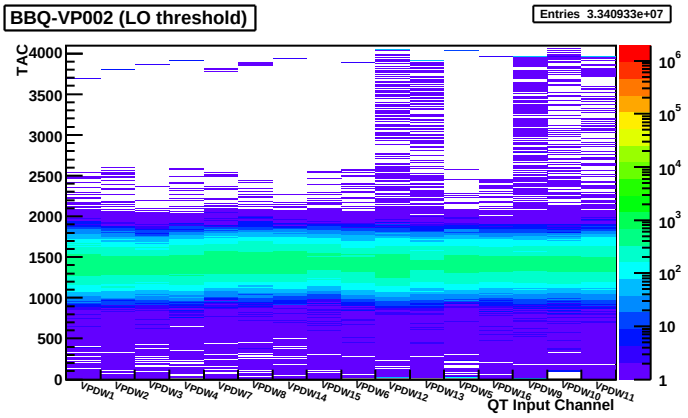

BBQ-ZD001 (ZDC TOWER)
Entries 3.340933e+07
ADC

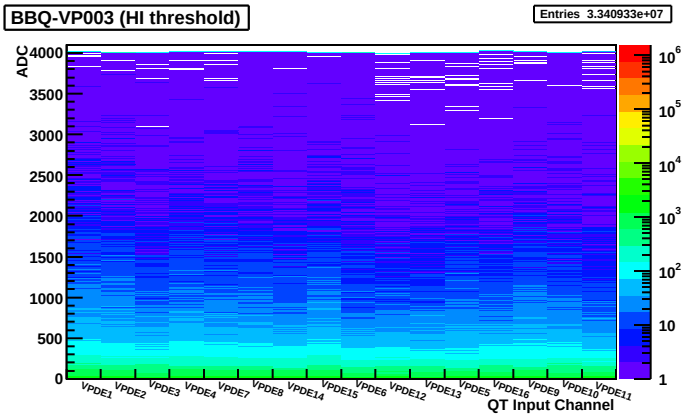

BBQ-ZD001 (ZDC TOWER)
Entries 3.340933e+07
TAC

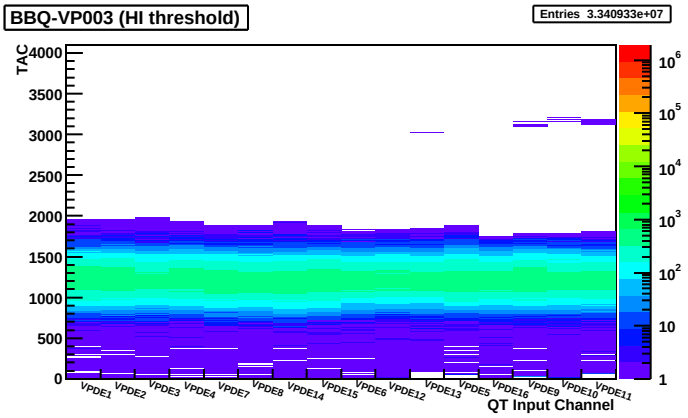

BBQ-VP001 (LO threshold)
Entries 3.340933e+07
ADC
QT Input Channel

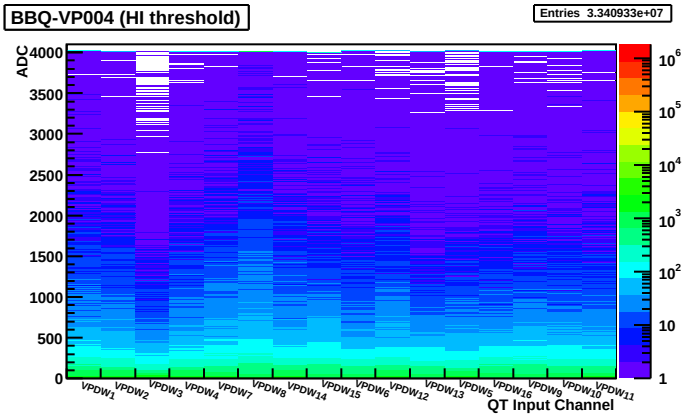

BBQ-VP001 (LO threshold)
Entries 3.340933e+07
TAC
QT Input Channel

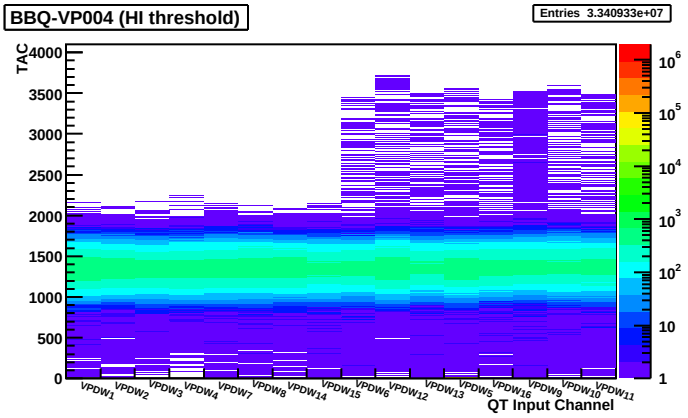

BBQ-VP002 (LO threshold)
Entries 3.340933e+07
ADC
QT Input Channel

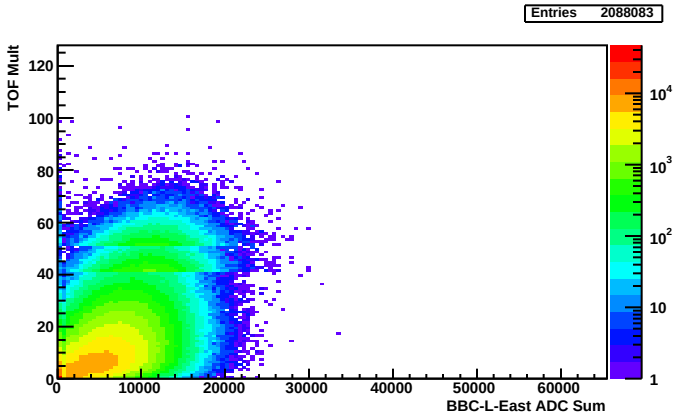

BBQ-VP002 (LO threshold)
Entries 3.340933e+07
TAC
QT Input Channel

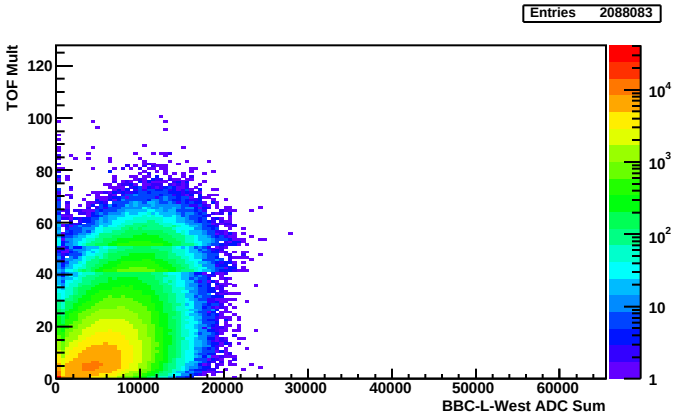

BBQ-VP003 (HI threshold)
Entries 3.340933e+07
ADC
QT Input Channel

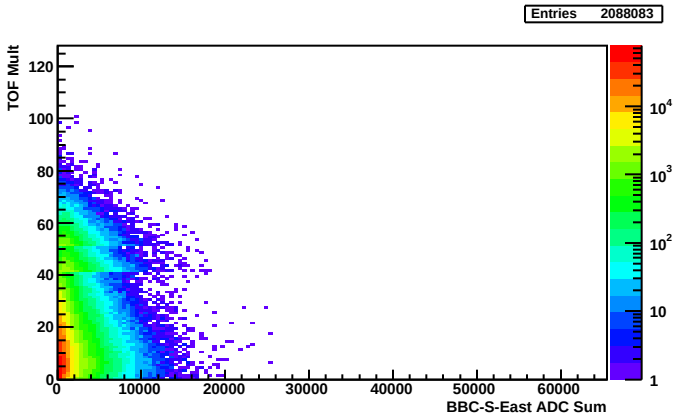

BBQ-VP003 (HI threshold)
Entries 3.340933e+07
TAC
QT Input Channel

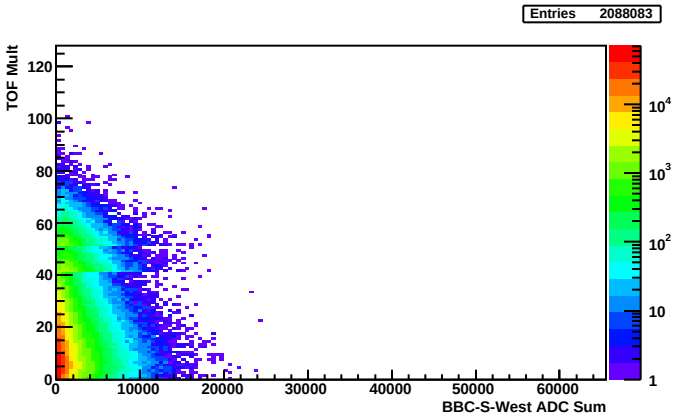

BBQ-VP004 (HI threshold)
Entries 3.340933e+07
ADC
QT Input Channel

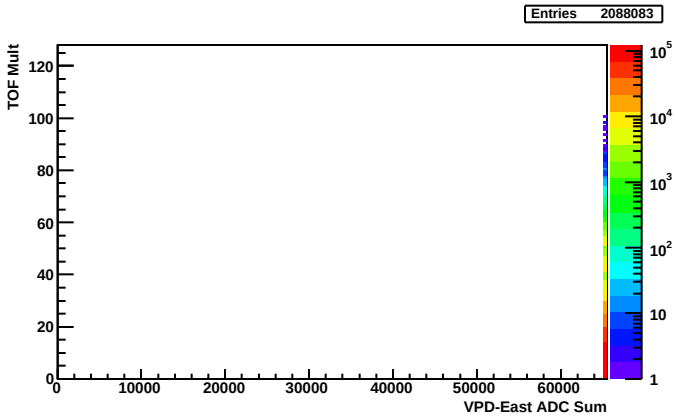

BBQ-VP004 (HI threshold)
Entries 3.340933e+07
TAC
QT Input Channel

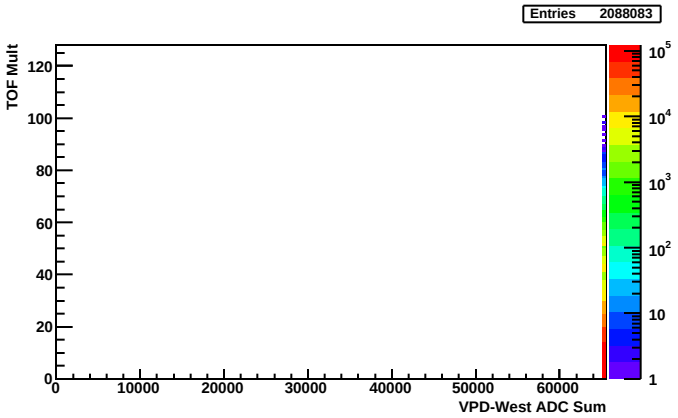

Entries 2088083
TOF Mult
BBC-L-East ADC Sum

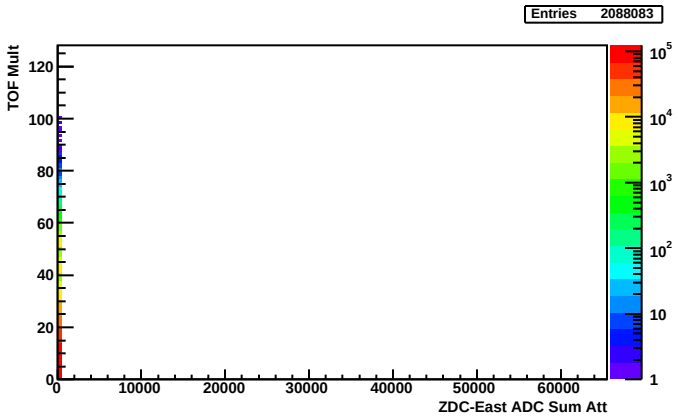

Entries 2088083
TOF Mult
BBC-L-West ADC Sum

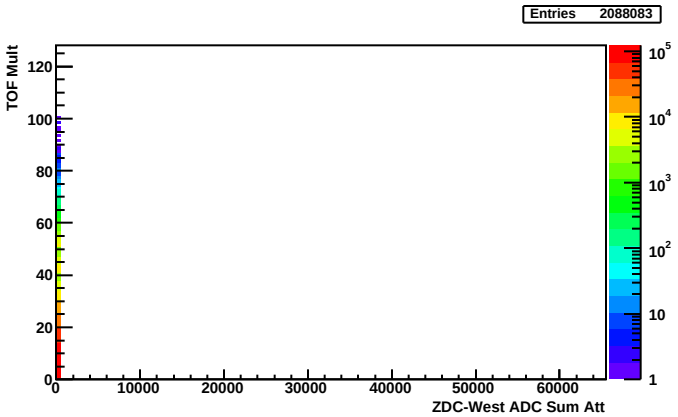

Entries 2088083
TOF Mult
BBC-S-East ADC Sum

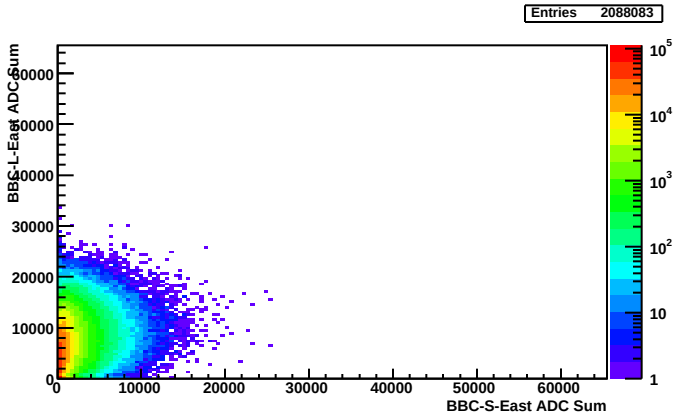

Entries 2088083
TOF Mult
BBC-S-West ADC Sum

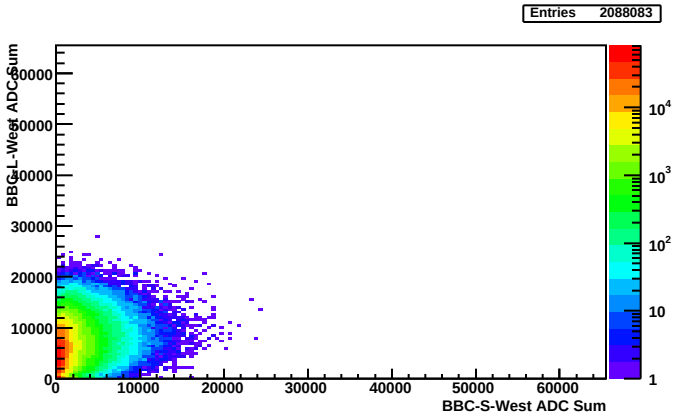

Entries 2088083
TOF Mult
VPD-East ADC Sum

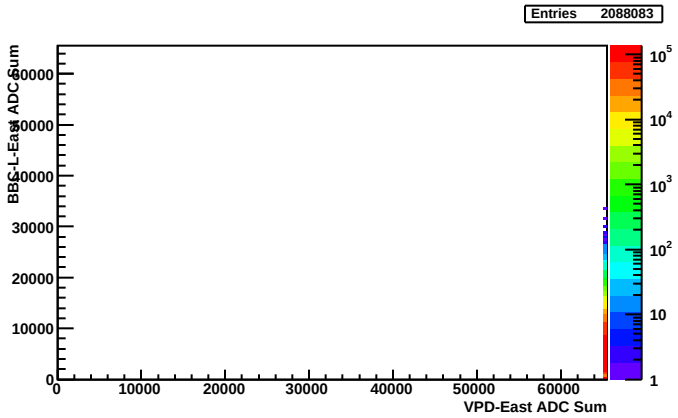

Entries 2088083
TOF Mult
VPD-West ADC Sum

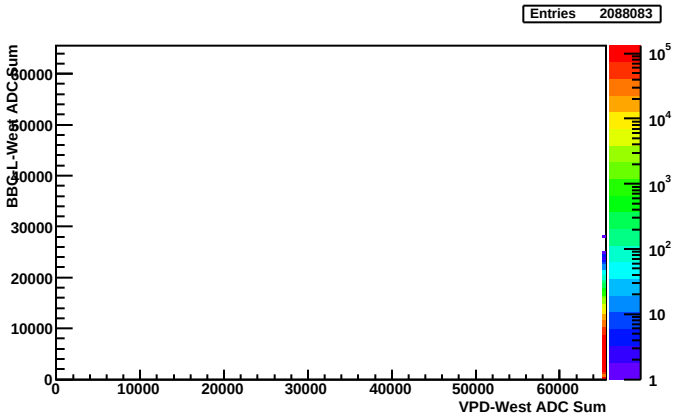

Entries 2088083
TOF Mult
ZDC-East ADC Sum Att

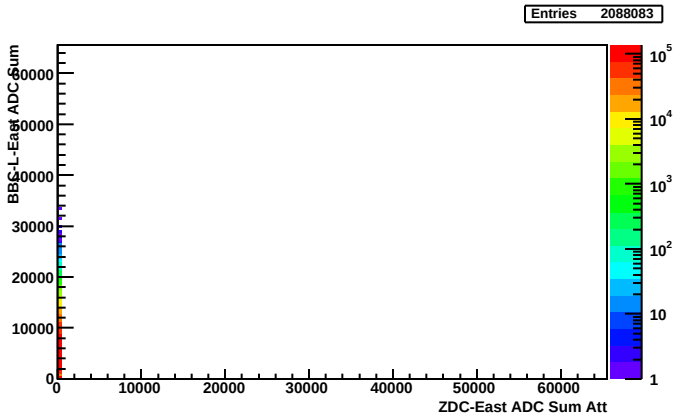

Entries 2088083
TOF Mult
ZDC-West ADC Sum Att

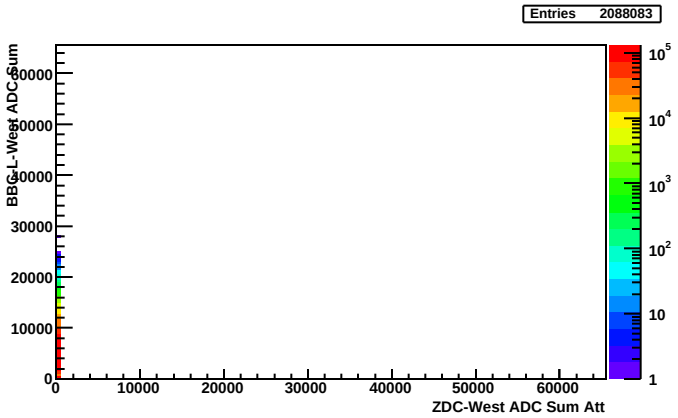

Entries 2088083
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 2088083
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 2088083
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 2088083
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 2088083
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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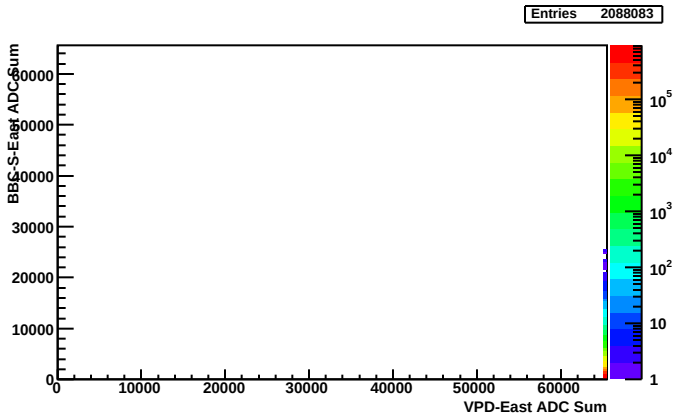
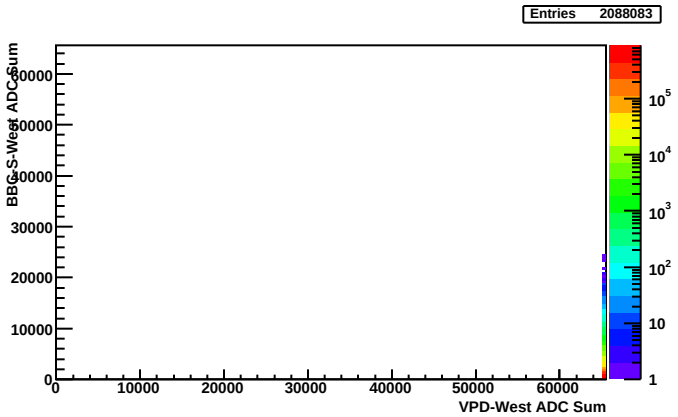
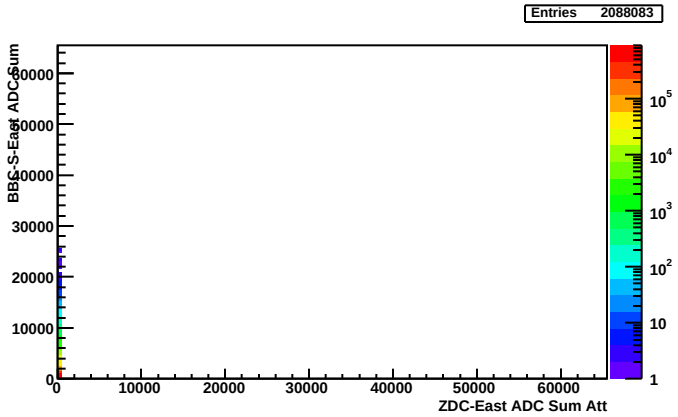
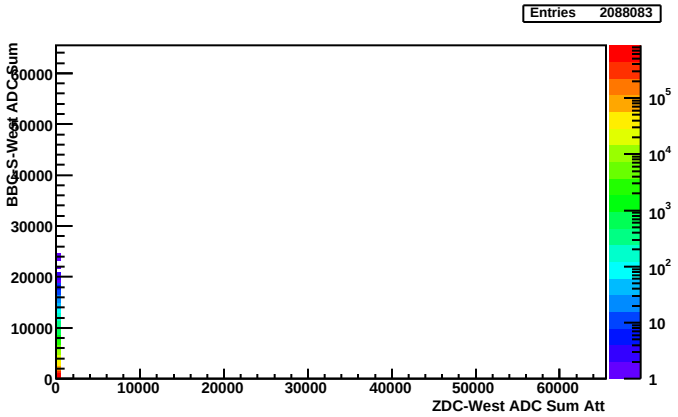
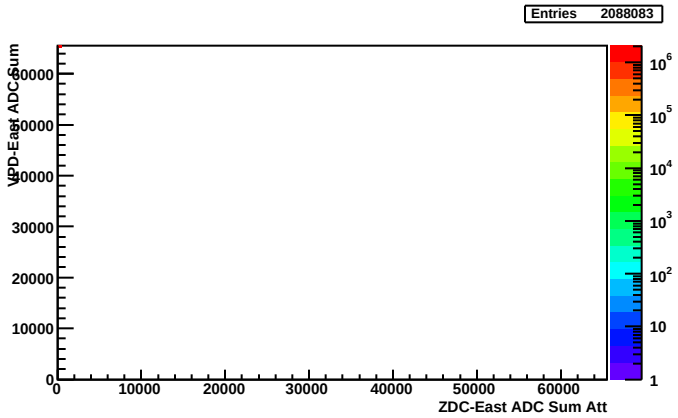
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

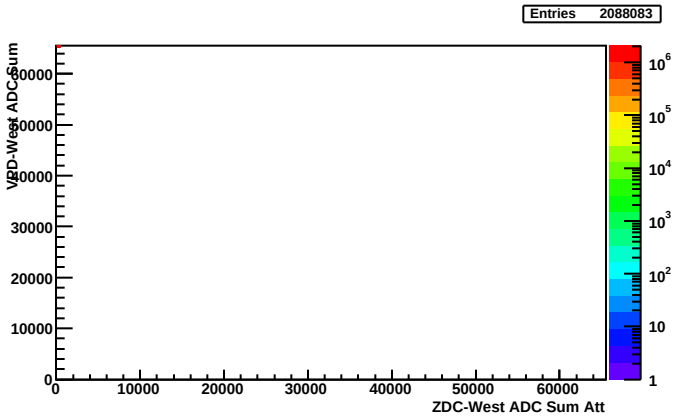

Entries 2088083
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 2088083
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 2088083
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 2088083
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 2088083
VPD-East ADC Sum
ZDC-East ADC Sum Att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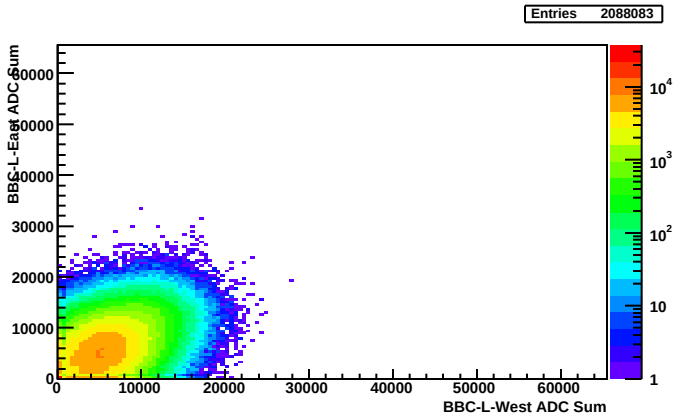
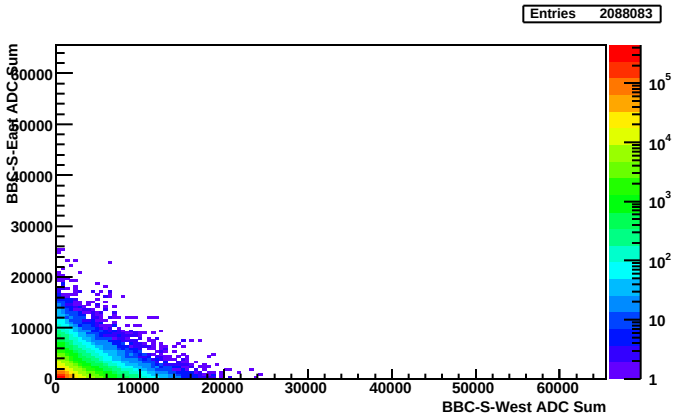
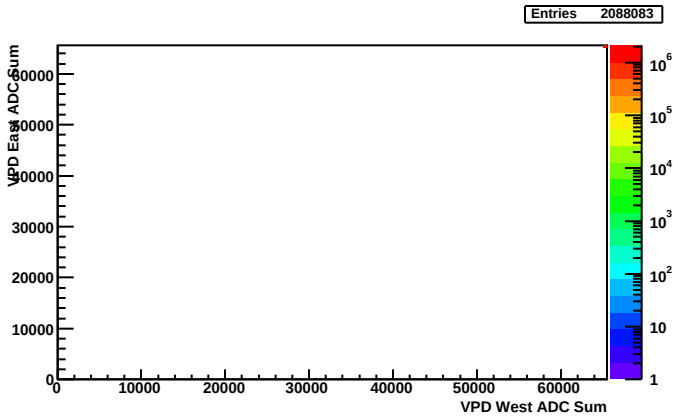
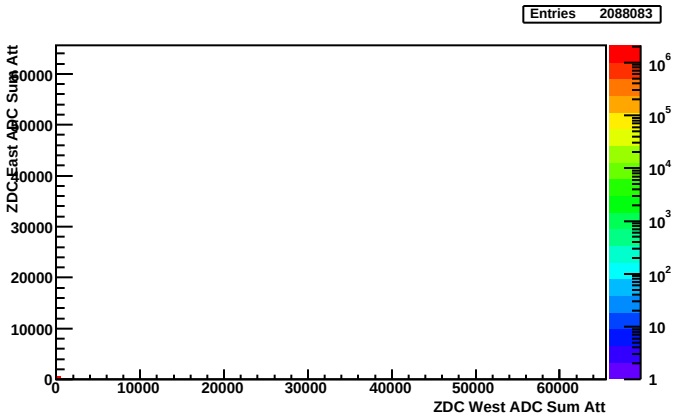
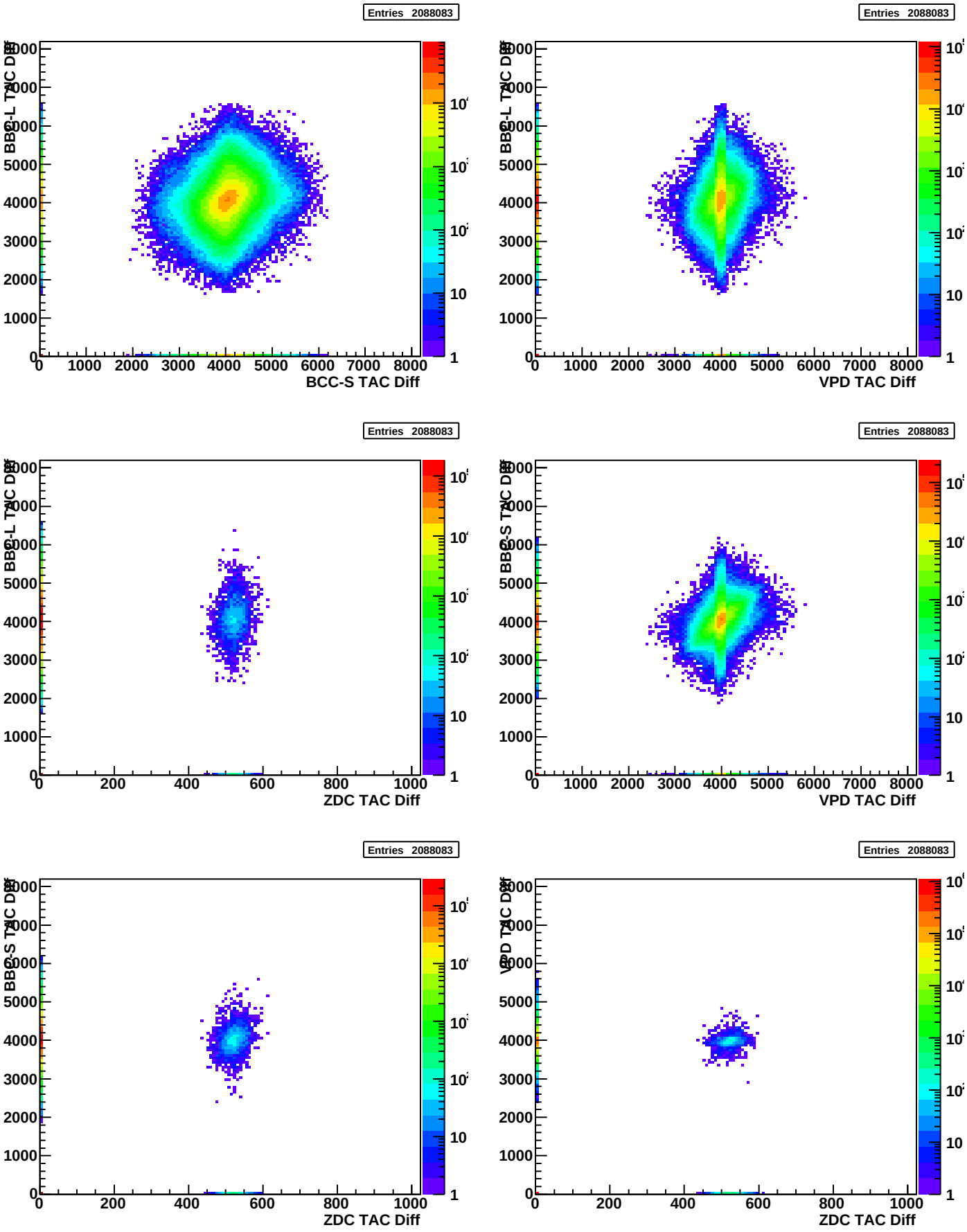
VPD-West ADC Sum
ZDC-West ADC Sum Att

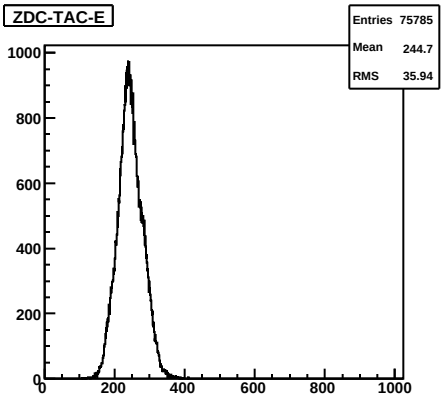

Entries 2088083
BBC-L-East ADC Sum
BBC-L-West ADC Sum

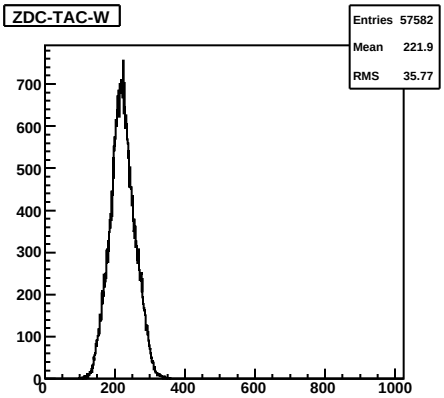

Entries 2088083
BBC-S-East ADC Sum
BBC-S-West ADC Sum

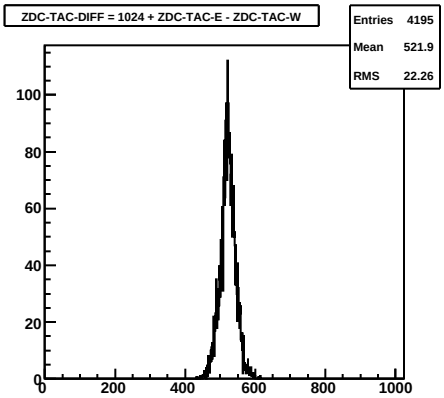

Entries 2088083
VPD East ADC Sum
VPD West ADC Sum

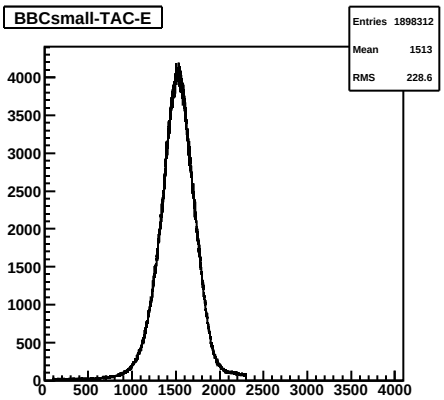

Entries 2088083
ZDC East ADC Sum Att
ZDC West ADC Sum Att

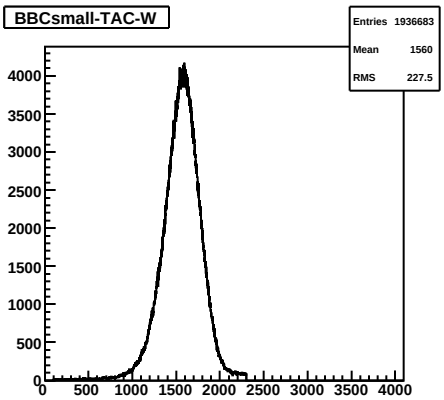

Entries 2088083
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2088083
BBC-L TAC Diff
VPD TAC Diff
Entries 2088083
BBC-L TAC Diff
ZDC TAC Diff
Entries 2088083
BBC-S TAC Diff
VPD TAC Diff
Entries 2088083
BBC-S TAC Diff
ZDC TAC Diff
Entries 2088083
VPD TAC Diff
ZDC TAC Diff

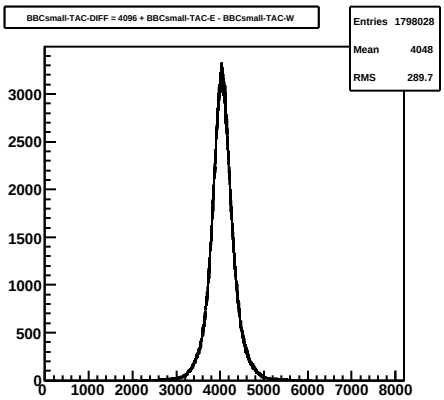

ZDC-TAC-E
Entries 75785
Mean 244.7
RMS 35.94

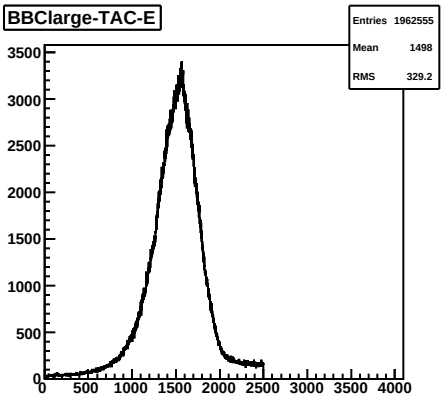

ZDC-TAC-W
Entries 57582
Mean 221.9
RMS 35.77

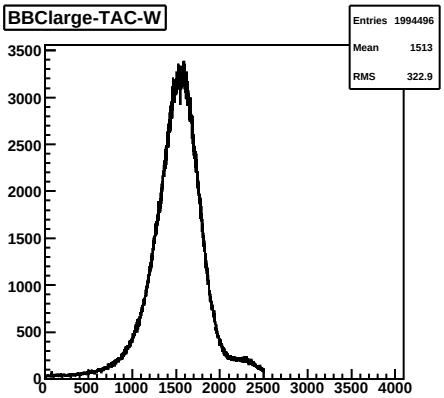

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 4195
Mean 521.9
RMS 22.26

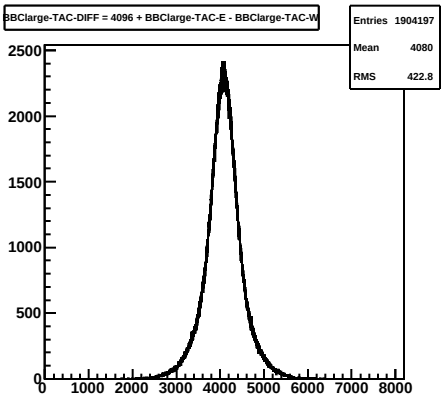

BBCsmall-TAC-E
Entries 1898312
Mean 1513
RMS 228.6

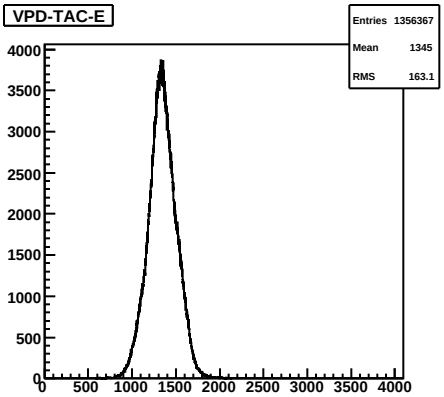

BBCsmall-TAC-W
Entries 1936683
Mean 1560
RMS 227.5

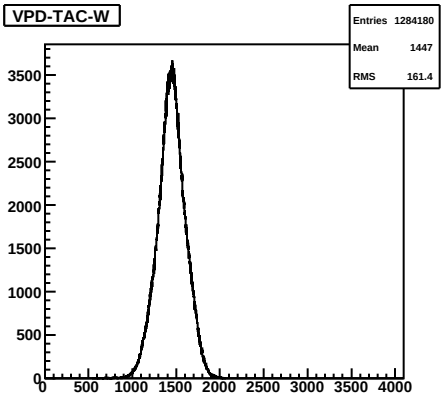

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1798028
Mean 4048
RMS 289.7

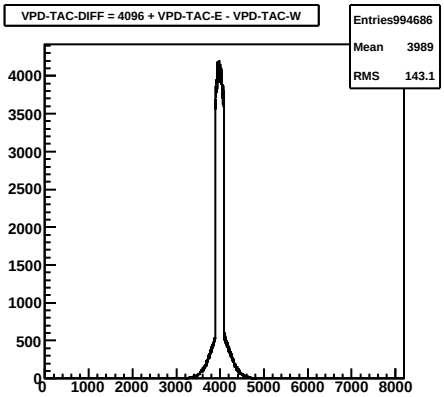

BBClarge-TAC-E
Entries 1962555
Mean 1498
RMS 329.2

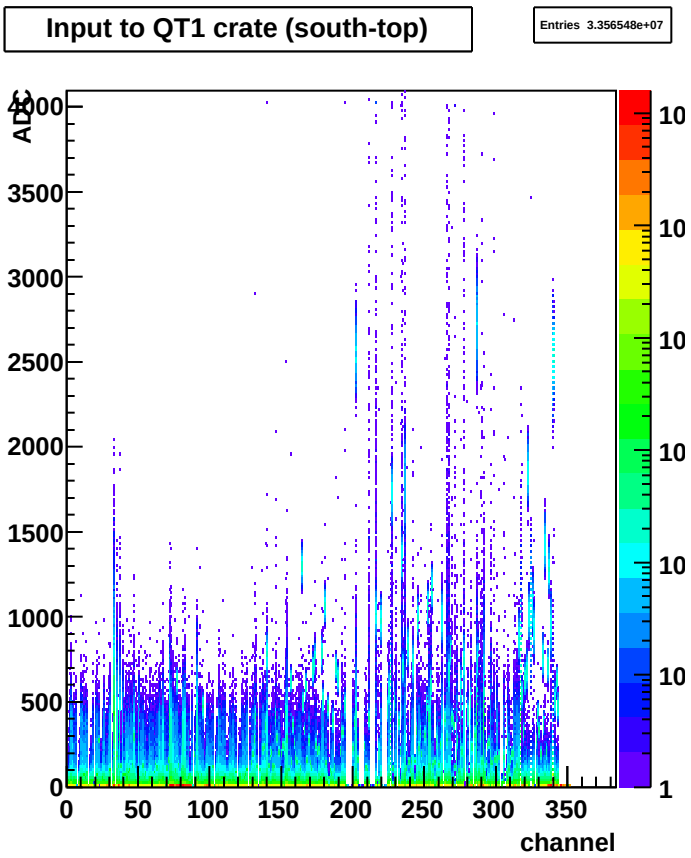

BBClarge-TAC-W
Entries 1994496
Mean 1513
RMS 322.9

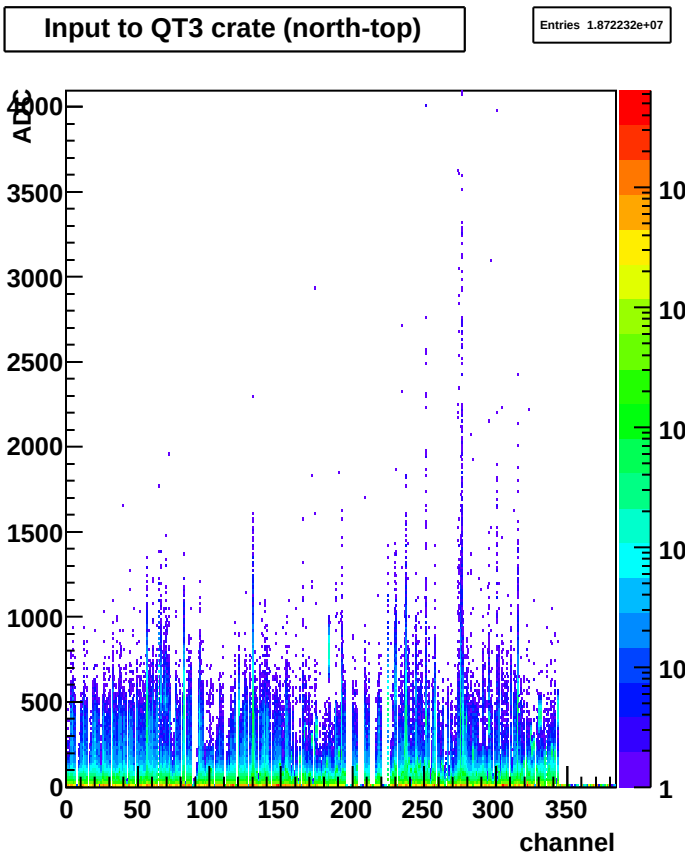

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1904197
Mean 4080
RMS 422.8

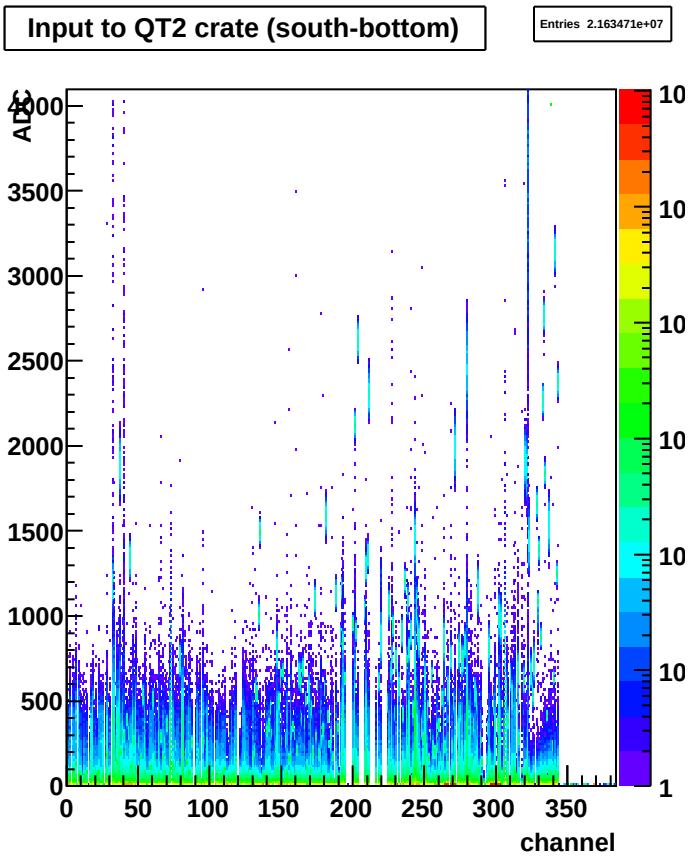

VPD-TAC-E
Entries 1356367
Mean 1345
RMS 163.1

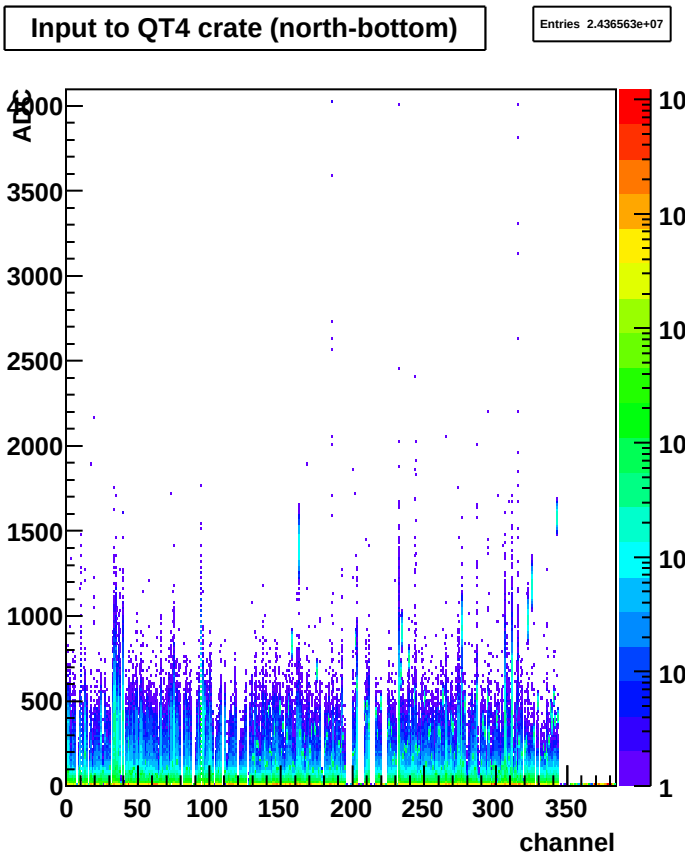

VPD-TAC-W
Entries 1284180
Mean 1447
RMS 161.4

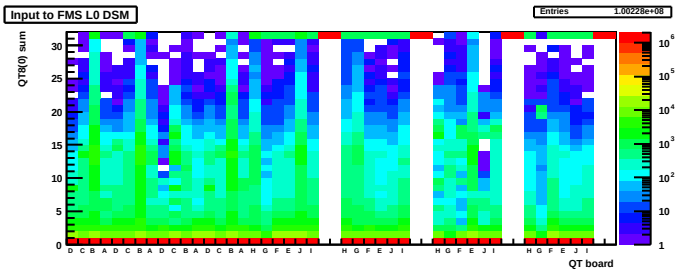

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries994686
Mean 3989
RMS 143.1

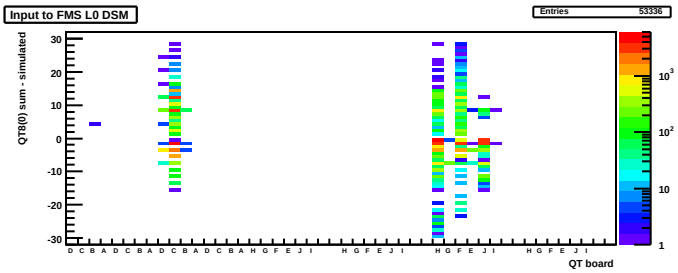

Input to QT1 crate (south-top)
Entries 3.356548e+07
ADC
channel

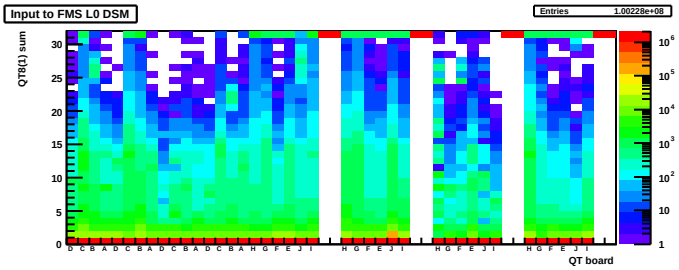

Input to QT3 crate (north-top)
Entries 1.872232e+07
ADC
channel

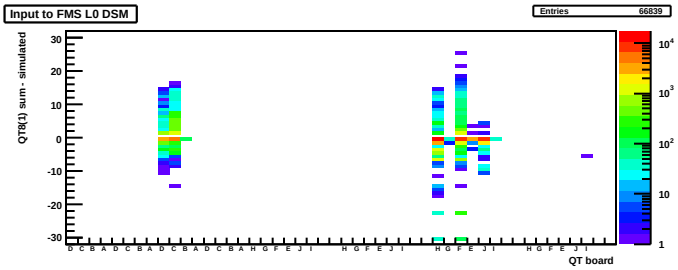

Input to QT2 crate (south-bottom)
Entries 2.163471e+07
ADC
channel

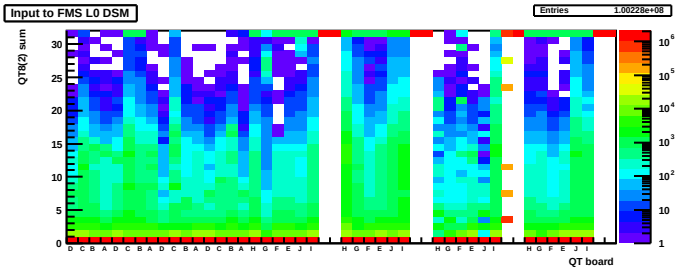

Input to QT4 crate (north-bottom)
Entries 2.436563e+07
ADC
channel

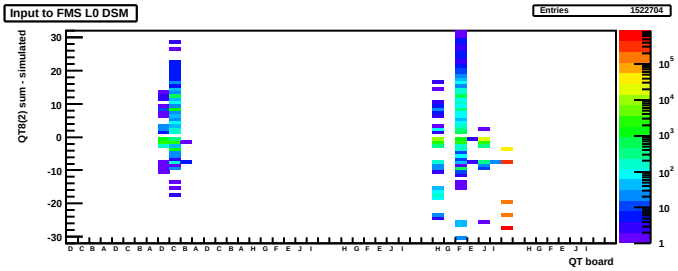

Input to FMS L0 DSM
Entries 1.00228e+08
QT8(0) sum
QT board

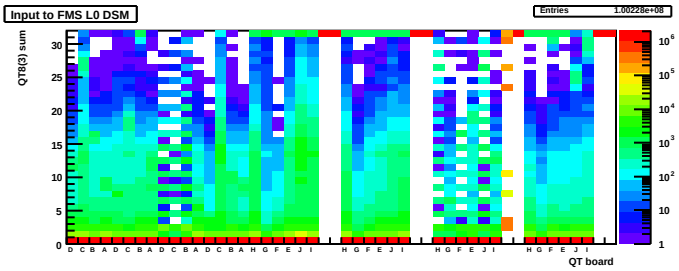

Input to FMS L0 DSM
Entries 53336
QT8(0) sum - simulated
QT board

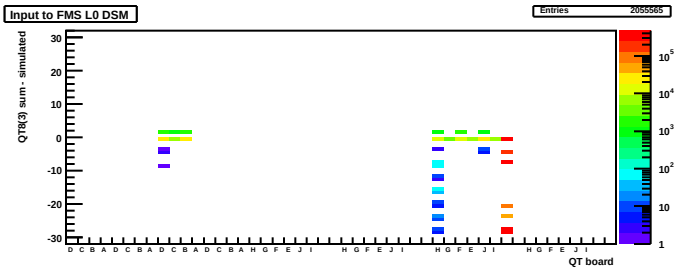

Input to FMS L0 DSM
Entries 1.00228e+08
QT8(1) sum
QT board

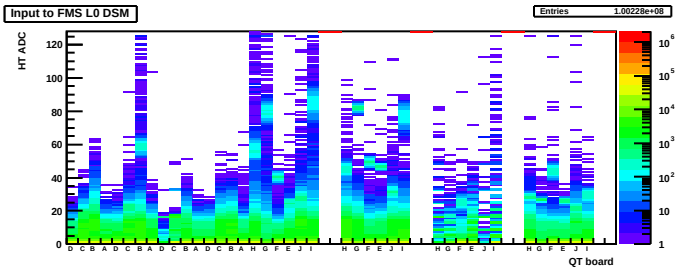

Input to FMS L0 DSM
Entries 66839
QT8(1) sum - simulated
QT board

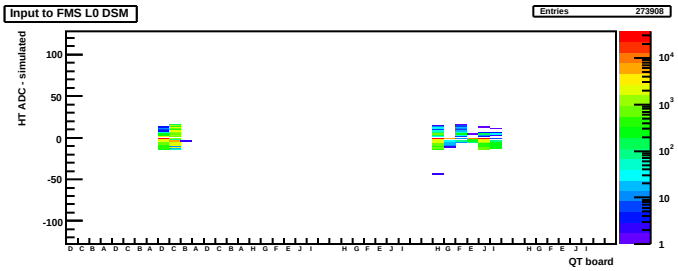

Input to FMS L0 DSM
Entries 1.00228e+08
QT8(2) sum
QT board

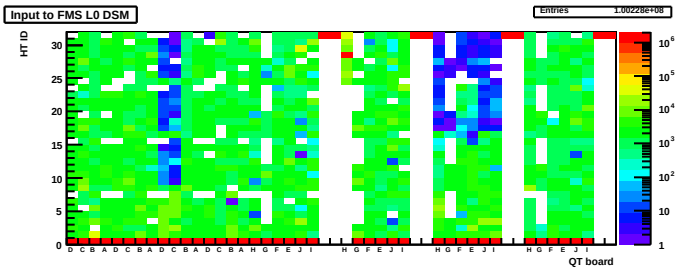

Input to FMS L0 DSM
Entries 1522704
QT8(2) sum - simulated
QT board

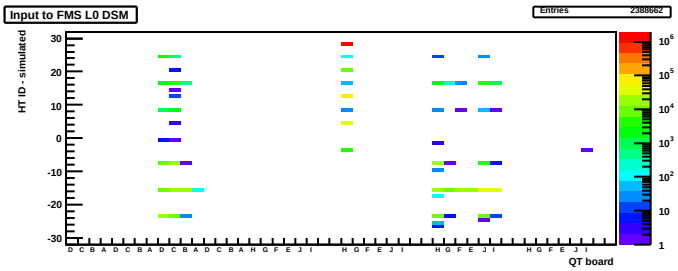

Input to FMS L0 DSM
Entries 1.00228e+08
QT8(3) sum
QT board

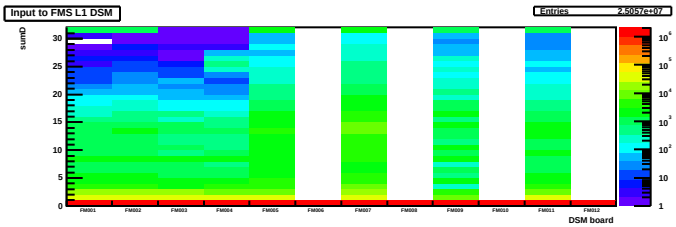

Input to FMS L0 DSM
Entries 2055565
QT8(3) sum - simulated
QT board

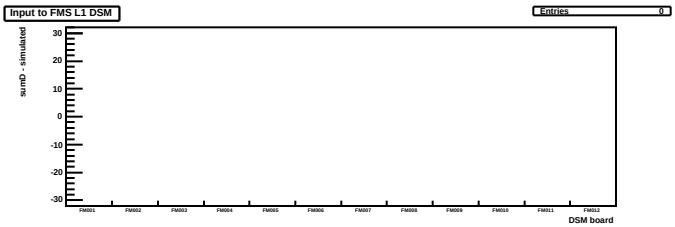

Input to FMS L0 DSM
Entries 1.00228e+08
HT ADC
QT board

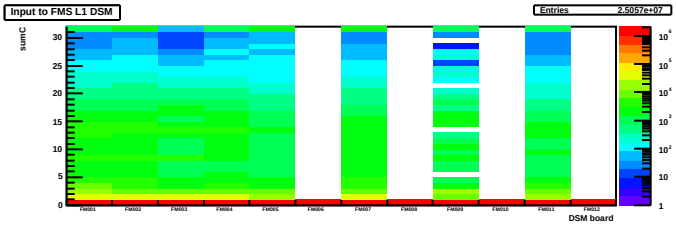

Input to FMS L0 DSM
Entries 273908
HT ADC - simulated
QT board

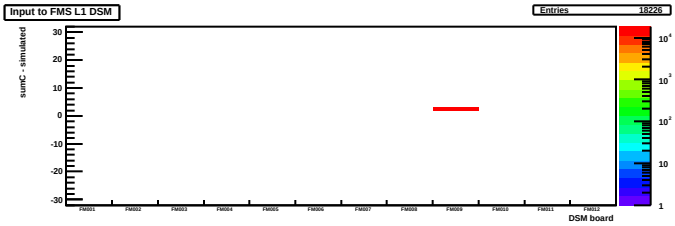

Input to FMS L0 DSM
Entries 1.00228e+08
HT ID
QT board

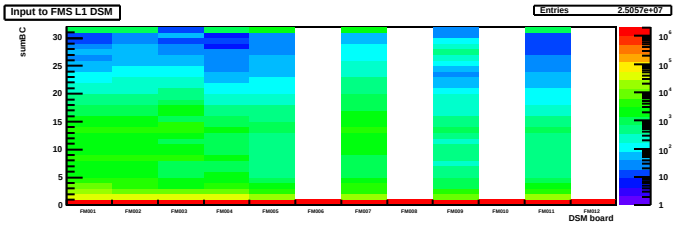

Input to FMS L0 DSM
Entries 2388662
HT ID - simulated
QT board

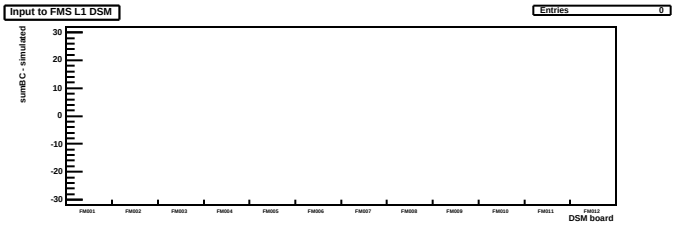

Input to FMS L1 DSM
Entries 2.5057e+07
sumD
DSM board

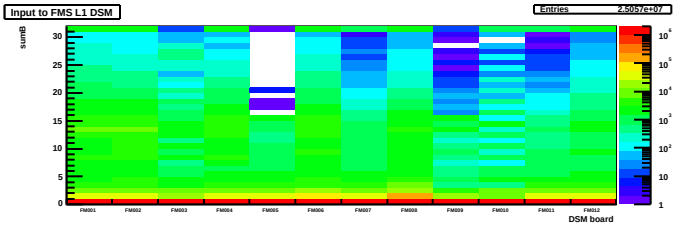

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

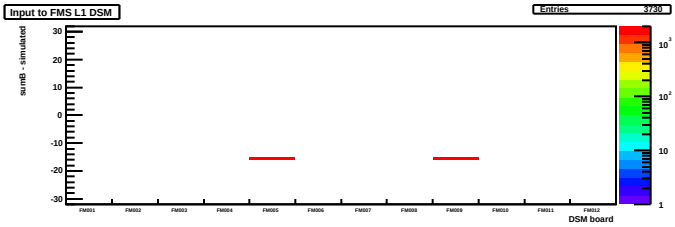

Input to FMS L1 DSM
Entries 2.5057e+07
sumC
DSM board

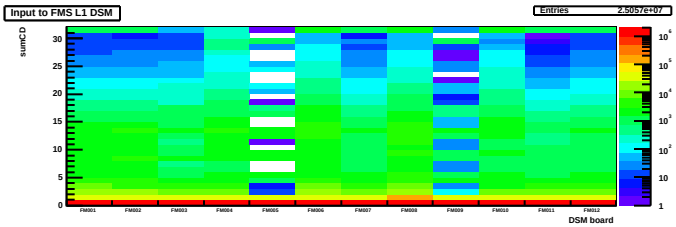

Input to FMS L1 DSM
Entries 18226
sumC - simulated
DSM board

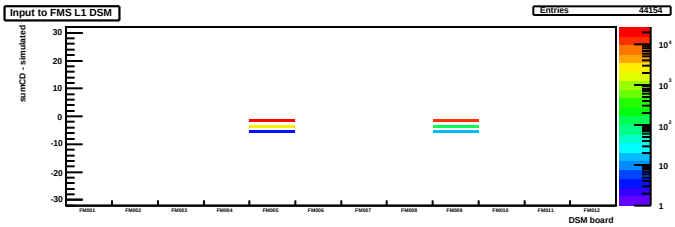

Input to FMS L1 DSM
Entries 2.5057e+07
sumBC
DSM board

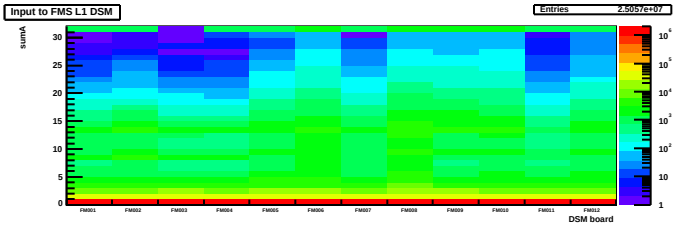

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

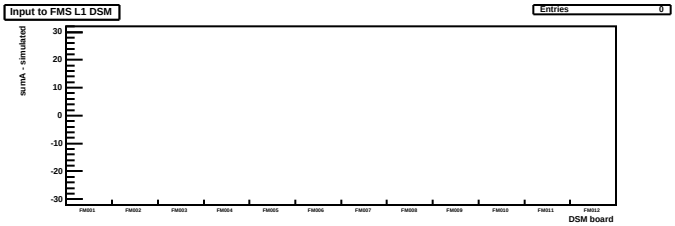

Input to FMS L1 DSM
Entries 2.5057e+07
sumB
DSM board

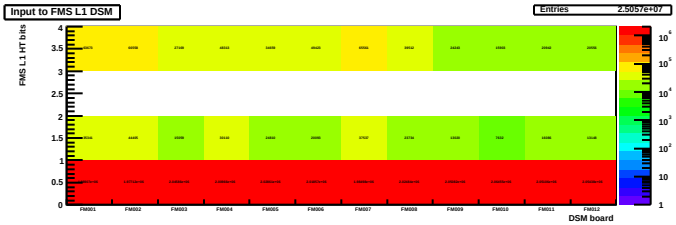

Input to FMS L1 DSM
Entries 3730
sumB - simulated
DSM board

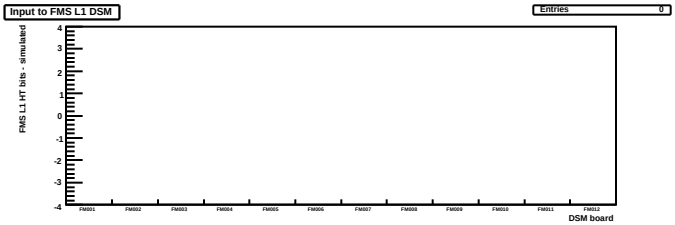

Input to FMS L1 DSM
Entries 2.5057e+07
sumCD
DSM board

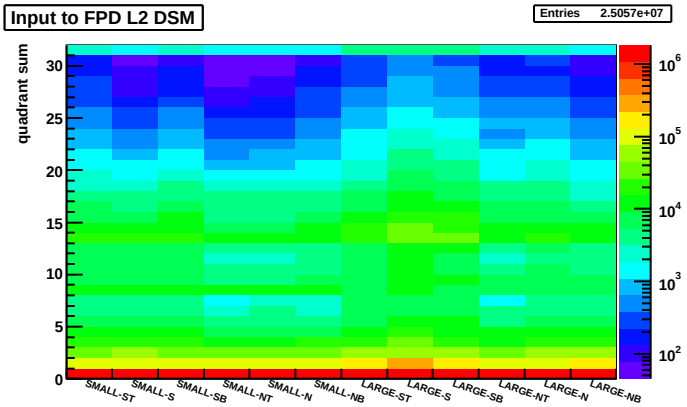

Input to FMS L1 DSM
Entries 44154
sumCD - simulated
DSM board

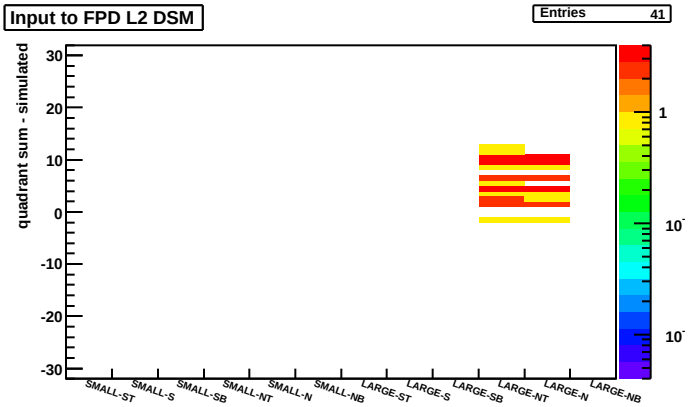

Input to FMS L1 DSM
Entries 2.5057e+07
sumA
DSM board

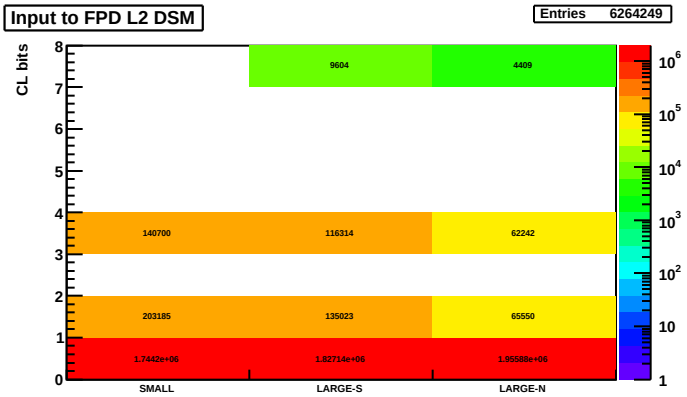

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

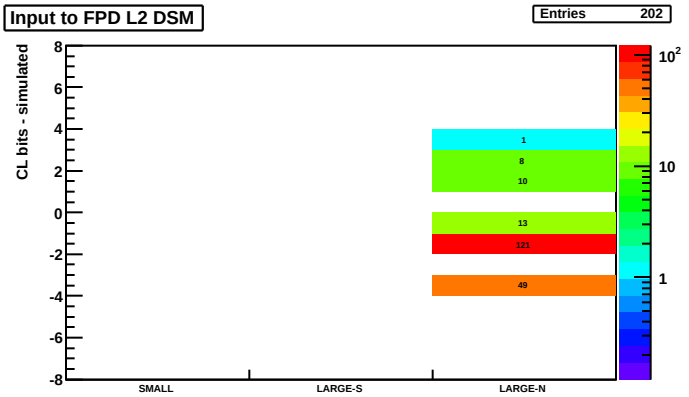

Input to FMS L1 DSM
Entries 2.5057e+07
FMS L1 HT bits
DSM board

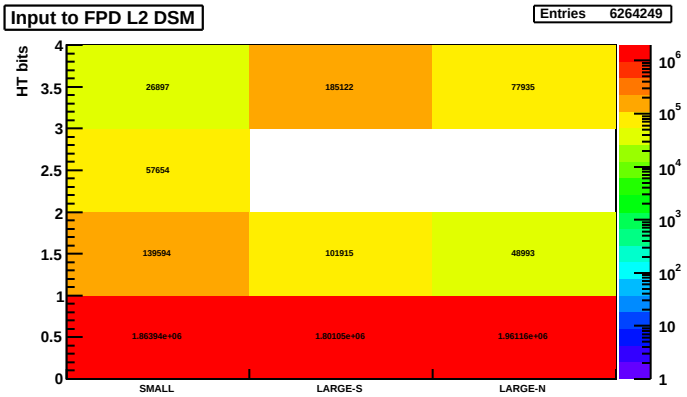

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

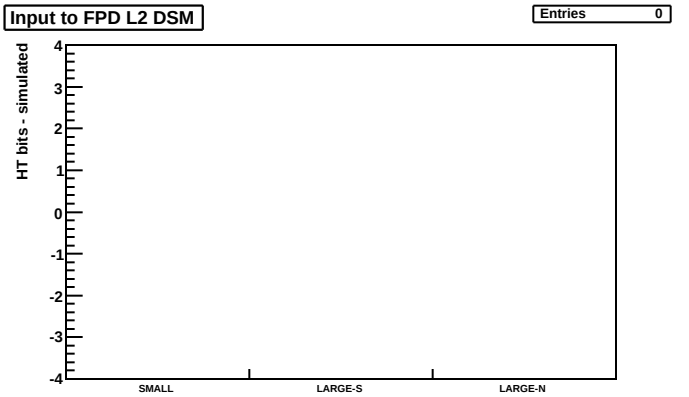

Input to FPD L2 DSM
Entries 2.5057e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

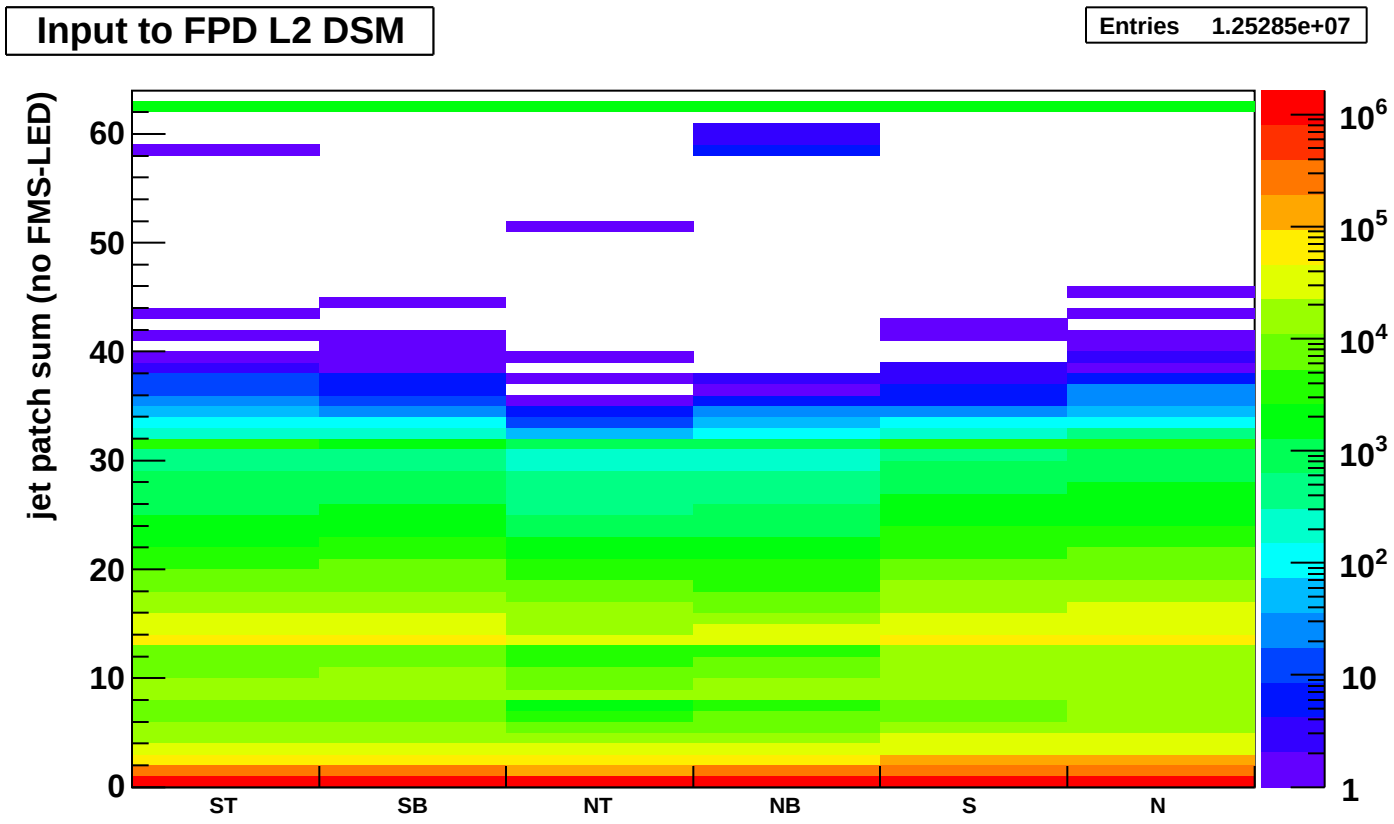

Input to FPD L2 DSM
Entries 41
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

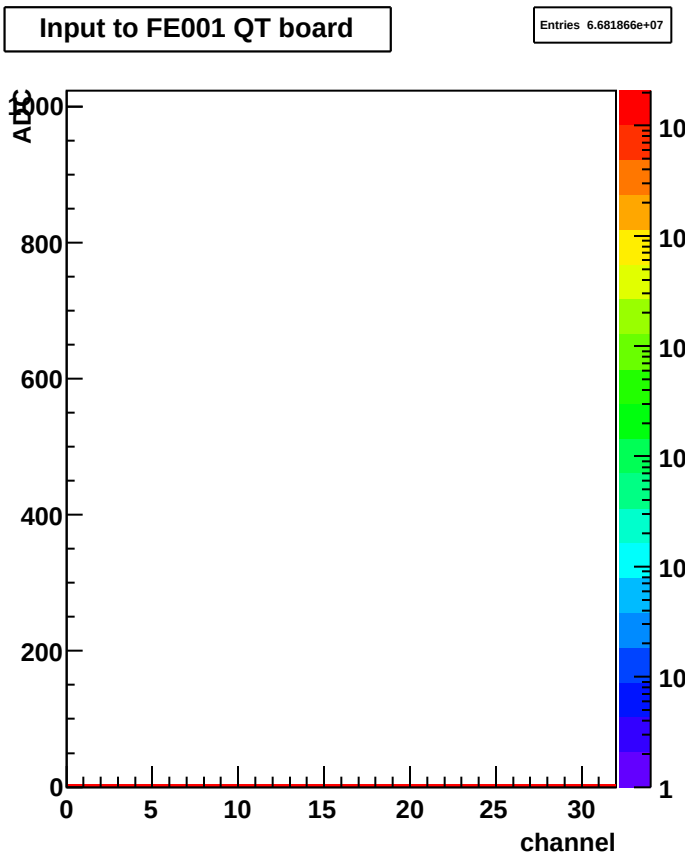

Input to FPD L2 DSM
Entries 6264249
CL bits
9604
4409
140700
116314
62242
203185
135023
65550
1.7442e+06
1.82714e+06
1.95588e+06
SMALL
LARGE-S
LARGE-N

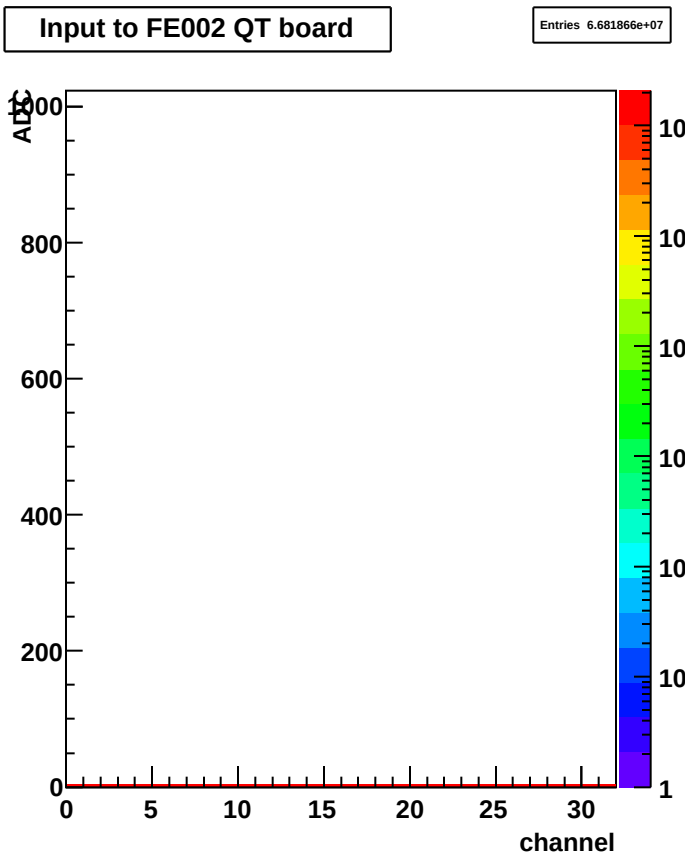

Input to FPD L2 DSM
Entries 202
CL bits - simulated
1
8
10
13
121
49
SMALL
LARGE-S
LARGE-N

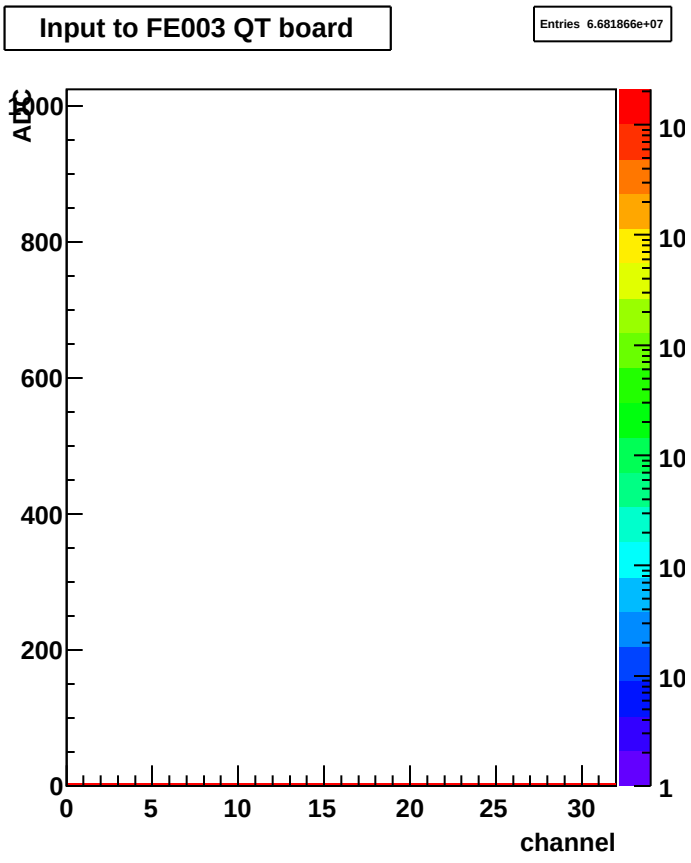

Input to FPD L2 DSM
Entries 6264249
HT bits
26897
185122
77935
57654
139594
101915
48993
1.86394e+06
1.80105e+06
1.96116e+06
SMALL
LARGE-S
LARGE-N

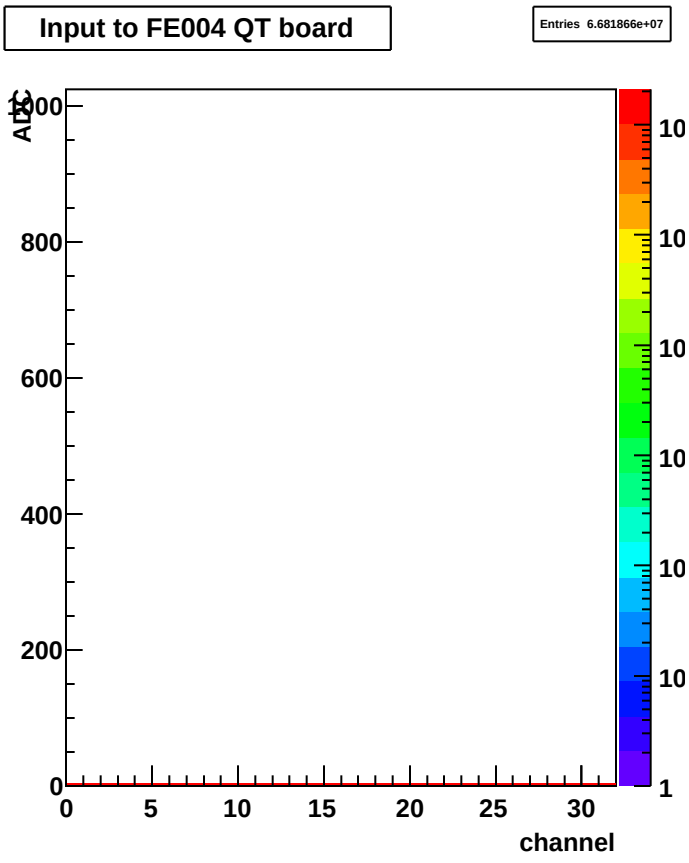

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

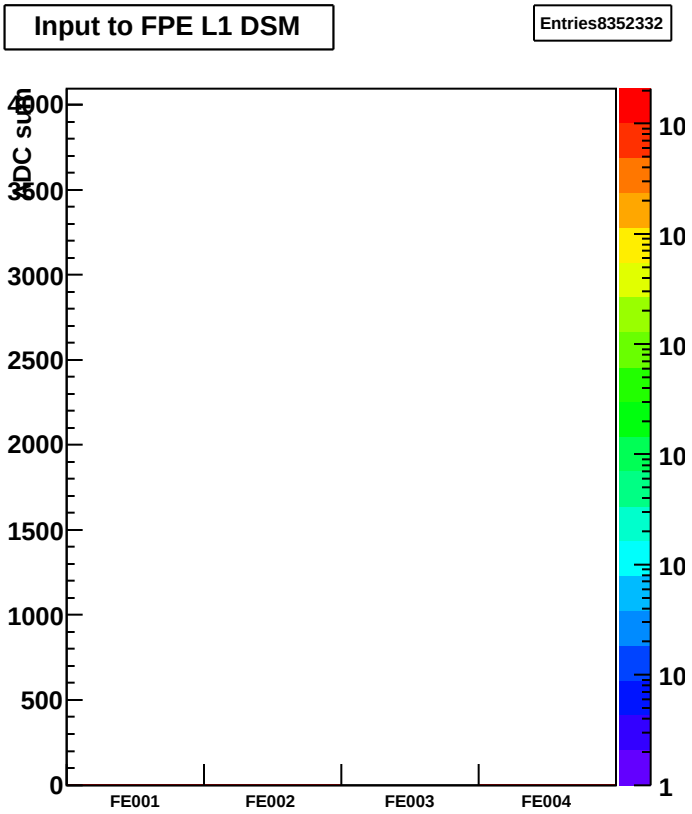

Input to FPD L2 DSM
Entries 1.25285e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

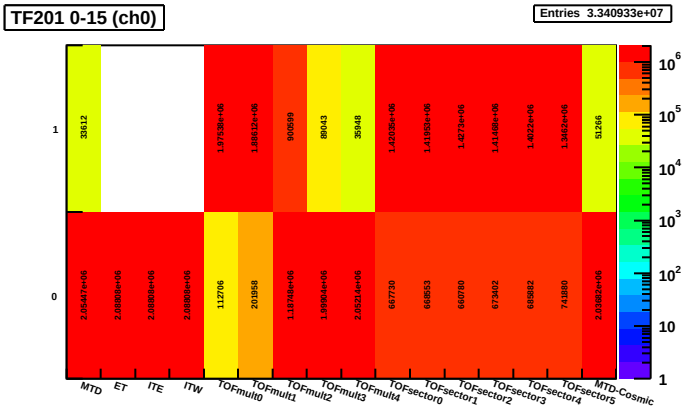

Input to FE001 QT board
Entries 6.681866e+07
ADC
channel

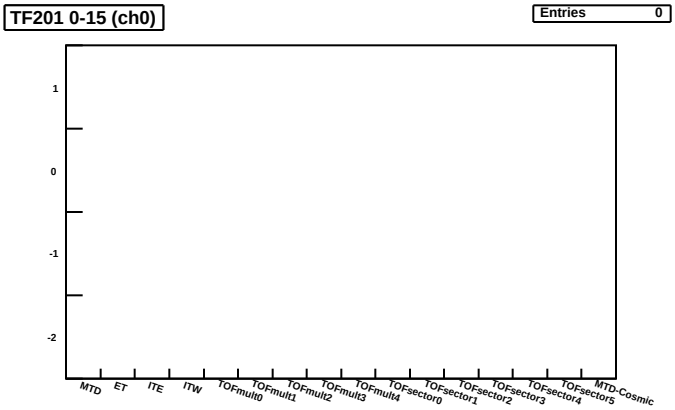

Input to FE002 QT board
Entries 6.681866e+07
ADC
channel

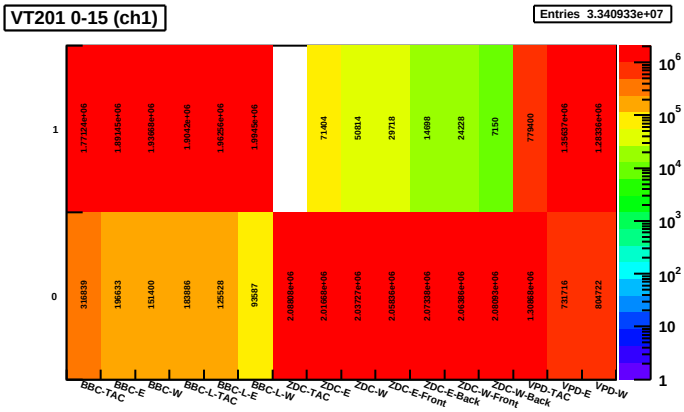

Input to FE003 QT board
Entries 6.681866e+07
ADC
channel


Input to FE004 QT board
Entries 6.681866e+07
ADC
channel


Input to FPE L1 DSM
Entries8352332
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 3.340933e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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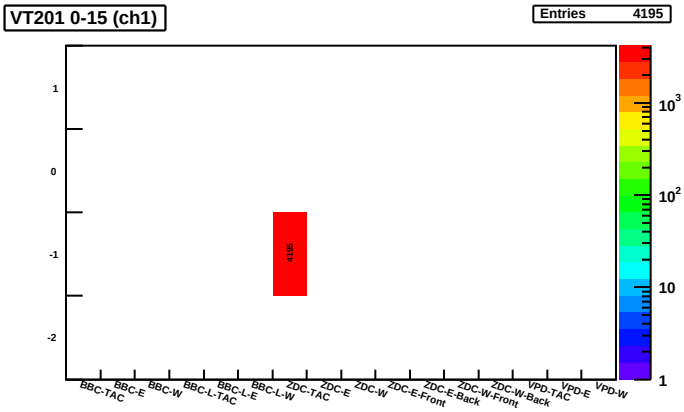
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4195
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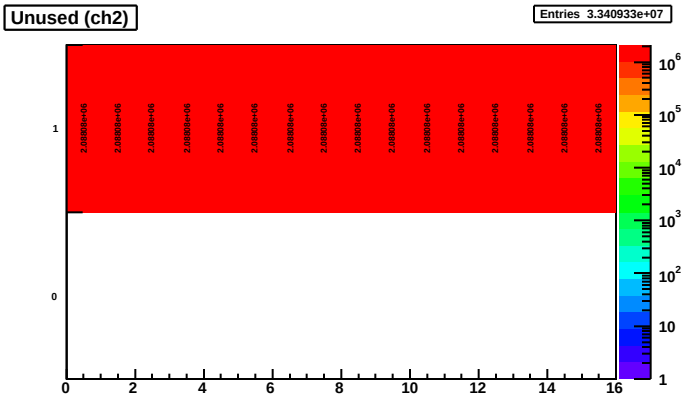
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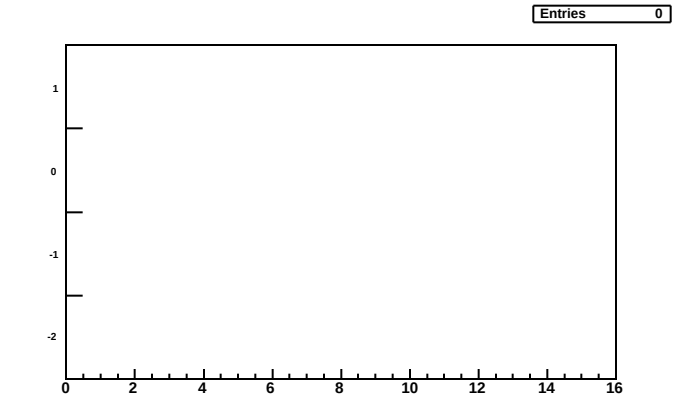
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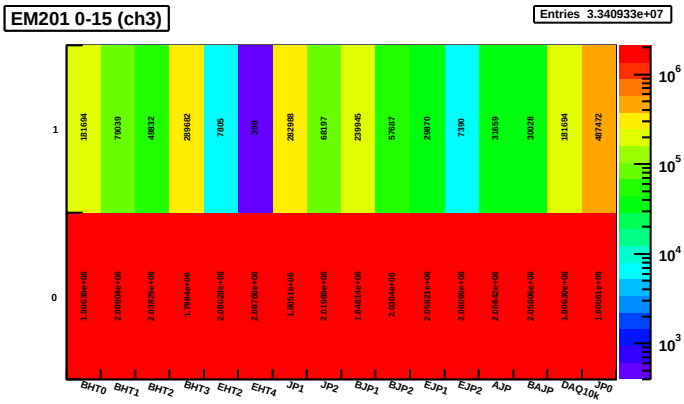
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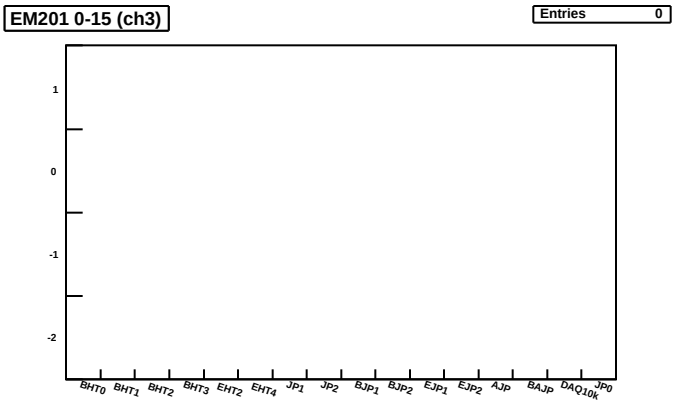
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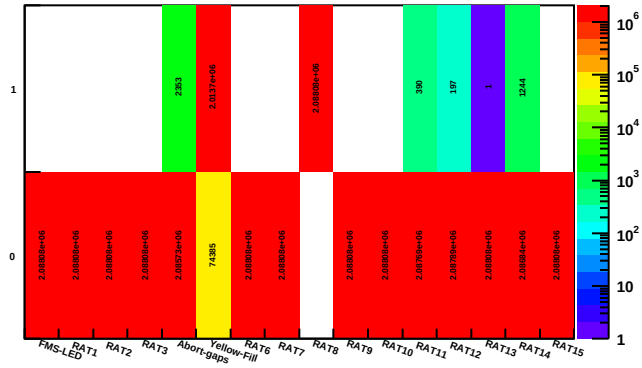
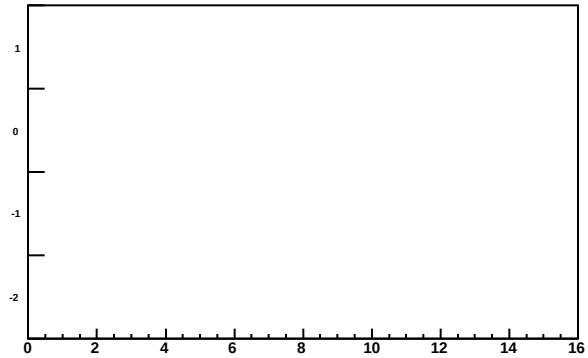
RAT board (ch4)





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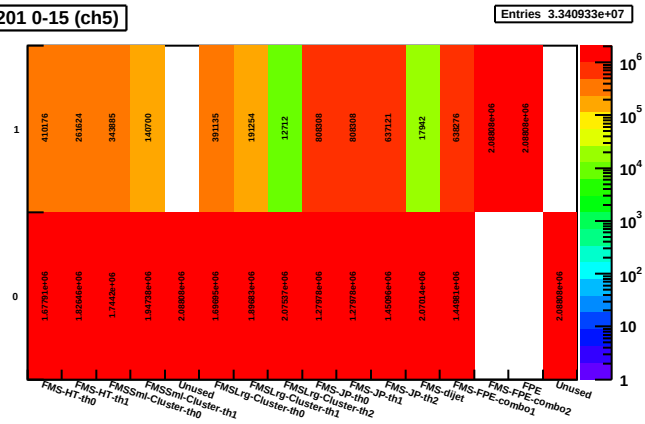

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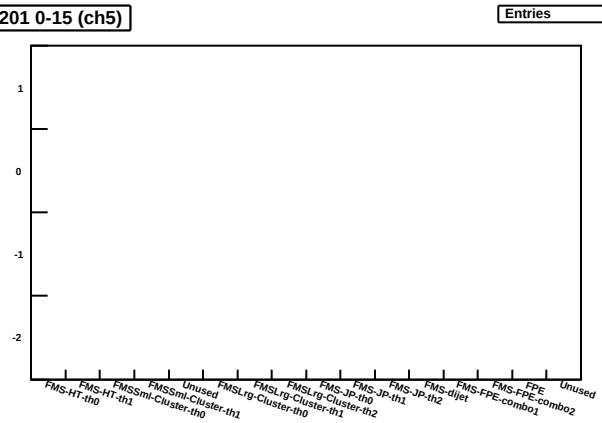
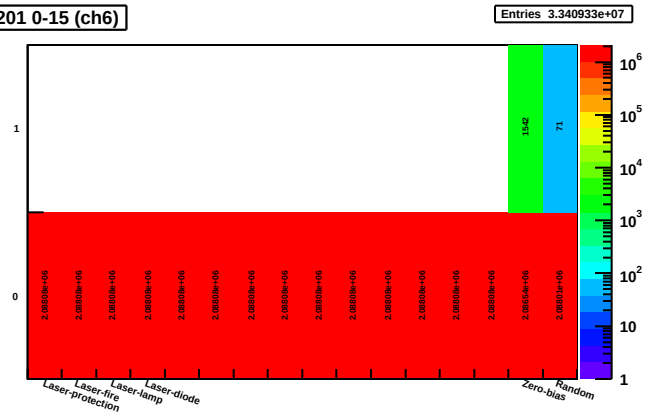
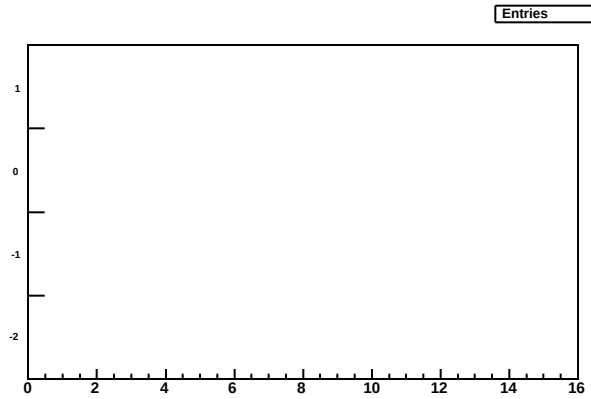
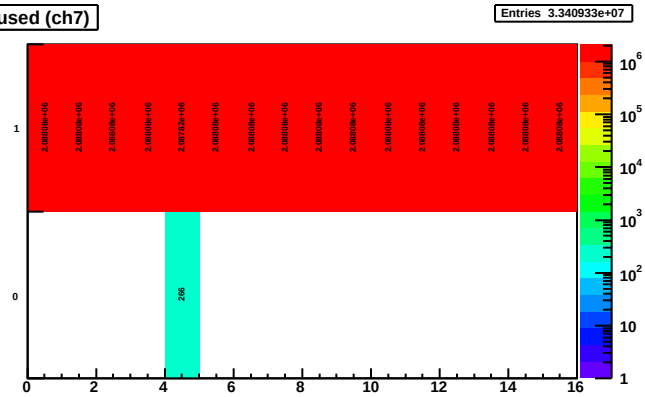
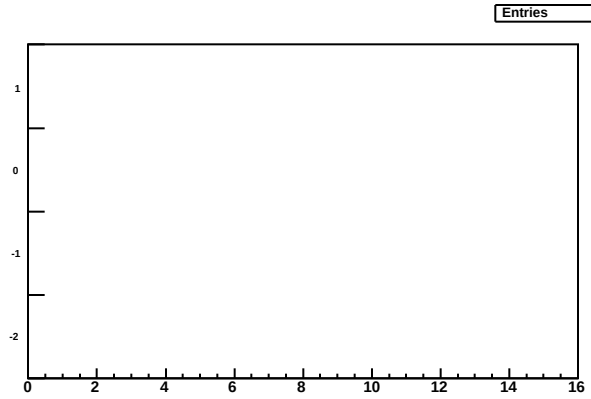


ST201 0-15 (ch6)





Unused (ch7)