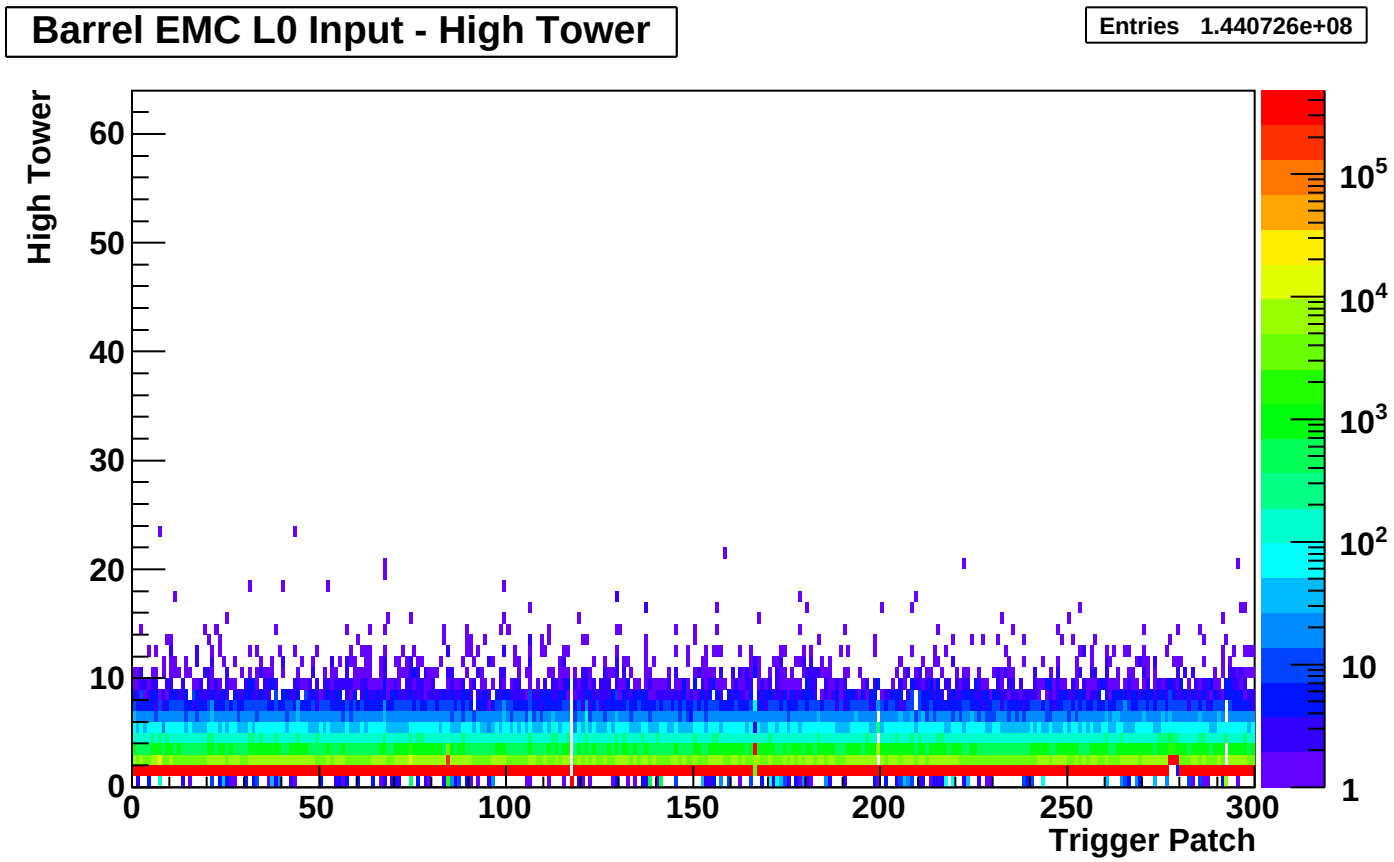
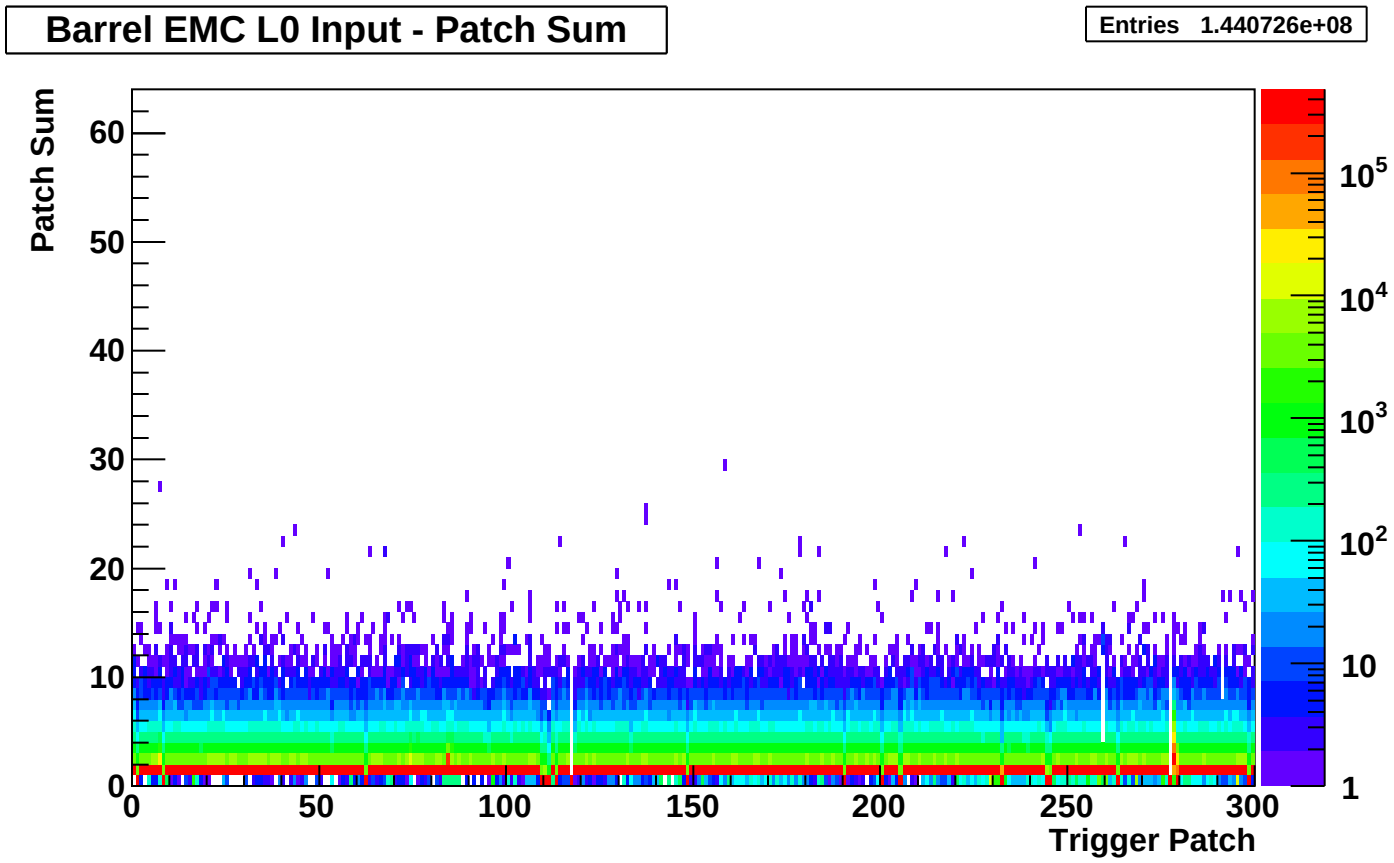
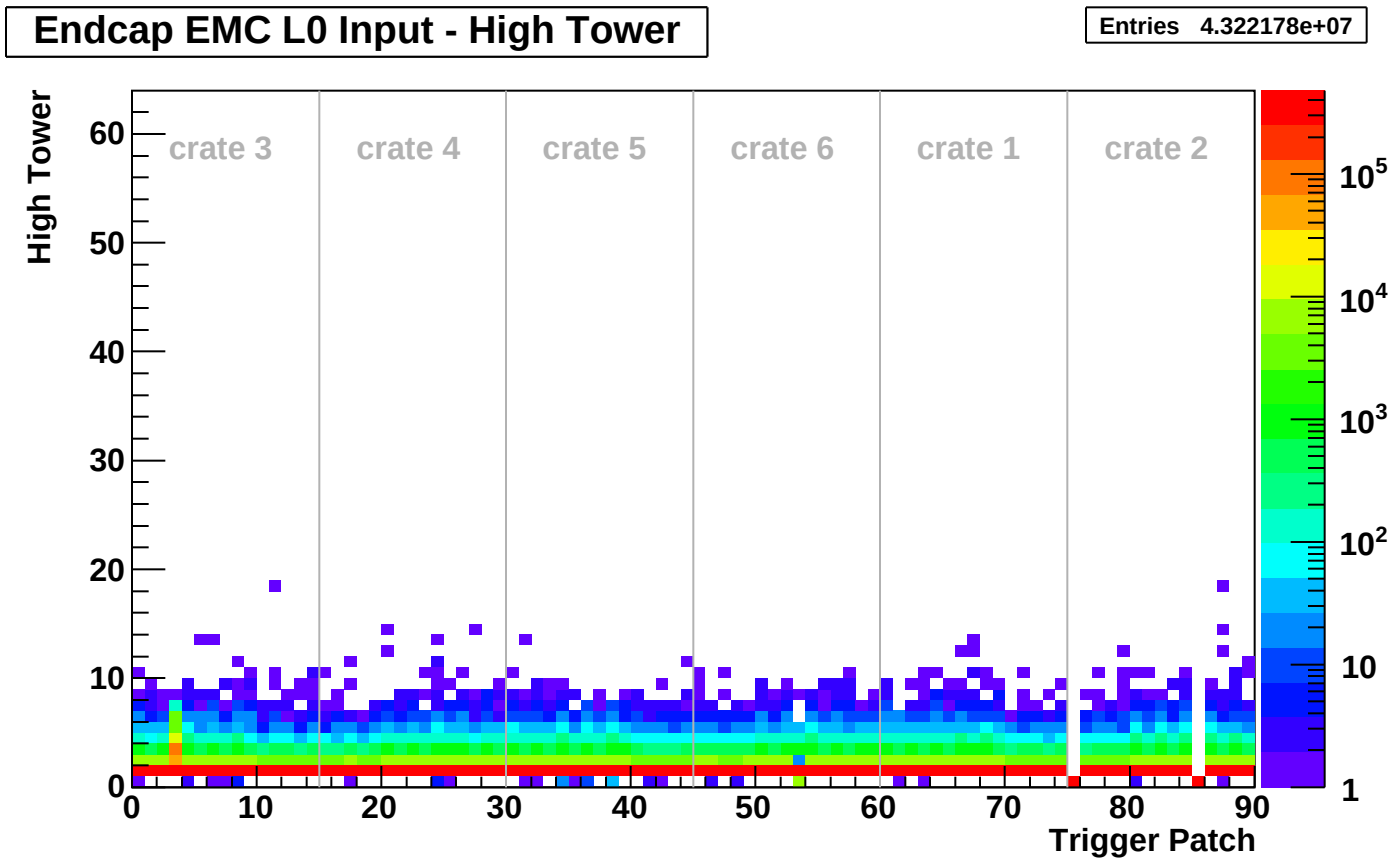
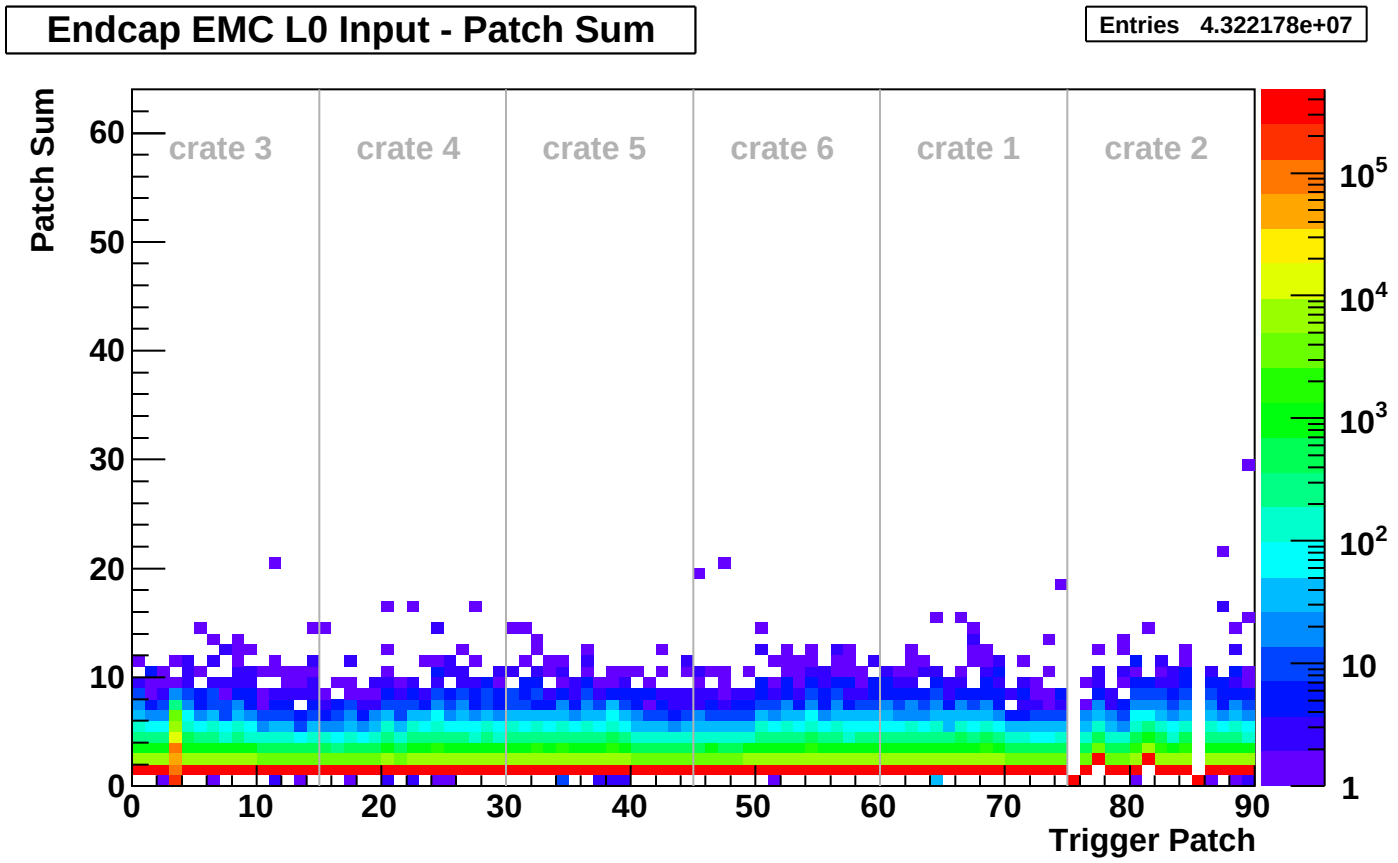
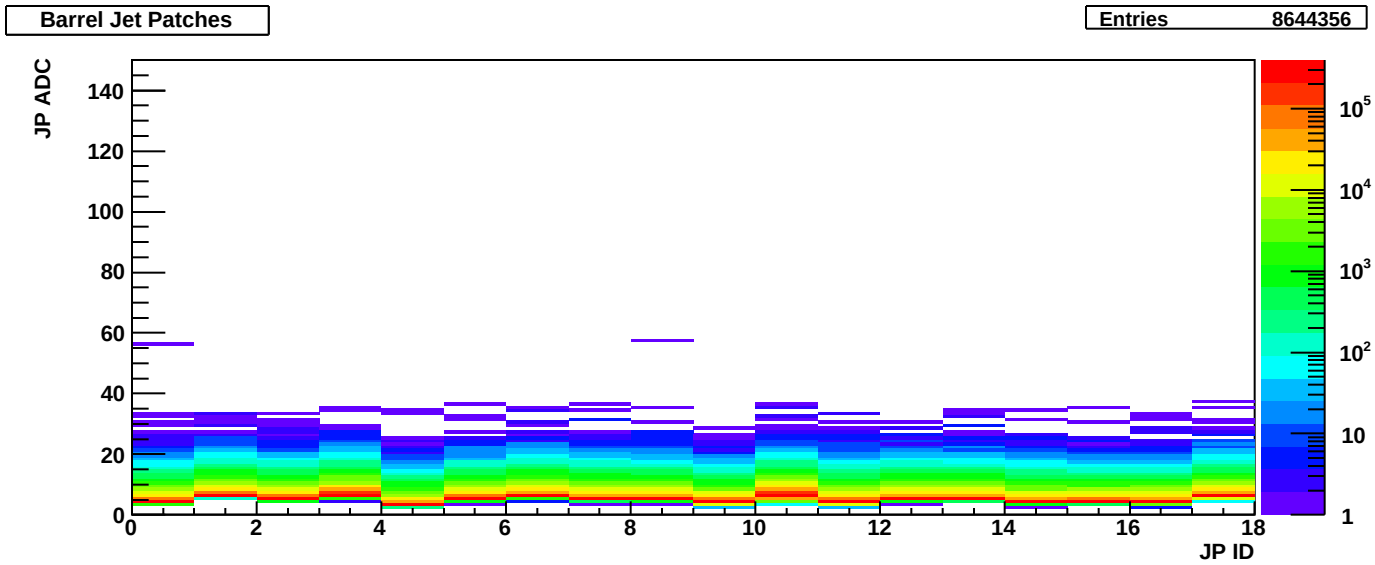
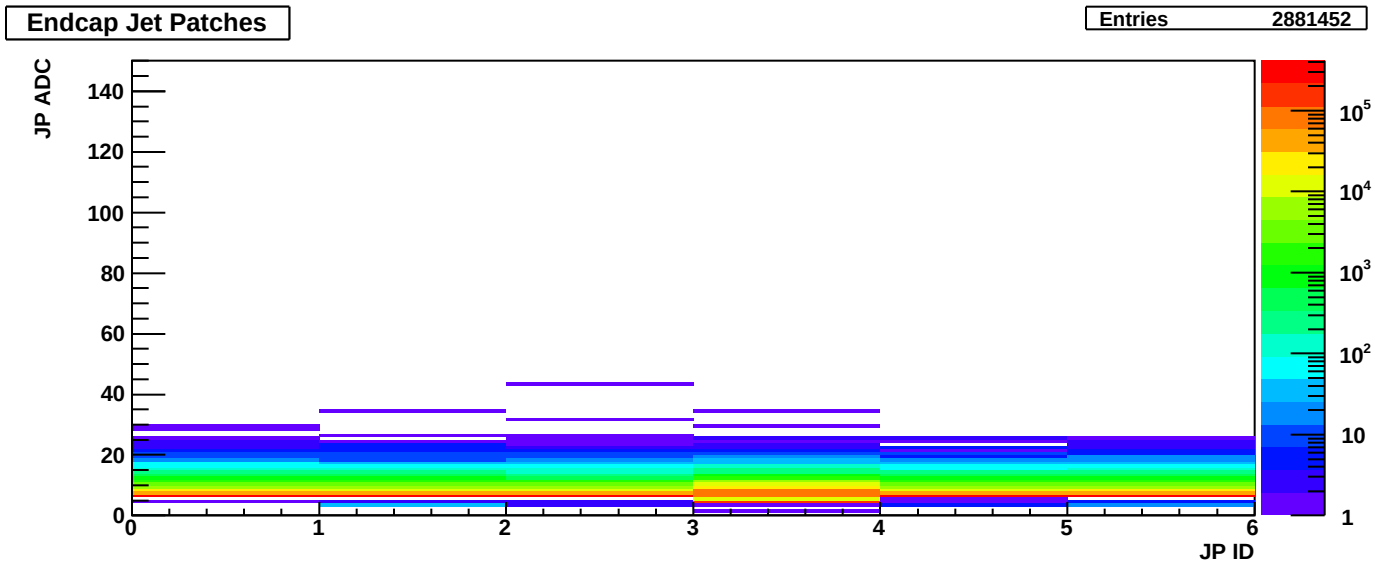

Barrel EMC L0 Input - High Tower
Entries 1.440726e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

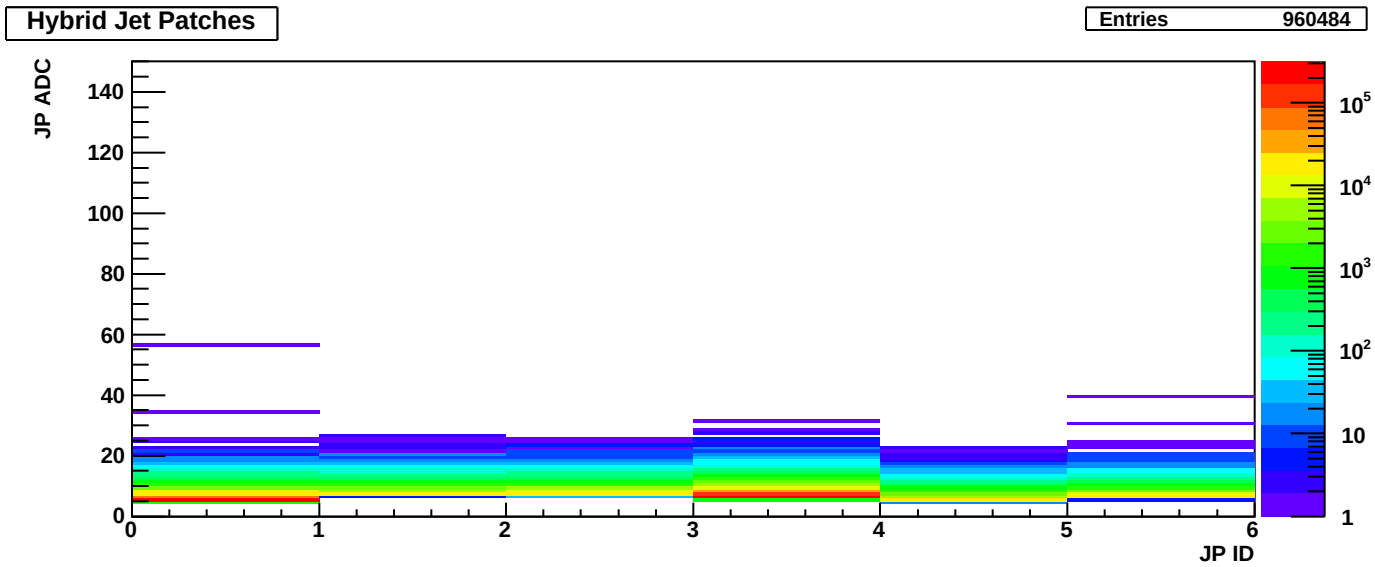

Barrel EMC L0 Input - Patch Sum
Entries 1.440726e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

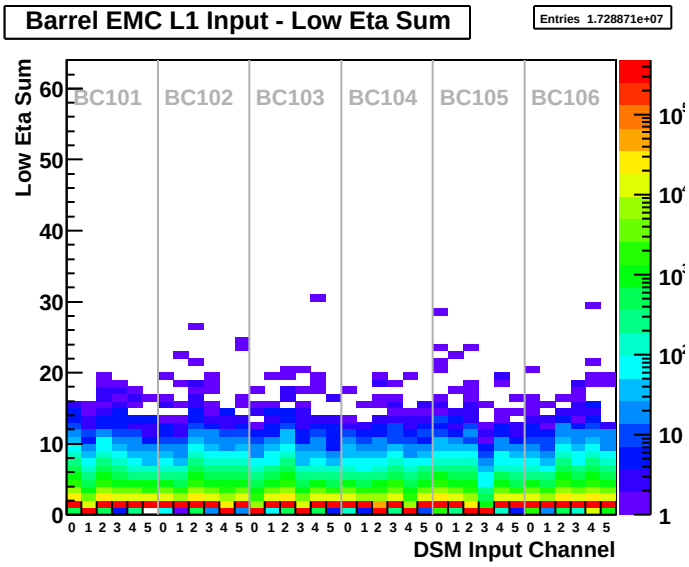

Endcap EMC L0 Input - High Tower
Entries 4.322178e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

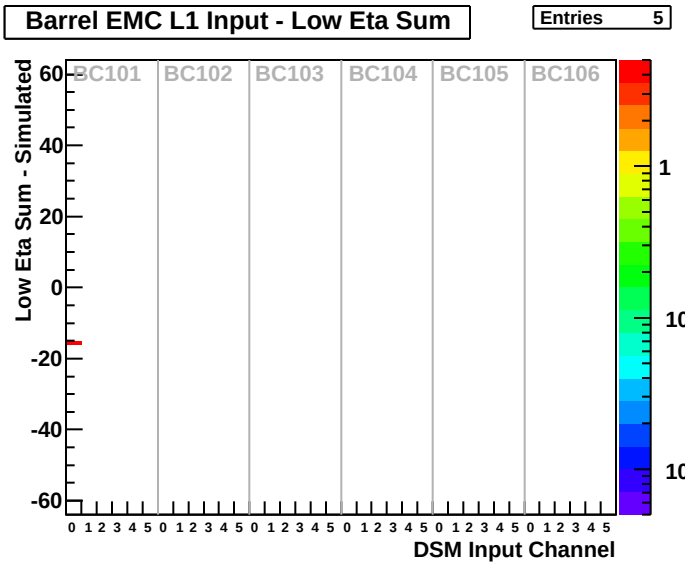

Endcap EMC L0 Input - Patch Sum
Entries 4.322178e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

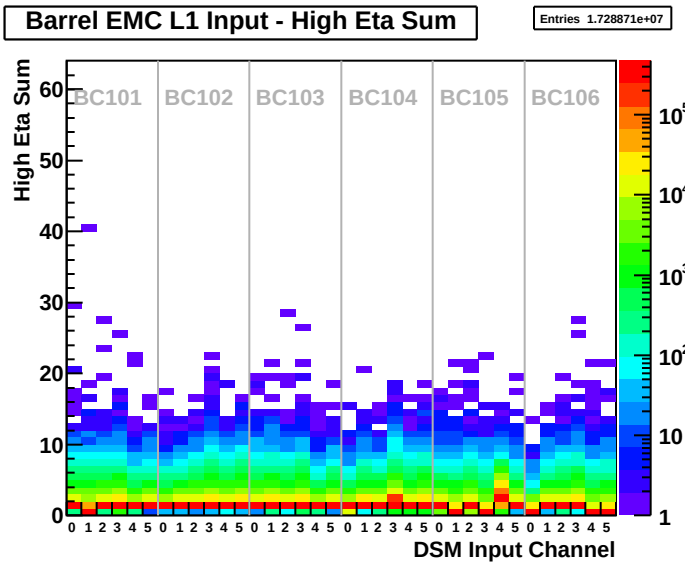

Barrel Jet Patches
Entries 8644356
JP ADC
JP ID

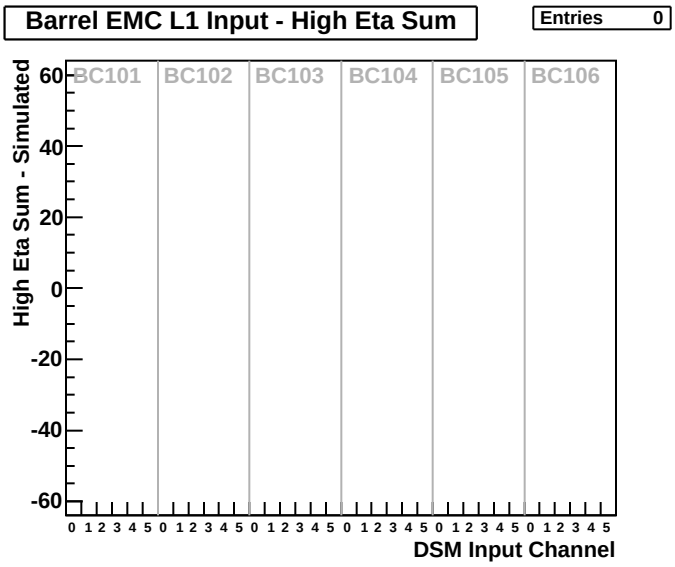

Endcap Jet Patches
Entries 2881452
JP ADC
JP ID

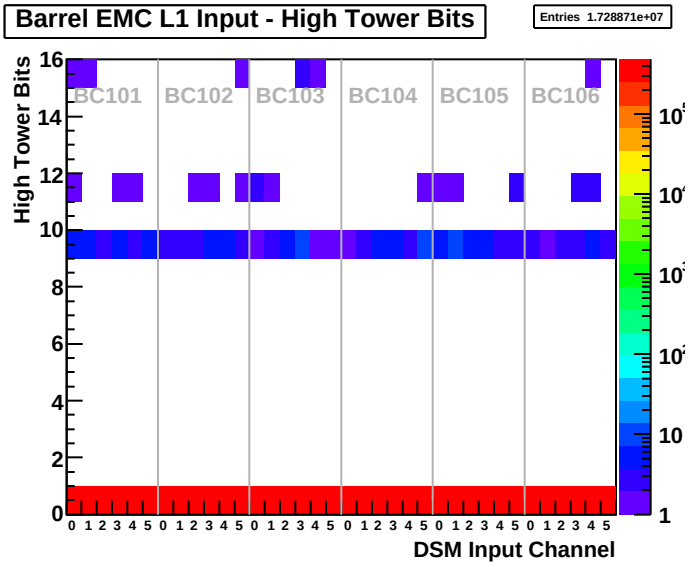

Hybrid Jet Patches
Entries 960484
JP ADC
JP ID

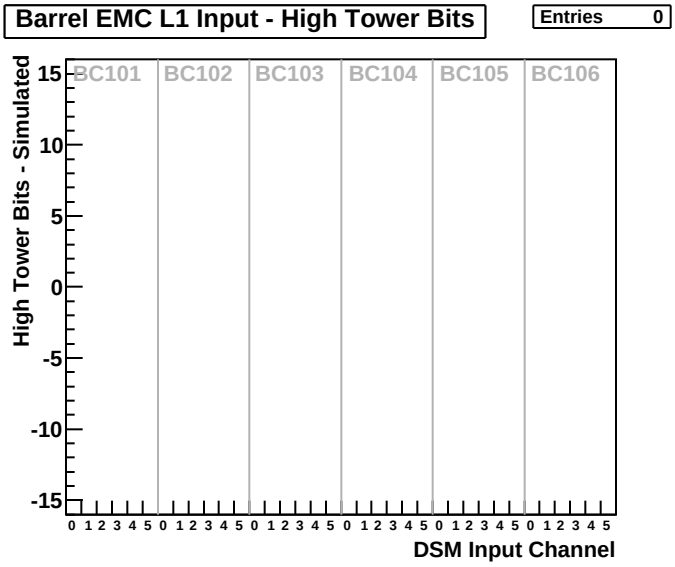

Barrel EMC L1 Input - Low Eta Sum
Entries 1.728871e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

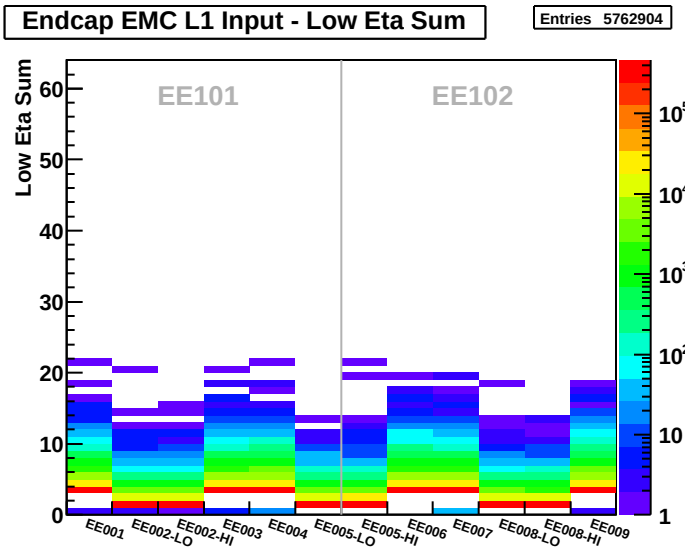

Barrel EMC L1 Input - Low Eta Sum
Entries 5
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

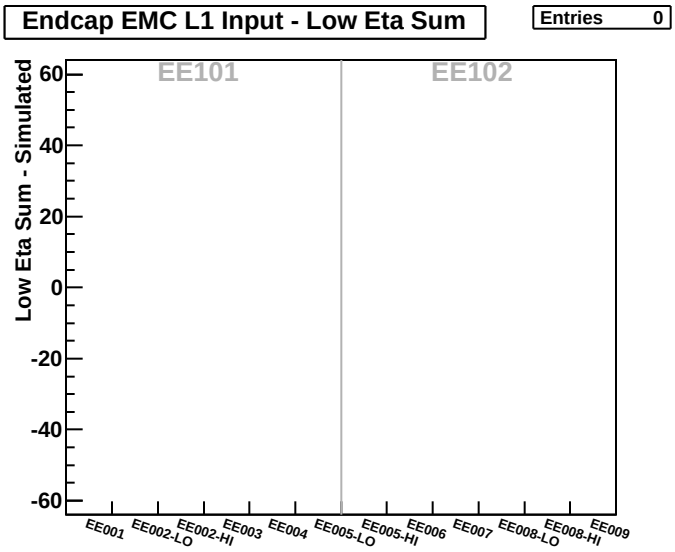

Barrel EMC L1 Input - High Eta Sum
Entries 1.728871e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

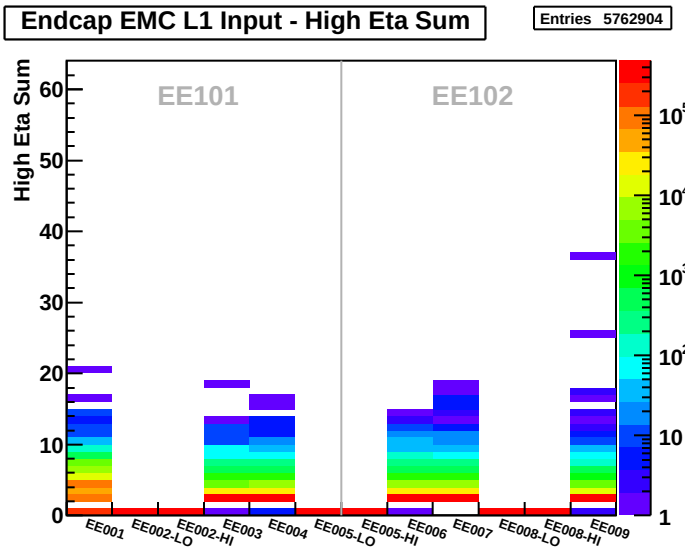

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

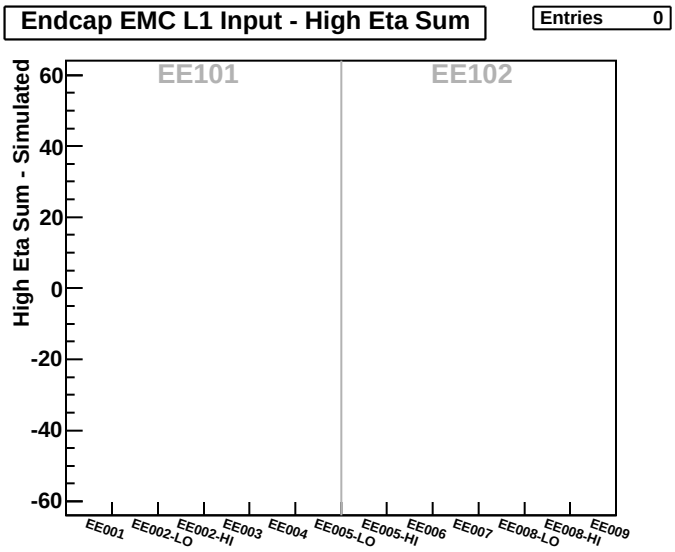

Barrel EMC L1 Input - High Tower Bits
Entries 1.728871e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

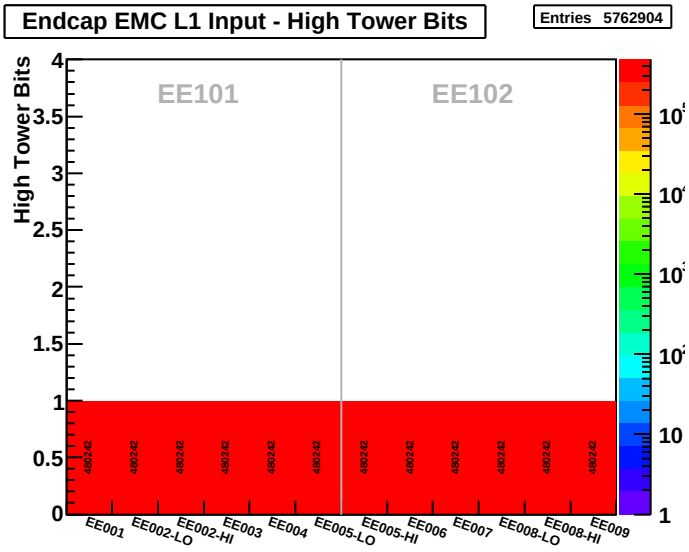

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

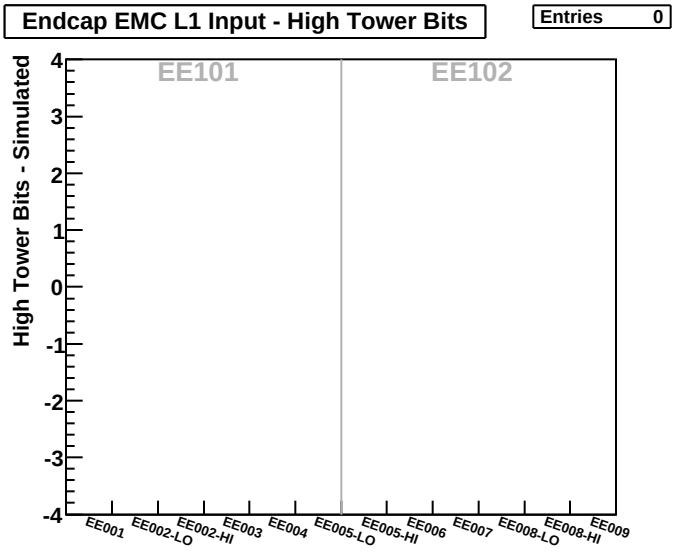

Endcap EMC L1 Input - Low Eta Sum
Entries 5762904
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

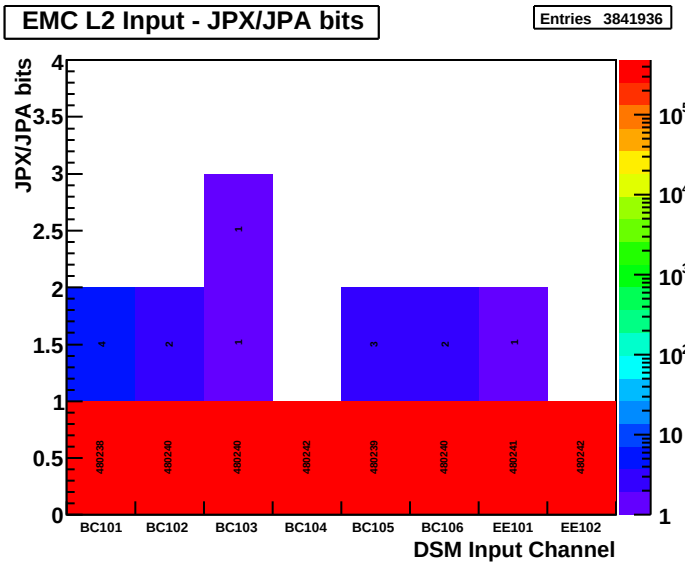

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

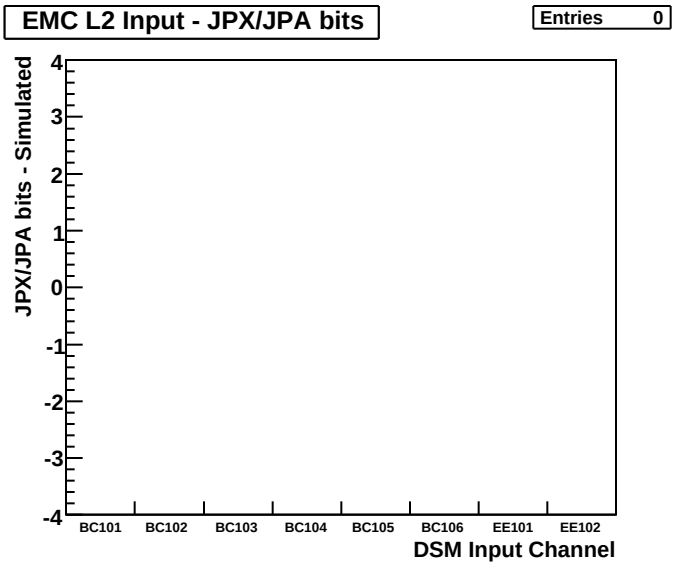

Endcap EMC L1 Input - High Eta Sum
Entries 5762904
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

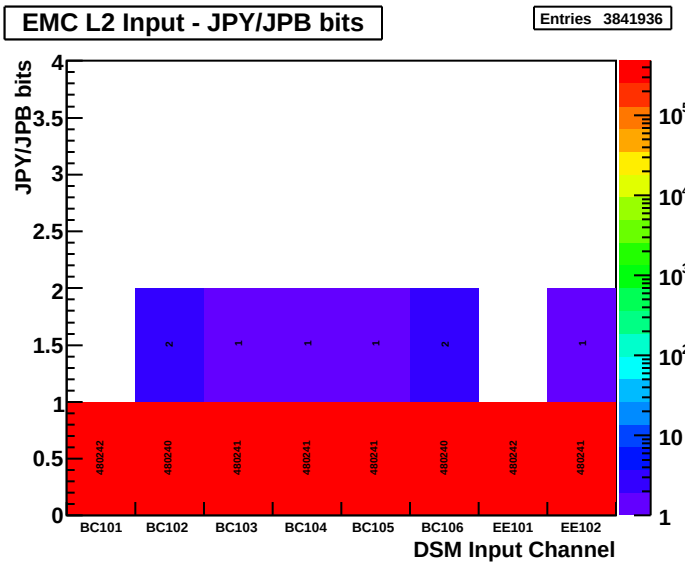

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

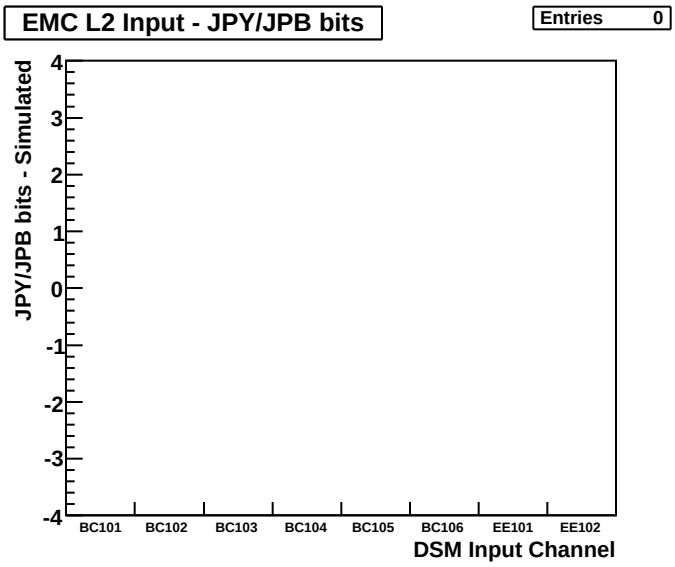

Endcap EMC L1 Input - High Tower Bits
Entries 5762904
High Tower Bits
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

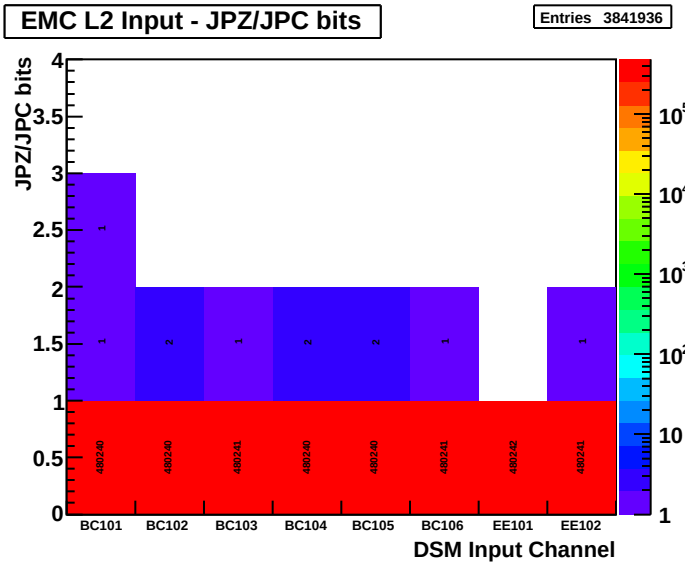

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

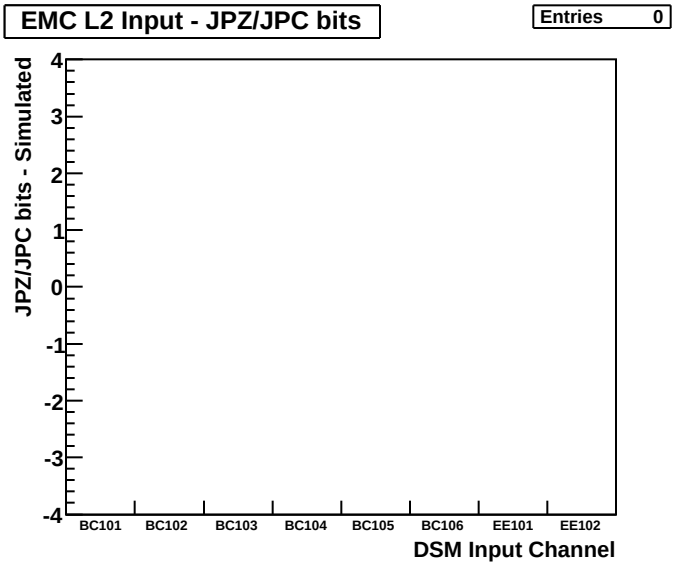

EMC L2 Input - JPX/JPA bits
Entries 3841936
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

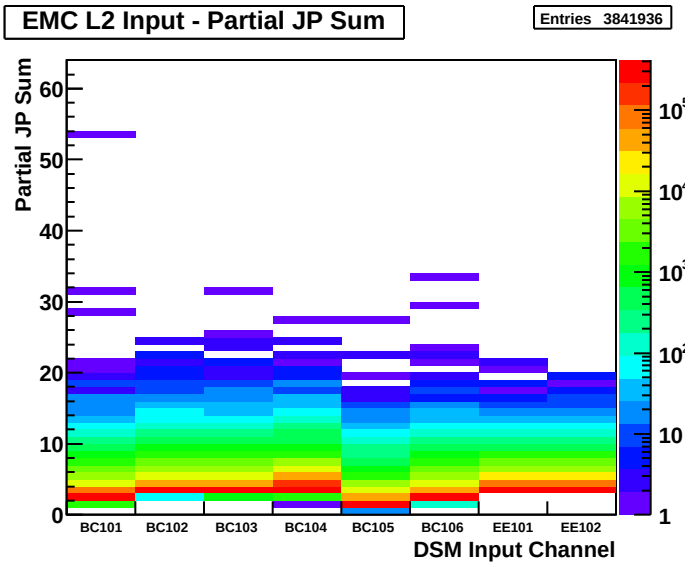

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

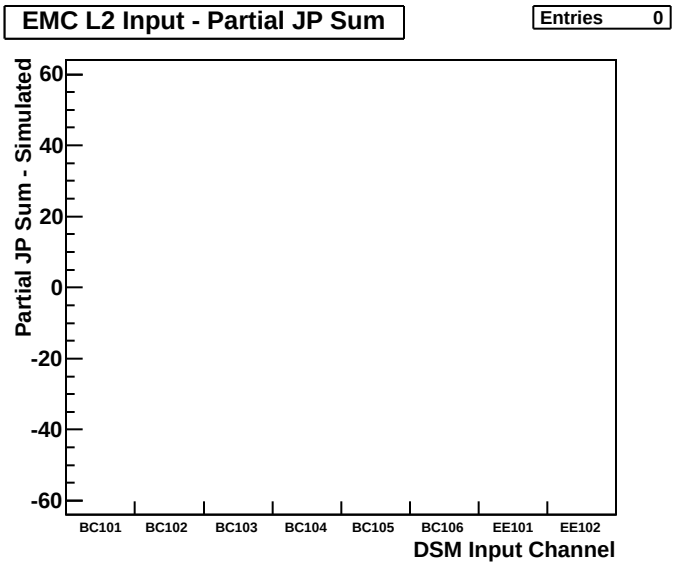

EMC L2 Input - JPY/JPB bits
Entries 3841936
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

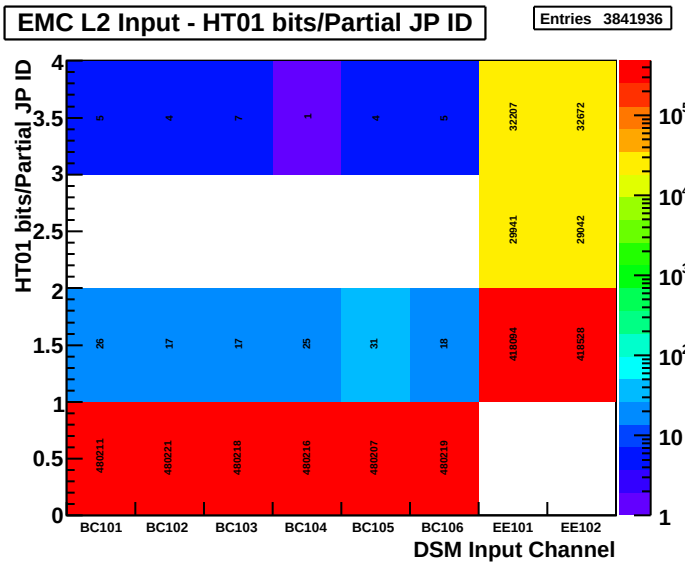

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

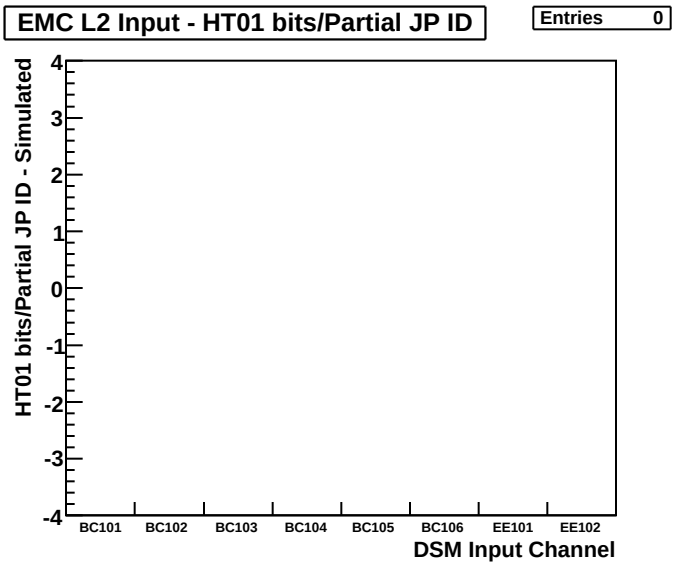

EMC L2 Input - JPZ/JPC bits
Entries 3841936
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

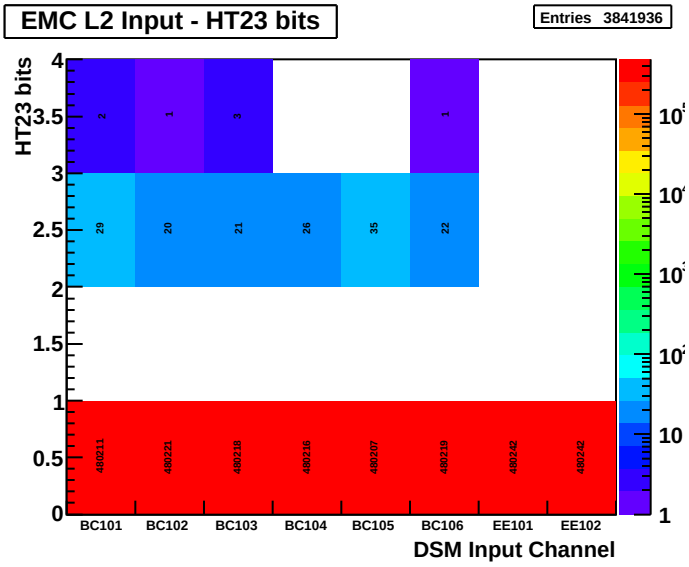

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

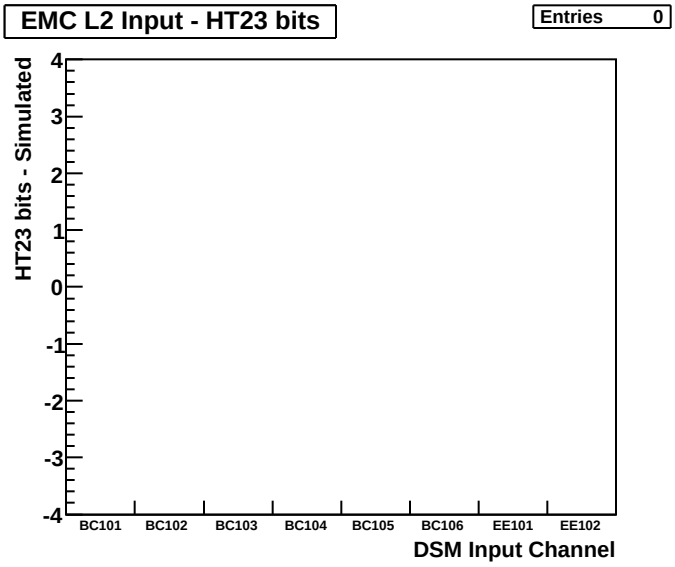

EMC L2 Input - Partial JP Sum
Entries 3841936
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

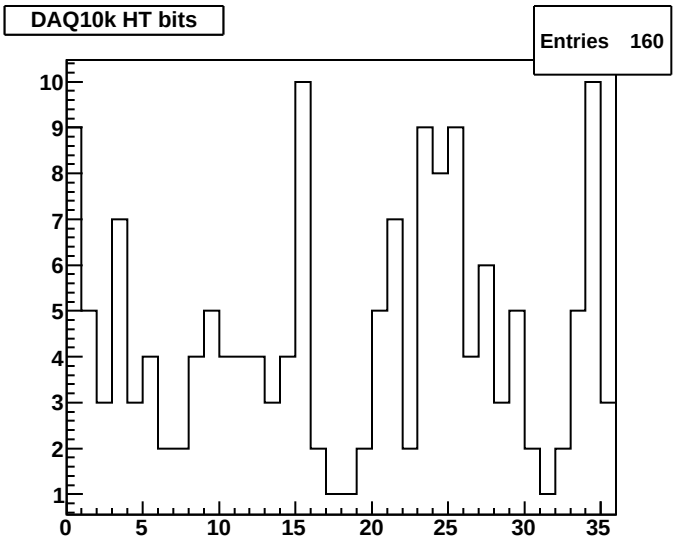

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

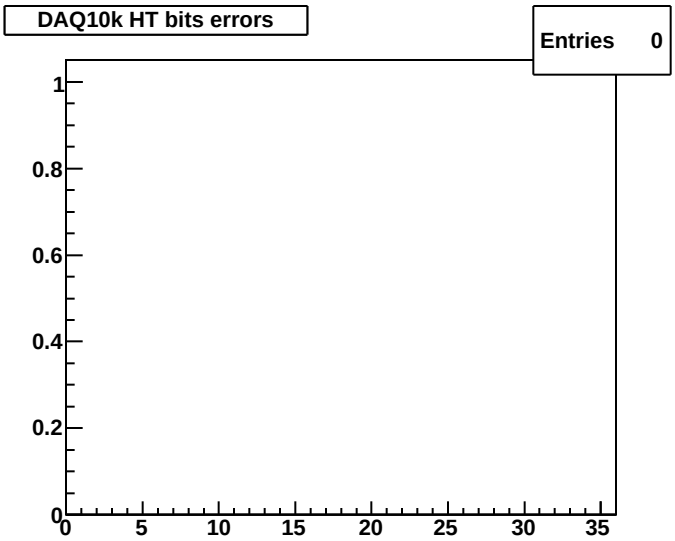

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3841936
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

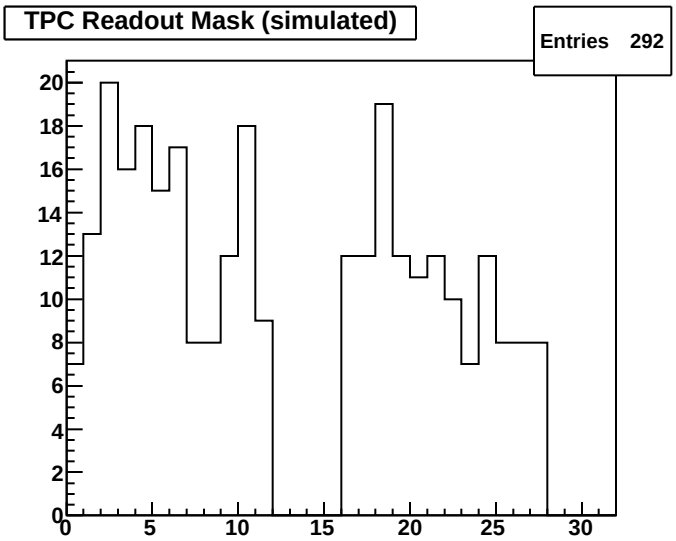

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

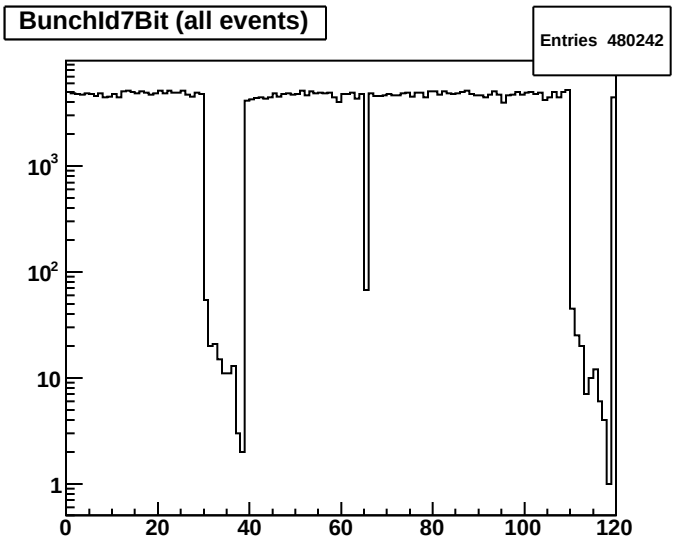

EMC L2 Input - HT23 bits
Entries 3841936
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

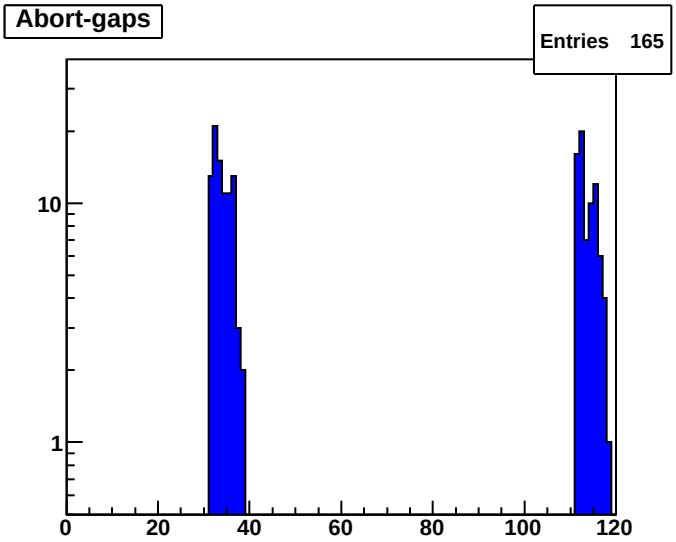

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

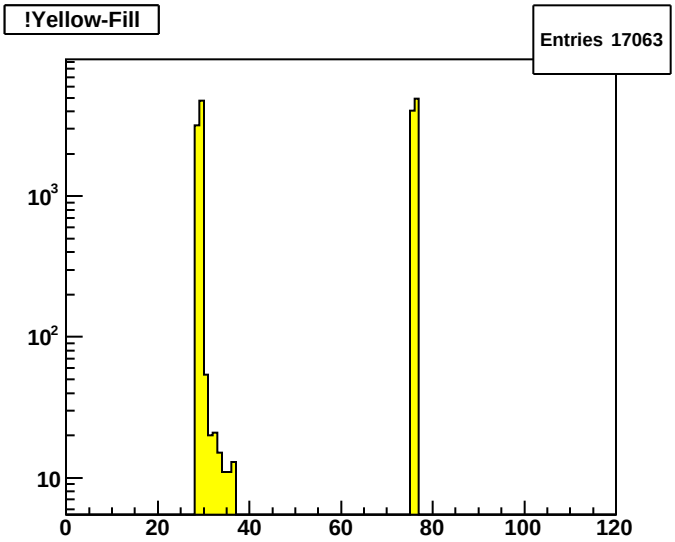

DAQ10k HT bits
Entries 160

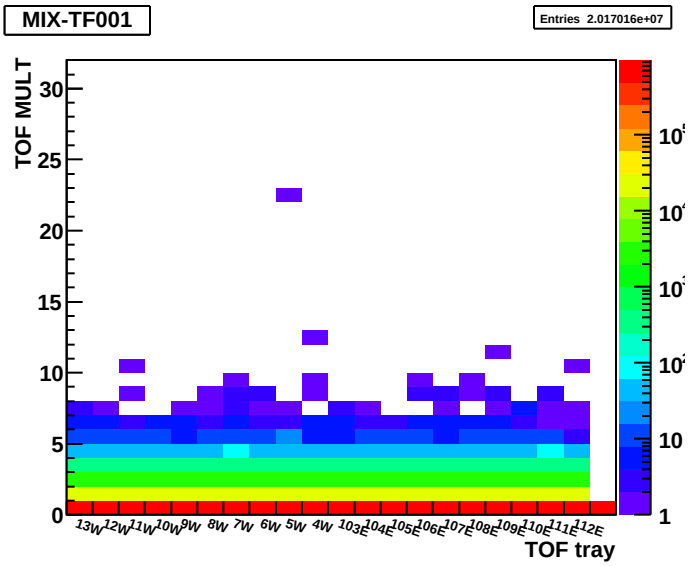

DAQ10k HT bits errors
Entries 0

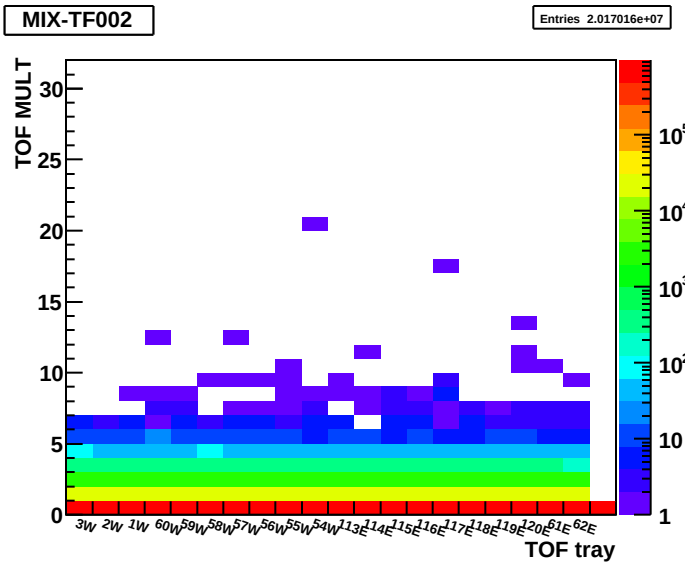

TPC Readout Mask (simulated)
Entries 292

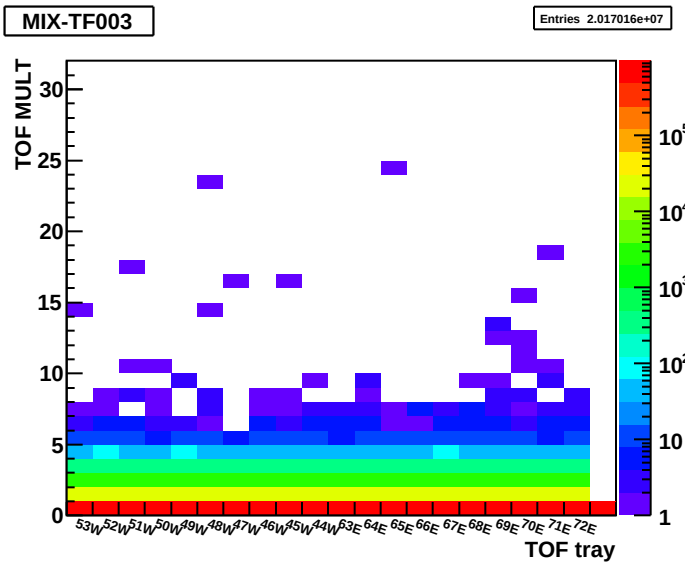

BunchId7Bit (all events)
Entries 480242

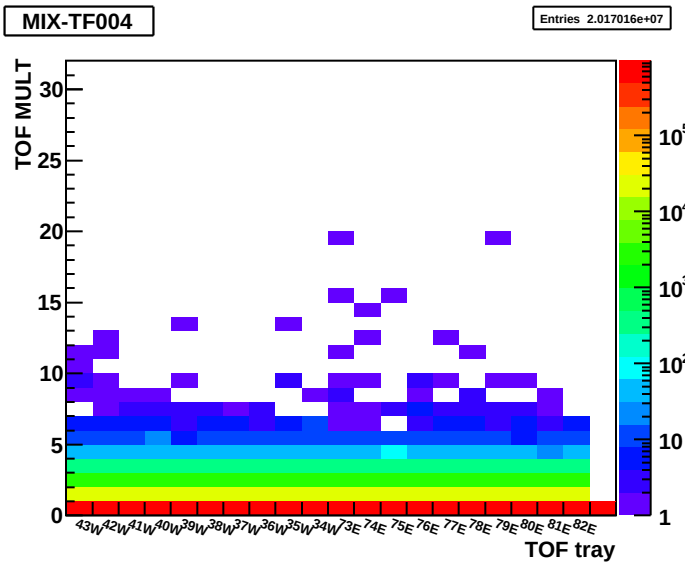

Abort-gaps
Entries 165

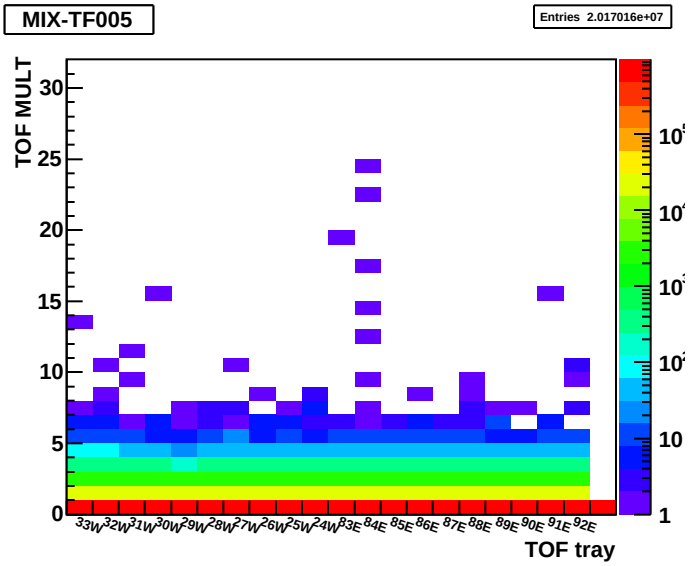

!Yellow-Fill
Entries 17063

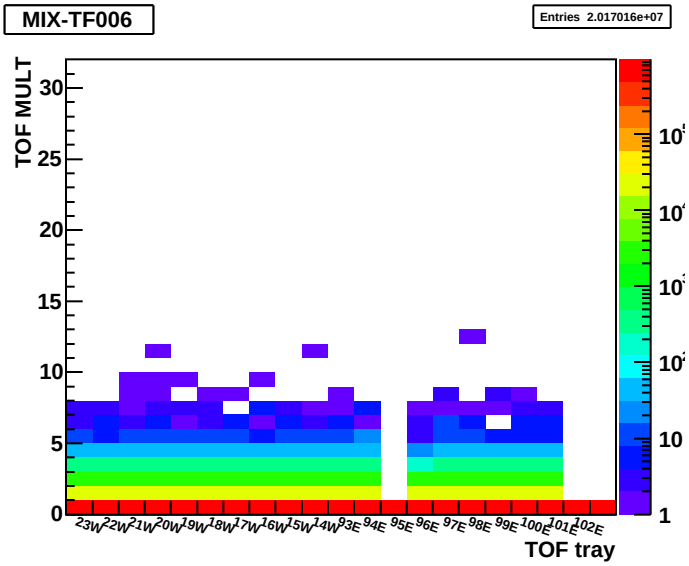

MIX-TF001
Entries 2.017016e+07
TOF MULT
TOF tray

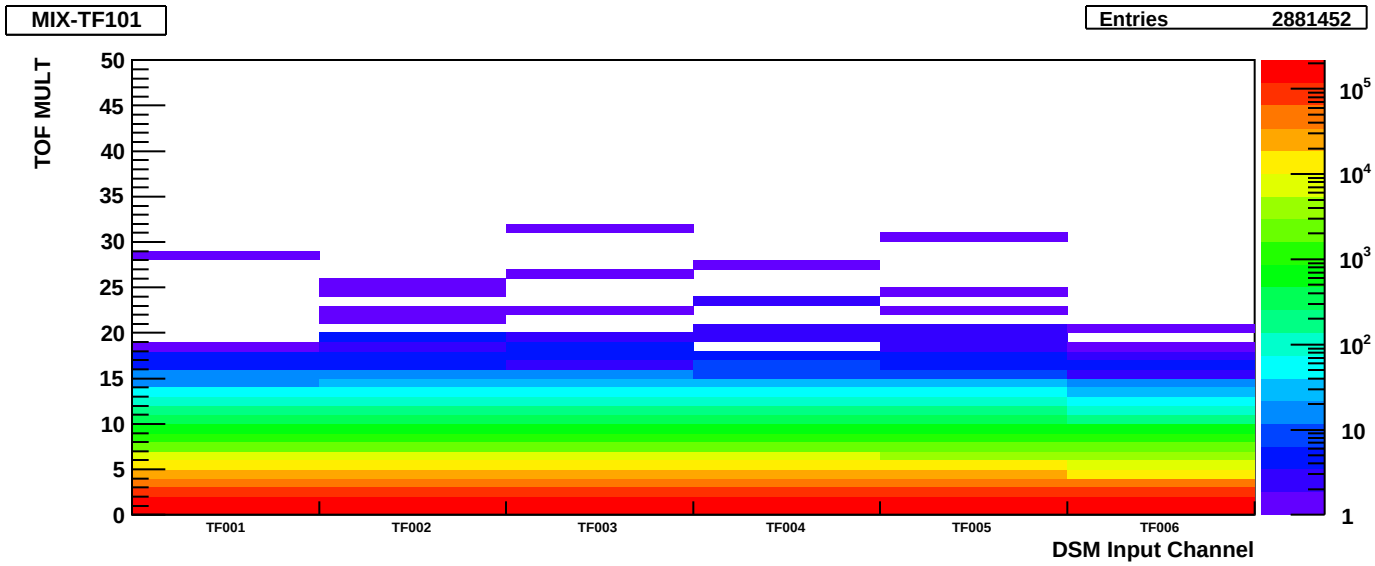

MIX-TF002
Entries 2.017016e+07
TOF MULT
TOF tray

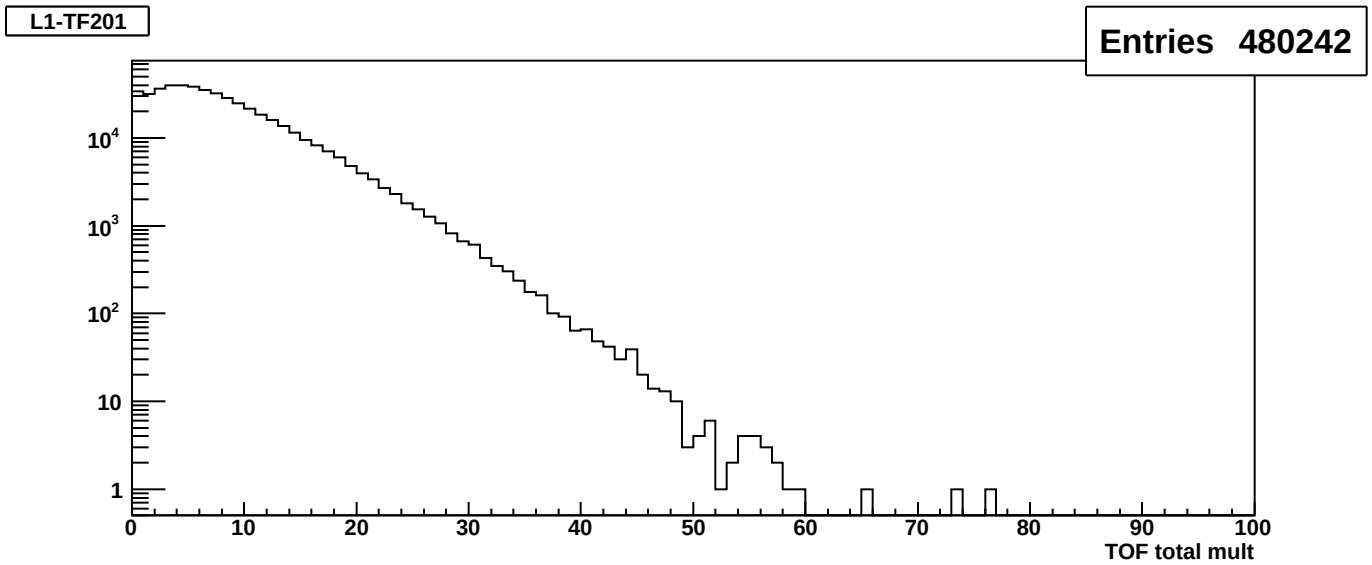

MIX-TF003
Entries 2.017016e+07
TOF MULT
TOF tray

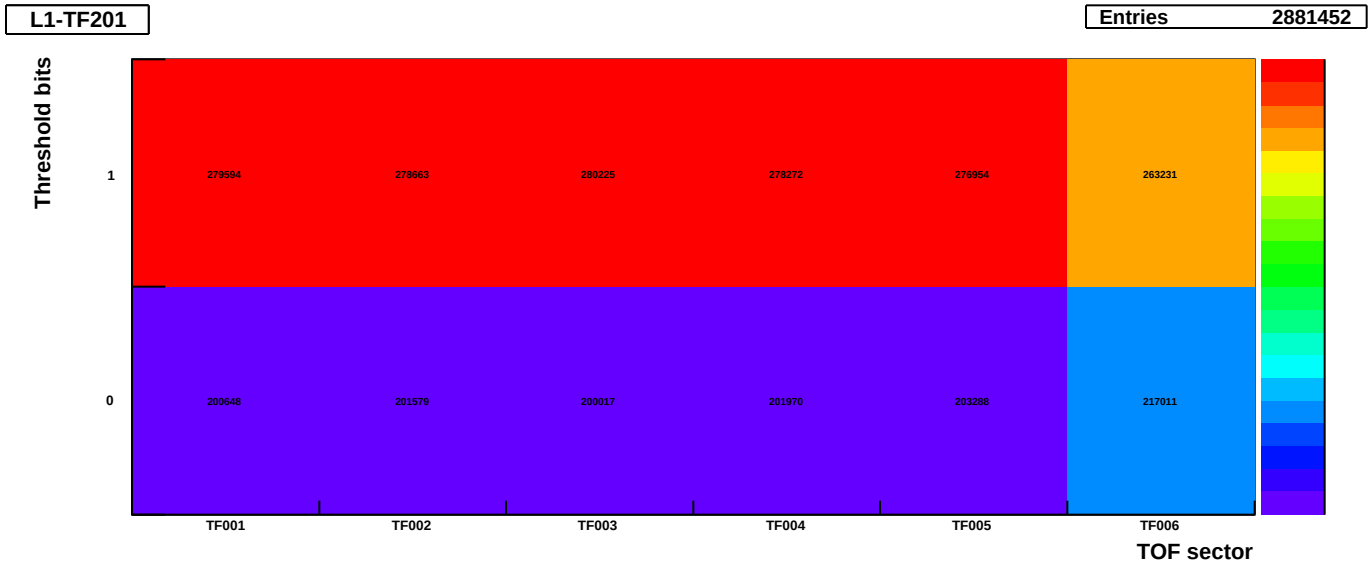

MIX-TF004
Entries 2.017016e+07
TOF MULT
TOF tray

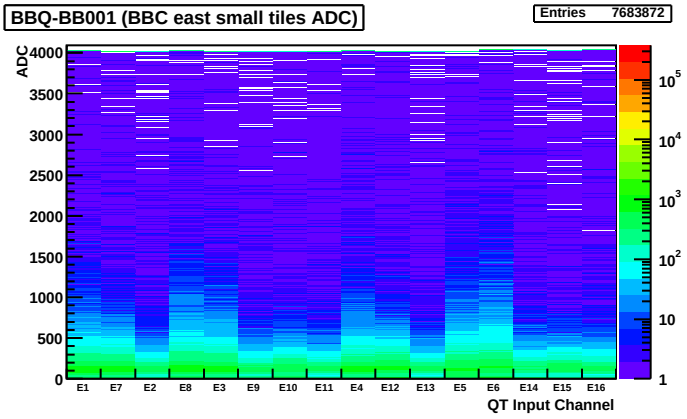

MIX-TF005
Entries 2.017016e+07
TOF MULT
TOF tray

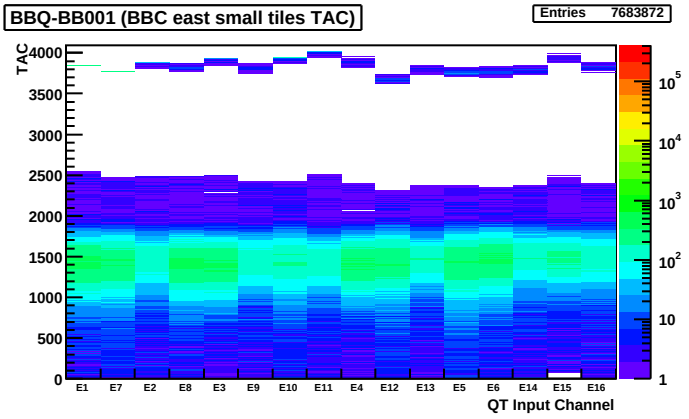

MIX-TF006
Entries 2.017016e+07
TOF MULT
TOF tray

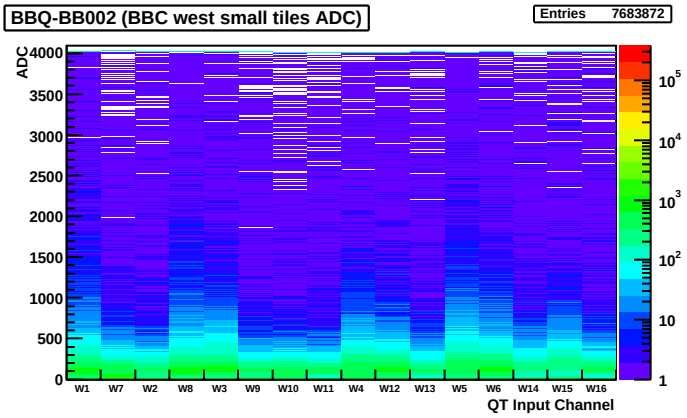

MIX-TF101
Entries 2881452
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

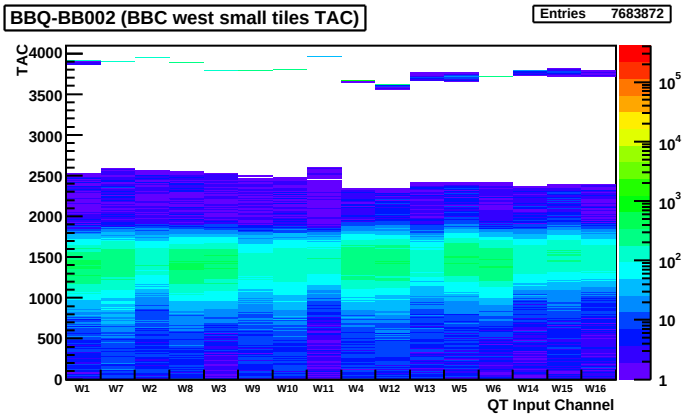

L1-TF201
Entries 480242
TOF total mult

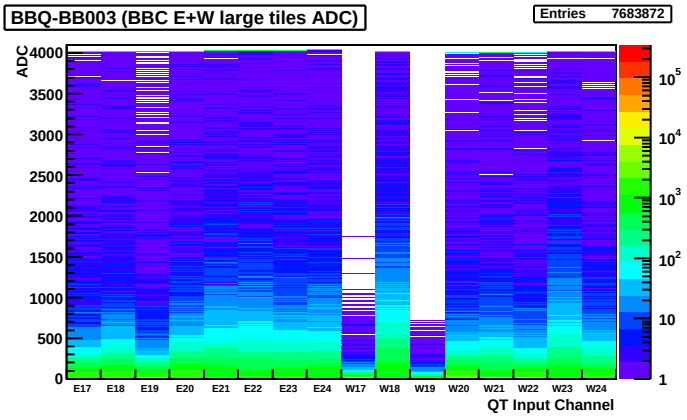

L1-TF201
Entries 2881452
Threshold bits
279594
278663
280225
278272
276954
263231
200648
201579
200017
201970
203288
217011
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

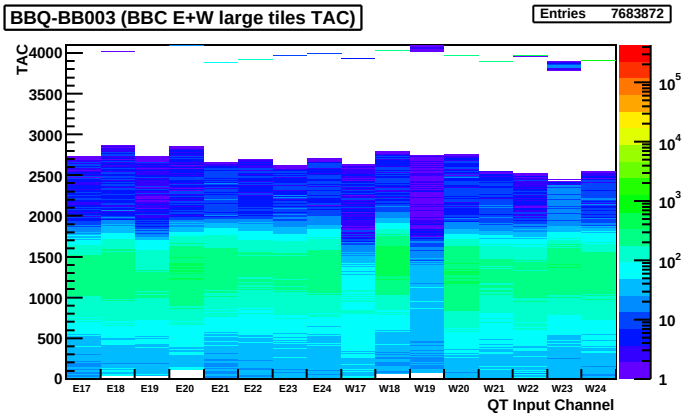

BBQ-BB001 (BBC east small tiles ADC)
Entries 7683872
ADC
QT Input Channel

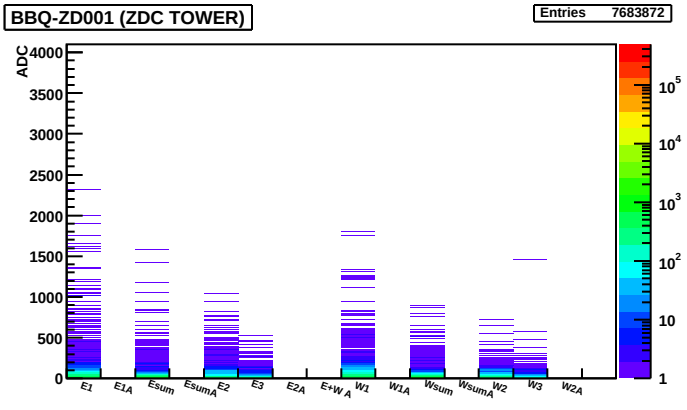

BBQ-BB001 (BBC east small tiles TAC)
Entries 7683872
TAC
QT Input Channel

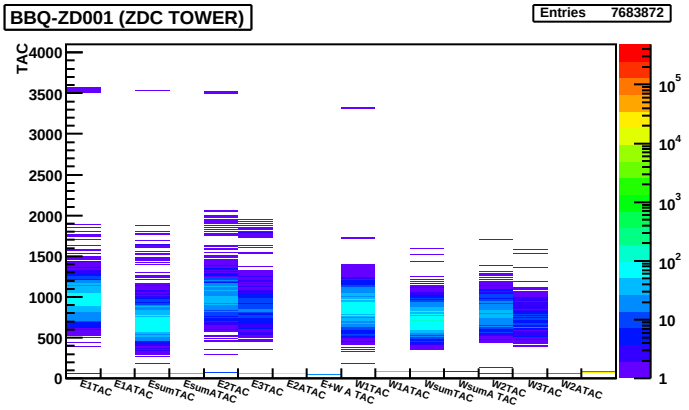

BBQ-BB002 (BBC west small tiles ADC)
Entries 7683872
ADC
QT Input Channel

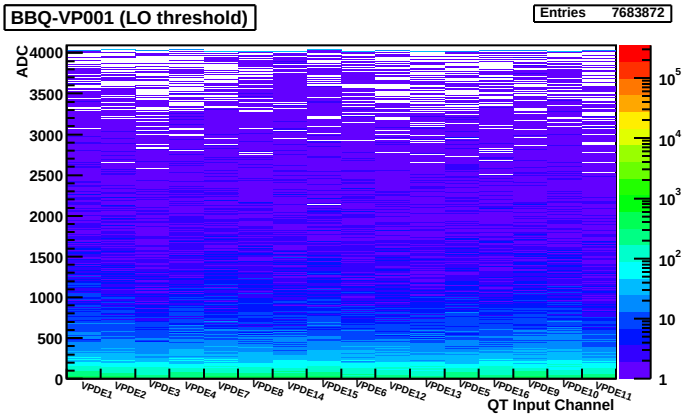

BBQ-BB002 (BBC west small tiles TAC)
Entries 7683872
TAC
QT Input Channel

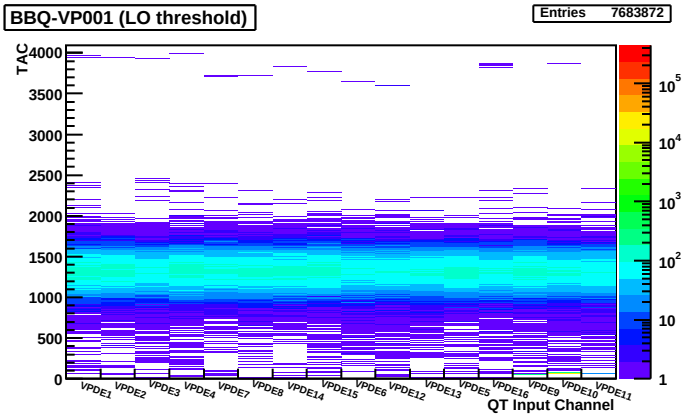

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7683872
ADC
QT Input Channel

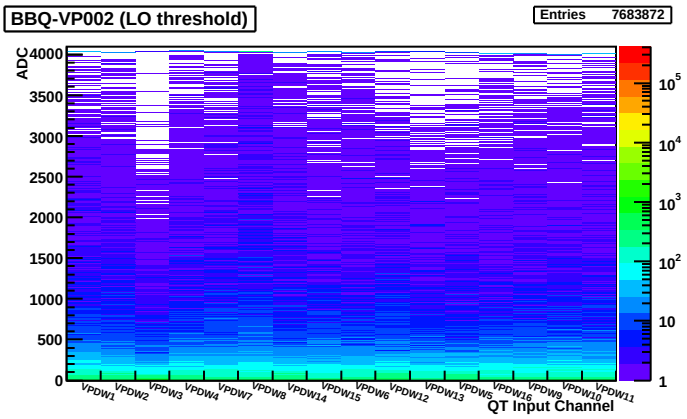

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7683872
TAC
QT Input Channel

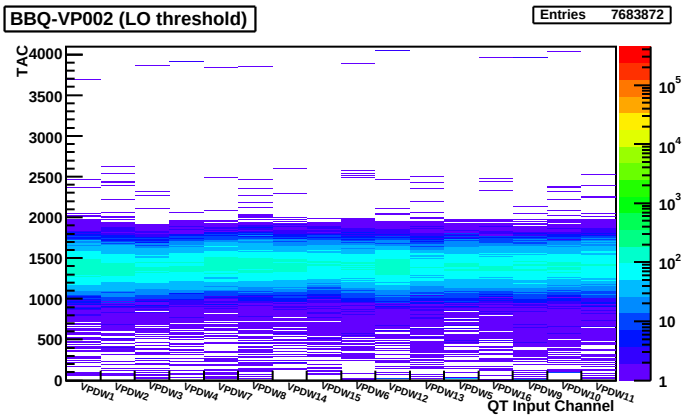

BBQ-ZD001 (ZDC TOWER)
Entries 7683872
ADC

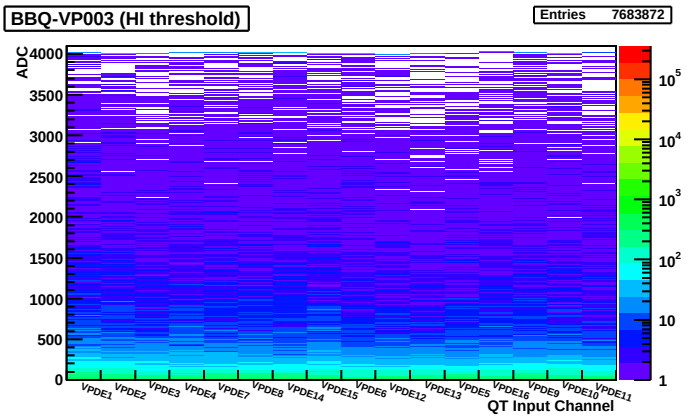

BBQ-ZD001 (ZDC TOWER)
Entries 7683872
TAC

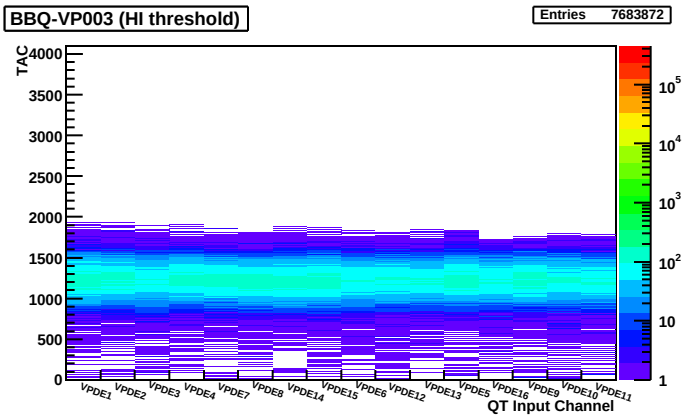

BBQ-VP001 (LO threshold)
Entries 7683872
ADC
QT Input Channel

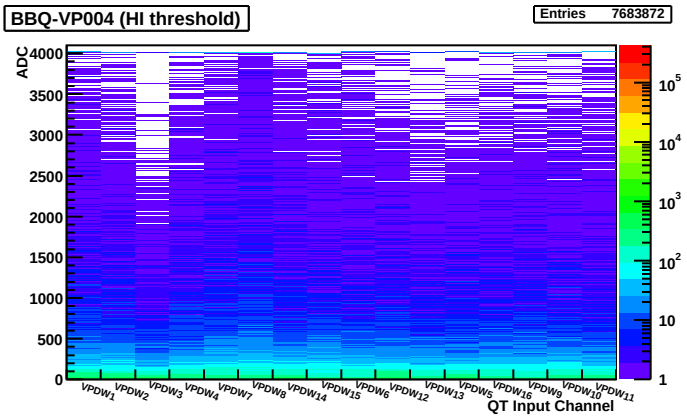

BBQ-VP001 (LO threshold)
Entries 7683872
TAC
QT Input Channel

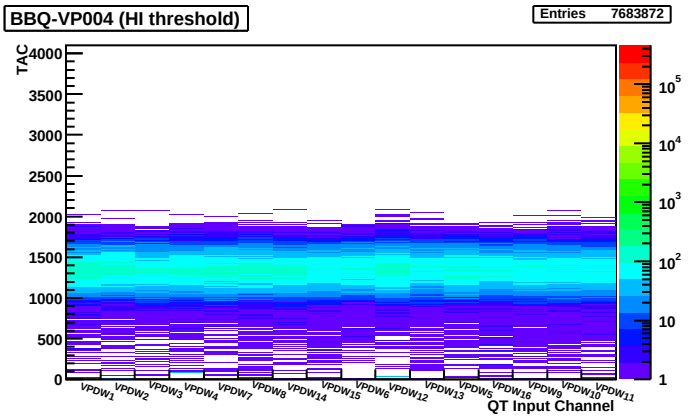

BBQ-VP002 (LO threshold)
Entries 7683872
ADC
QT Input Channel

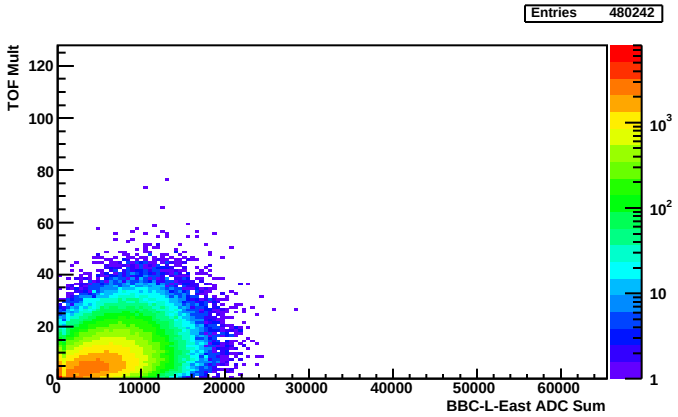

BBQ-VP002 (LO threshold)
Entries 7683872
TAC
QT Input Channel

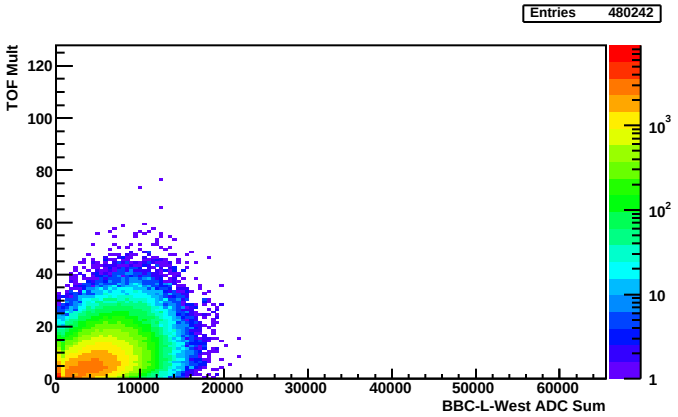

BBQ-VP003 (HI threshold)
Entries 7683872
ADC
QT Input Channel

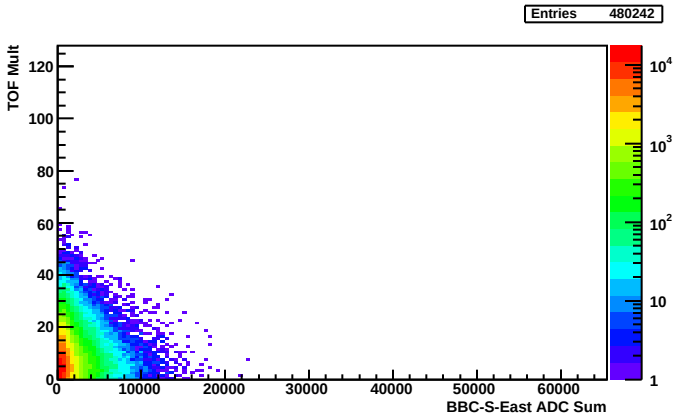

BBQ-VP003 (HI threshold)
Entries 7683872
TAC
QT Input Channel

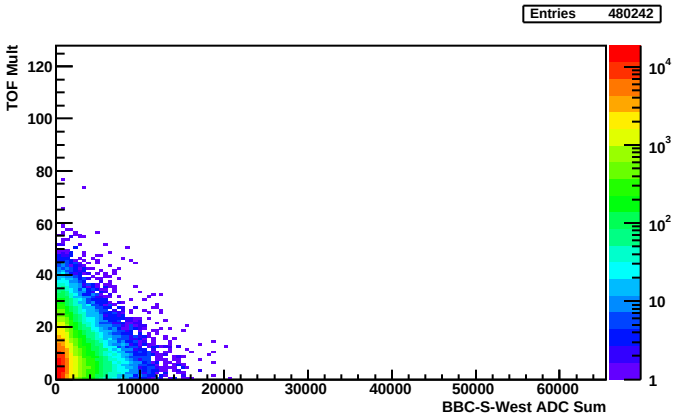

BBQ-VP004 (HI threshold)
Entries 7683872
ADC
QT Input Channel

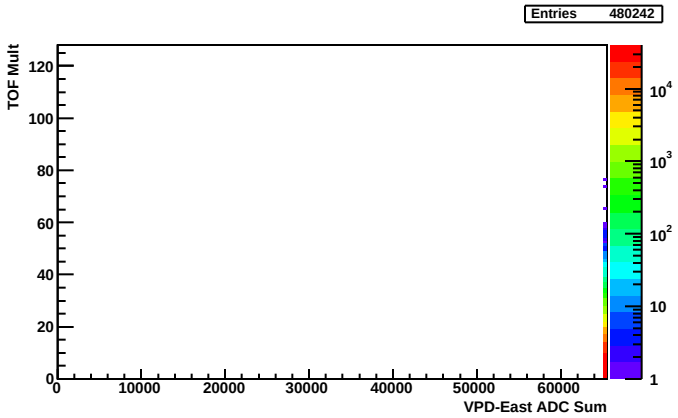

BBQ-VP004 (HI threshold)
Entries 7683872
TAC
QT Input Channel

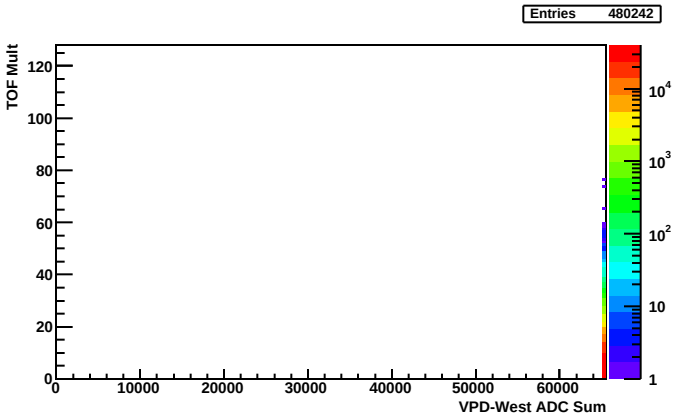

Entries 480242
TOF Mult
BBC-L-East ADC Sum

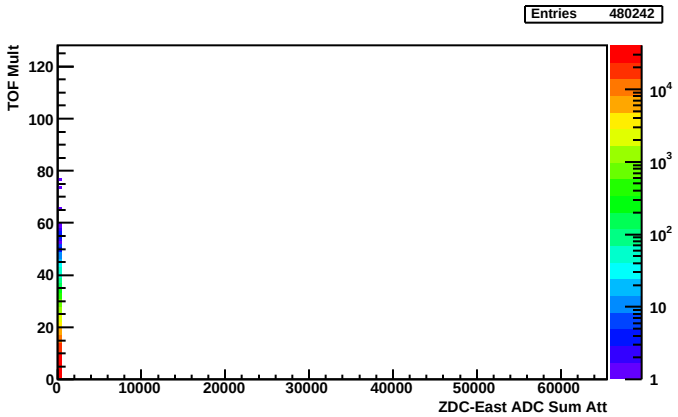

Entries 480242
TOF Mult
BBC-L-West ADC Sum

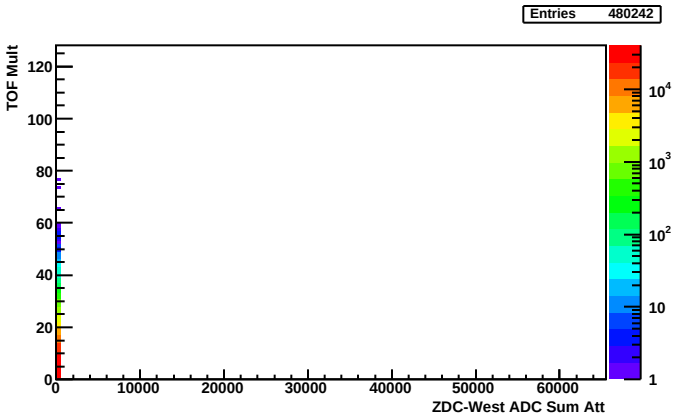

Entries 480242
TOF Mult
BBC-S-East ADC Sum

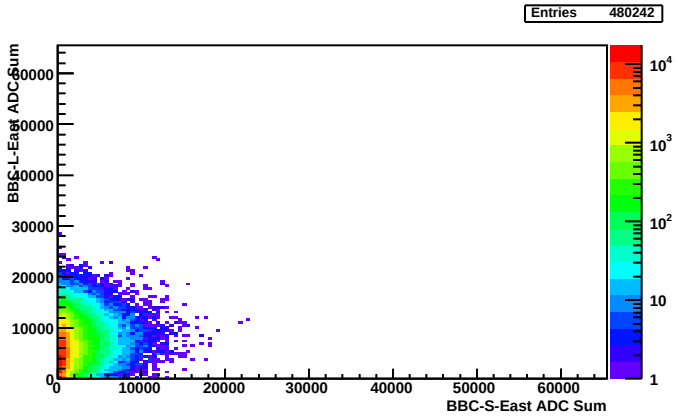

Entries 480242
TOF Mult
BBC-S-West ADC Sum

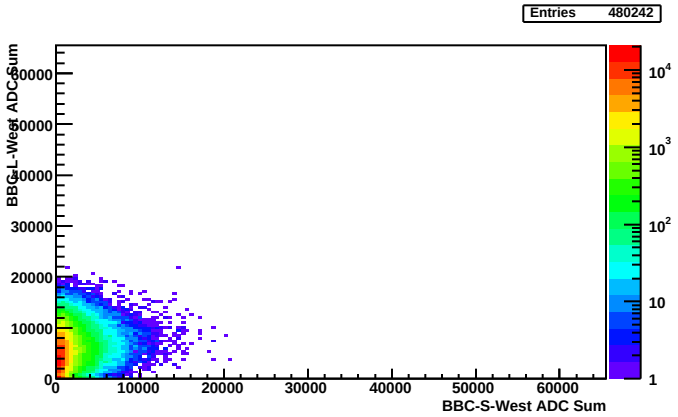

Entries 480242
TOF Mult
VPD-East ADC Sum

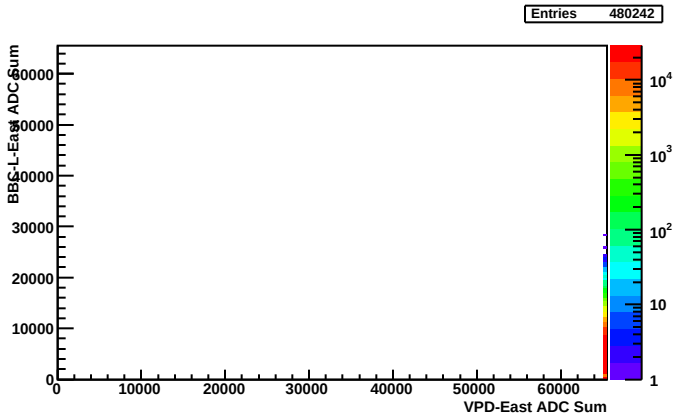

Entries 480242
TOF Mult
VPD-West ADC Sum

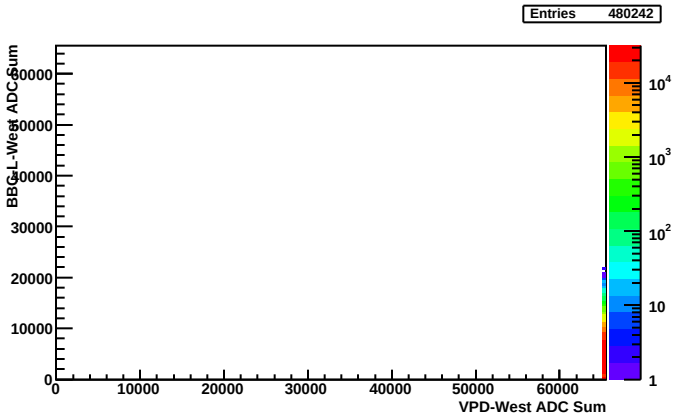

Entries 480242
TOF Mult
ZDC-East ADC Sum Att


Entries 480242
TOF Mult
ZDC-West ADC Sum Att


Entries 480242
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 480242
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 480242
BBC-L-East ADC Sum
VPD-East ADC Sum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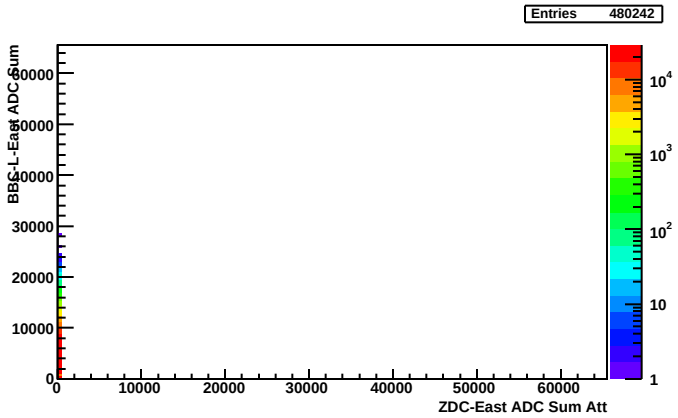
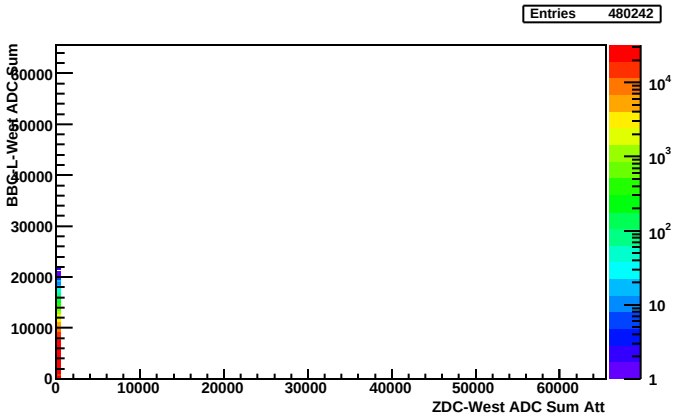
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 480242
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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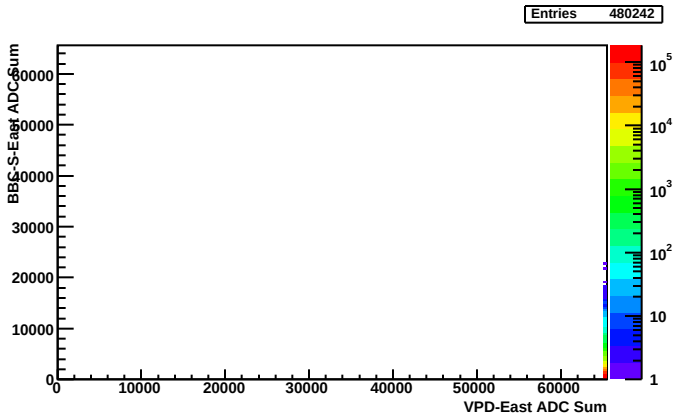
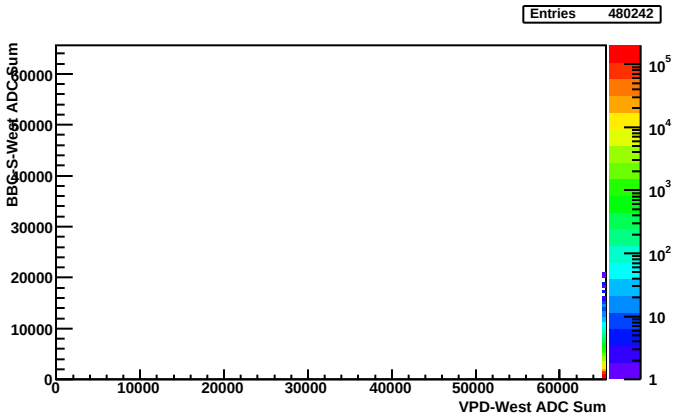
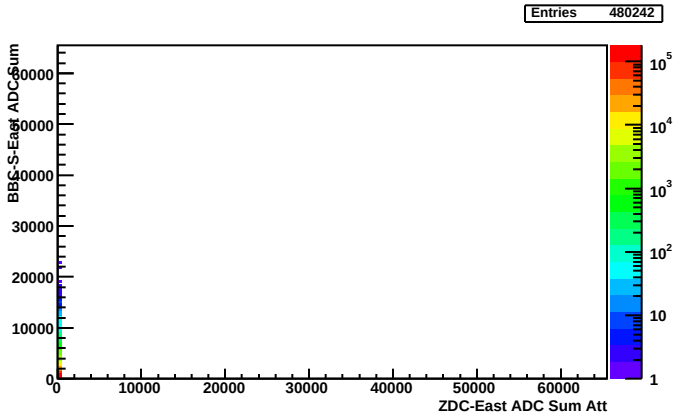
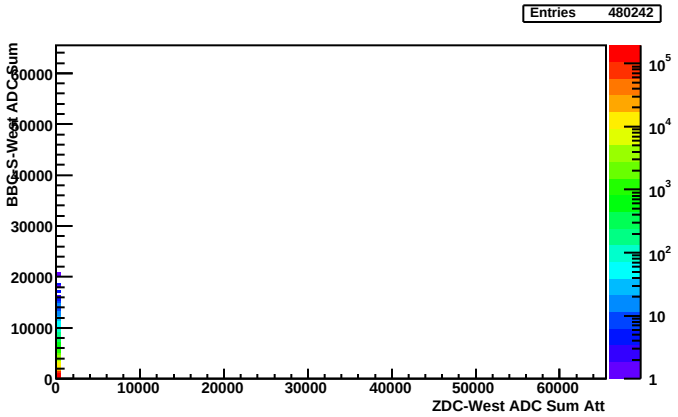
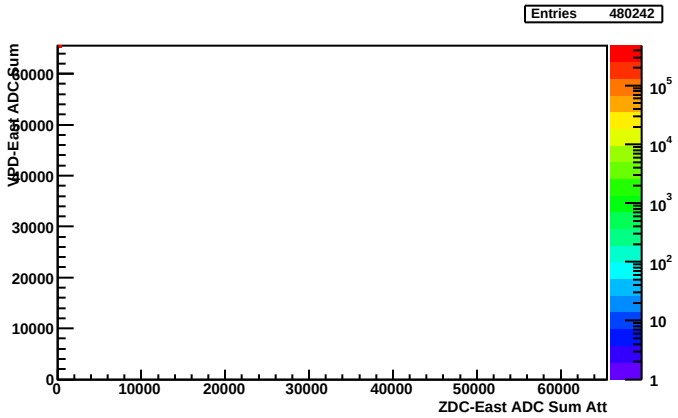
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 480242
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 480242
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 480242
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 480242
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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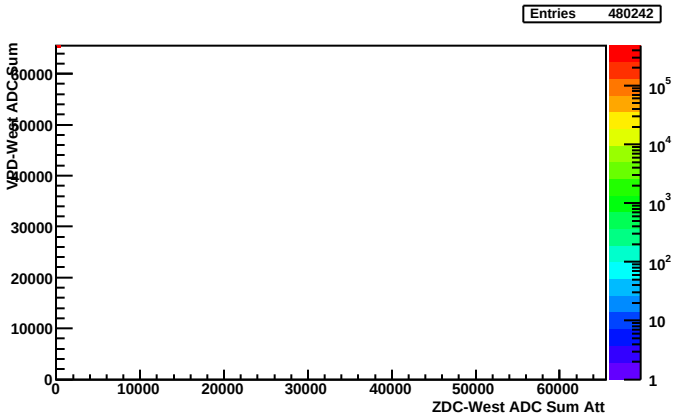
VPD-East ADC Sum
ZDC-East ADC Sum Att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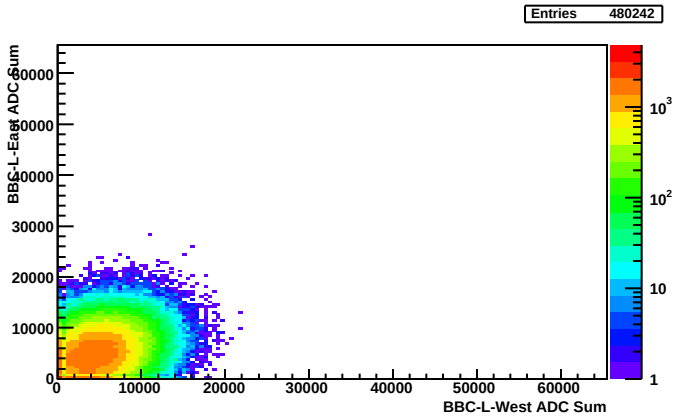
ZDC-West ADC Sum Att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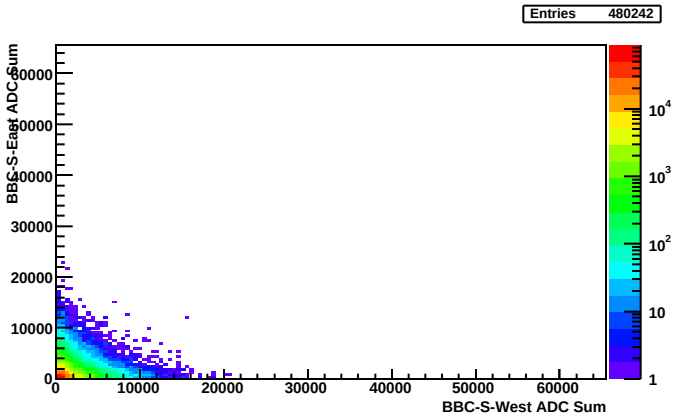
BBC-L-East ADC Sum
BBC-L-West ADC Sum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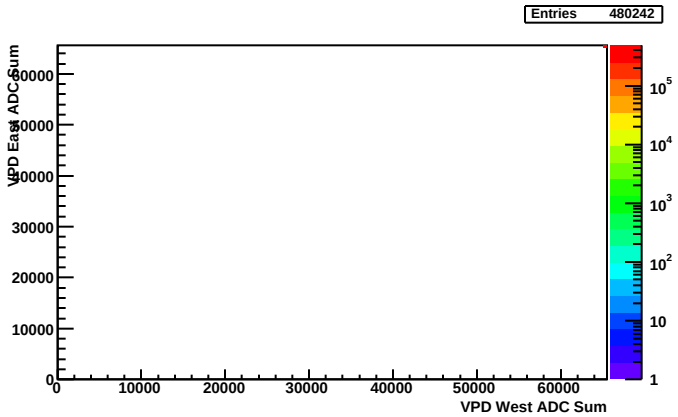
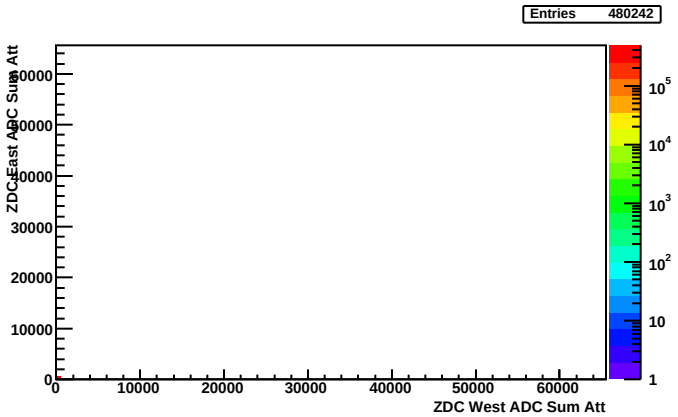
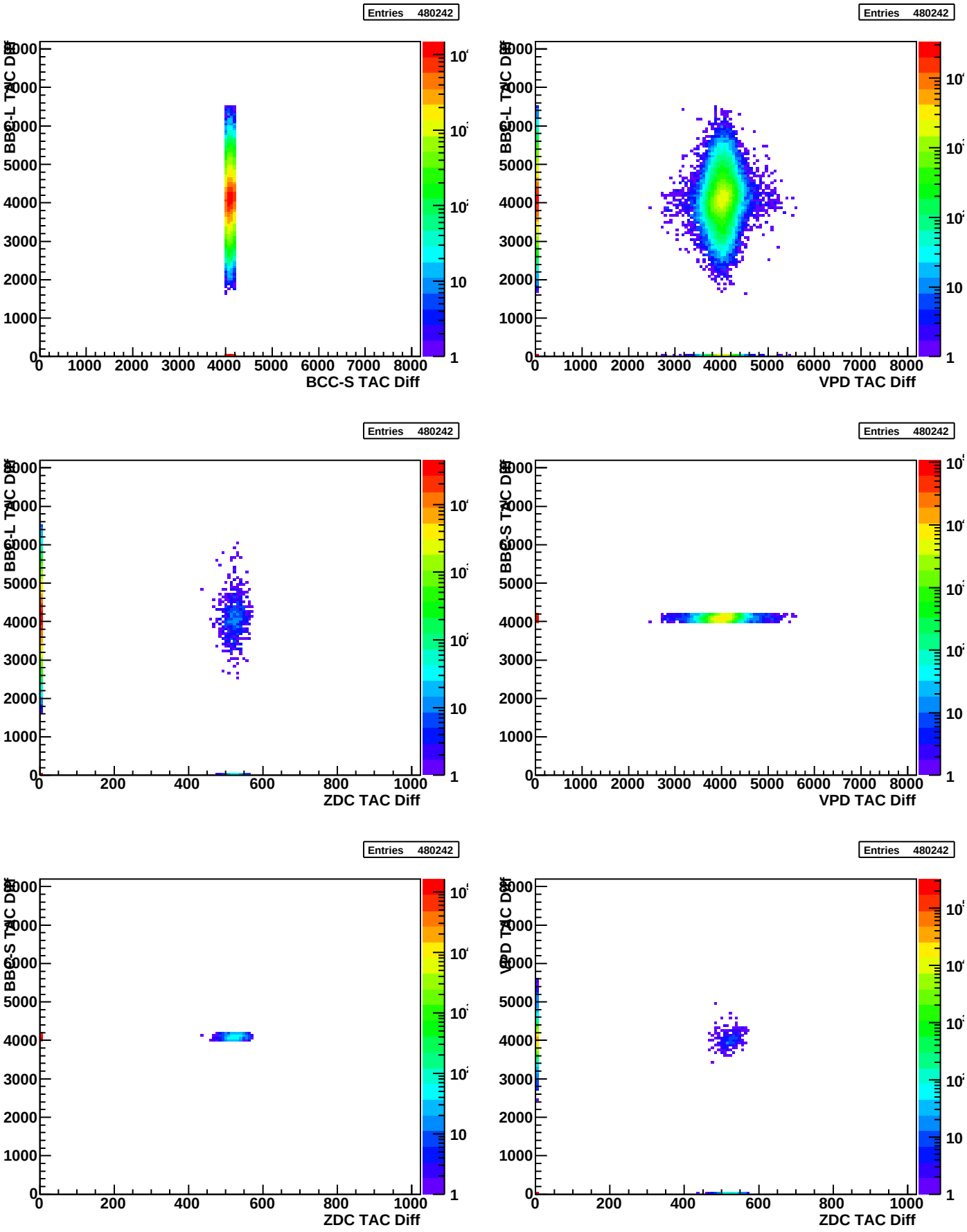
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 480242
VPD East ADC Sum
VPD West ADC Sum


Entries 480242
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 480242
BBC-L TAC Diff
BCC-S TAC Diff
Entries 480242
BBC-L TAC Diff
VPD TAC Diff
Entries 480242
BBC-L TAC Diff
ZDC TAC Diff
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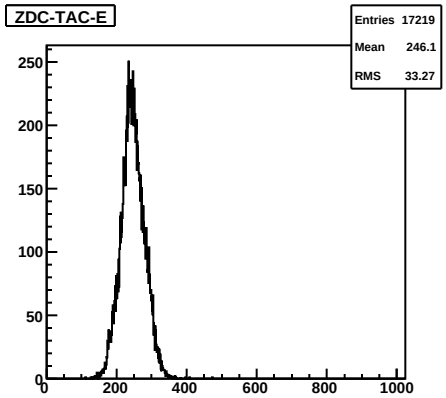
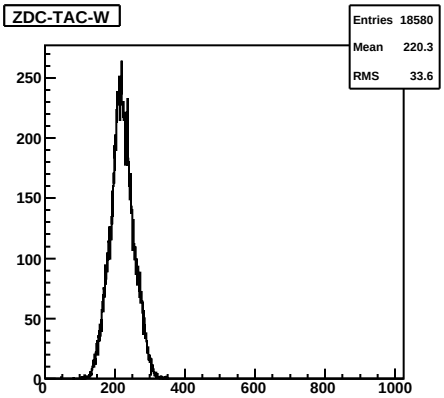
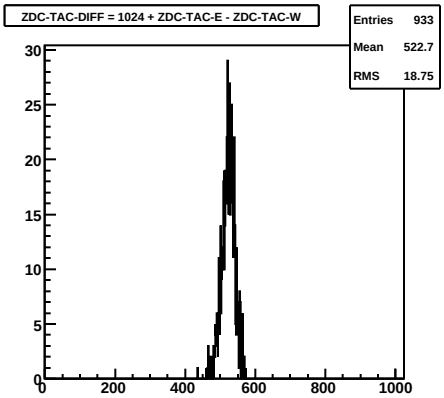
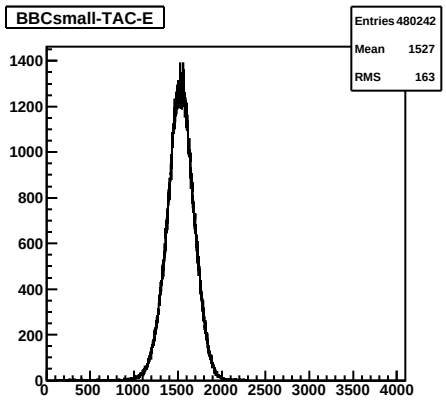
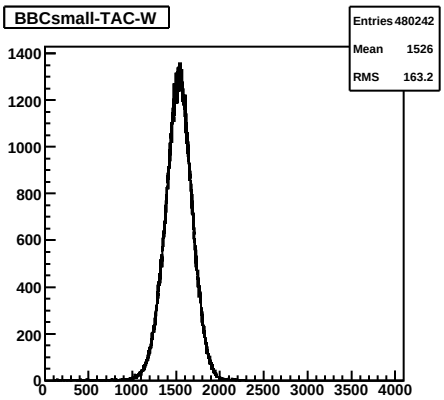
VPD TAC Diff
Entries 480242
BBC-S TAC Diff
ZDC TAC Diff
Entries 480242
VPD TAC Diff
ZDC TAC Diff

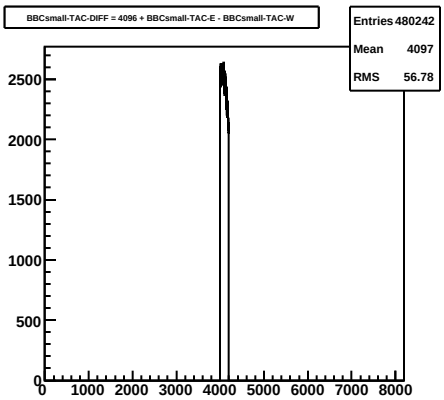

ZDC-TAC-E
Entries 17219
Mean 246.1
RMS 33.27

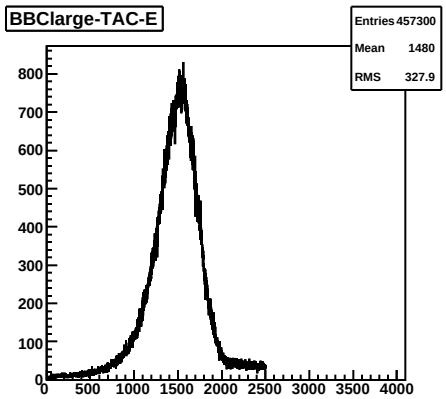

ZDC-TAC-W
Entries 18580
Mean 220.3
RMS 33.6

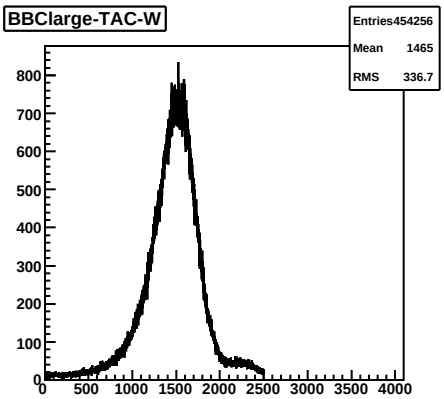

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 933
Mean 522.7
RMS 18.75

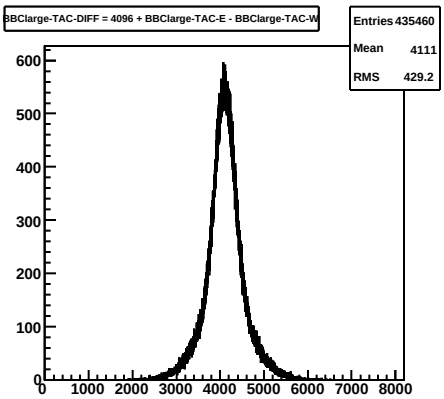

BBCsmall-TAC-E
Entries 480242
Mean 1527
RMS 163

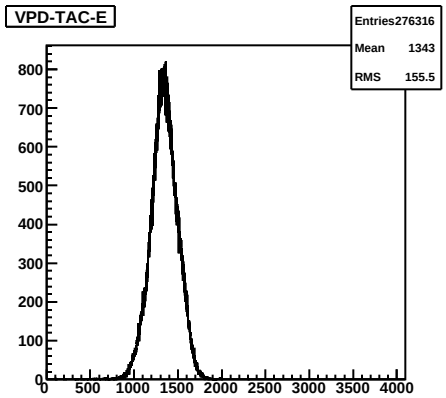

BBCsmall-TAC-W
Entries 480242
Mean 1526
RMS 163.2

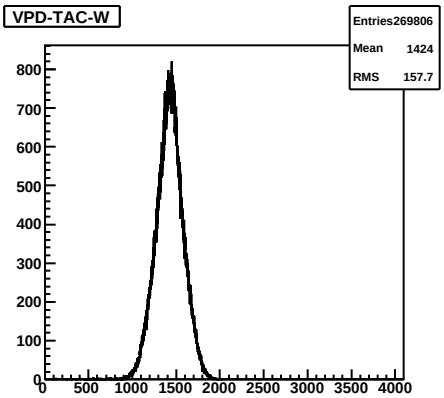

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 480242
Mean 4097
RMS 56.78

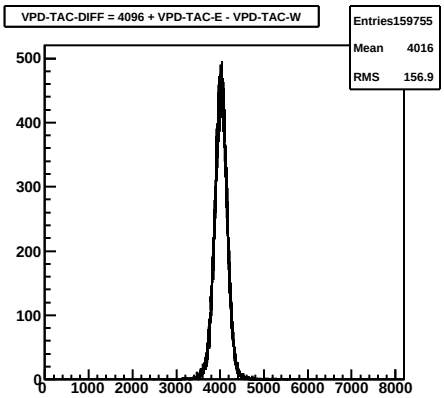

BBClarge-TAC-E
Entries 457300
Mean 1480
RMS 327.9

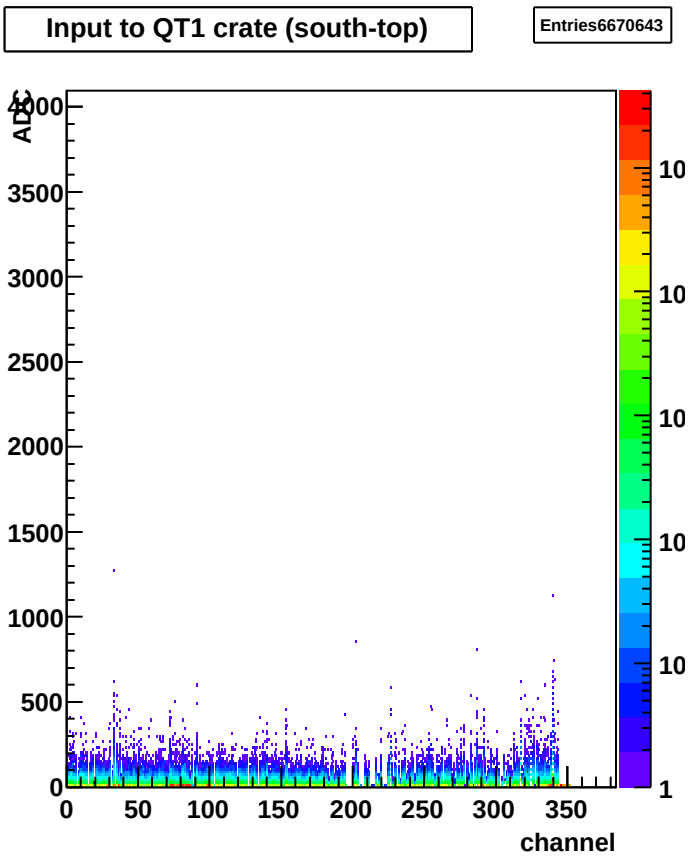

BBClarge-TAC-W
Entries 454256
Mean 1465
RMS 336.7

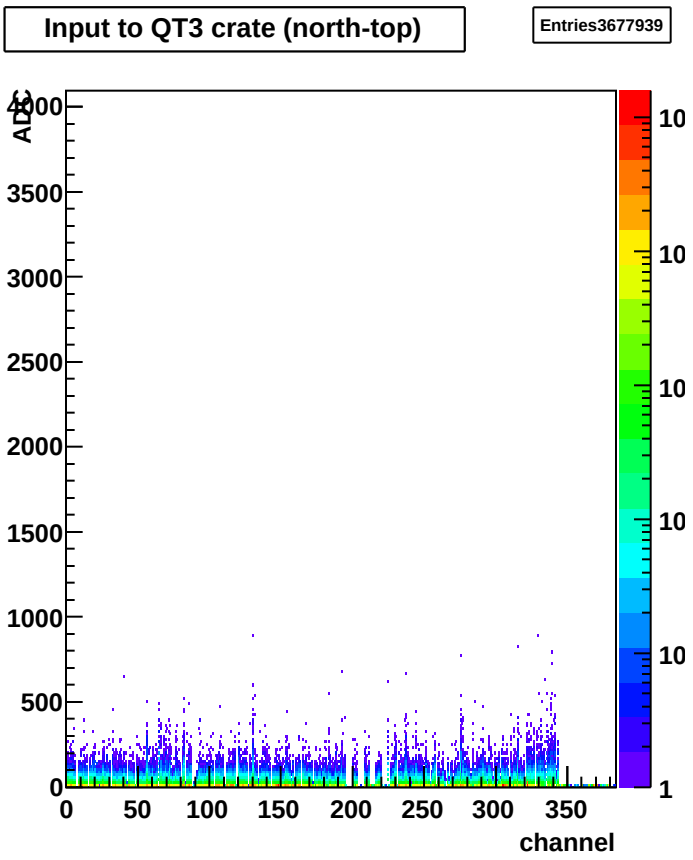

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 435460
Mean 4111
RMS 429.2

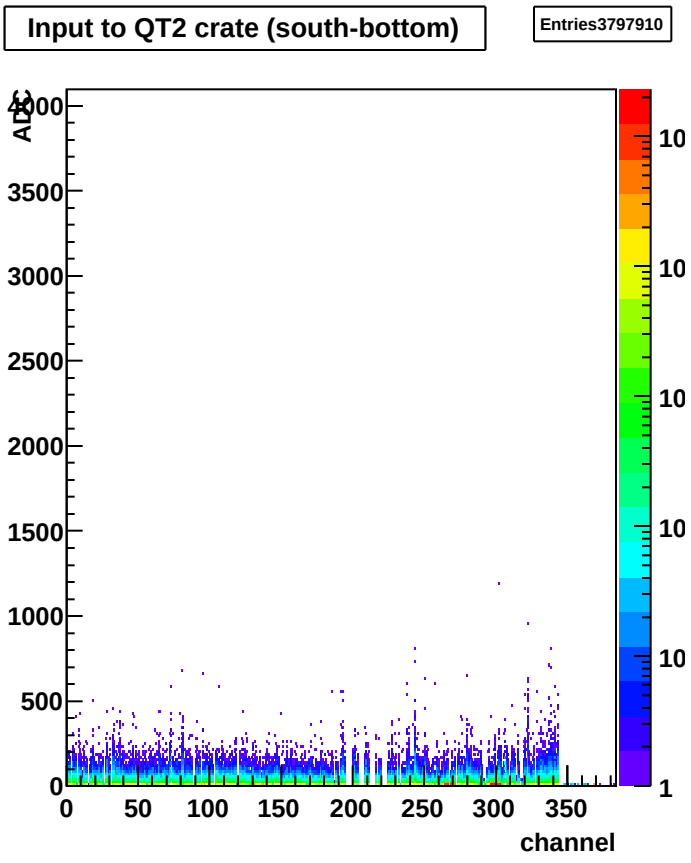

VPD-TAC-E
Entries 276316
Mean 1343
RMS 155.5

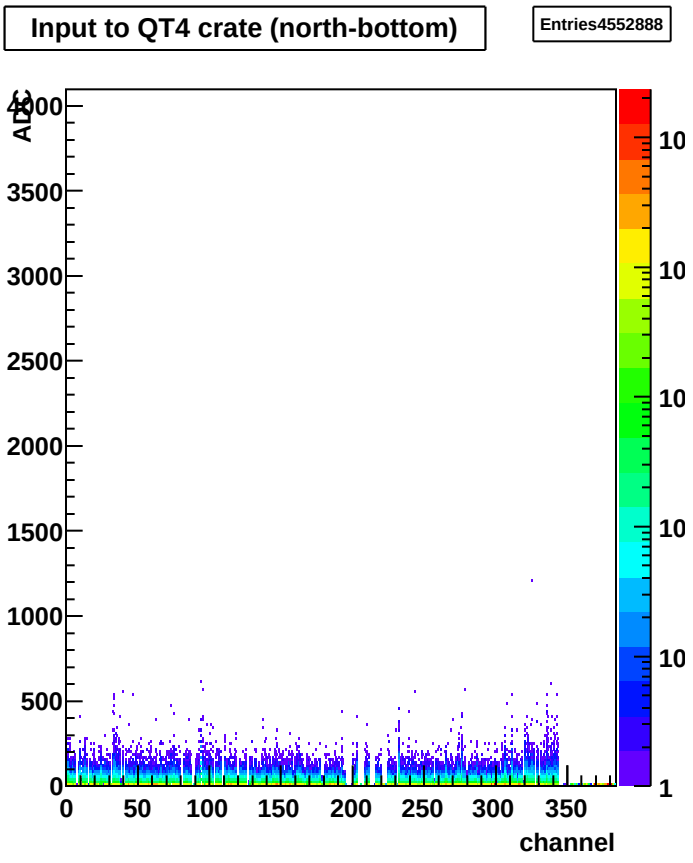

VPD-TAC-W
Entries 269806
Mean 1424
RMS 157.7

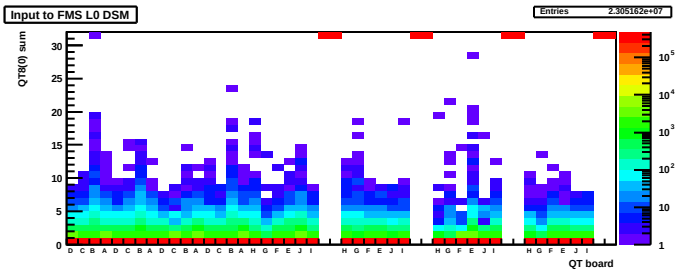

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 159755
Mean 4016
RMS 156.9

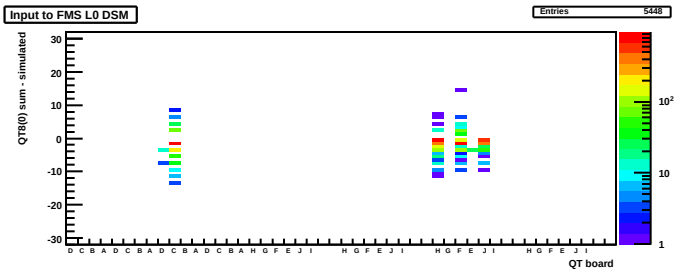

Input to QT1 crate (south-top)
Entries6670643
ADC
channel

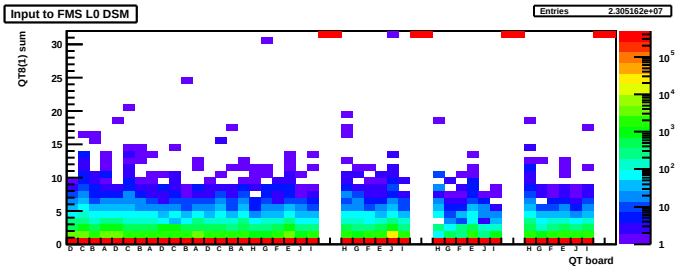

Input to QT3 crate (north-top)
Entries3677939
ADC
channel

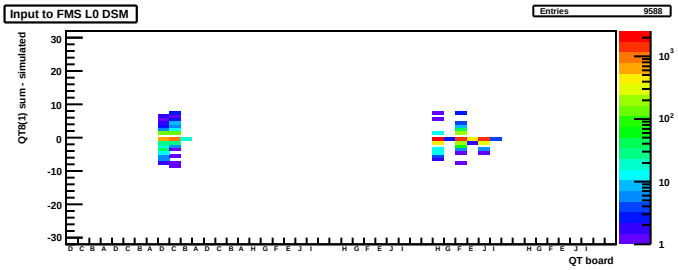

Input to QT2 crate (south-bottom)
Entries3797910
ADC
channel

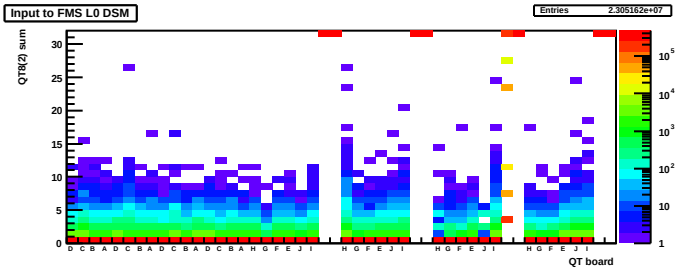

Input to QT4 crate (north-bottom)
Entries4552888
ADC
channel

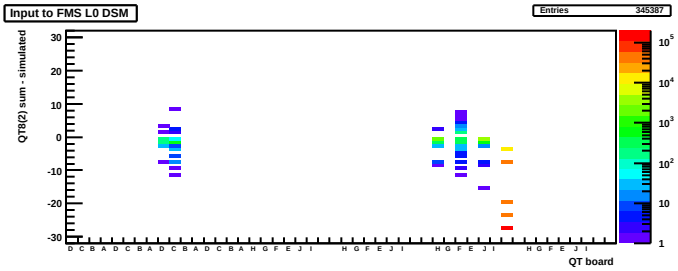

Input to FMS L0 DSM
Entries 2.305162e+07
QT8(0) sum
QT board

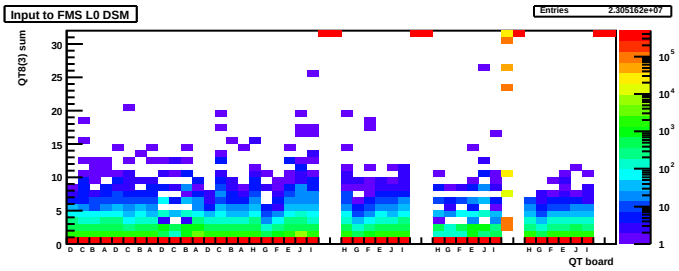

Input to FMS L0 DSM
Entries 5448
QT8(0) sum - simulated
QT board

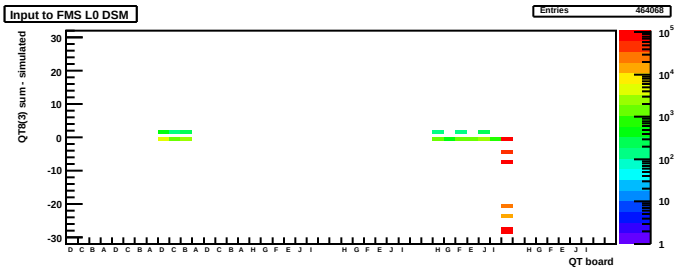

Input to FMS L0 DSM
Entries 2.305162e+07
QT8(1) sum
QT board

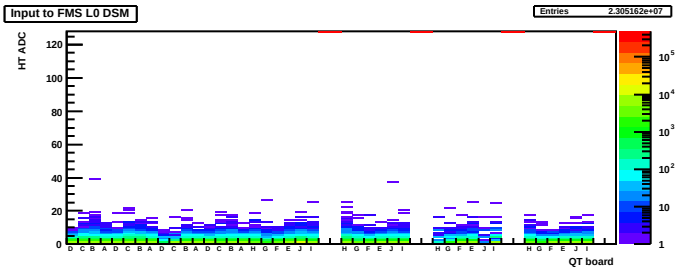

Input to FMS L0 DSM
Entries 9588
QT8(1) sum - simulated
QT board

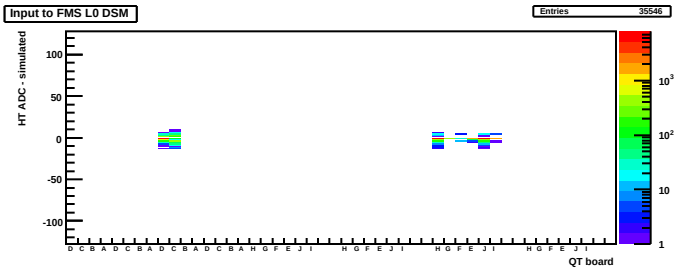

Input to FMS L0 DSM
Entries 2.305162e+07
QT8(2) sum
QT board

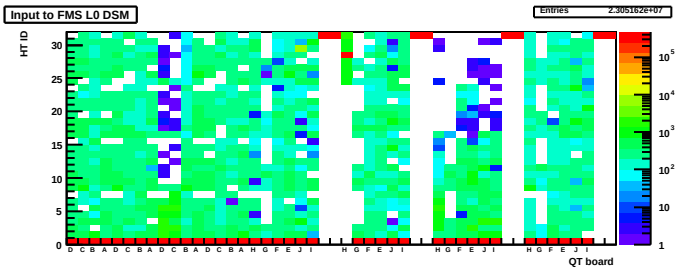

Input to FMS L0 DSM
Entries 345387
QT8(2) sum - simulated
QT board

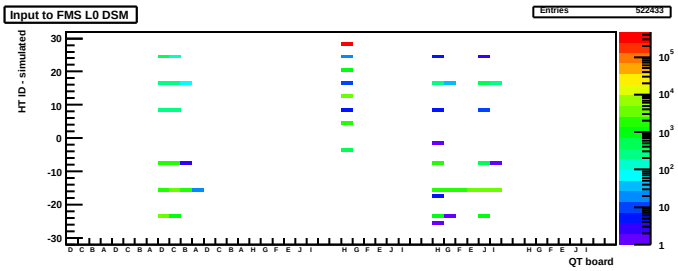

Input to FMS L0 DSM
Entries 2.305162e+07
QT8(3) sum
QT board

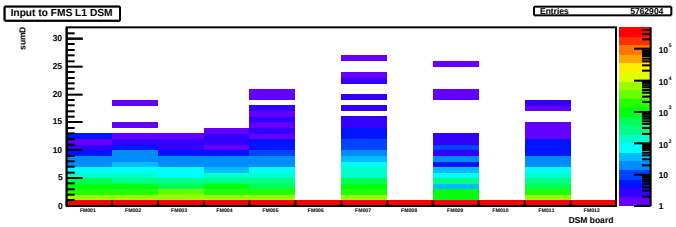

Input to FMS L0 DSM
Entries 464068
QT8(3) sum - simulated
QT board

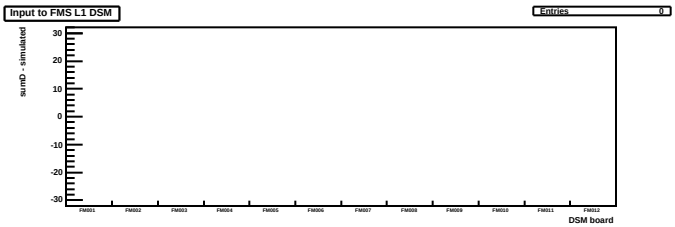

Input to FMS L0 DSM
Entries 2.305162e+07
HT ADC
QT board

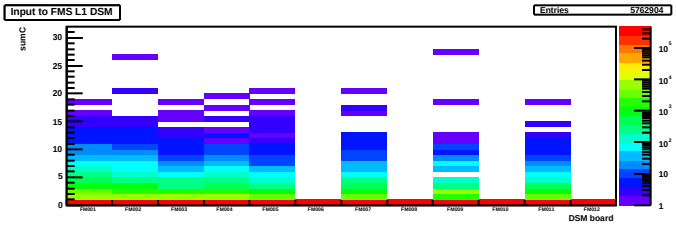

Input to FMS L0 DSM
Entries 35546
HT ADC - simulated
QT board

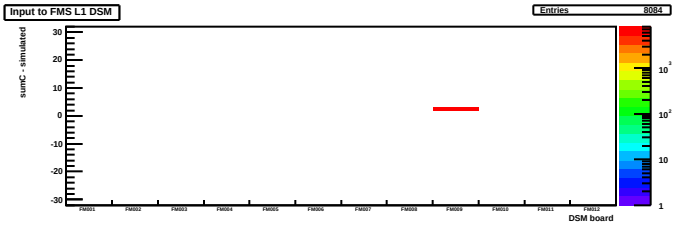

Input to FMS L0 DSM
Entries 2.305162e+07
HT ID
QT board

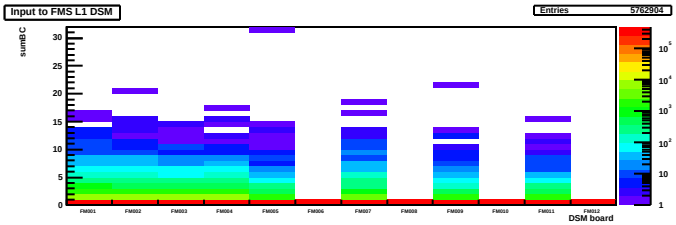

Input to FMS L0 DSM
Entries 522433
HT ID - simulated
QT board

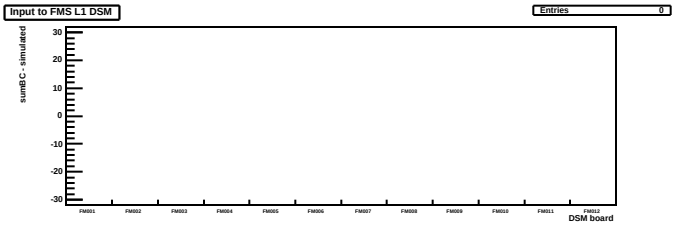

Input to FMS L1 DSM
Entries 5762904
sumD
DSM board

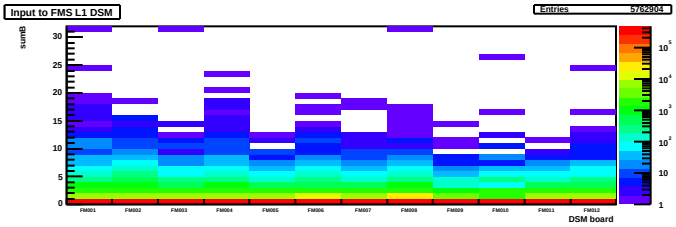

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

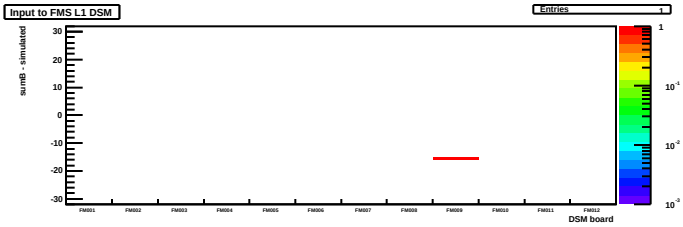

Input to FMS L1 DSM
Entries 5762904
sumC
DSM board

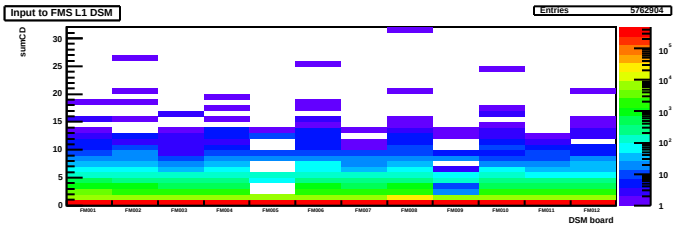

Input to FMS L1 DSM
Entries 8084
sumC - simulated
DSM board

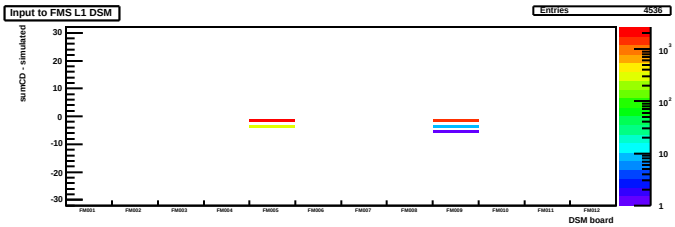

Input to FMS L1 DSM
Entries 5762904
sumBC
DSM board

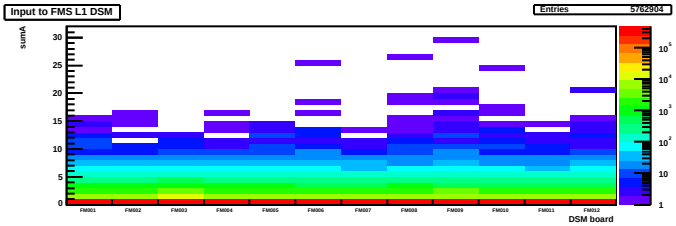

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

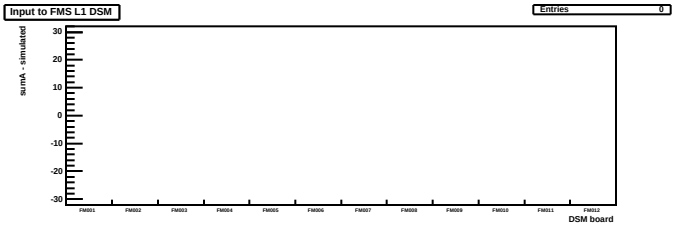

Input to FMS L1 DSM
Entries 5762904
sumB
DSM board

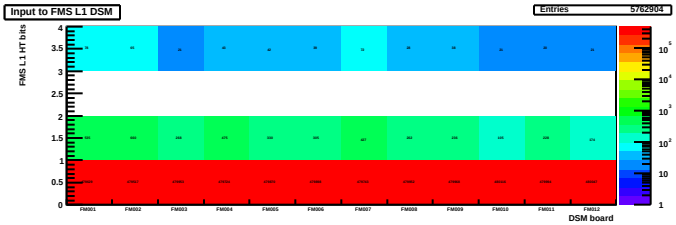

Input to FMS L1 DSM
Entries 1
sumB - simulated
DSM board

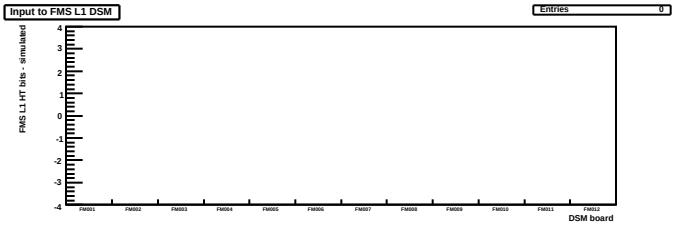

Input to FMS L1 DSM
Entries 5762904
sumCD
DSM board

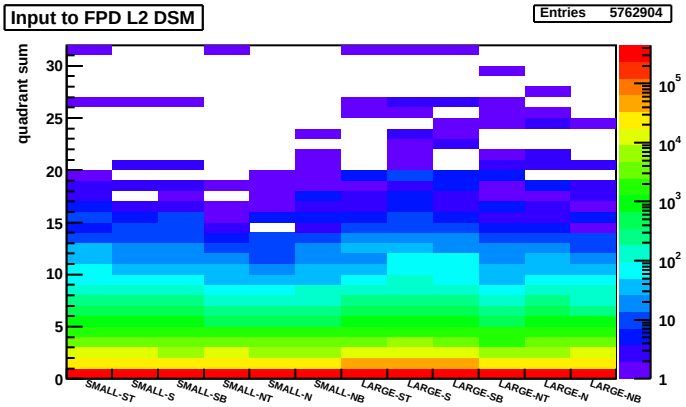

Input to FMS L1 DSM
Entries 4536
sumCD - simulated
DSM board

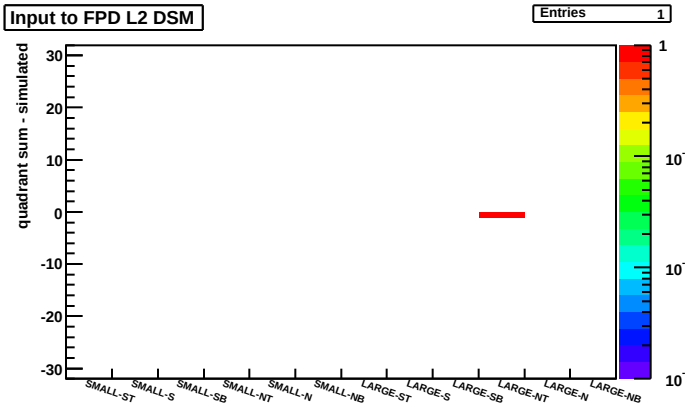

Input to FMS L1 DSM
Entries 5762904
sumA
DSM board

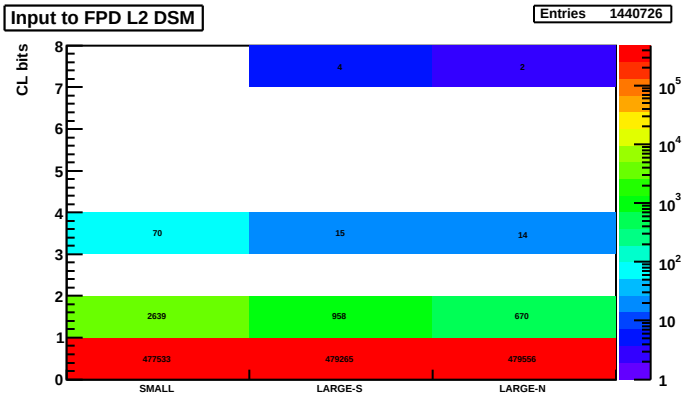

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

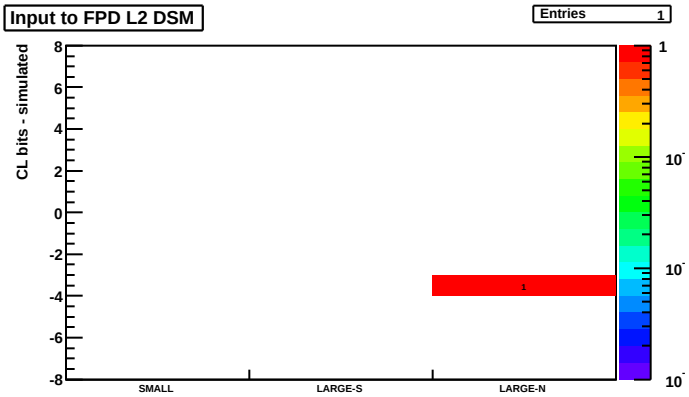

Input to FMS L1 DSM
Entries 5762904
FMS L1 HT bits
DSM board

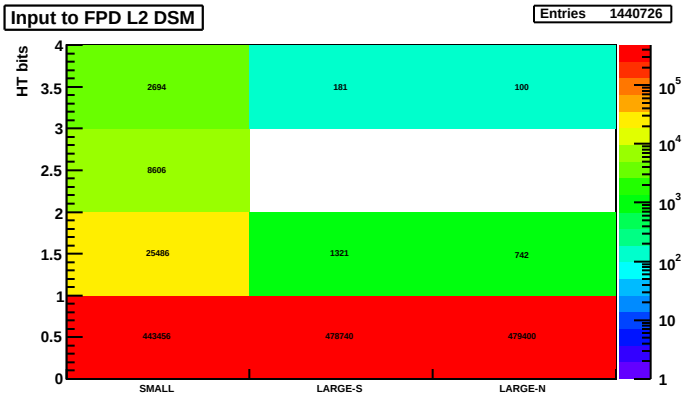

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

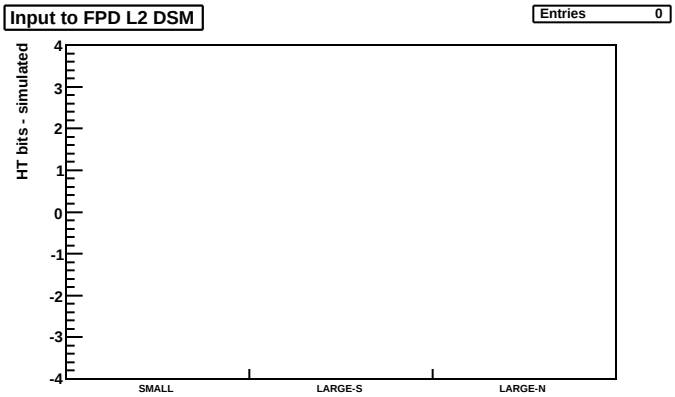

Input to FPD L2 DSM
Entries 5762904
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

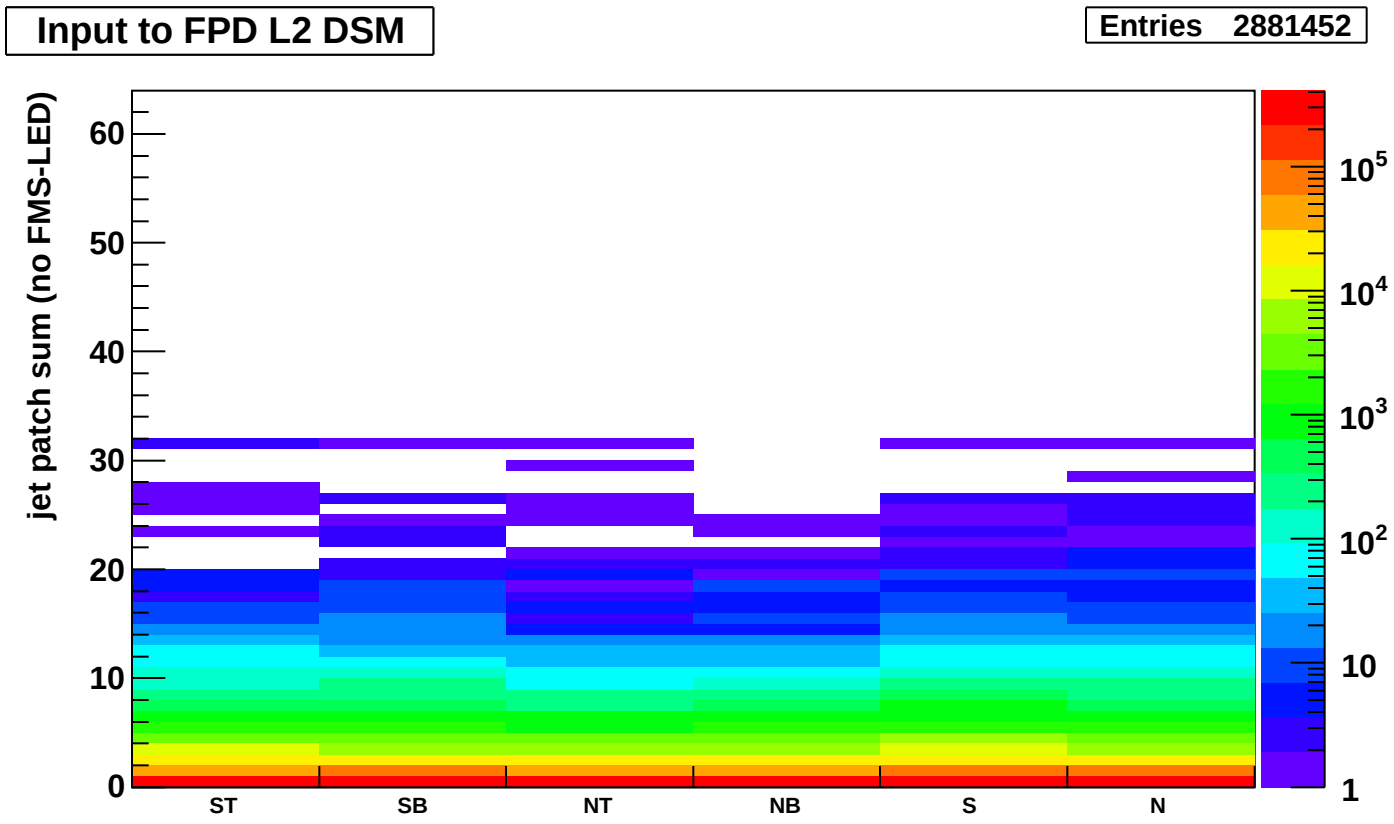

Input to FPD L2 DSM
Entries 1
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

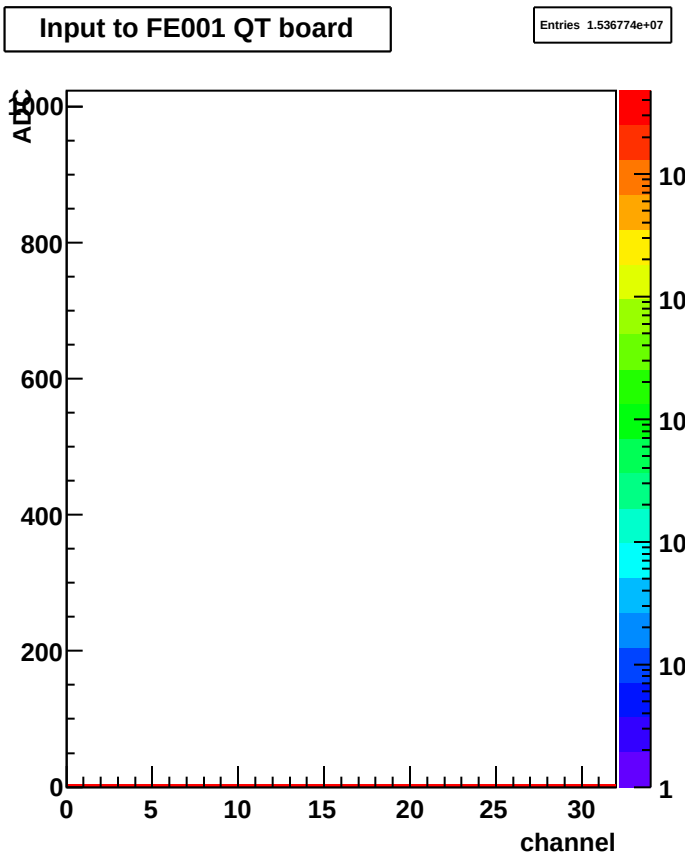

Input to FPD L2 DSM
Entries 1440726
CL bits
4
2
70
15
14
2639
958
670
477533
479265
479556
SMALL
LARGE-S
LARGE-N

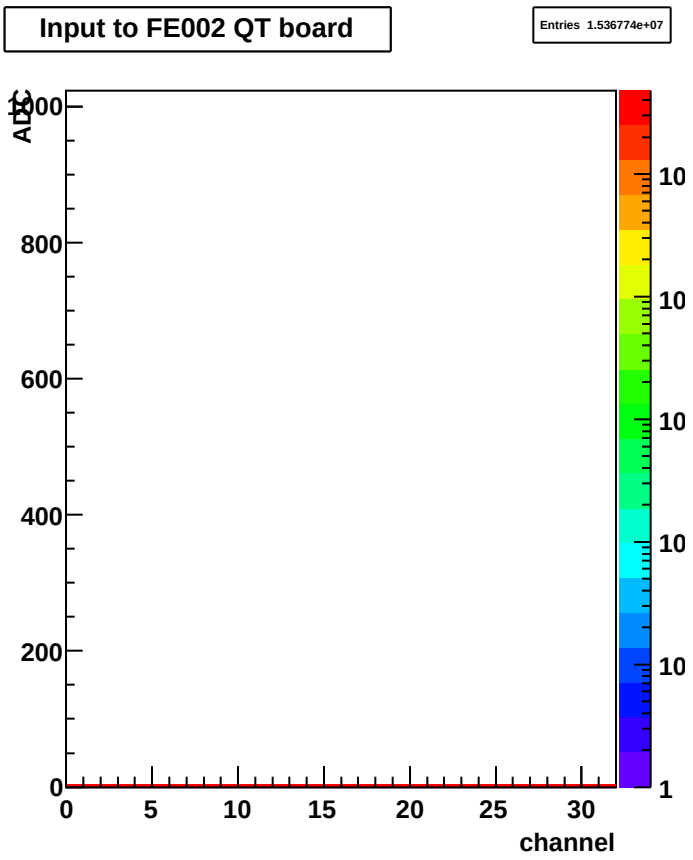

Input to FPD L2 DSM
Entries 1
CL bits - simulated
1
SMALL
LARGE-S
LARGE-N

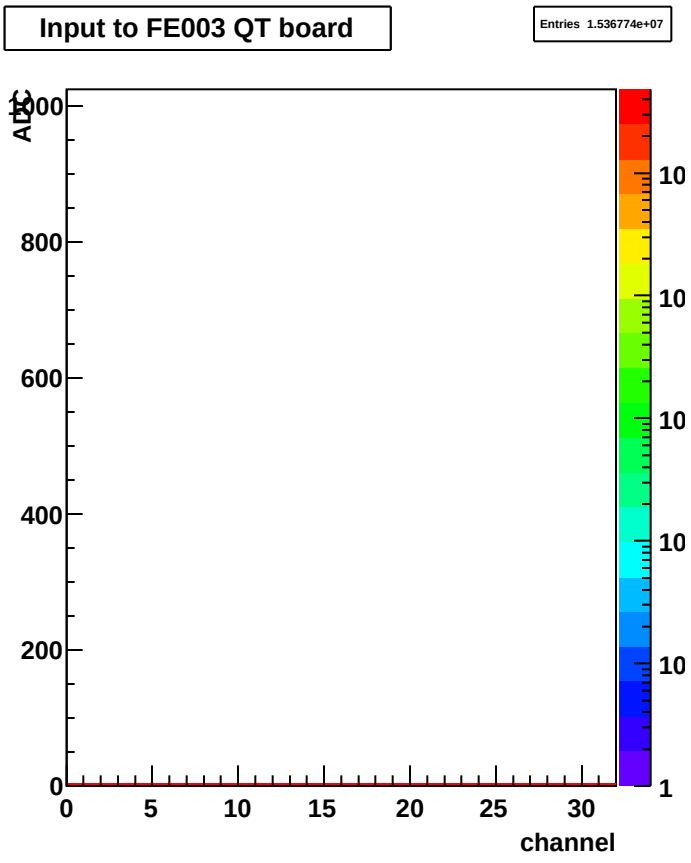

Input to FPD L2 DSM
Entries 1440726
HT bits
2694
181
100
8606
25486
1321
742
443456
478740
479400
SMALL
LARGE-S
LARGE-N

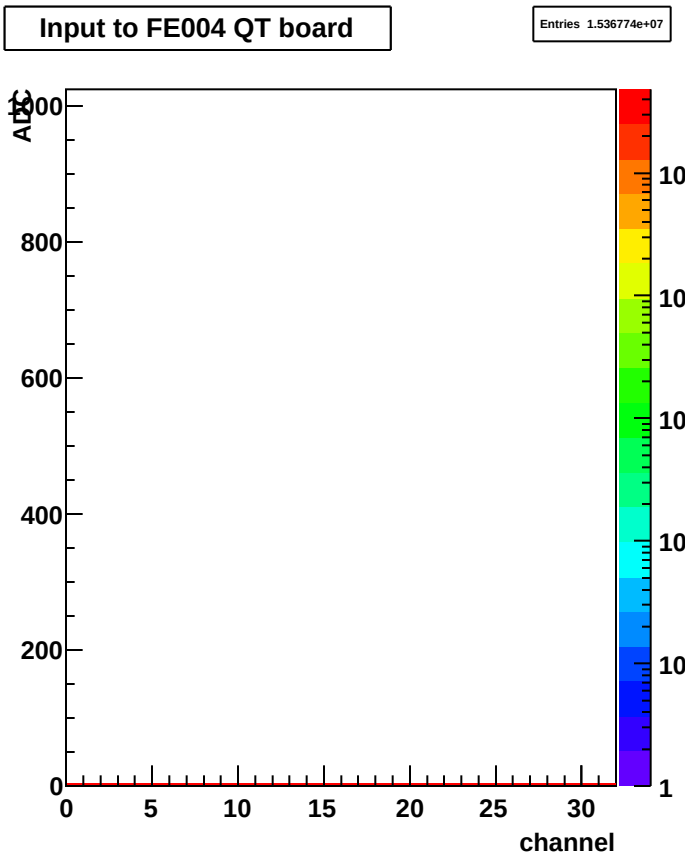

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

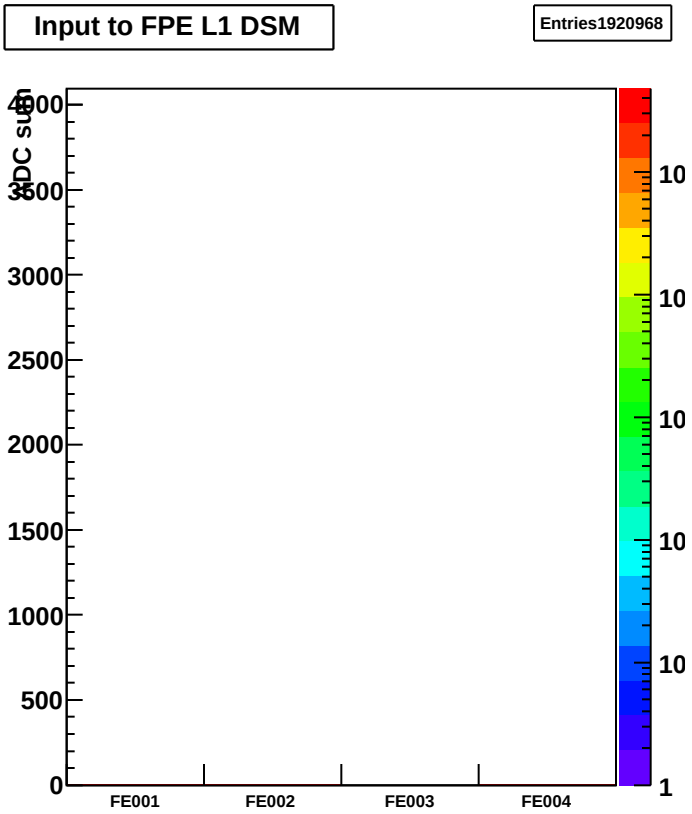

Input to FPD L2 DSM
Entries 2881452
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

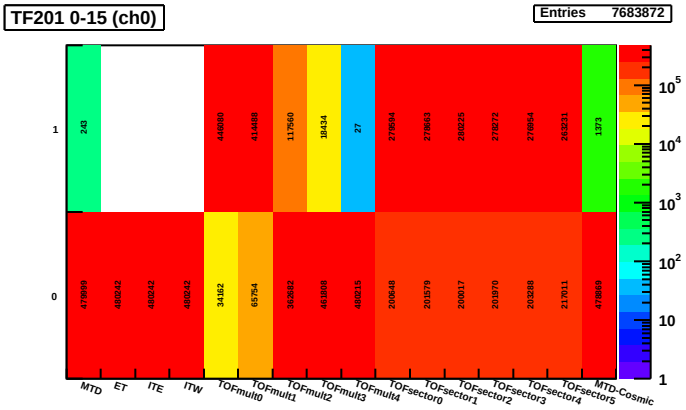

Input to FE001 QT board
Entries 1.536774e+07
ADC
channel

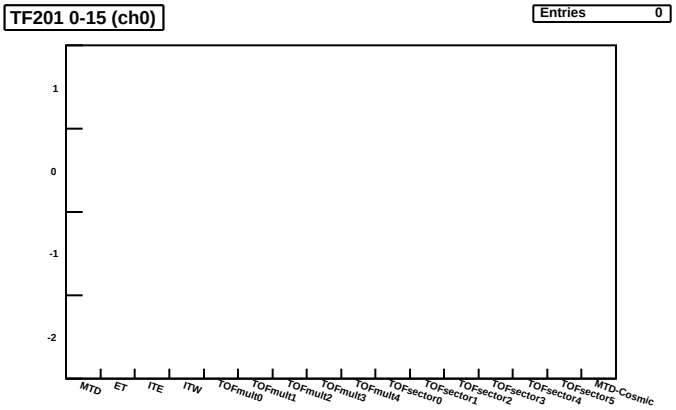

Input to FE002 QT board
Entries 1.536774e+07
ADC
channel

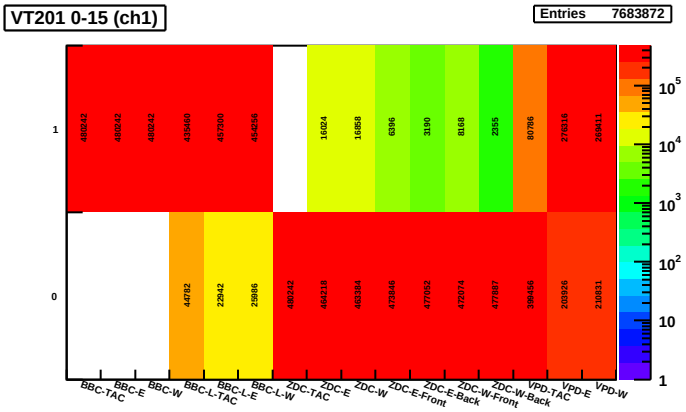

Input to FE003 QT board
Entries 1.536774e+07
ADC
channel

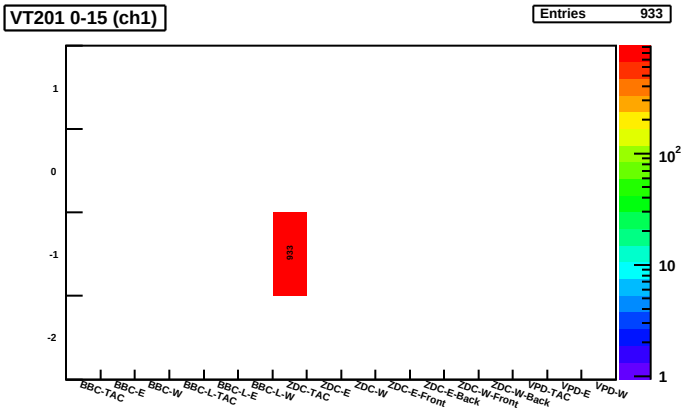

Input to FE004 QT board
Entries 1.536774e+07
ADC
channel

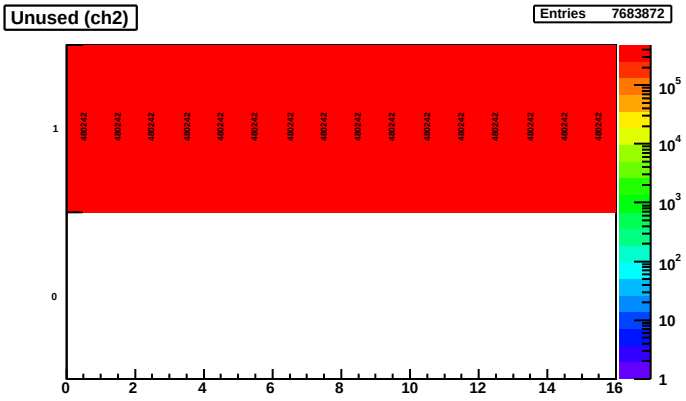

Input to FPE L1 DSM
Entries1920968
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

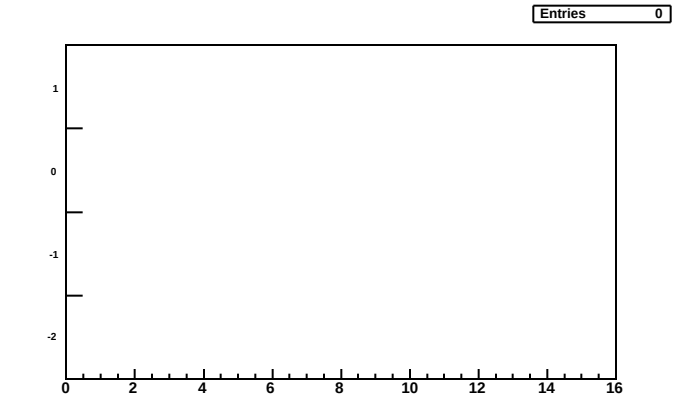

TF201 0-15 (ch0)
Entries 7683872
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 7683872
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 933
933
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 7683872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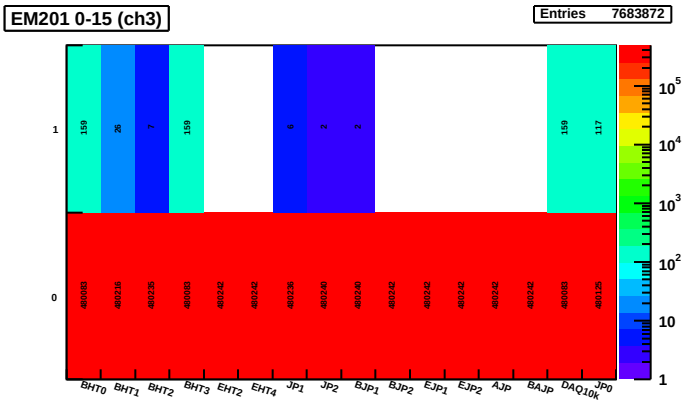
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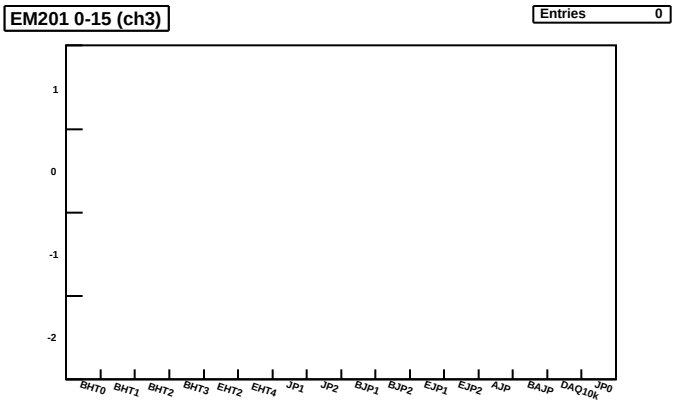
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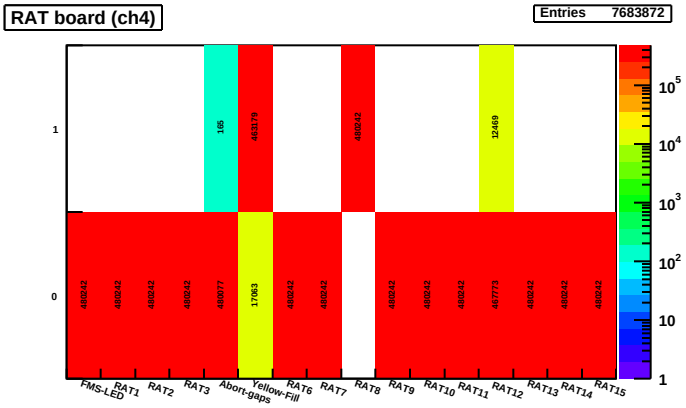
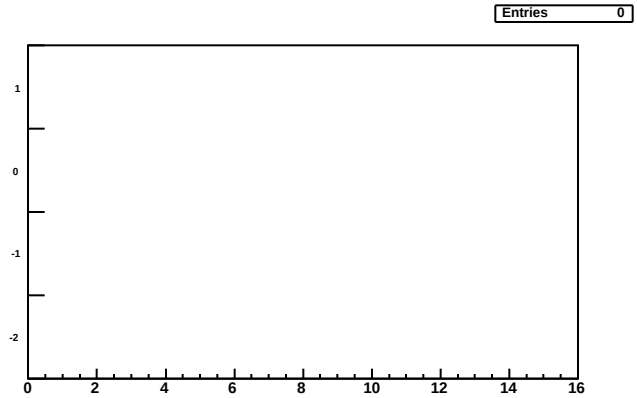
RAT board (ch4)





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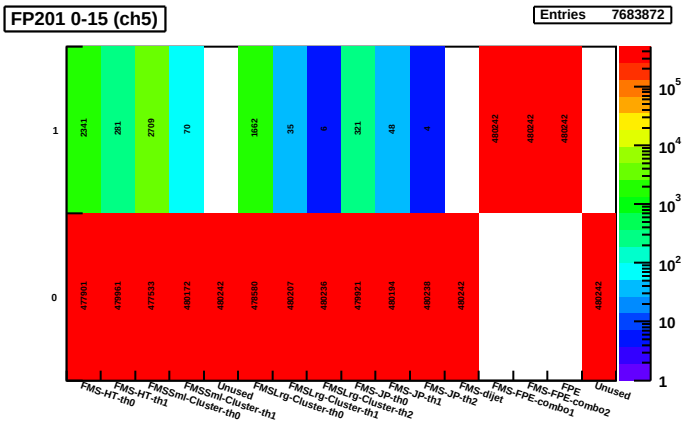

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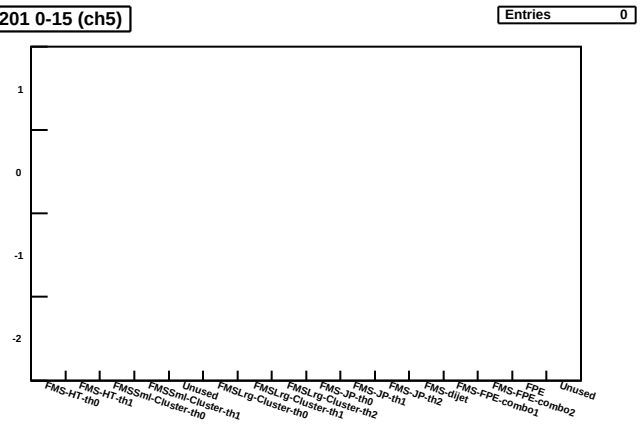

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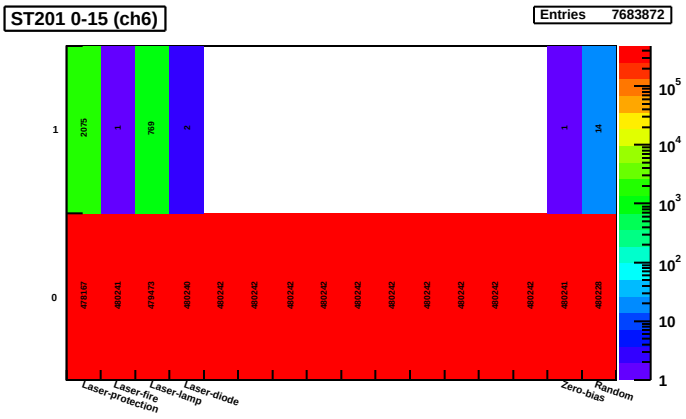

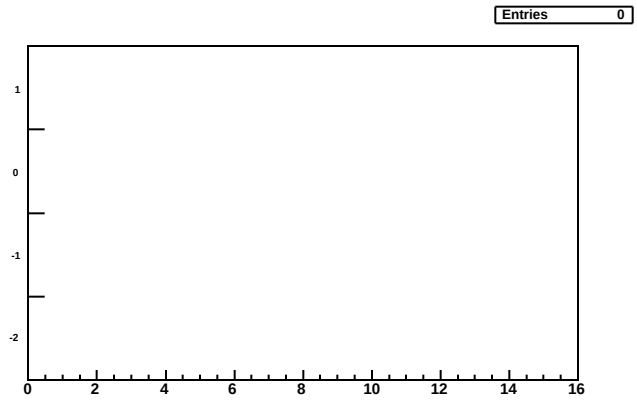
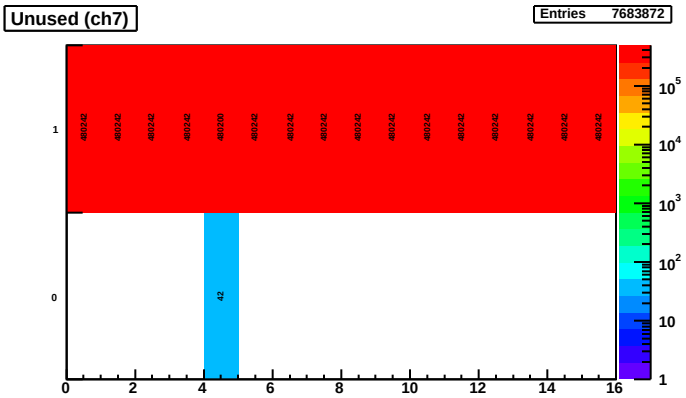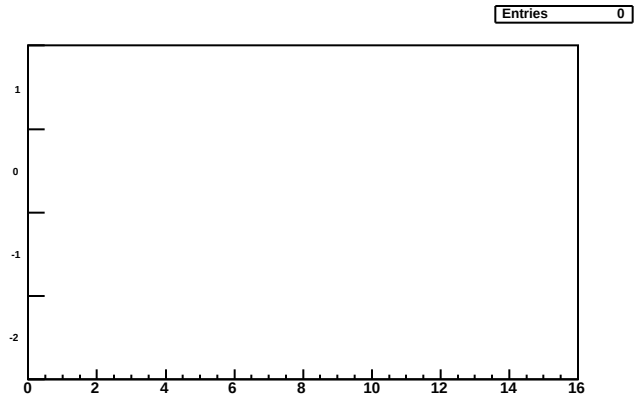
Unused (ch7)