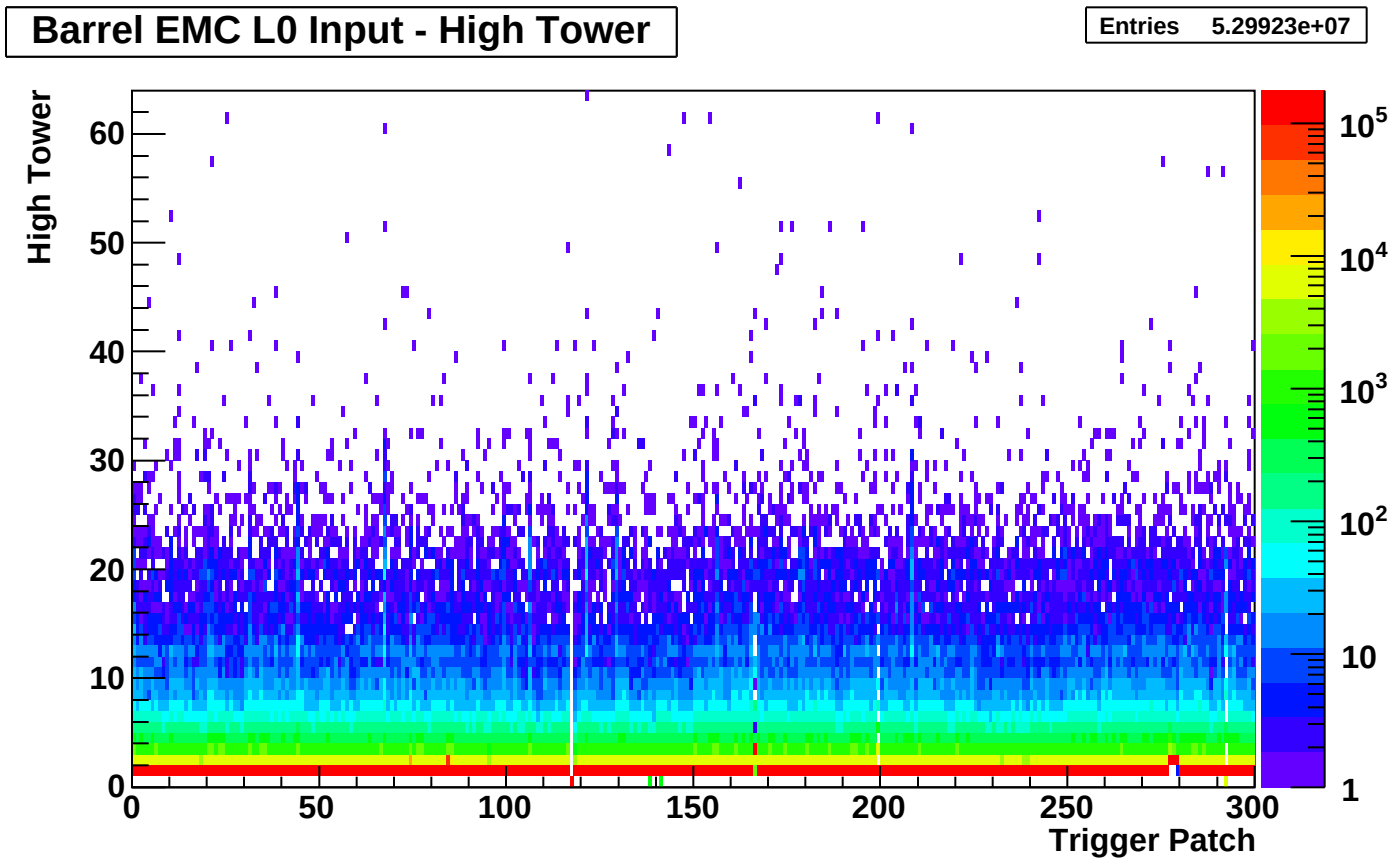
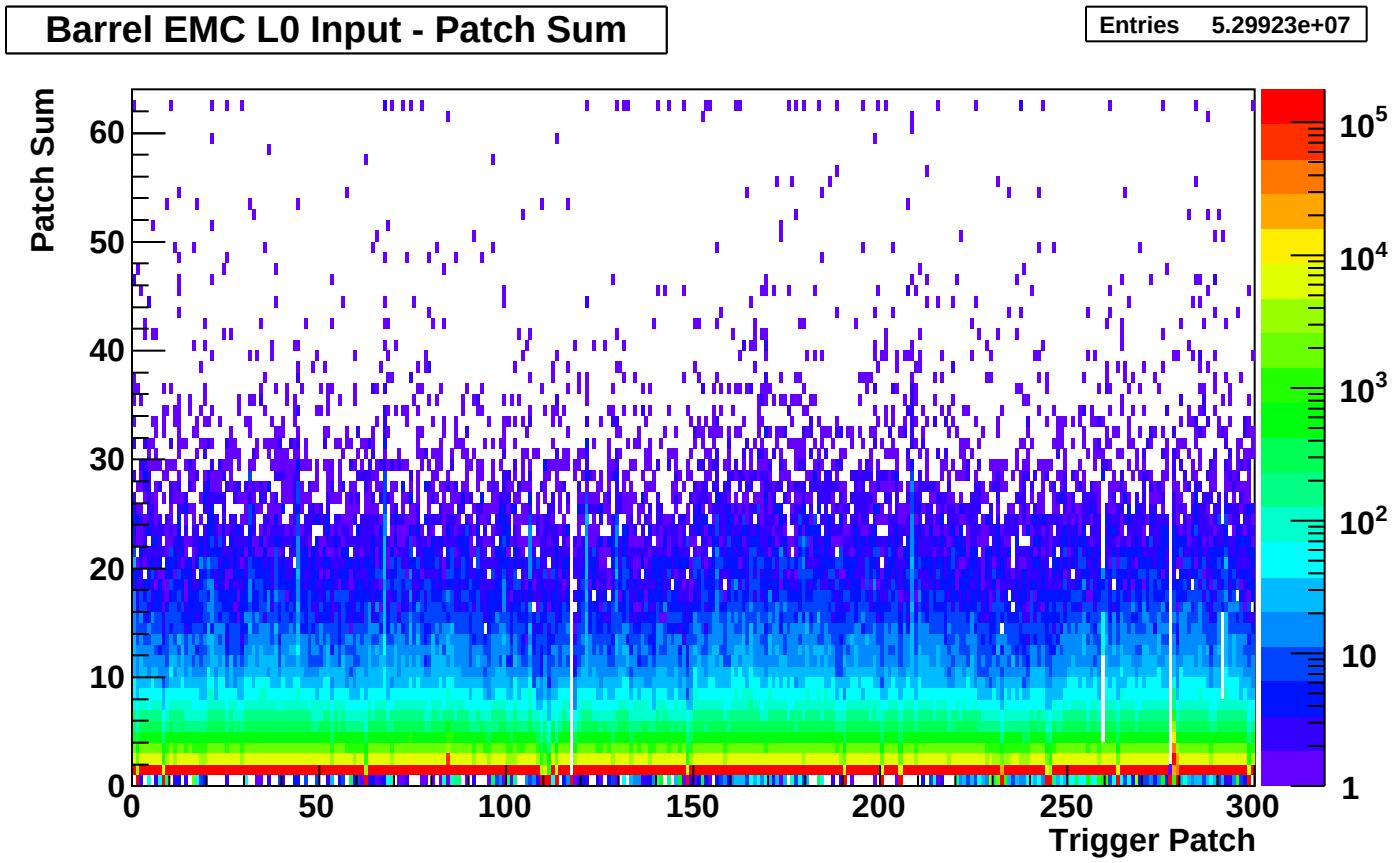
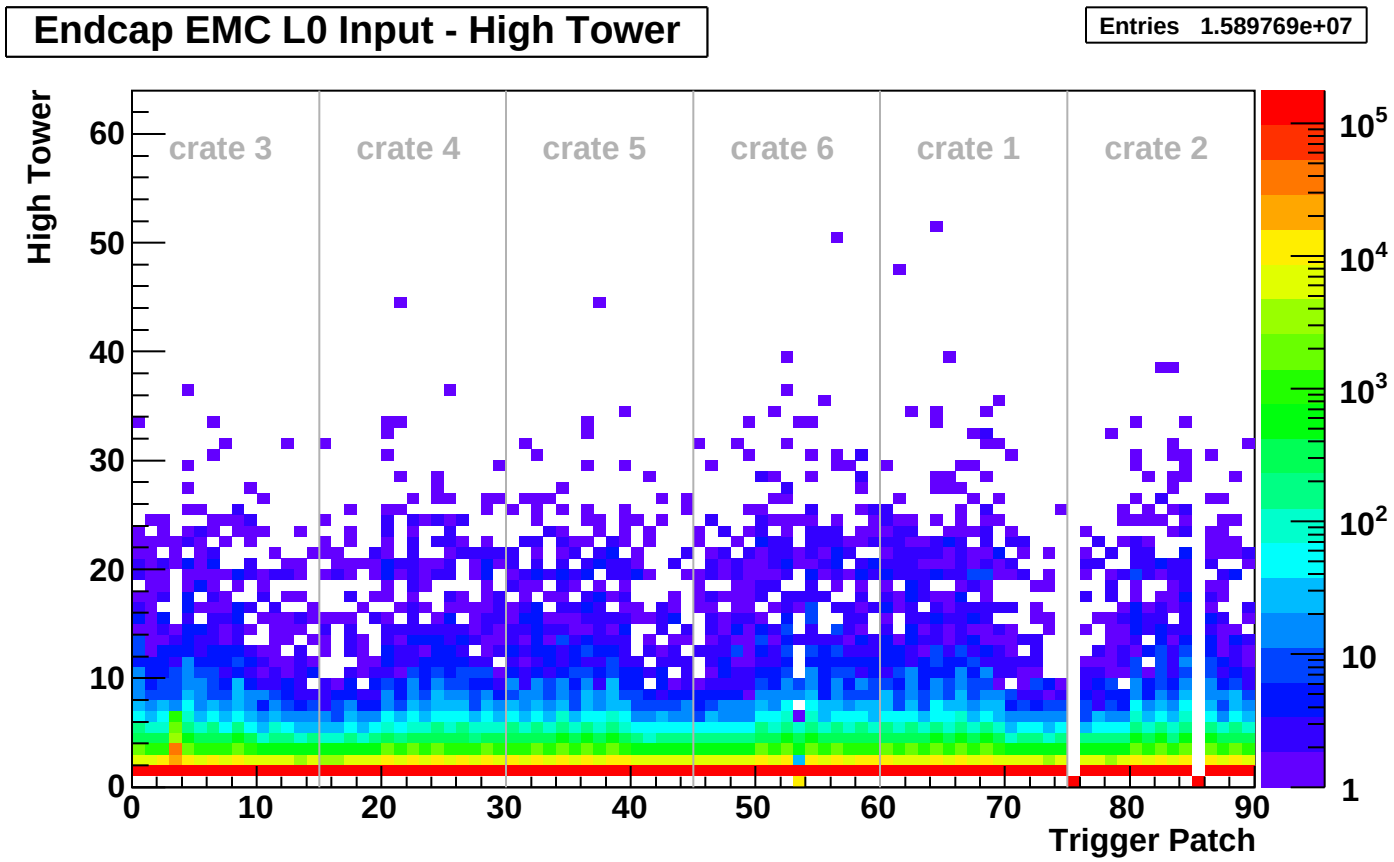
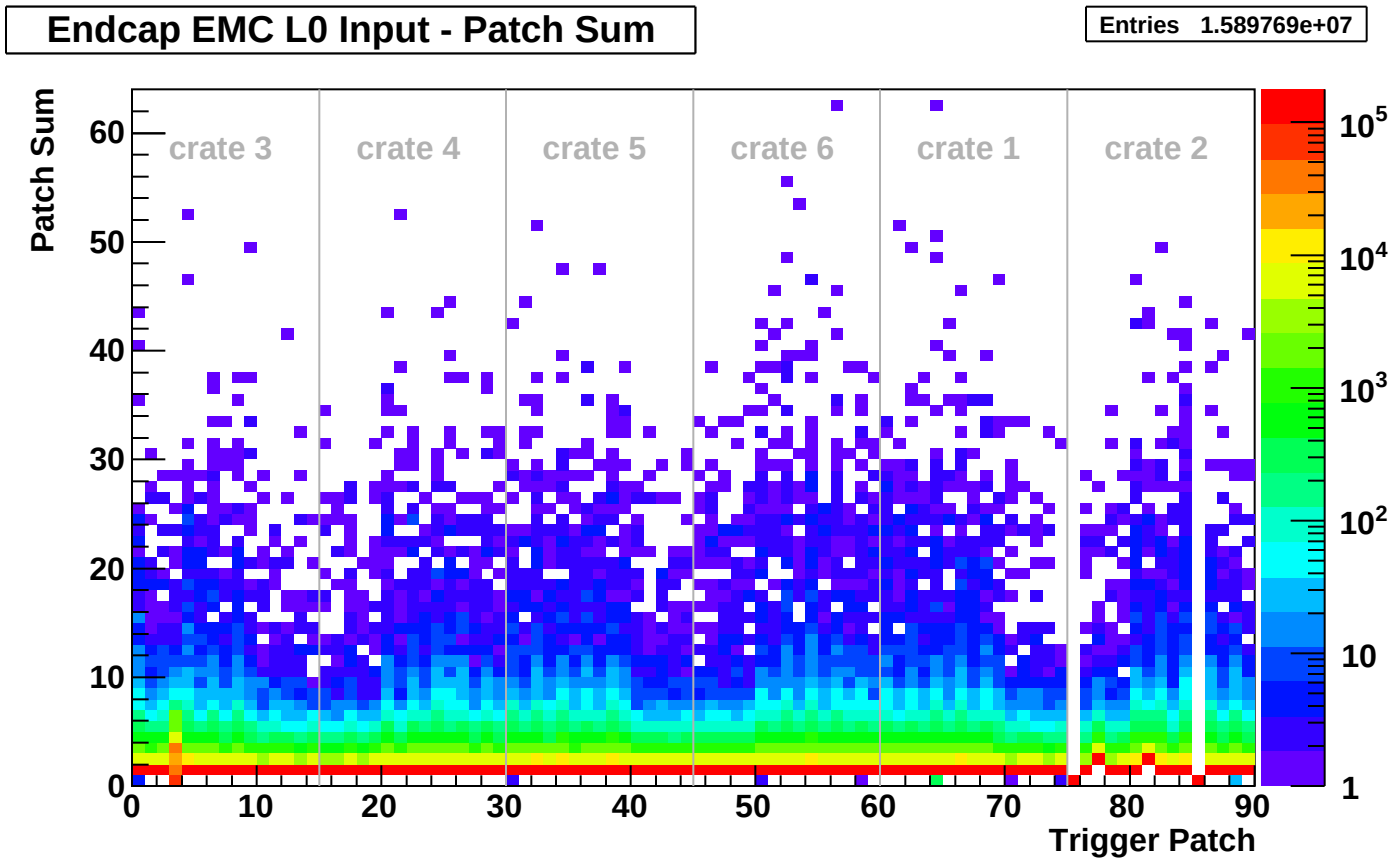
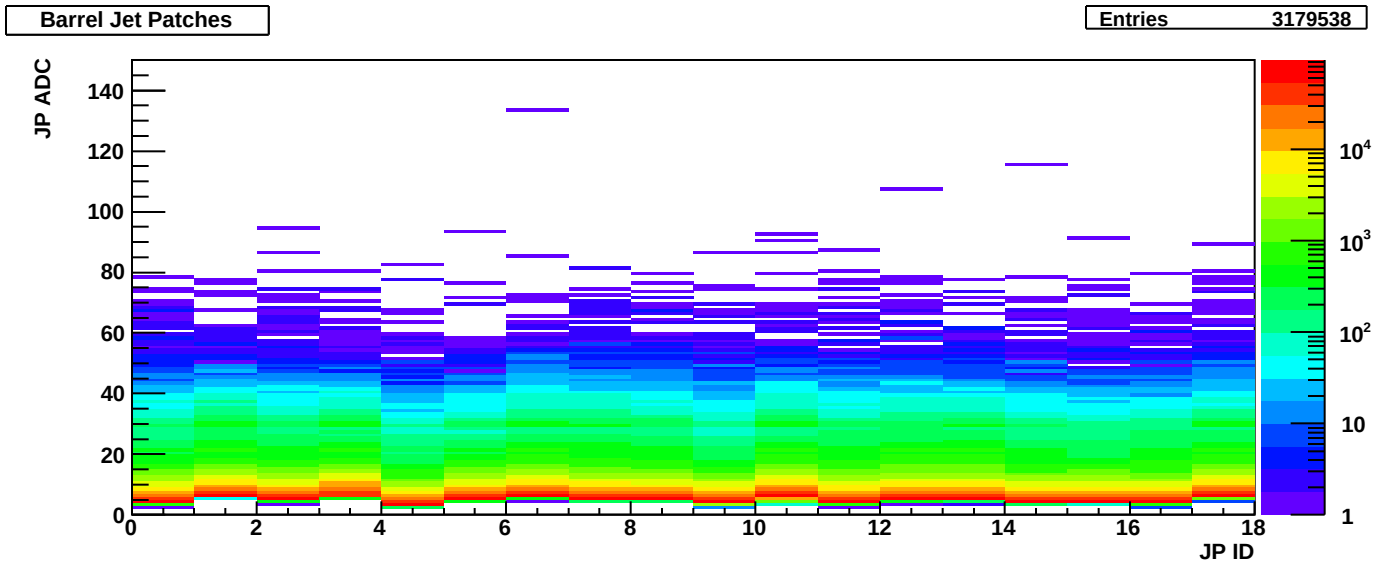
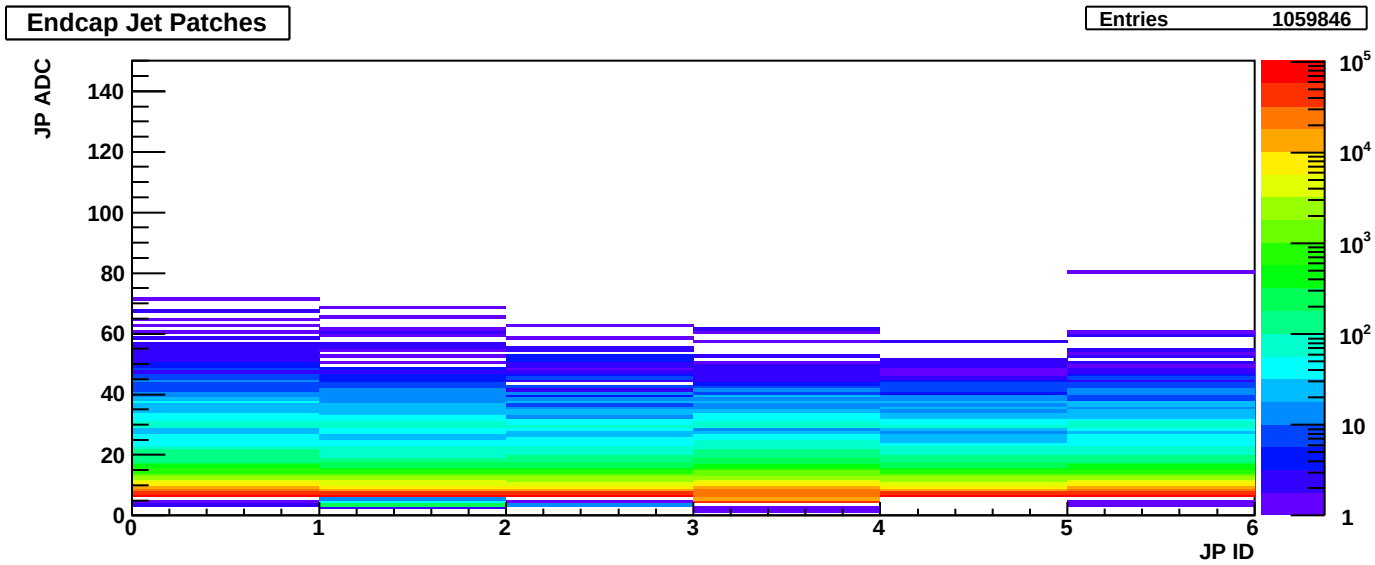

Barrel EMC L0 Input - High Tower
Entries 5.29923e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

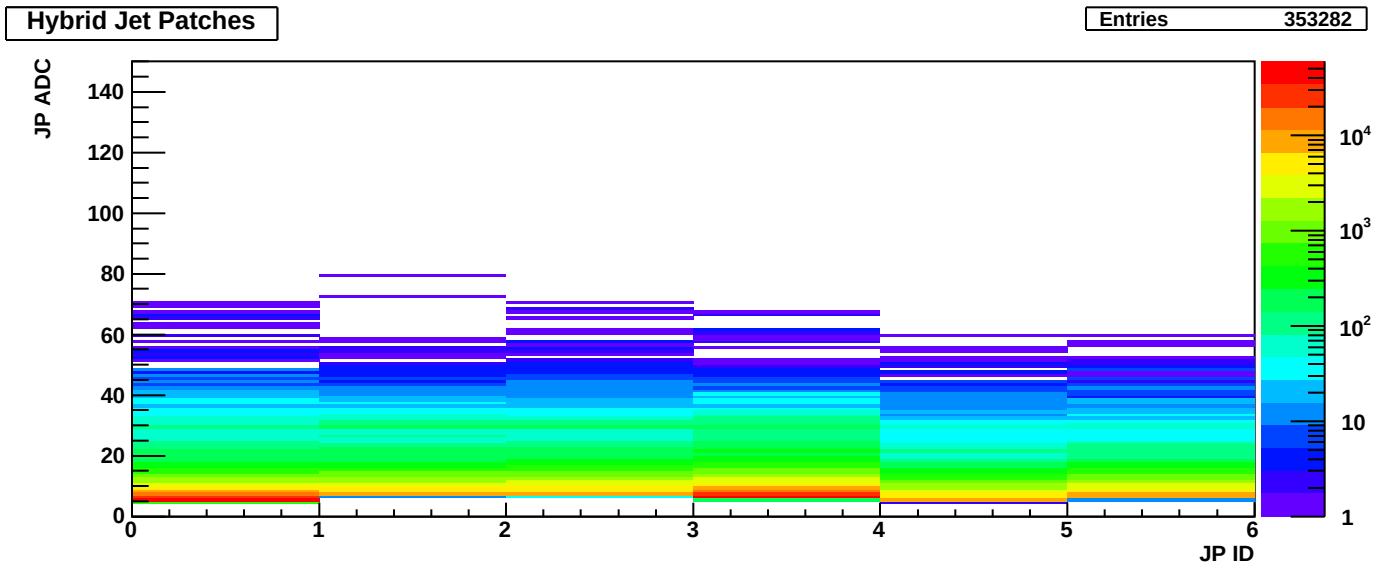

Barrel EMC L0 Input - Patch Sum
Entries 5.29923e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

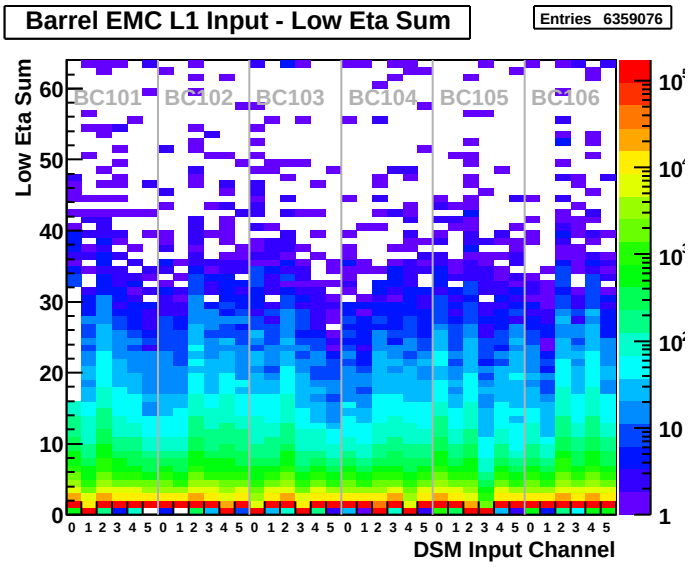

Endcap EMC L0 Input - High Tower
Entries 1.589769e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

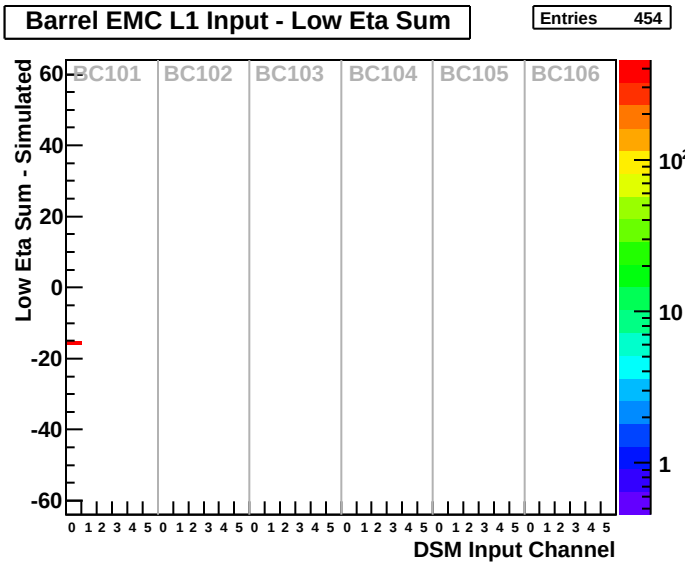

Endcap EMC L0 Input - Patch Sum
Entries 1.589769e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

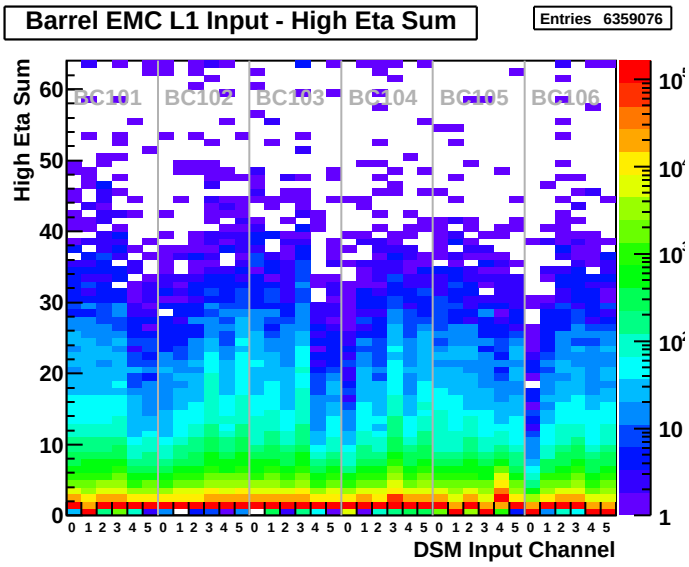

Barrel Jet Patches
Entries 3179538
JP ADC
JP ID

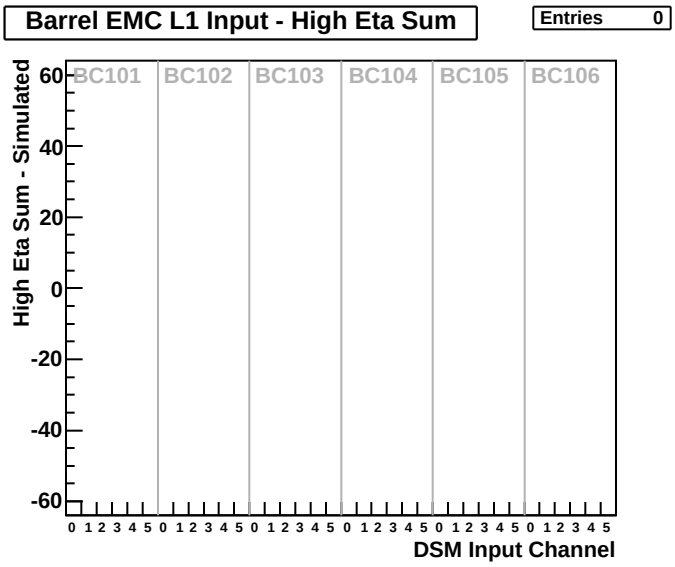

Endcap Jet Patches
Entries 1059846
JP ADC
JP ID

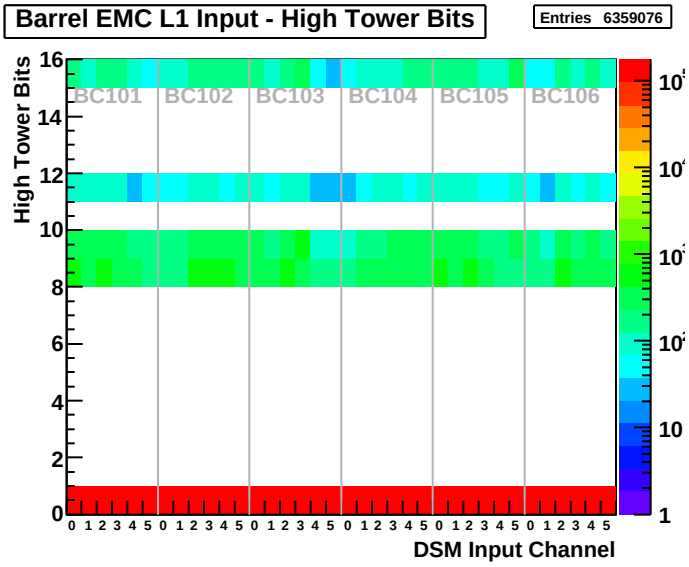

Hybrid Jet Patches
Entries 353282
JP ADC
JP ID

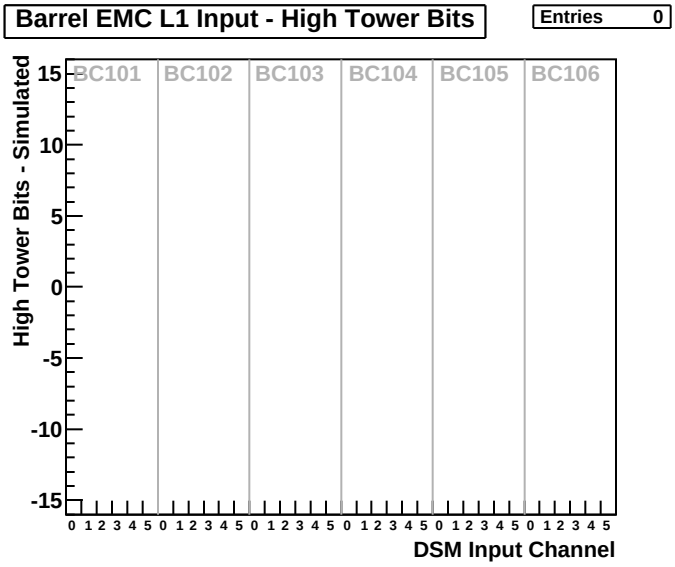

Barrel EMC L1 Input - Low Eta Sum
Entries 6359076
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

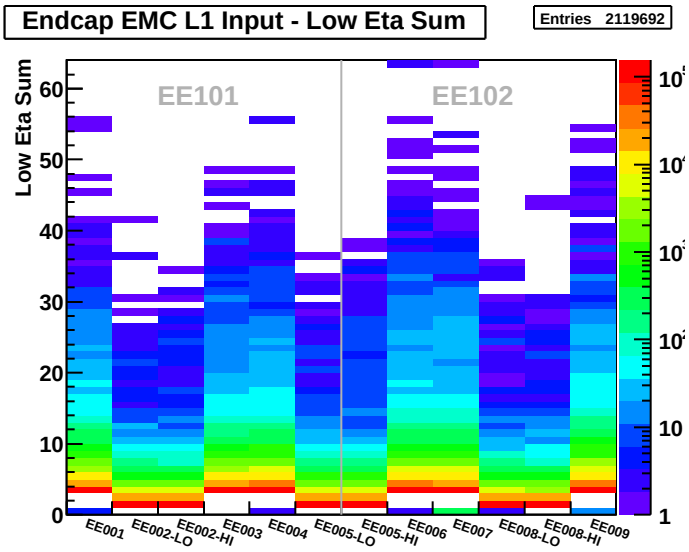

Barrel EMC L1 Input - Low Eta Sum
Entries 454
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

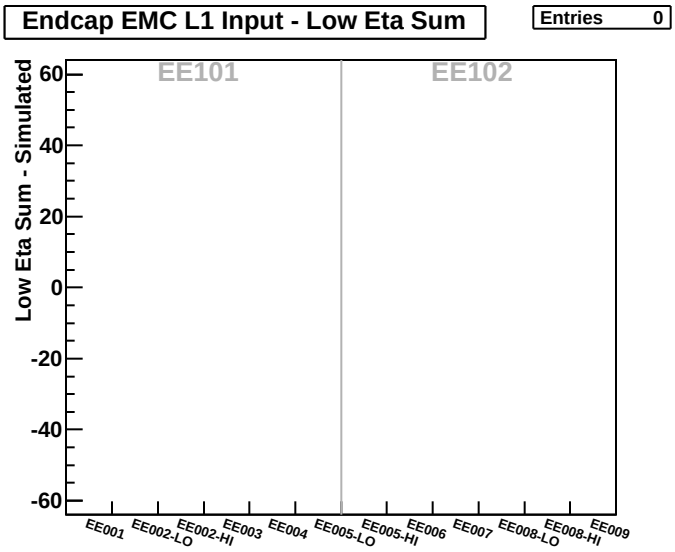

Barrel EMC L1 Input - High Eta Sum
Entries 6359076
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

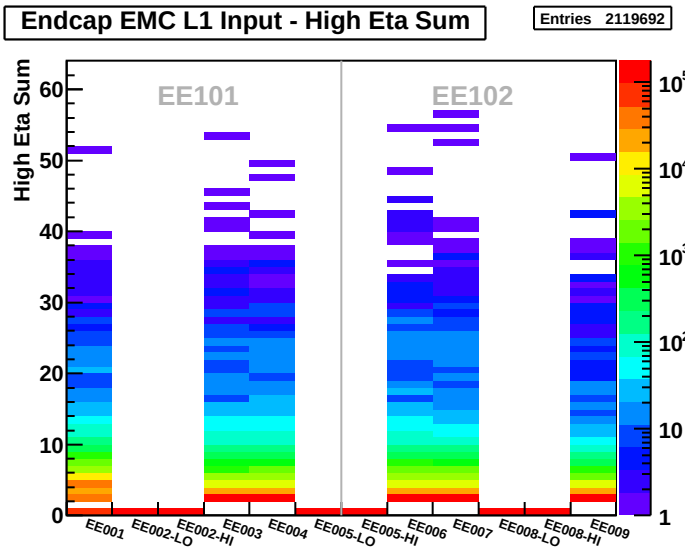

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

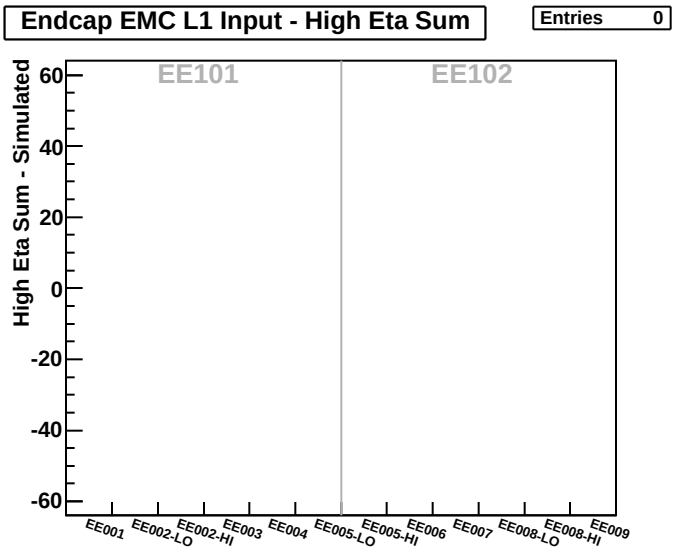

Barrel EMC L1 Input - High Tower Bits
Entries 6359076
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

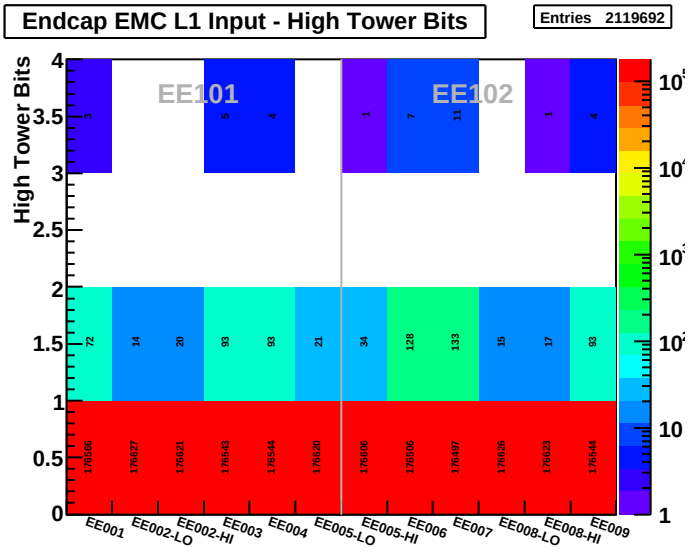

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

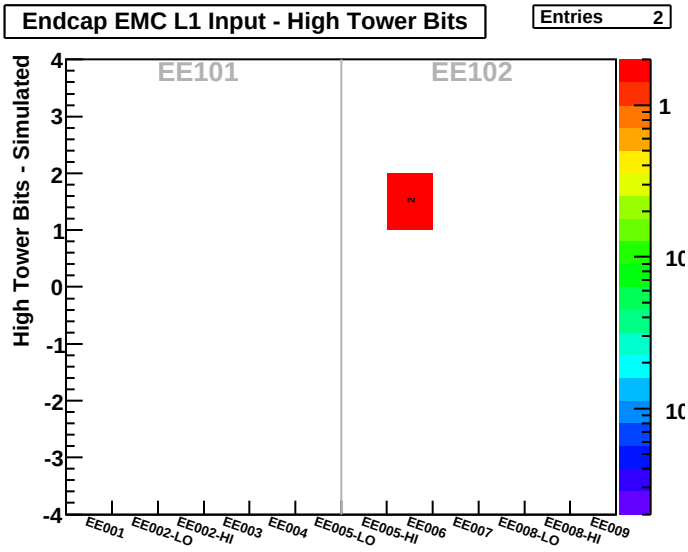

Endcap EMC L1 Input - Low Eta Sum
Entries 2119692
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

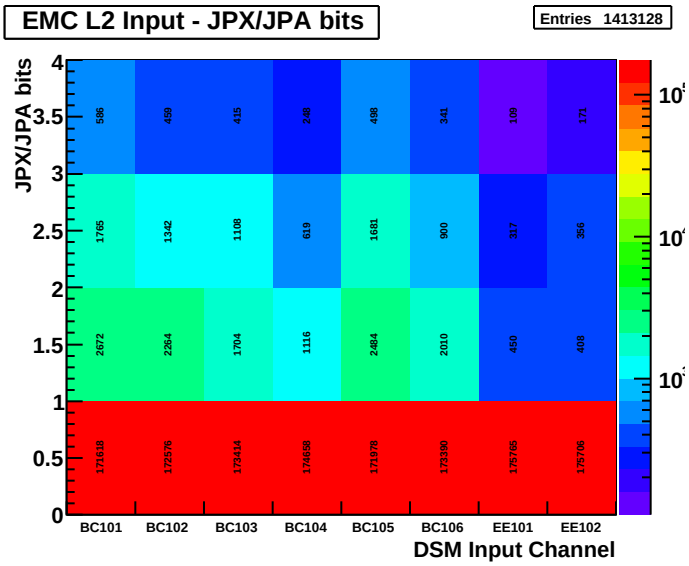

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

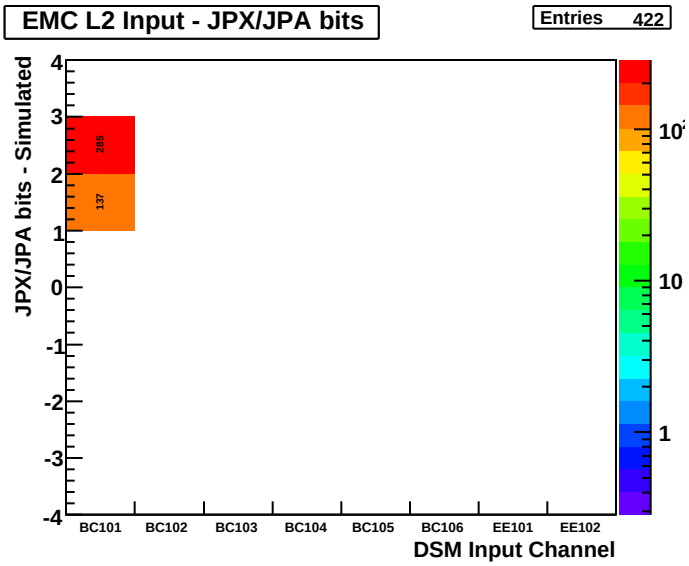

Endcap EMC L1 Input - High Eta Sum
Entries 2119692
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

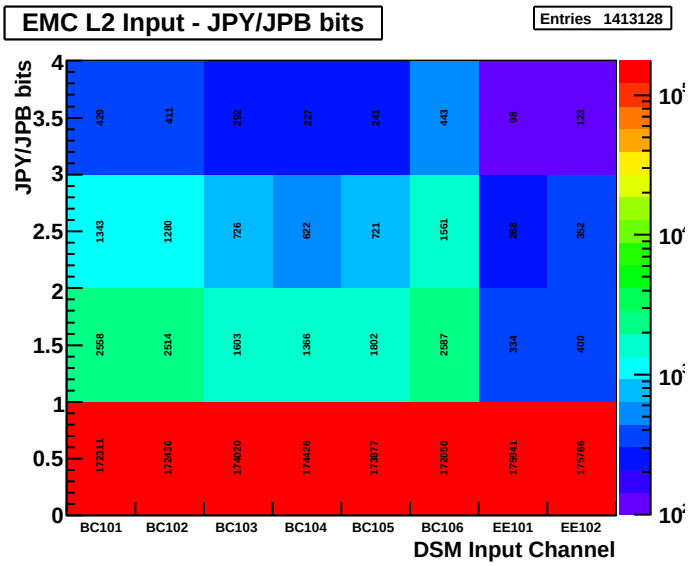

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

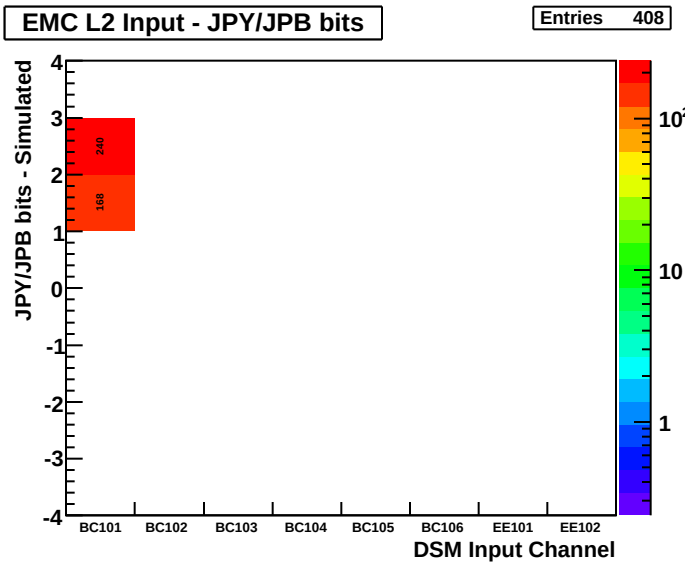

Endcap EMC L1 Input - High Tower Bits
Entries 2119692
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

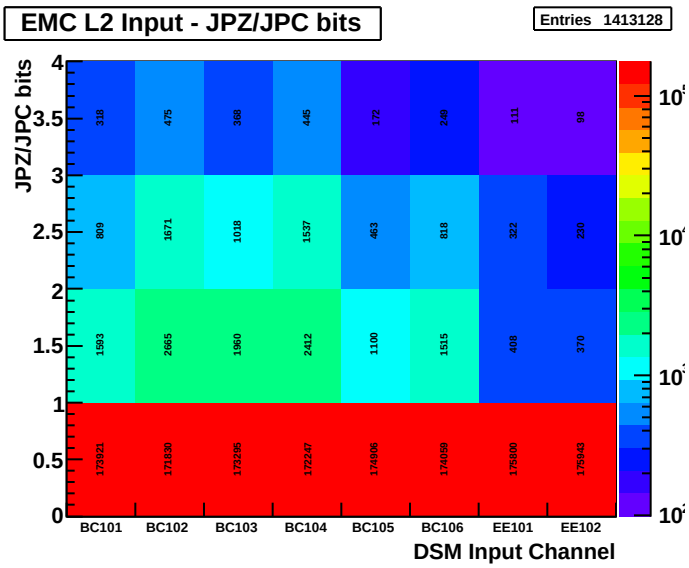

Endcap EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

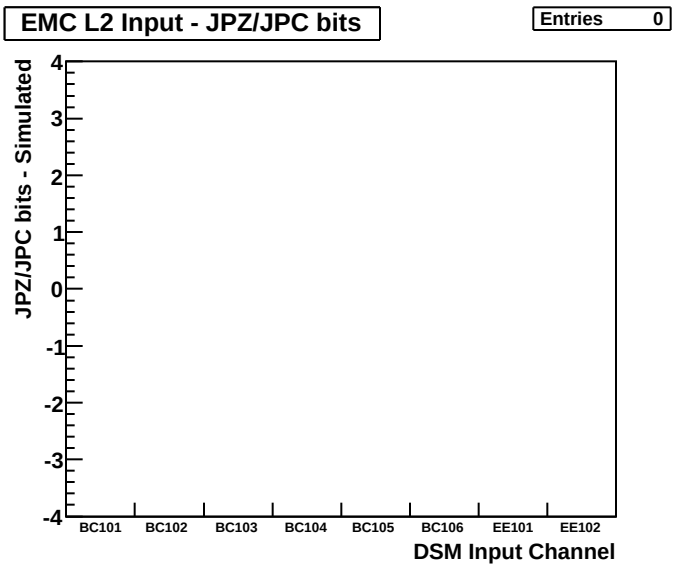

EMC L2 Input - JPX/JPA bits
Entries 1413128
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
586 499 415 248 498 341 109 171
1765 1342 1108 619 1681 900 317 356
2672 2264 1704 1116 2484 2010 450 408
171618 172576 172414 174658 171978 173390 175765 175706

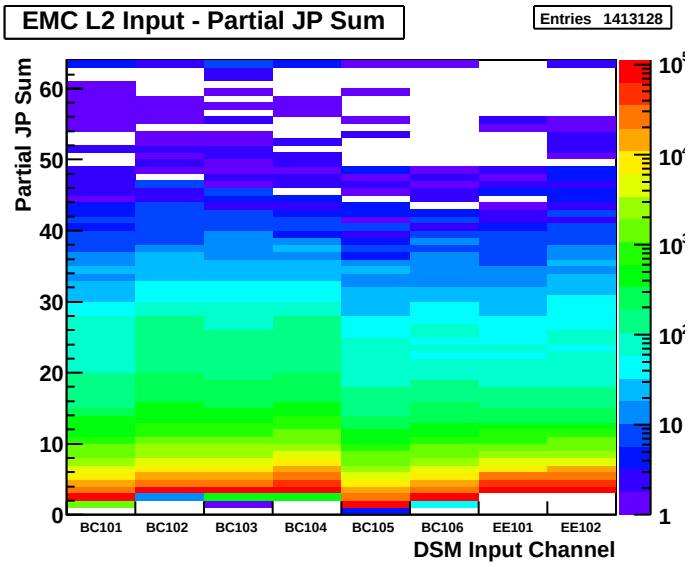

EMC L2 Input - JPX/JPA bits
Entries 422
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
285
137

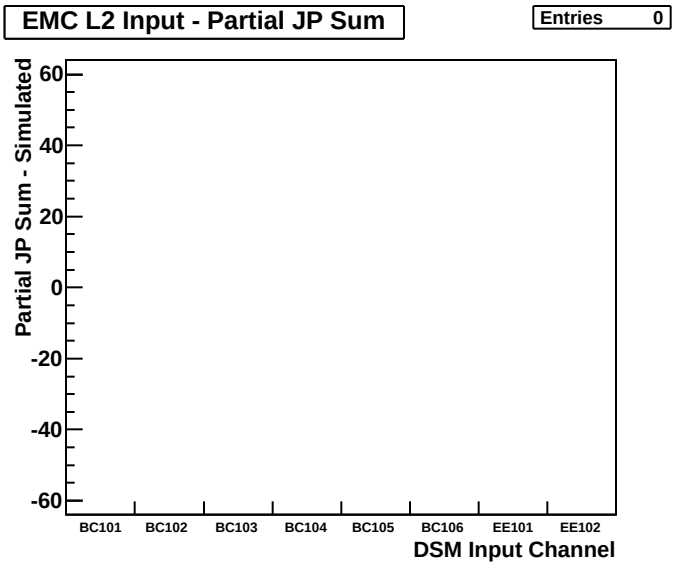

EMC L2 Input - JPY/JPB bits
Entries 1413128
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
429 411 292 227 241 443 98 123
1343 1280 726 622 721 1561 268 352
2558 2514 1603 1366 1802 2587 334 400
172311 172436 174020 174426 173877 172050 175941 175766

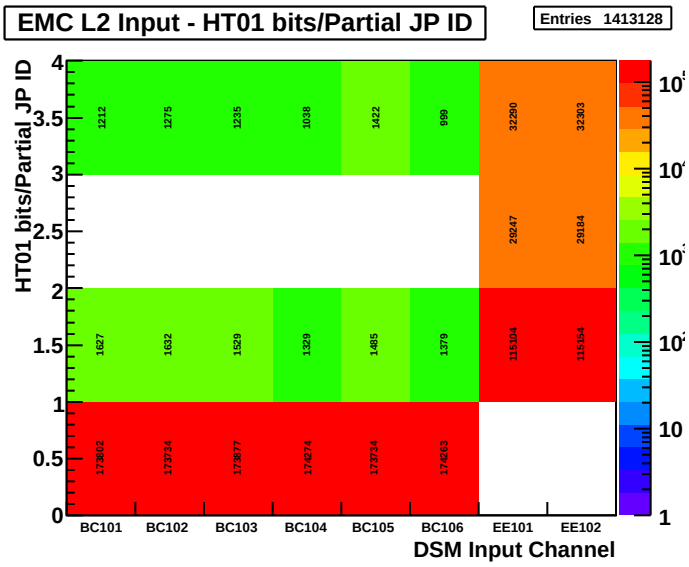

EMC L2 Input - JPY/JPB bits
Entries 408
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
240
168

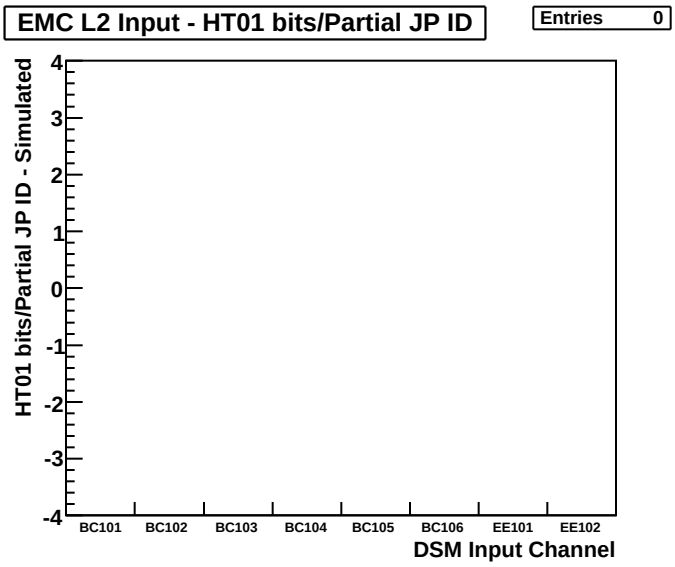

EMC L2 Input - JPZ/JPC bits
Entries 1413128
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
318 475 368 445 172 249 111 98
809 1671 1018 1537 463 818 322 230
1593 2665 1960 2412 1100 1515 408 370
173921 171830 173295 172247 174906 174099 175800 175843

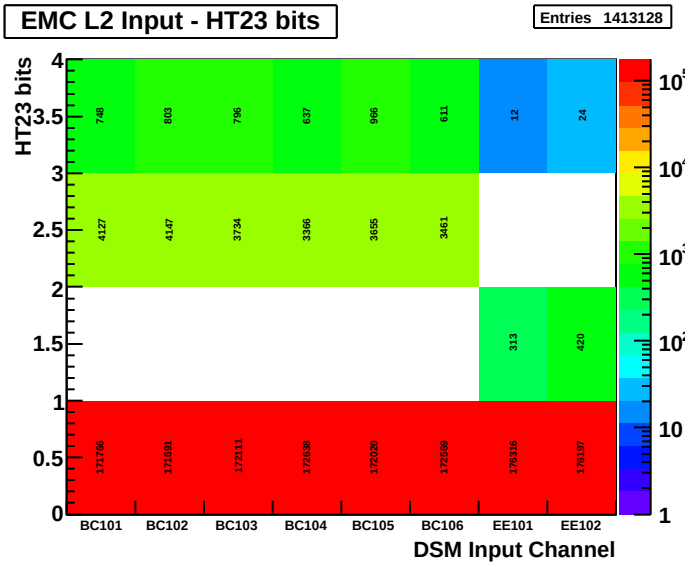

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

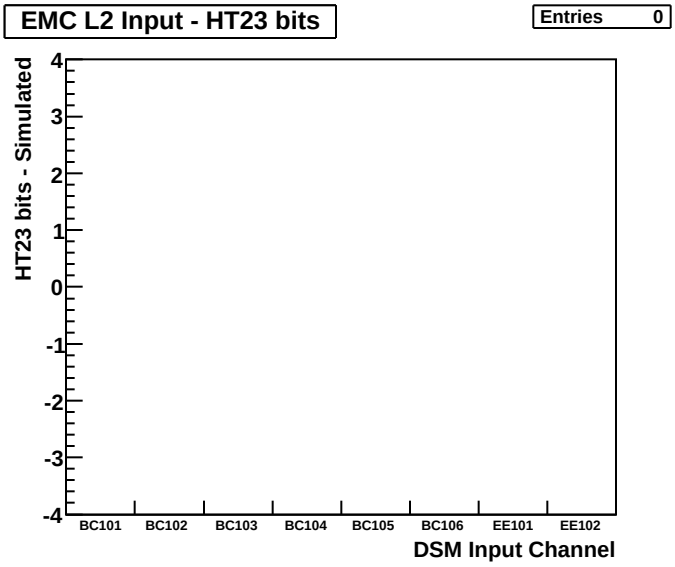

EMC L2 Input - Partial JP Sum
Entries 1413128
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

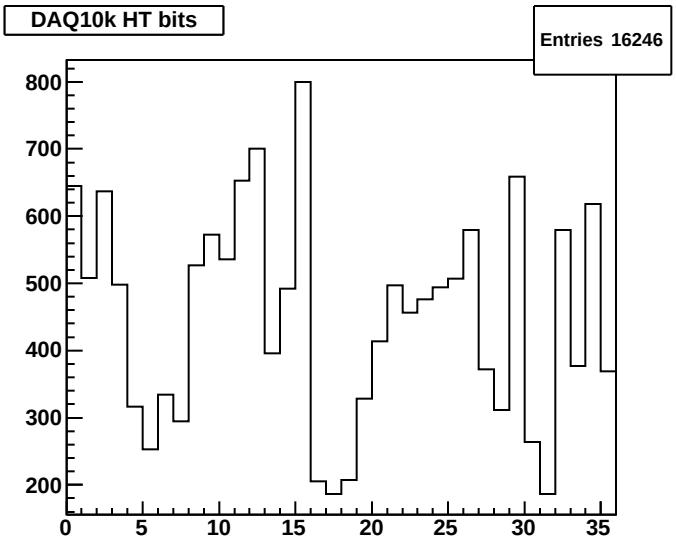

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

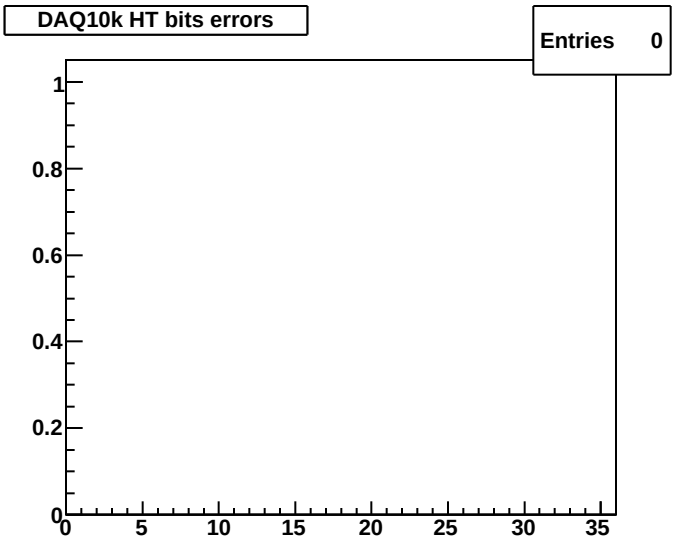

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1413128
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

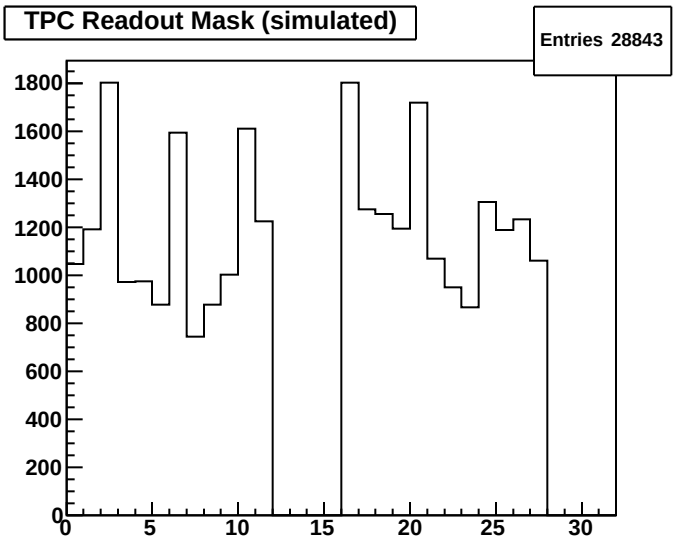

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

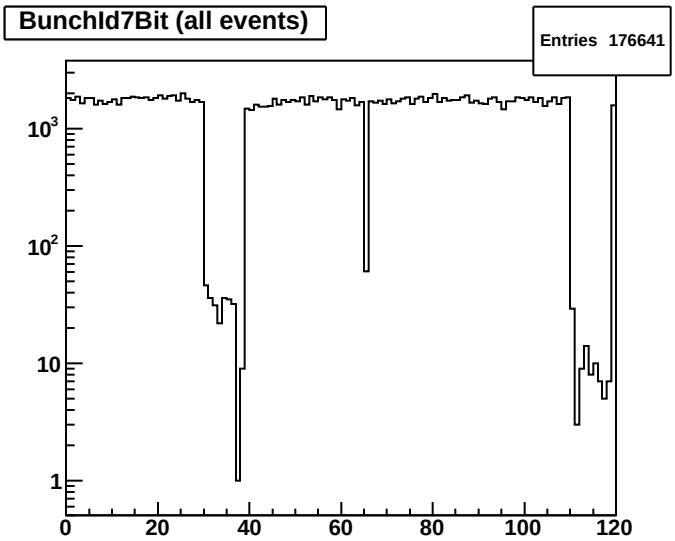

EMC L2 Input - HT23 bits
Entries 1413128
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

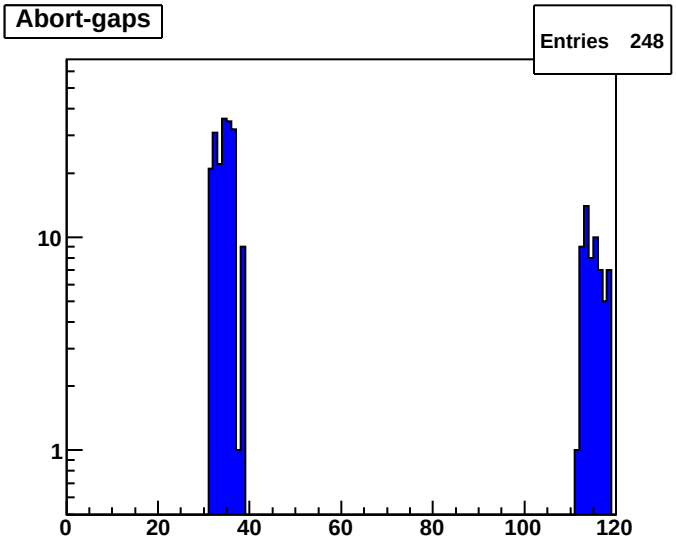

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

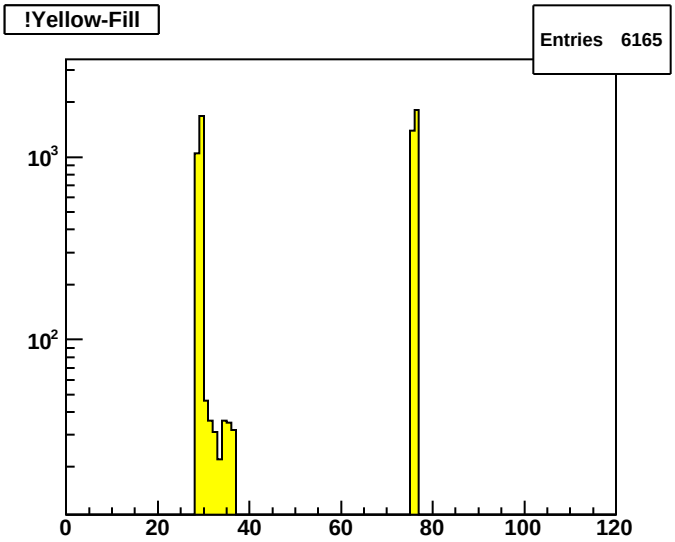

DAQ10k HT bits
Entries 16246

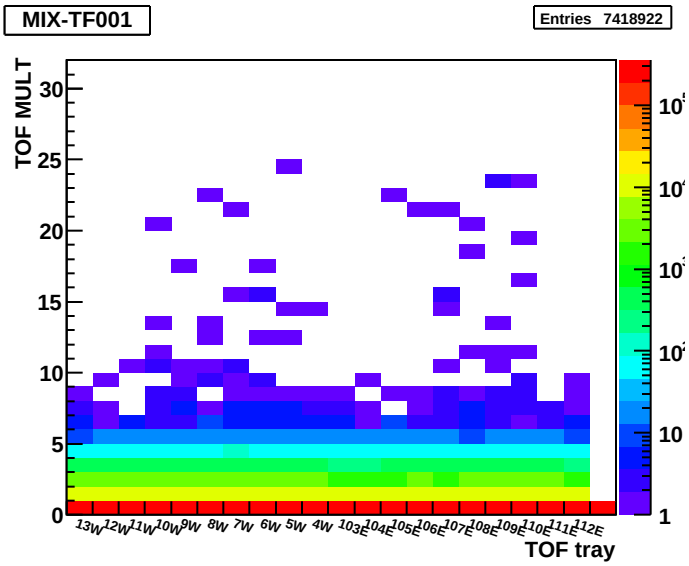

DAQ10k HT bits errors
Entries 0

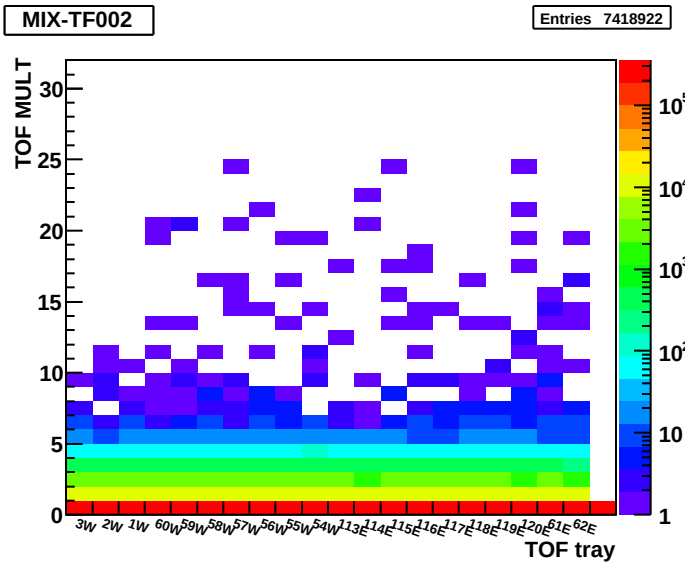

TPC Readout Mask (simulated)
Entries 28843

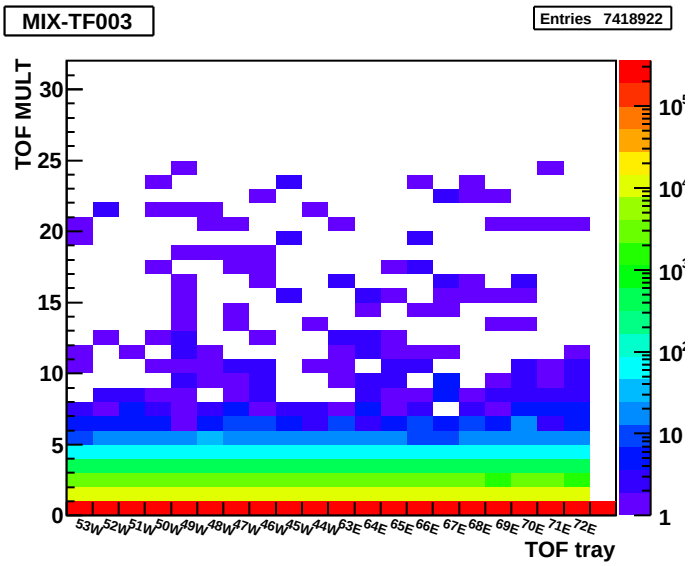

BunchId7Bit (all events)
Entries 176641

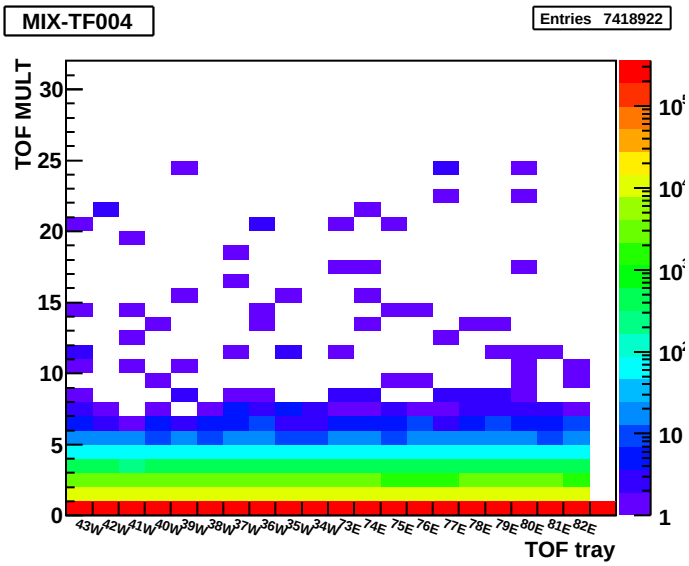

Abort-gaps
Entries 248

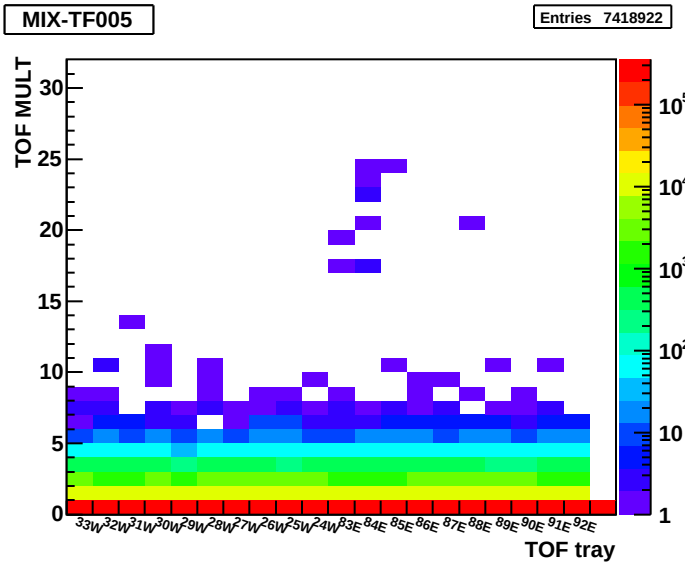

!Yellow-Fill
Entries 6165

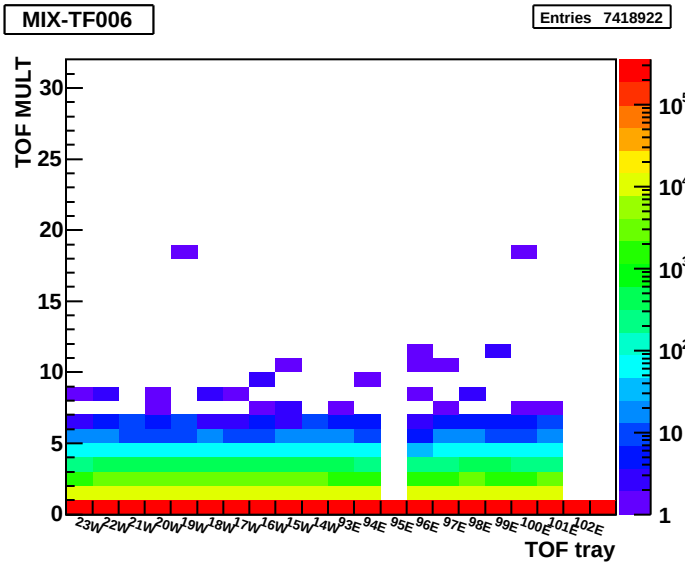

MIX-TF001
Entries 7418922
TOF MULT
TOF tray

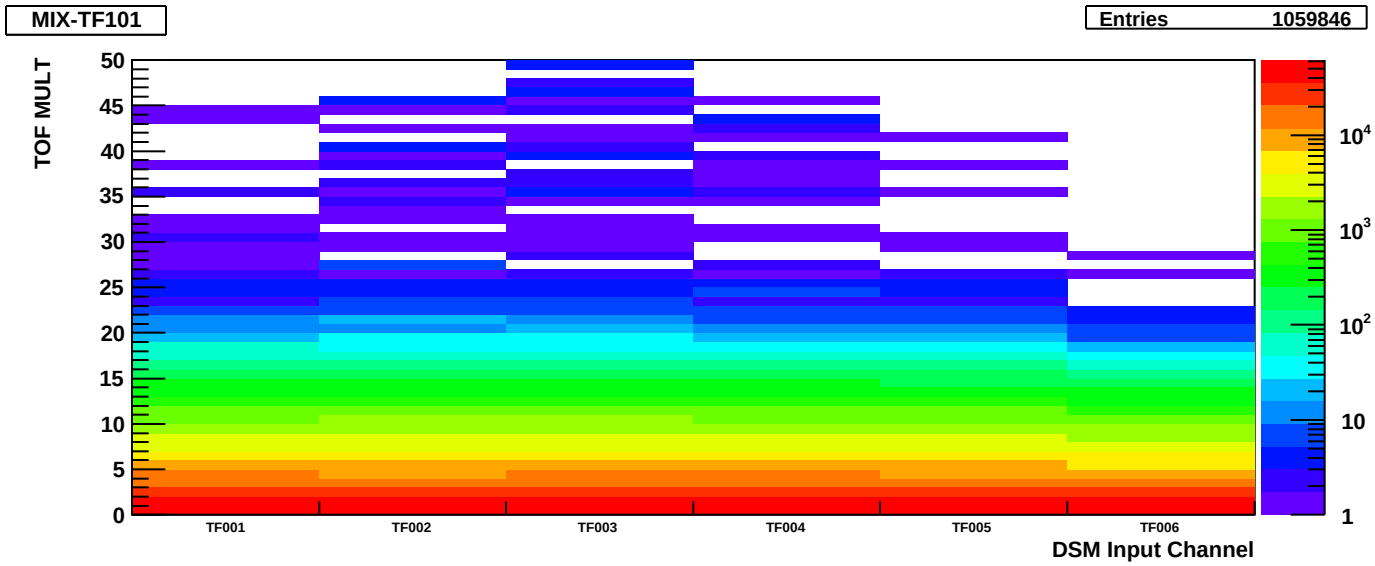

MIX-TF002
Entries 7418922
TOF MULT
TOF tray

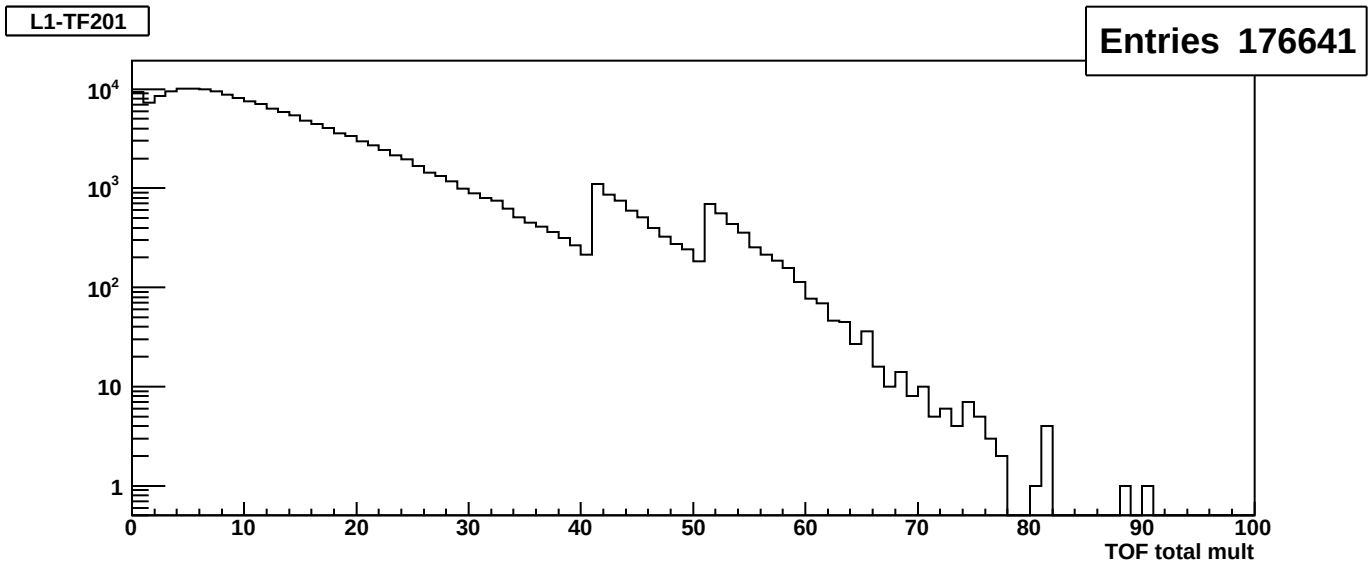

MIX-TF003
Entries 7418922
TOF MULT
TOF tray

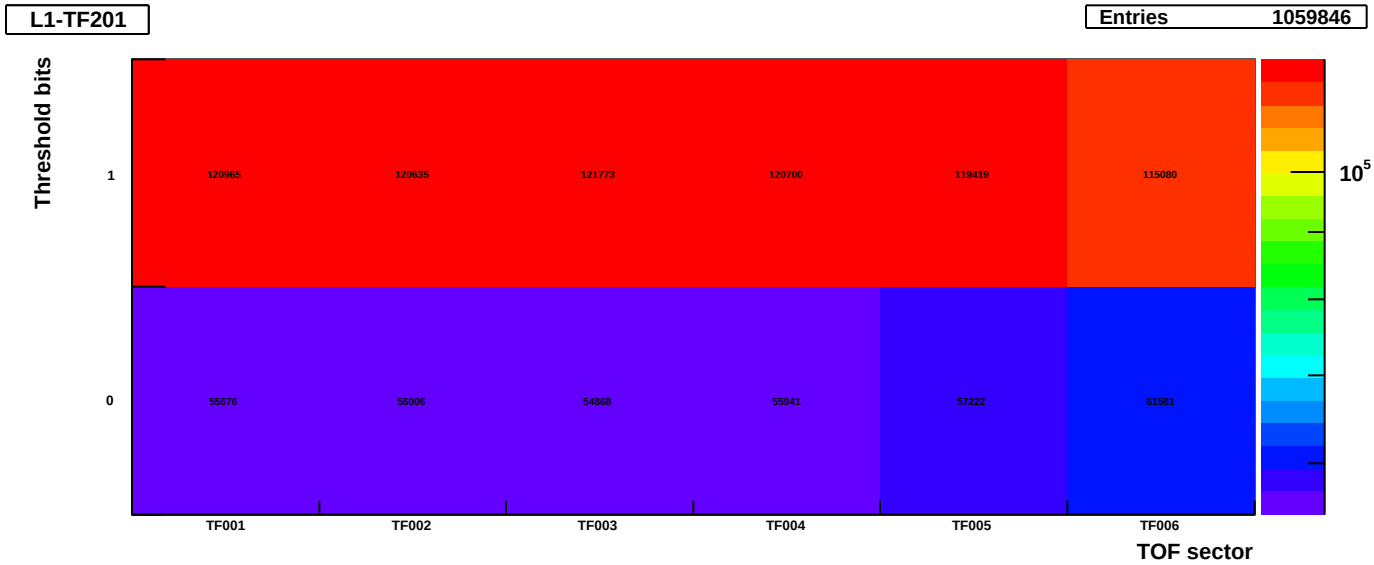

MIX-TF004
Entries 7418922
TOF MULT
TOF tray

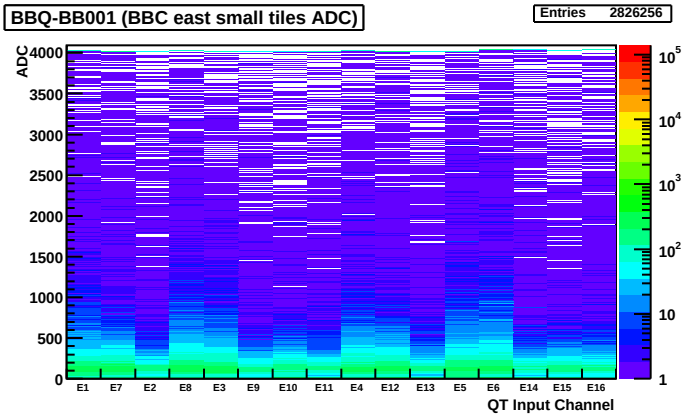

MIX-TF005
Entries 7418922
TOF MULT
TOF tray

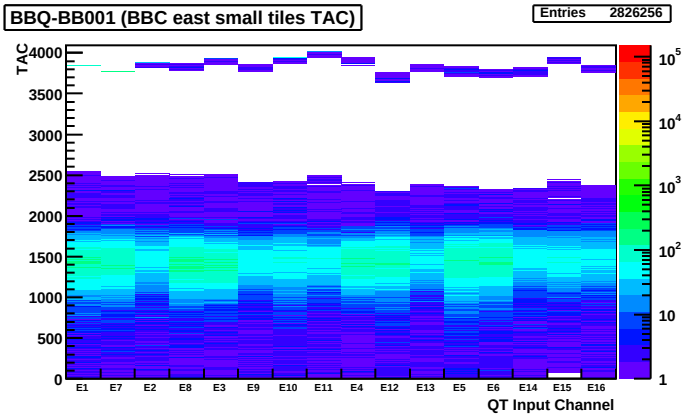

MIX-TF006
Entries 7418922
TOF MULT
TOF tray

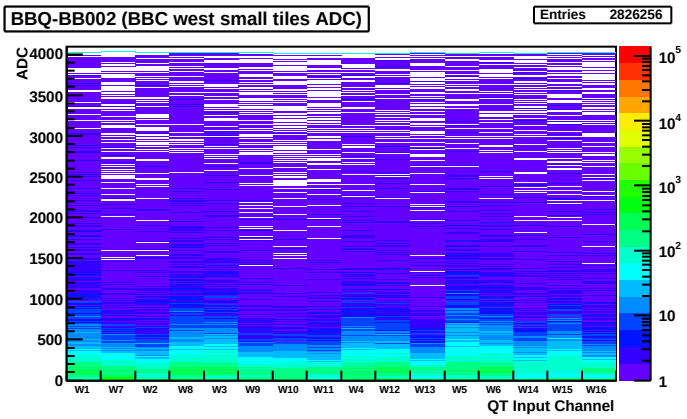

MIX-TF101
Entries 1059846
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

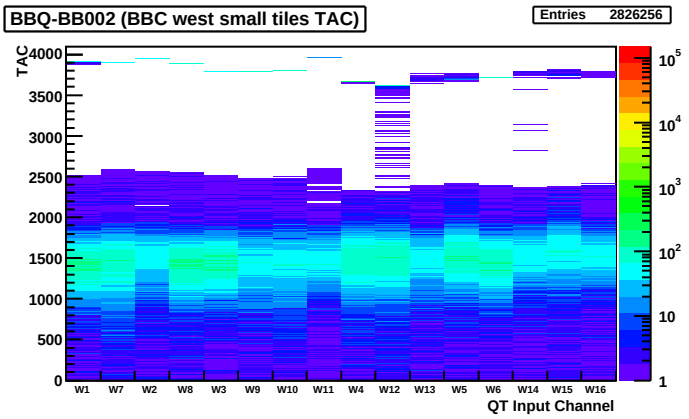

L1-TF201
Entries 176641
TOF total mult

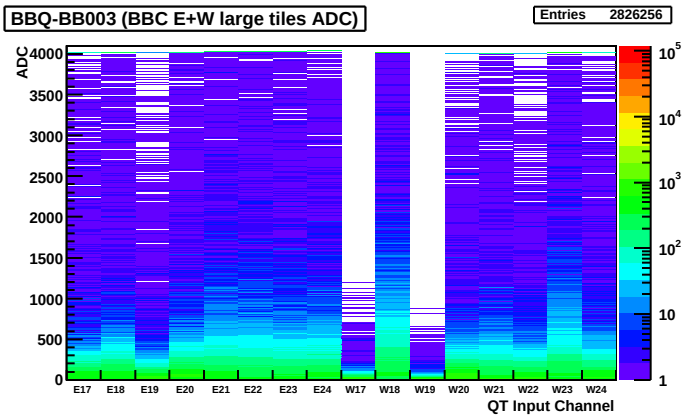

L1-TF201
Entries 1059846
Threshold bits
120965
120635
121773
120700
119419
115080
55676
56006
54868
55941
57222
61561
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

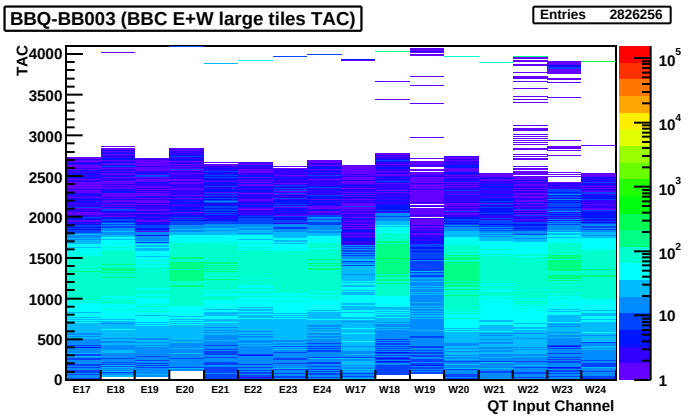

BBQ-BB001 (BBC east small tiles ADC)
Entries 2826256
ADC
QT Input Channel

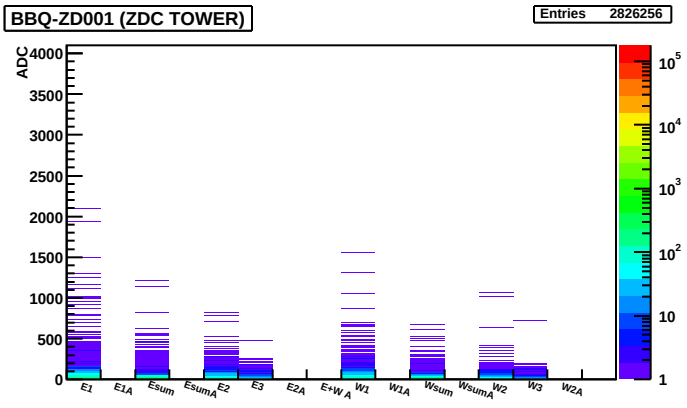

BBQ-BB001 (BBC east small tiles TAC)
Entries 2826256
TAC
QT Input Channel

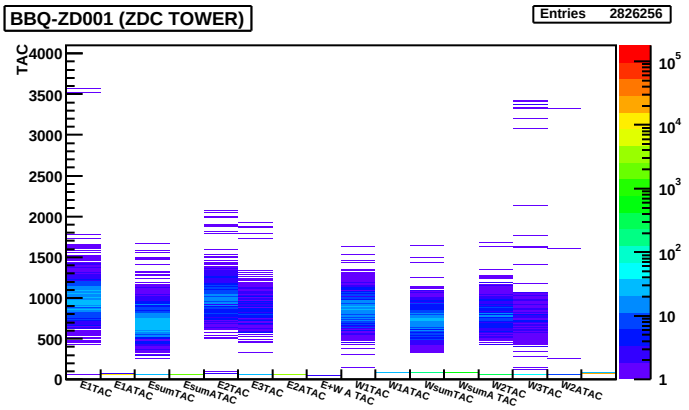

BBQ-BB002 (BBC west small tiles ADC)
Entries 2826256
ADC
QT Input Channel

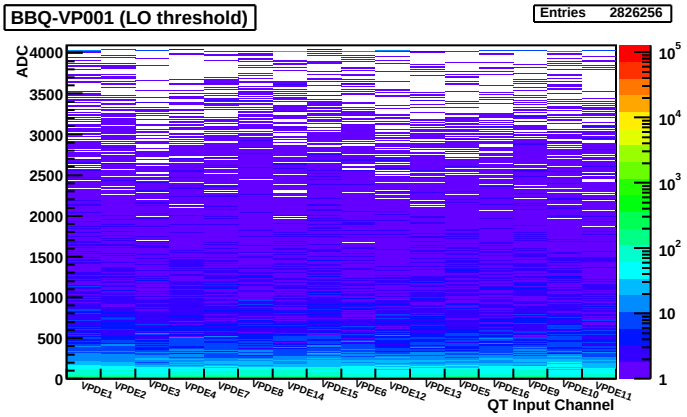

BBQ-BB002 (BBC west small tiles TAC)
Entries 2826256
TAC
QT Input Channel

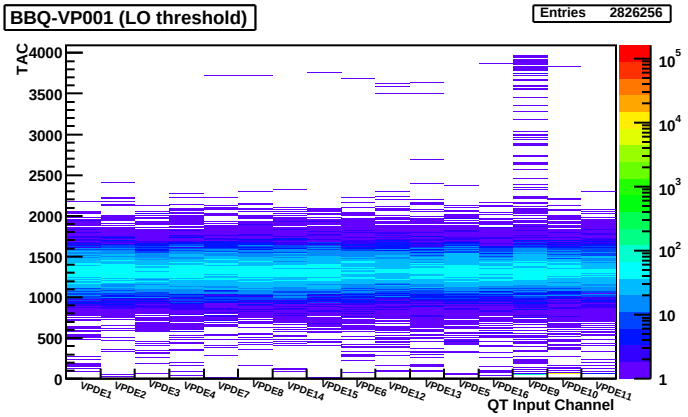

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2826256
ADC
QT Input Channel

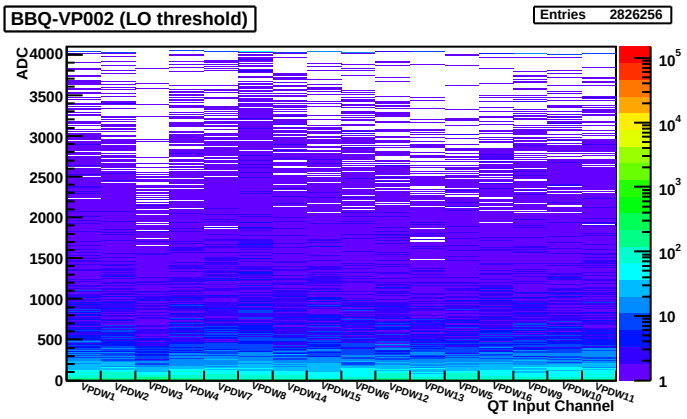

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2826256
TAC
QT Input Channel

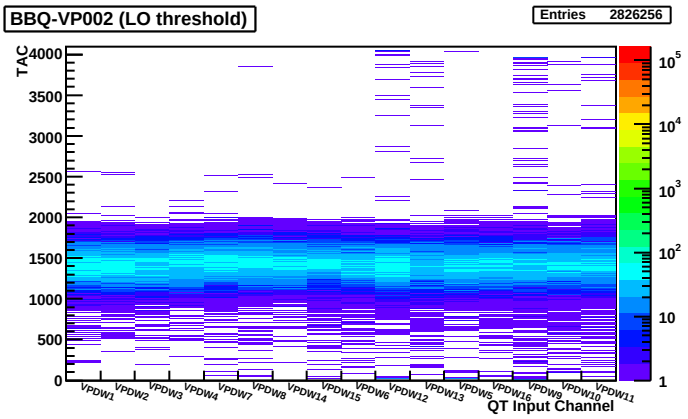

BBQ-ZD001 (ZDC TOWER)
Entries 2826256
ADC

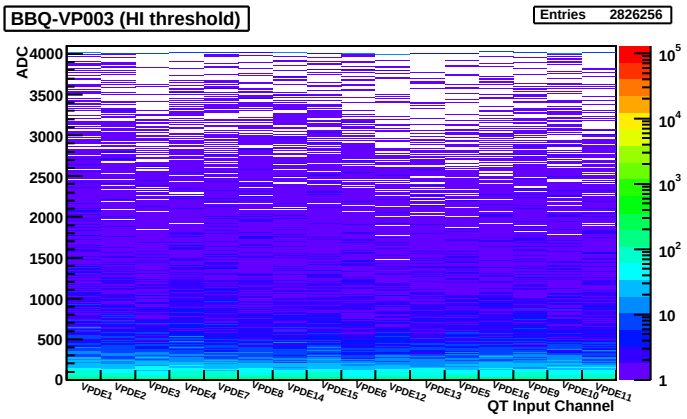

BBQ-ZD001 (ZDC TOWER)
Entries 2826256
TAC

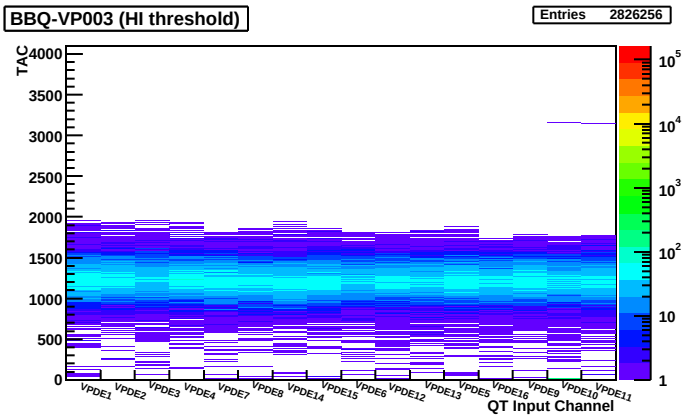

BBQ-VP001 (LO threshold)
Entries 2826256
ADC
QT Input Channel

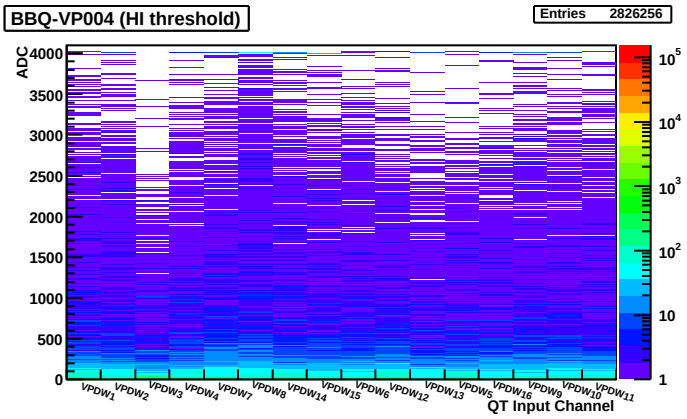

BBQ-VP001 (LO threshold)
Entries 2826256
TAC
QT Input Channel

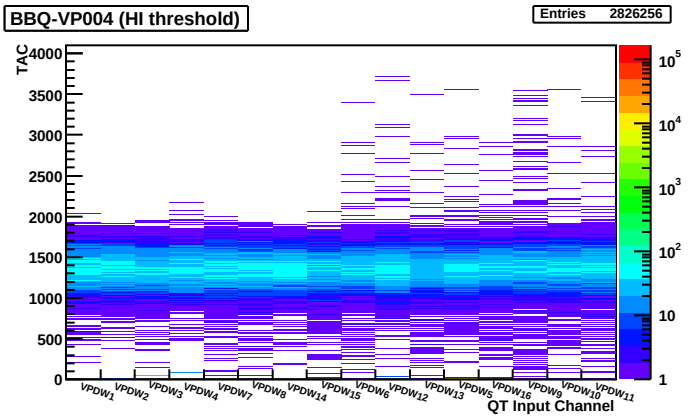

BBQ-VP002 (LO threshold)
Entries 2826256
ADC
QT Input Channel

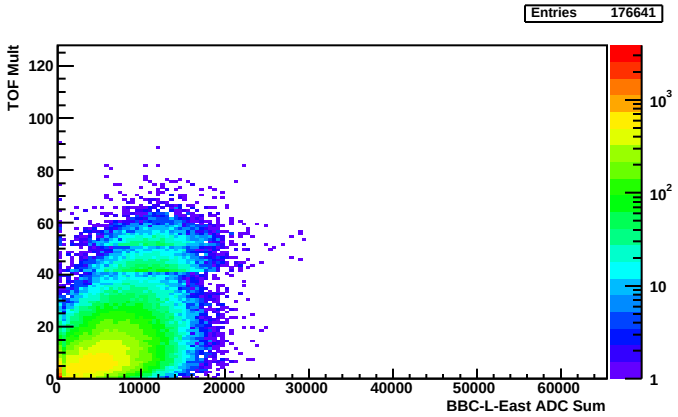

BBQ-VP002 (LO threshold)
Entries 2826256
TAC
QT Input Channel

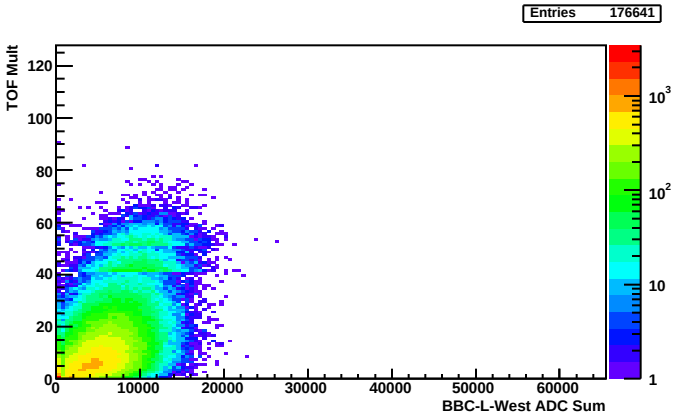

BBQ-VP003 (HI threshold)
Entries 2826256
ADC
QT Input Channel

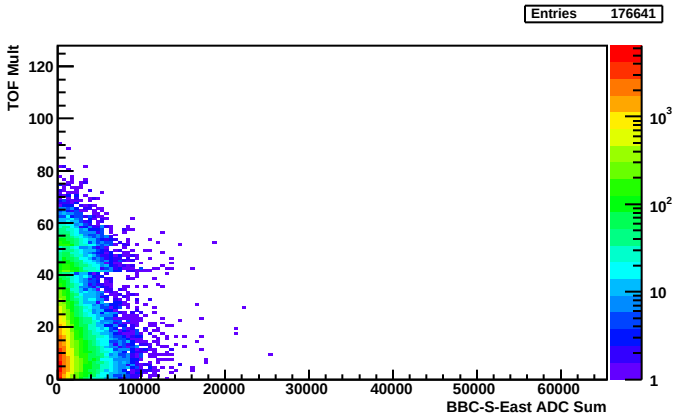

BBQ-VP003 (HI threshold)
Entries 2826256
TAC
QT Input Channel

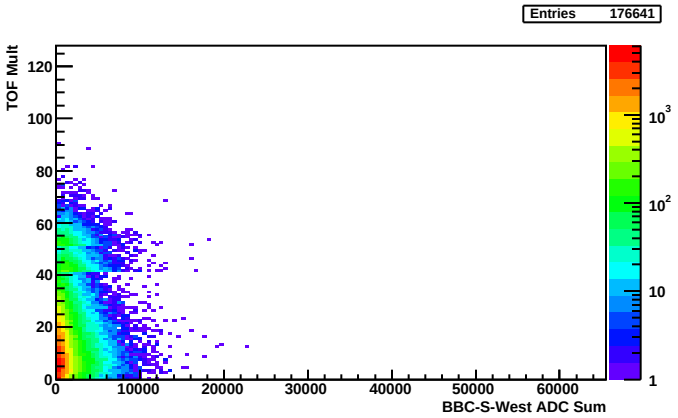

BBQ-VP004 (HI threshold)
Entries 2826256
ADC
QT Input Channel

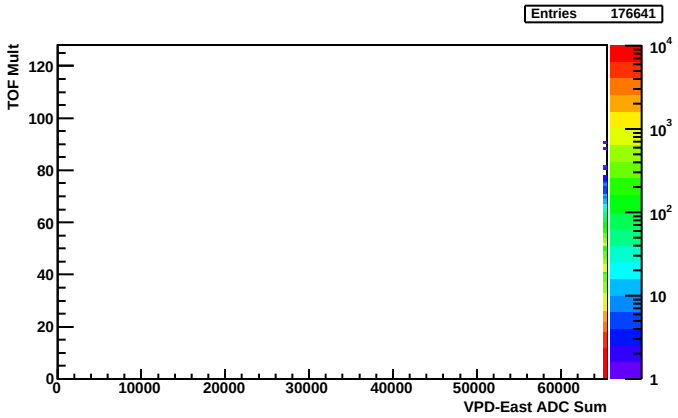

BBQ-VP004 (HI threshold)
Entries 2826256
TAC
QT Input Channel

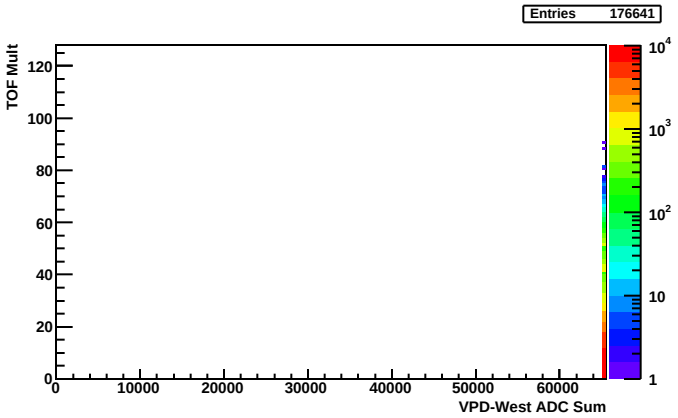

Entries 176641
TOF Mult
BBC-L-East ADC Sum

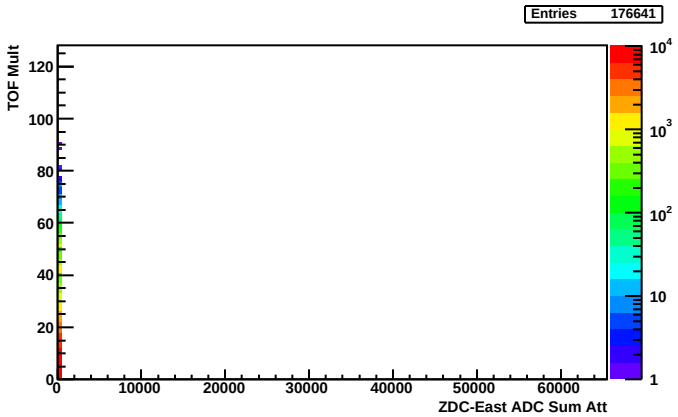

Entries 176641
TOF Mult
BBC-L-West ADC Sum

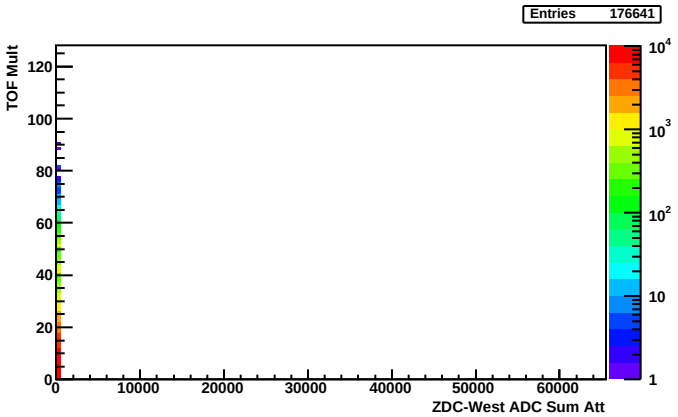

Entries 176641
TOF Mult
BBC-S-East ADC Sum

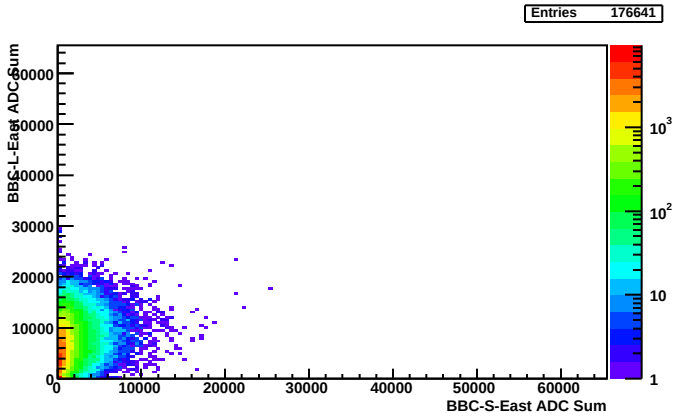

Entries 176641
TOF Mult
BBC-S-West ADC Sum

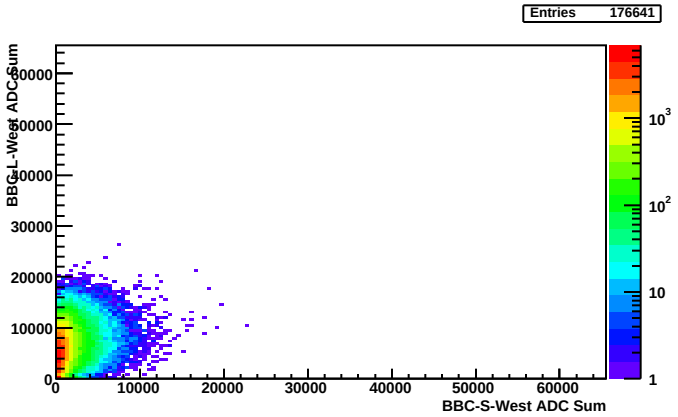

Entries 176641
TOF Mult
VPD-East ADC Sum

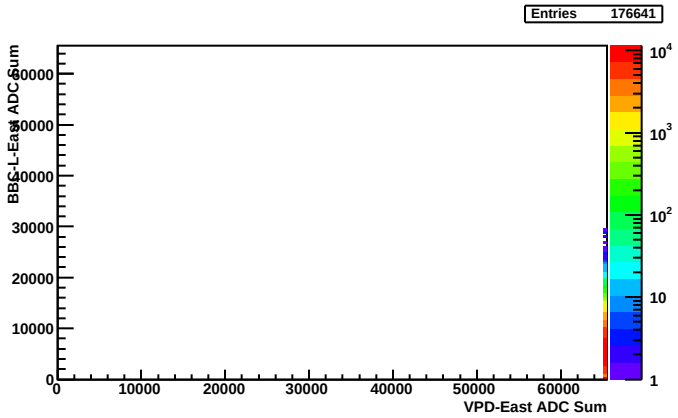

Entries 176641
TOF Mult
VPD-West ADC Sum

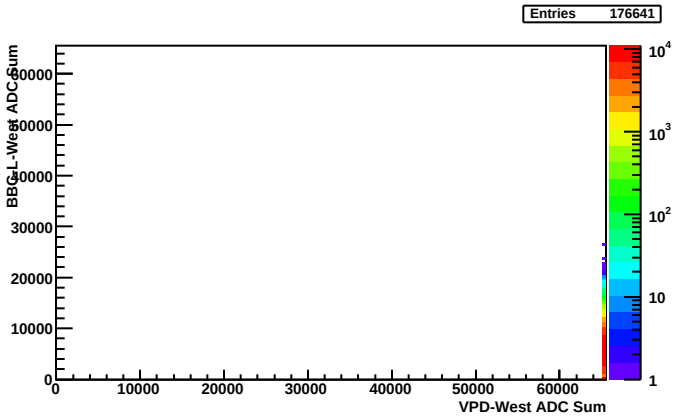

Entries 176641
TOF Mult
ZDC-East ADC Sum Att


Entries 176641
TOF Mult
ZDC-West ADC Sum Att


Entries 176641
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 176641
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 176641
BBC-L-East ADC Sum
VPD-East ADC Sum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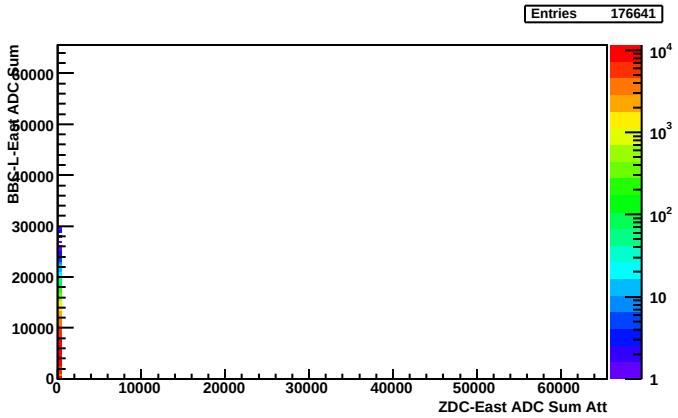
BBC-L-West ADC Sum
VPD-West ADC Sum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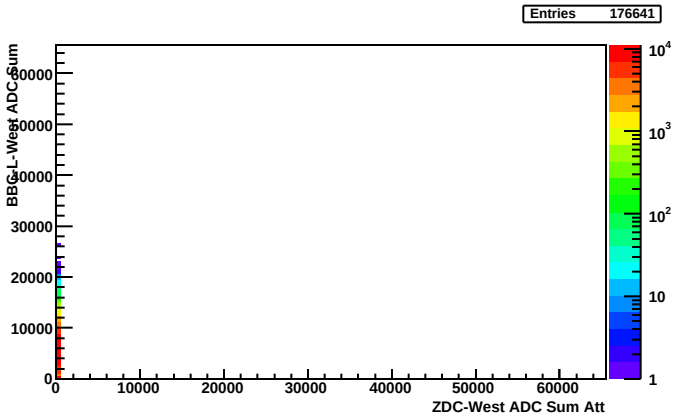
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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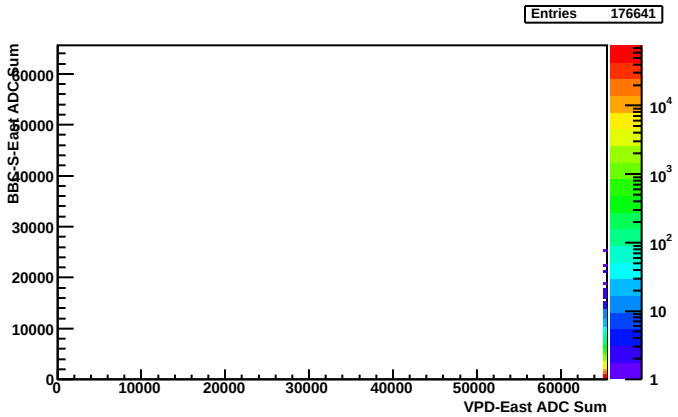
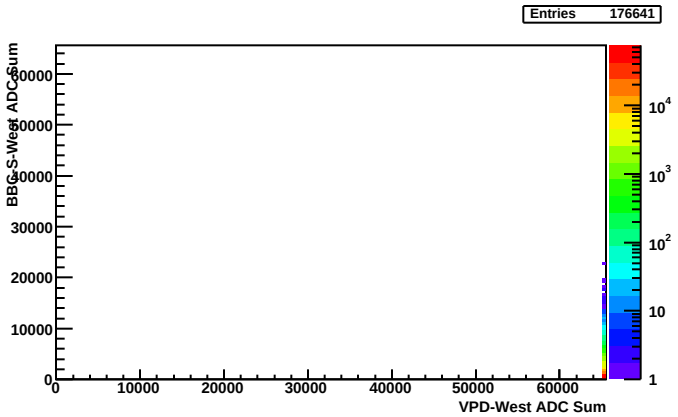
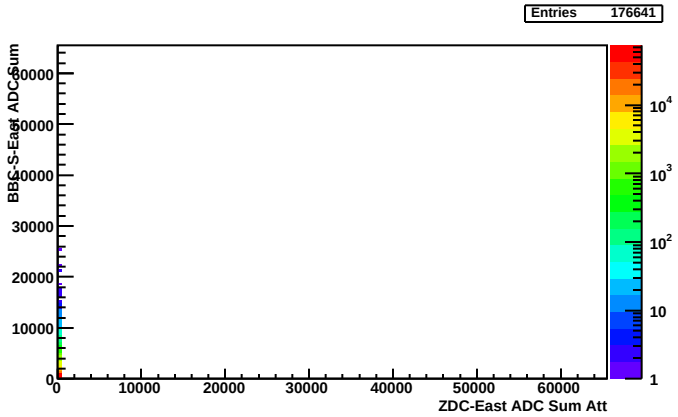
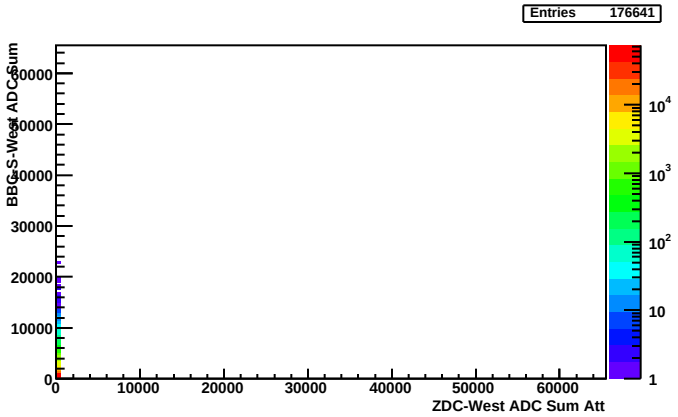
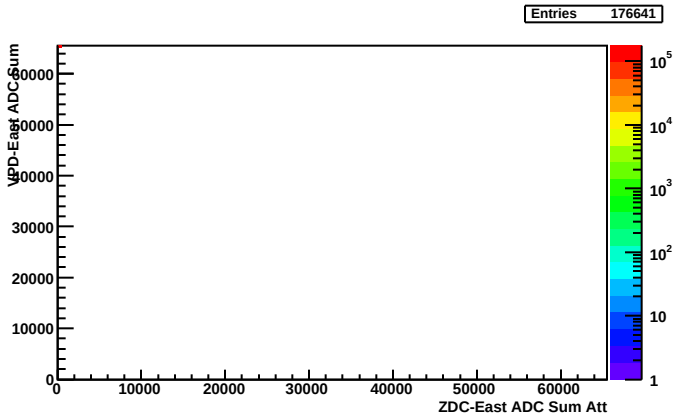
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

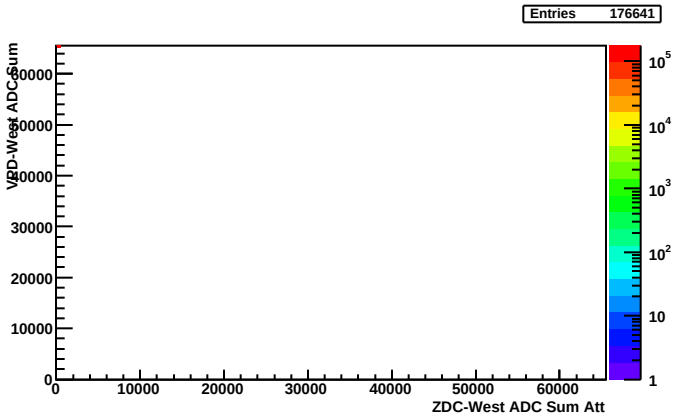

Entries 176641
BBC-S-East ADC Sum
VPD-East ADC Sum

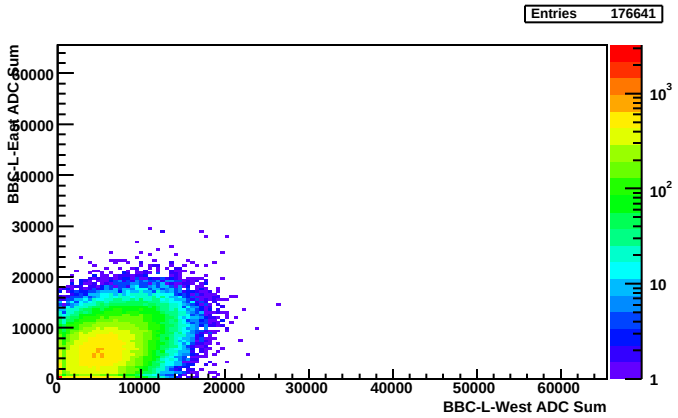

Entries 176641
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 176641
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 176641
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 176641
VPD-East ADC Sum
ZDC-East ADC Sum Att


Entries 176641
VPD-West ADC Sum
ZDC-West ADC Sum Att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BBC-L-East ADC Sum
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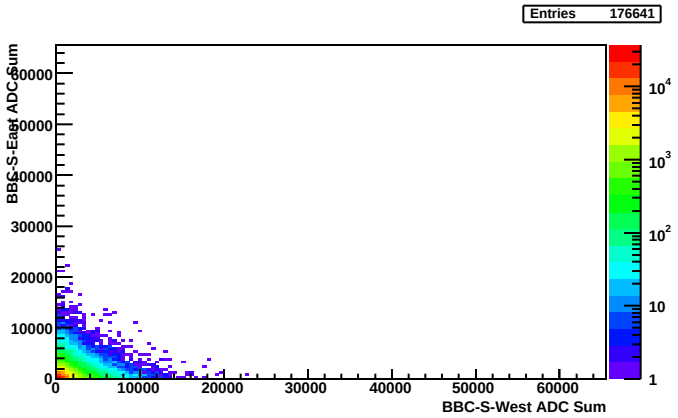
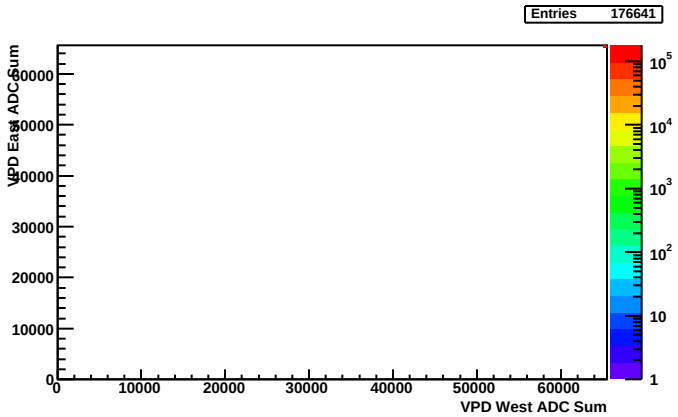
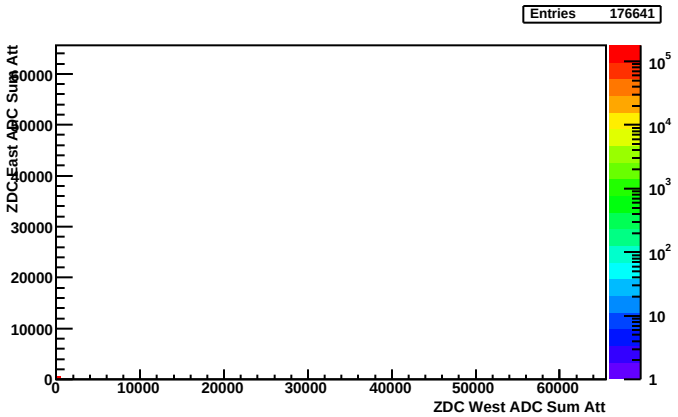
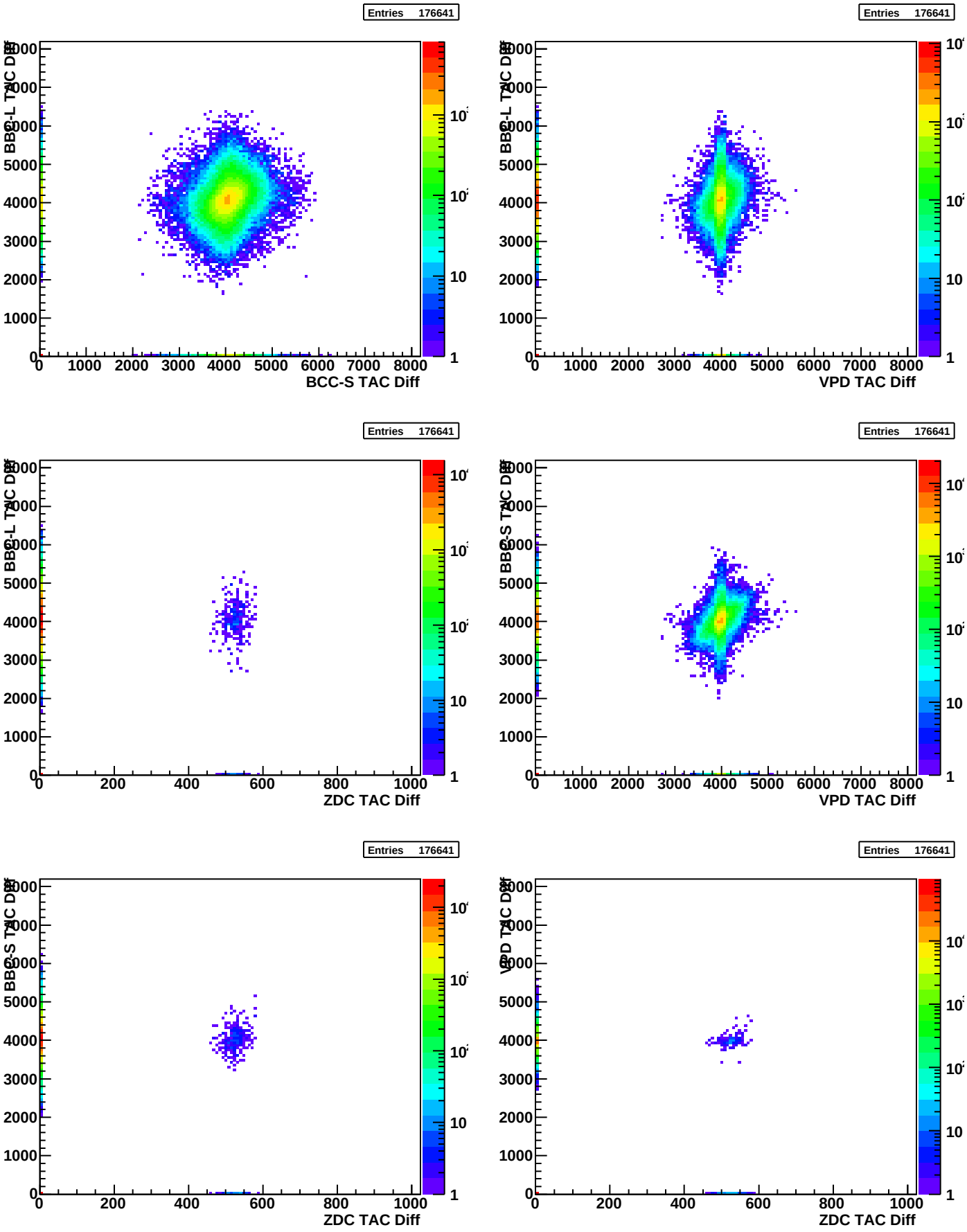

Entries 176641
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 176641
VPD East ADC Sum
VPD West ADC Sum


Entries 176641
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 176641
BBC-L TAC Diff
BCC-S TAC Diff
Entries 176641
BBC-L TAC Diff
VPD TAC Diff
Entries 176641
BBC-L TAC Diff
ZDC TAC Diff
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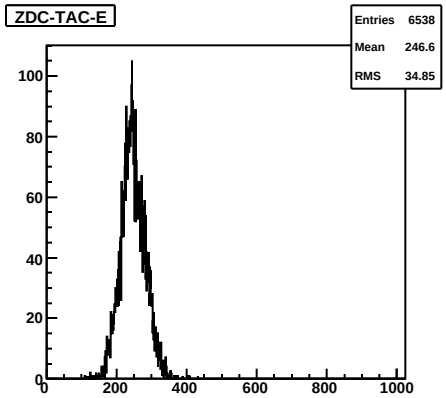
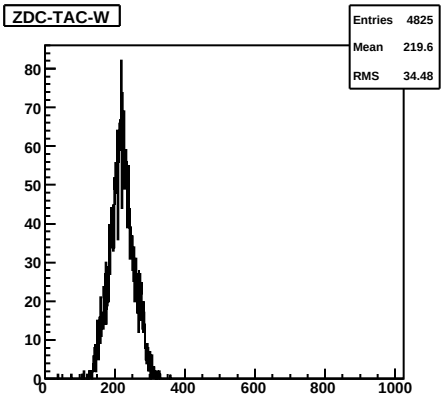
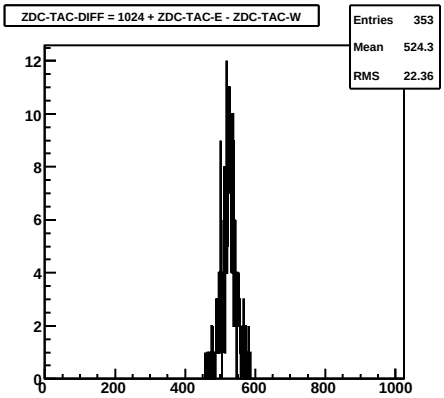
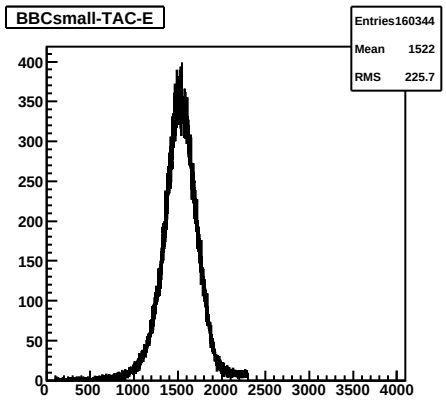
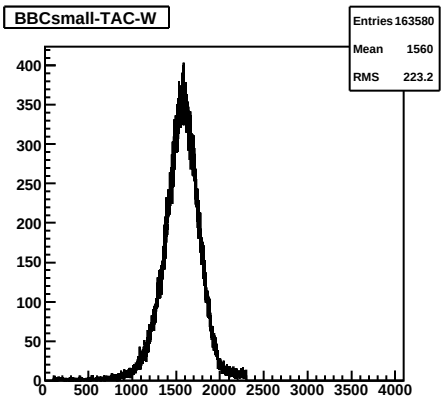
VPD TAC Diff
Entries 176641
BBC-S TAC Diff
ZDC TAC Diff
Entries 176641
VPD TAC Diff
ZDC TAC Diff

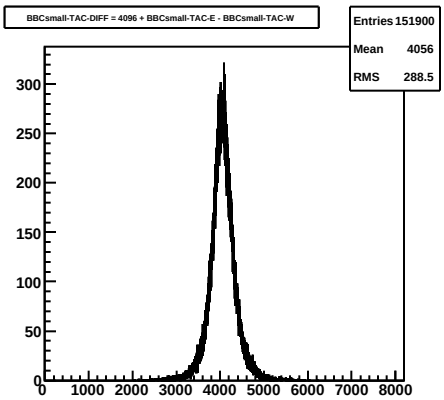

ZDC-TAC-E
Entries 6538
Mean 246.6
RMS 34.85

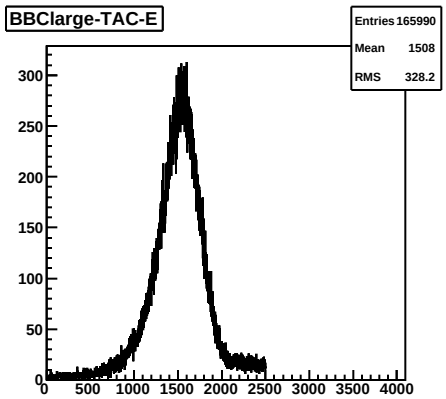

ZDC-TAC-W
Entries 4825
Mean 219.6
RMS 34.48

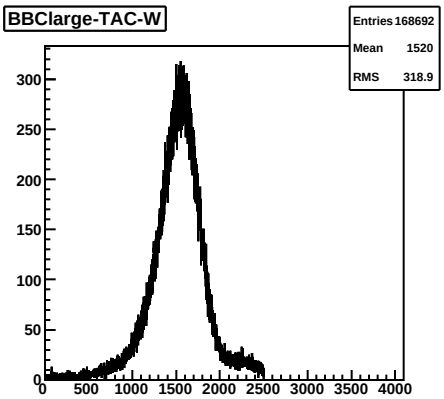

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 353
Mean 524.3
RMS 22.36

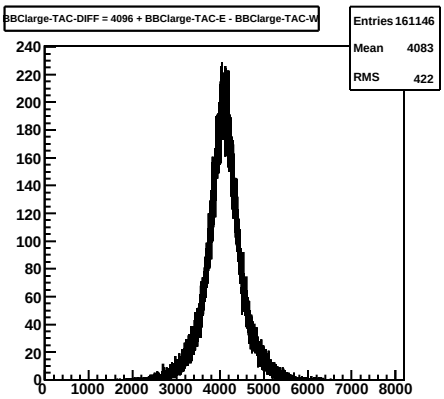

BBCsmall-TAC-E
Entries 160344
Mean 1522
RMS 225.7

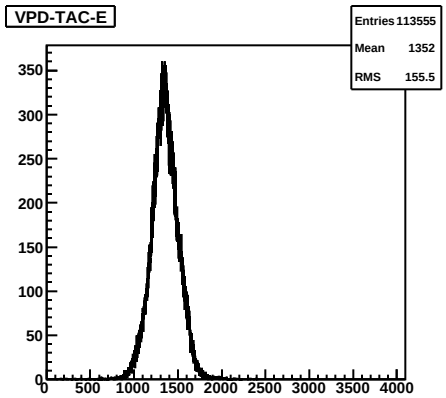

BBCsmall-TAC-W
Entries 163580
Mean 1560
RMS 223.2

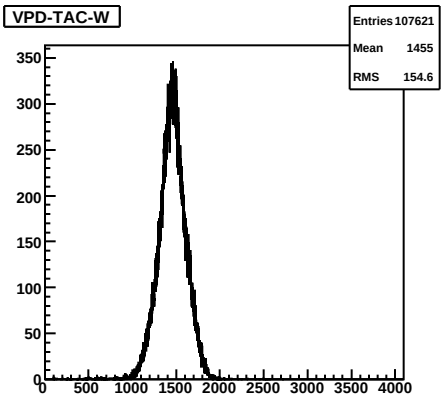

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 151900
Mean 4056
RMS 288.5

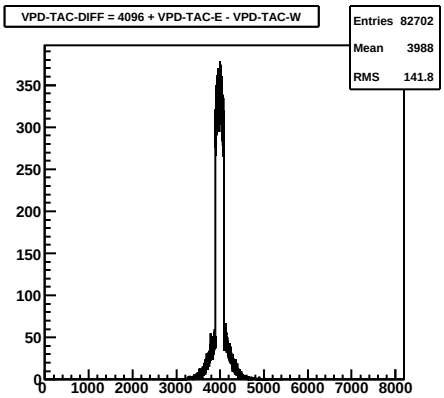

BBClarge-TAC-E
Entries 165990
Mean 1508
RMS 328.2

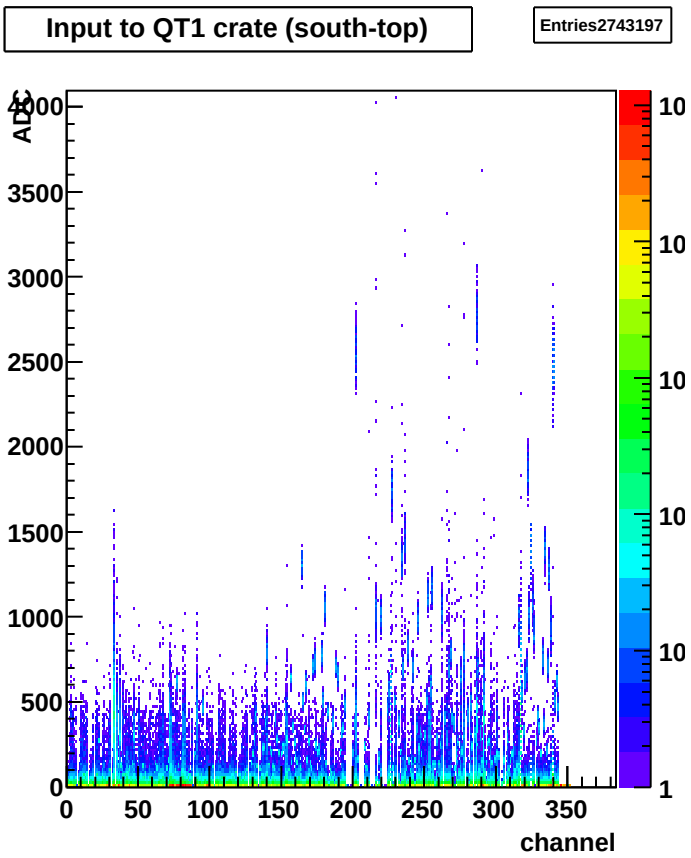

BBClarge-TAC-W
Entries 168692
Mean 1520
RMS 318.9

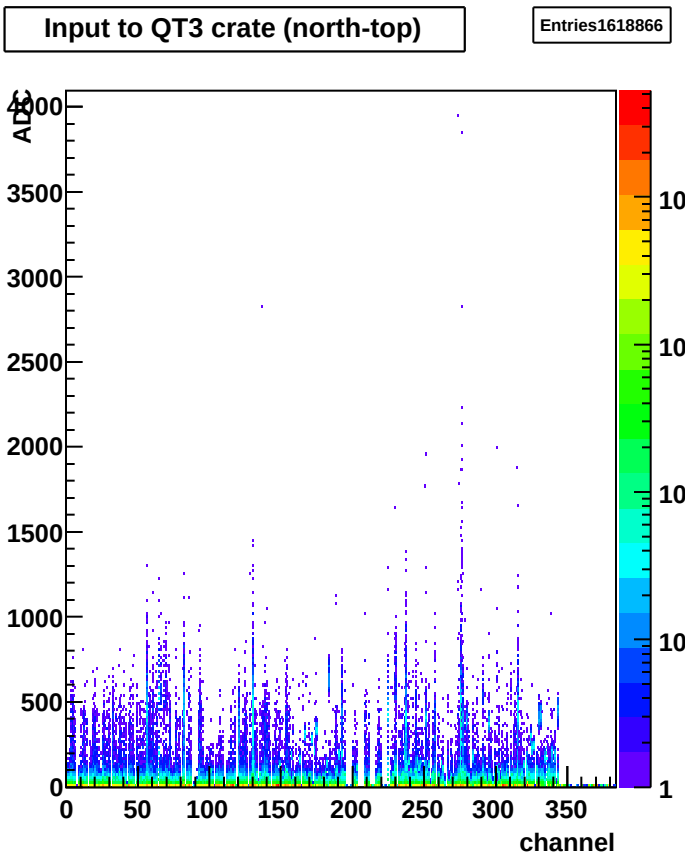

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 161146
Mean 4083
RMS 422

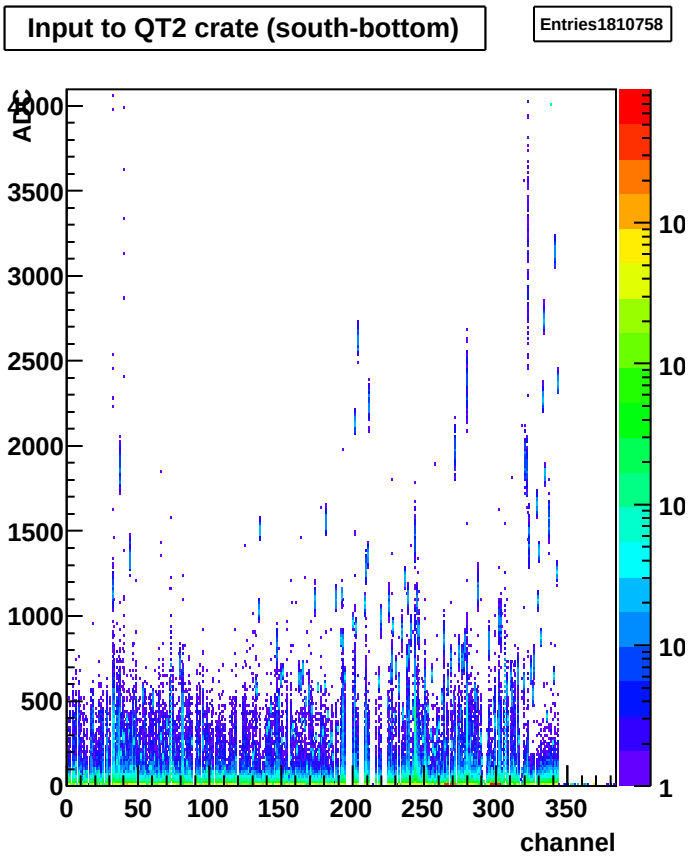

VPD-TAC-E
Entries 113555
Mean 1352
RMS 155.5

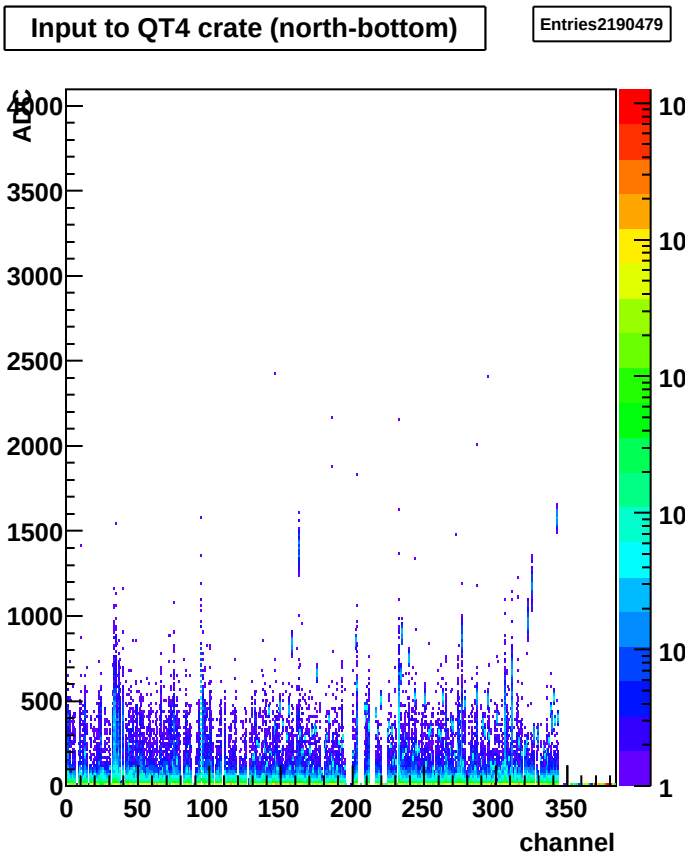

VPD-TAC-W
Entries 107621
Mean 1455
RMS 154.6

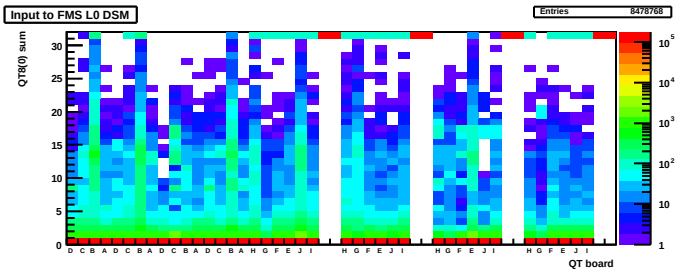

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 82702
Mean 3988
RMS 141.8

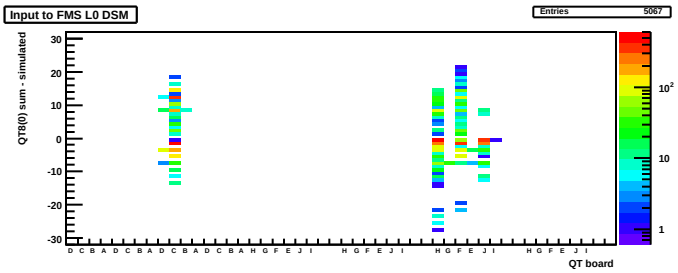

Input to QT1 crate (south-top)
Entries2743197
ADC
channel

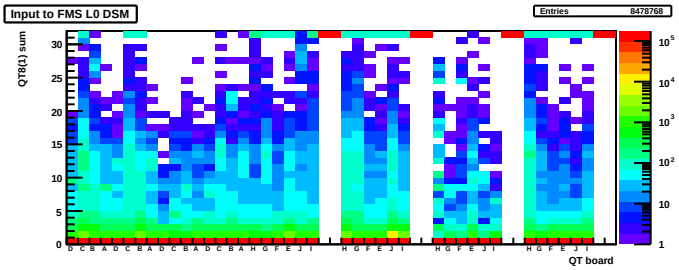

Input to QT3 crate (north-top)
Entries1618866
ADC
channel

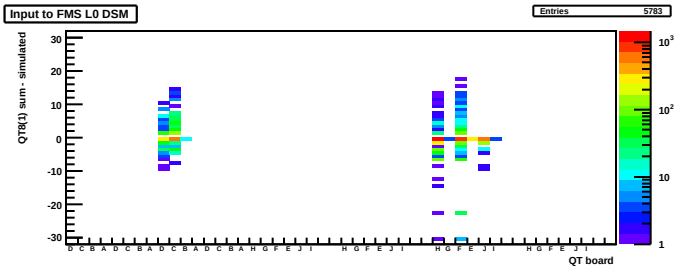

Input to QT2 crate (south-bottom)
Entries1810758
ADC
channel

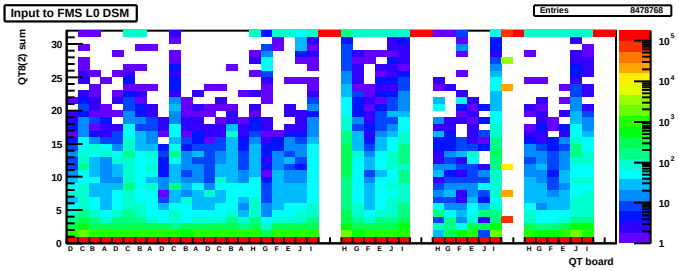

Input to QT4 crate (north-bottom)
Entries2190479
ADC
channel

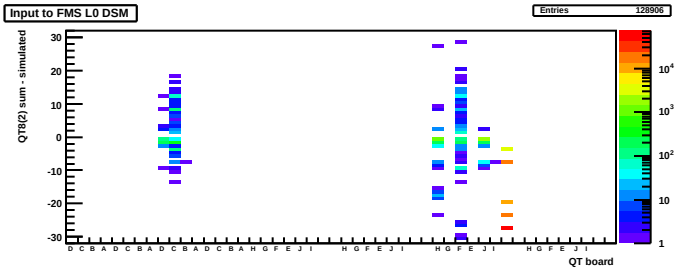

Input to FMS L0 DSM
Entries 8478768
QT8(0) sum
QT board

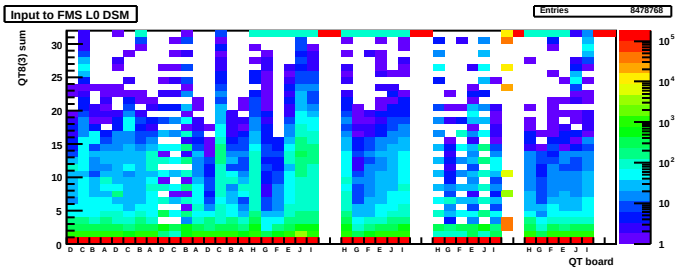

Input to FMS L0 DSM
Entries 5067
QT8(0) sum - simulated
QT board

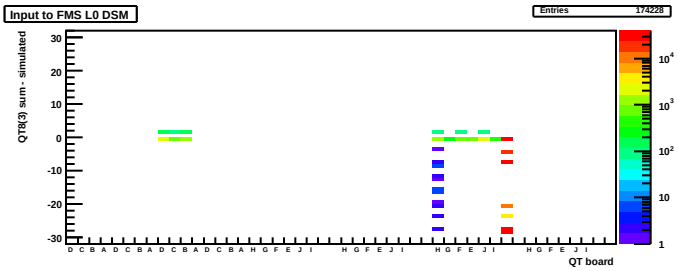

Input to FMS L0 DSM
Entries 8478768
QT8(1) sum
QT board

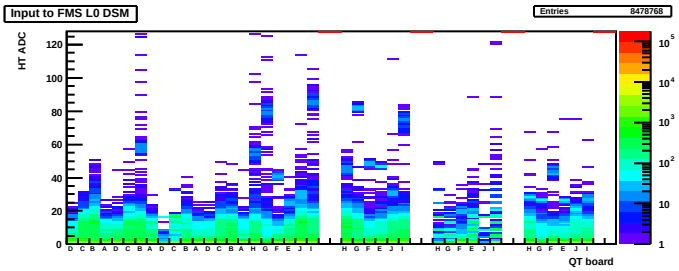

Input to FMS L0 DSM
Entries 5783
QT8(1) sum - simulated
QT board

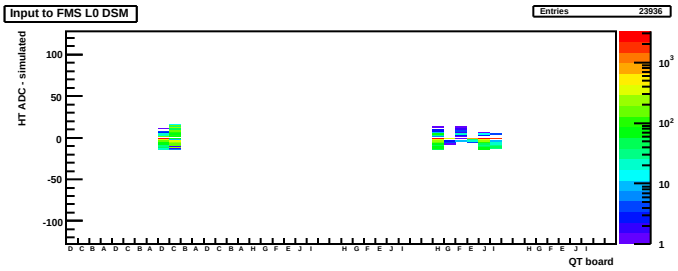

Input to FMS L0 DSM
Entries 8478768
QT8(2) sum
QT board

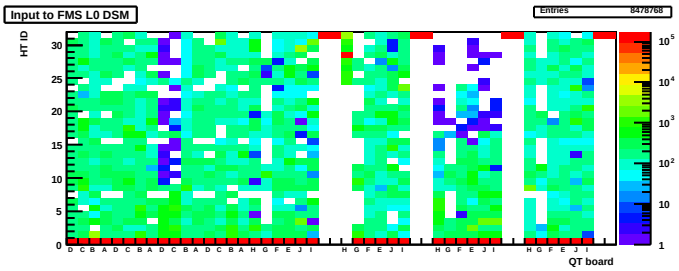

Input to FMS L0 DSM
Entries 128906
QT8(2) sum - simulated
QT board

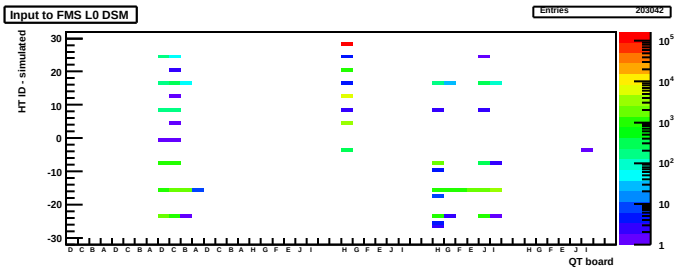

Input to FMS L0 DSM
Entries 8478768
QT8(3) sum
QT board

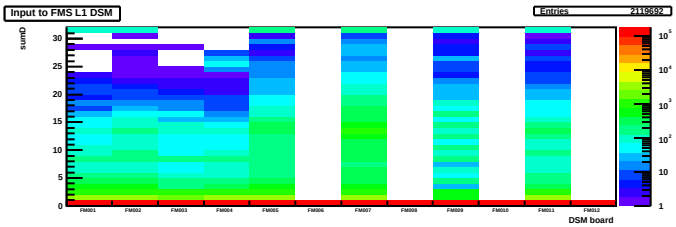

Input to FMS L0 DSM
Entries 174228
QT8(3) sum - simulated
QT board

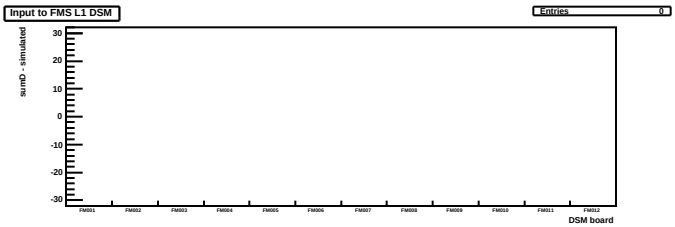

Input to FMS L0 DSM
Entries 8478768
HT ADC
QT board

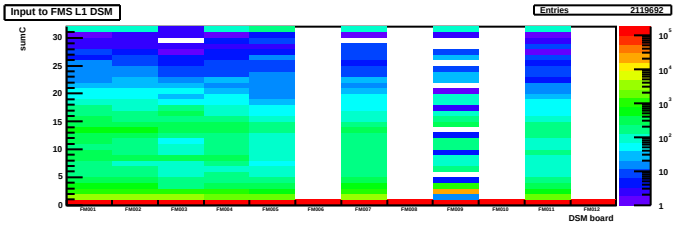

Input to FMS L0 DSM
Entries 23936
HT ADC - simulated
QT board

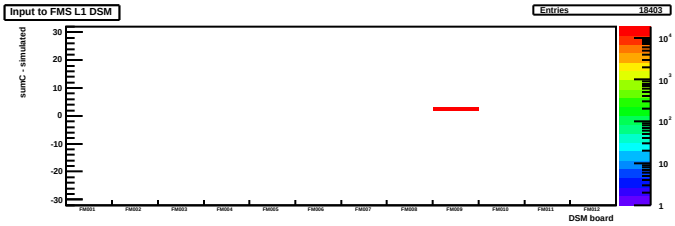

Input to FMS L0 DSM
Entries 8478768
HT ID
QT board

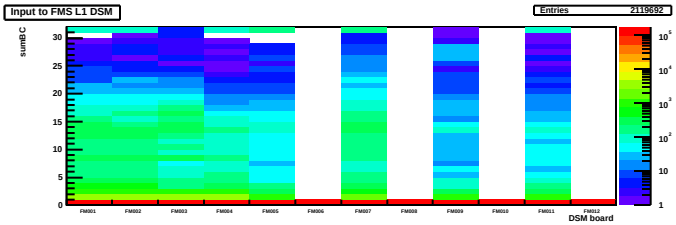

Input to FMS L0 DSM
Entries 203042
HT ID - simulated
QT board

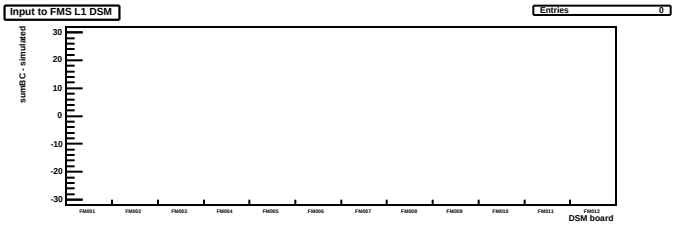

Input to FMS L1 DSM
Entries 2119692
sumD
DSM board

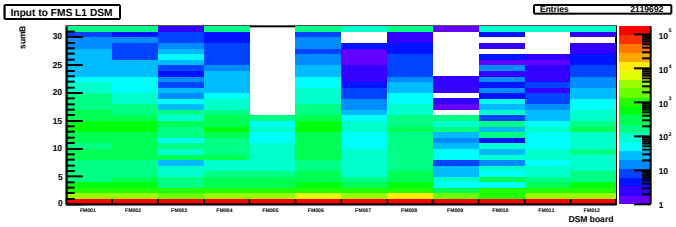

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

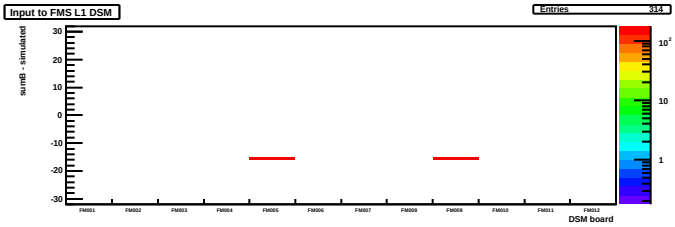

Input to FMS L1 DSM
Entries 2119692
sumC
DSM board

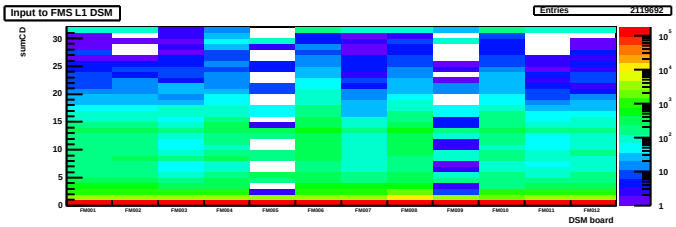

Input to FMS L1 DSM
Entries 18403
sumC - simulated
DSM board

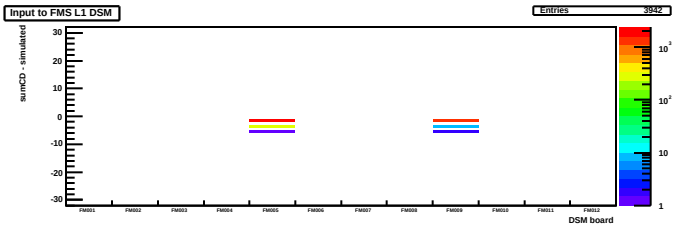

Input to FMS L1 DSM
Entries 2119692
sumBC
DSM board

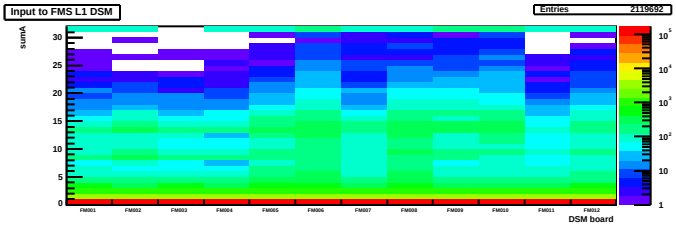

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

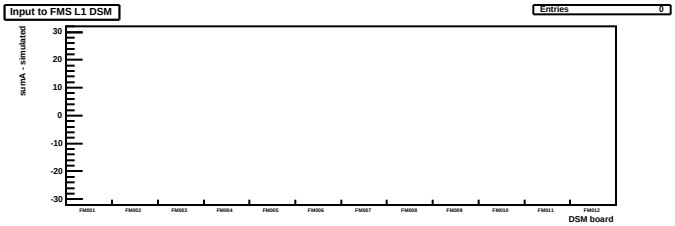

Input to FMS L1 DSM
Entries 2119692
sumB
DSM board

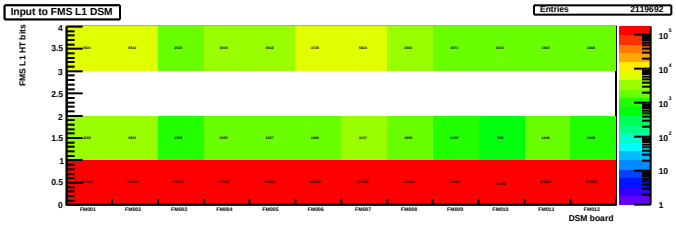

Input to FMS L1 DSM
Entries 314
sumB - simulated
DSM board

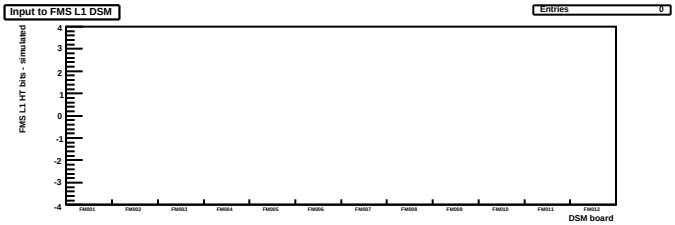

Input to FMS L1 DSM
Entries 2119692
sumCD
DSM board

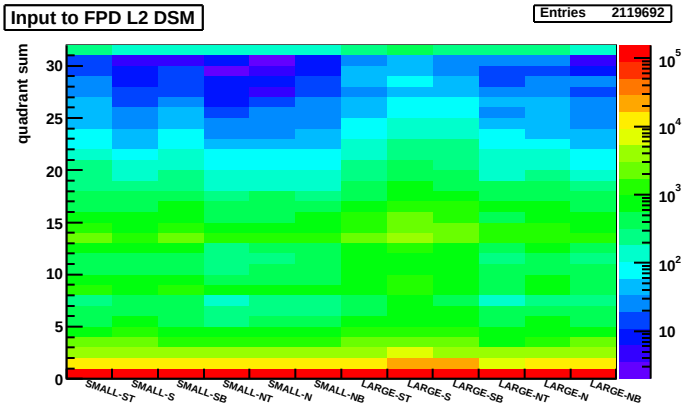

Input to FMS L1 DSM
Entries 3942
sumCD - simulated
DSM board

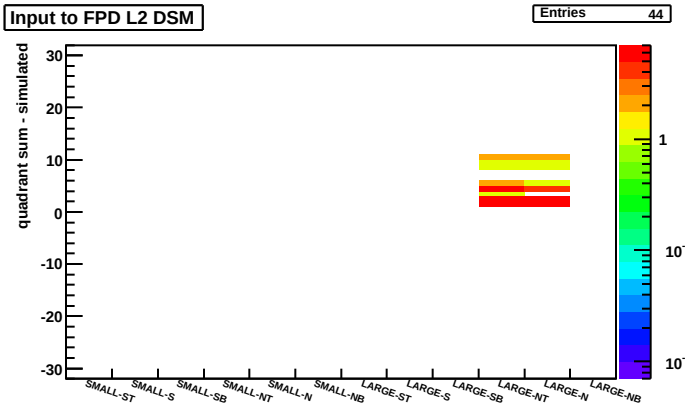

Input to FMS L1 DSM
Entries 2119692
sumA
DSM board

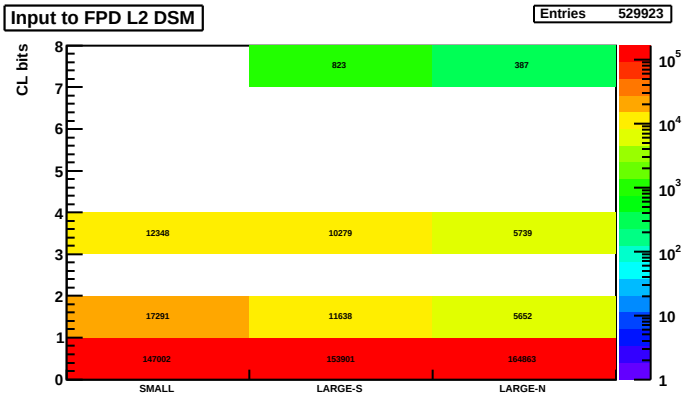

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

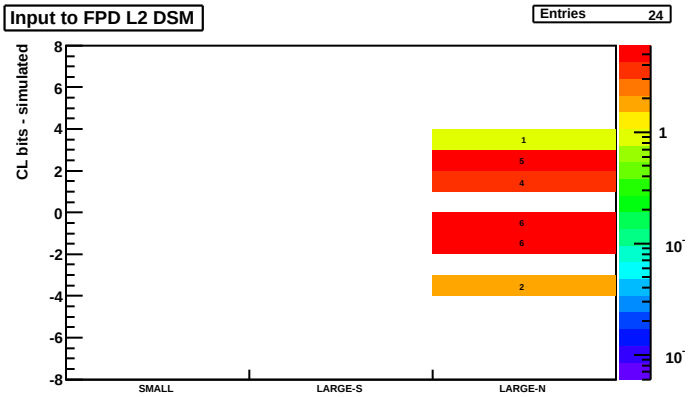

Input to FMS L1 DSM
Entries 2119692
FMS L1 HT bits
DSM board

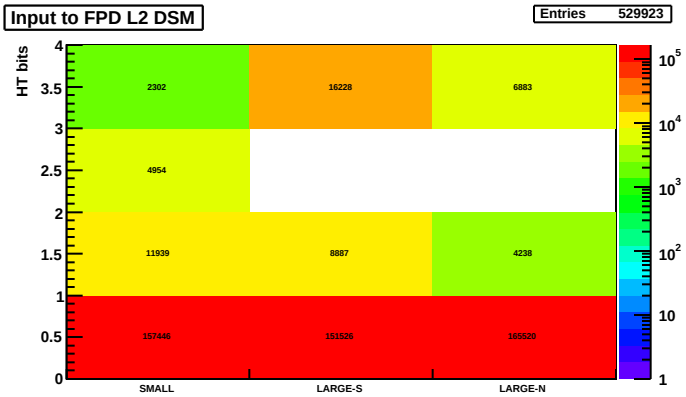

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

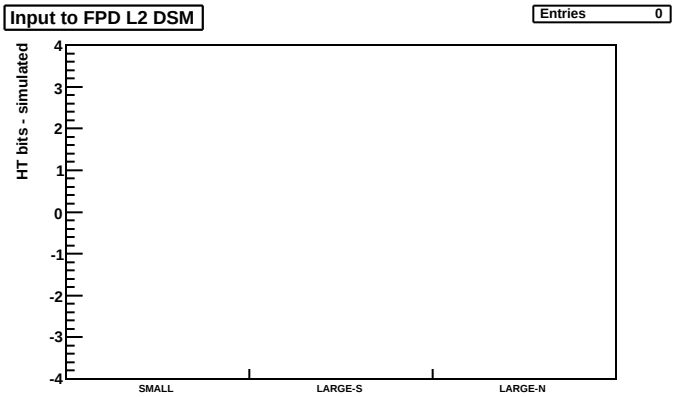

Input to FPD L2 DSM
Entries 2119692
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

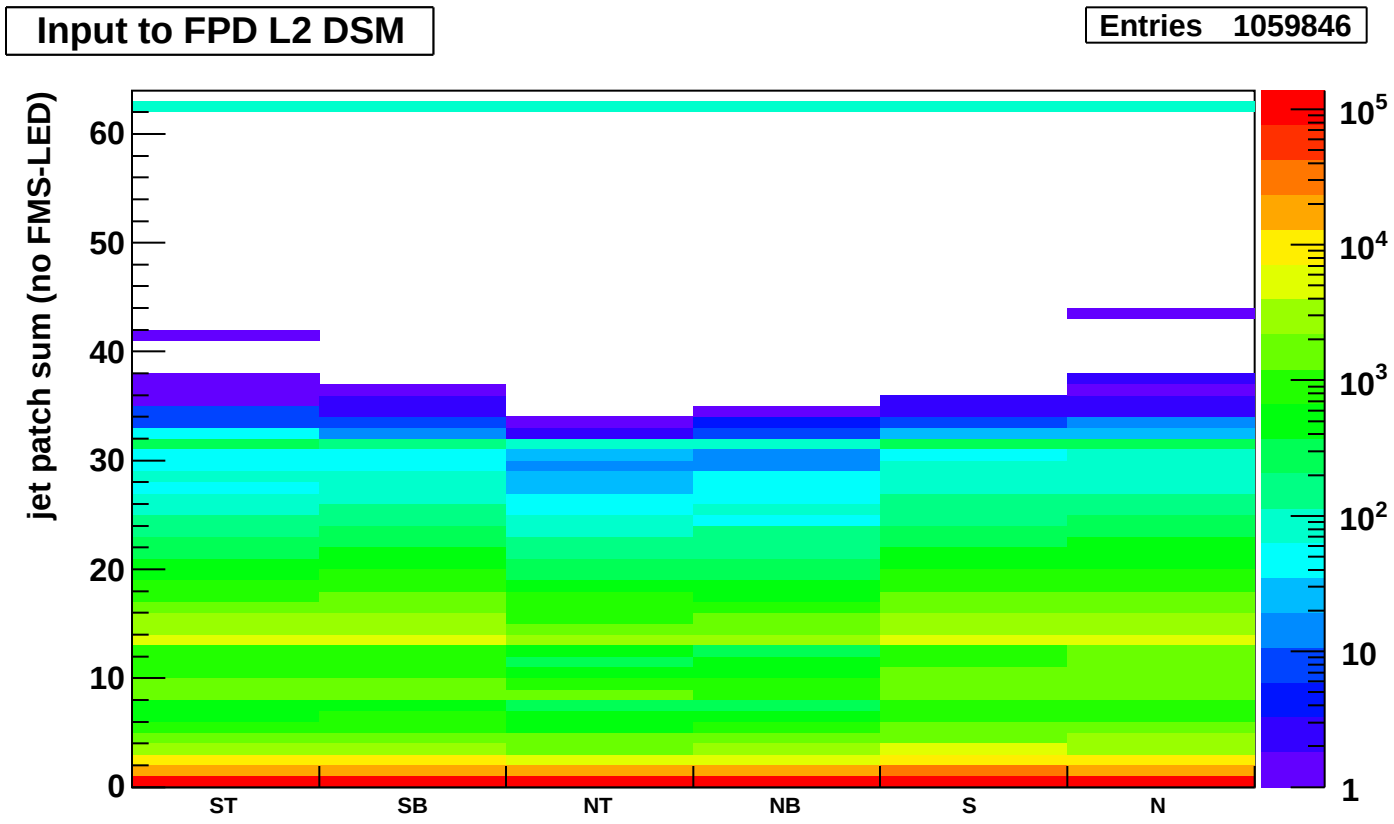

Input to FPD L2 DSM
Entries 44
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

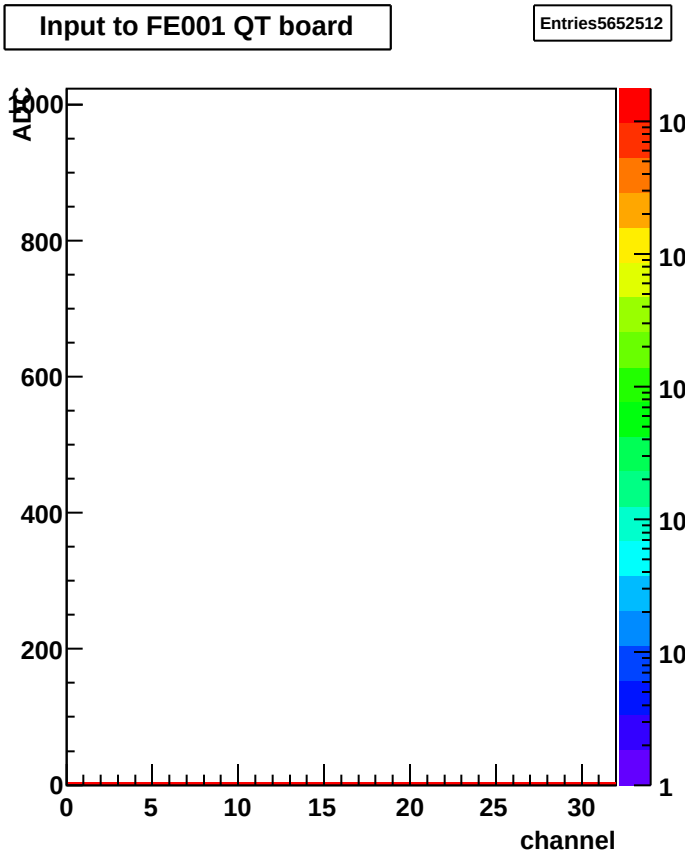

Input to FPD L2 DSM
Entries 529923
CL bits
823
387
12348
10279
5739
17291
11638
5652
147002
153901
164863
SMALL
LARGE-S
LARGE-N

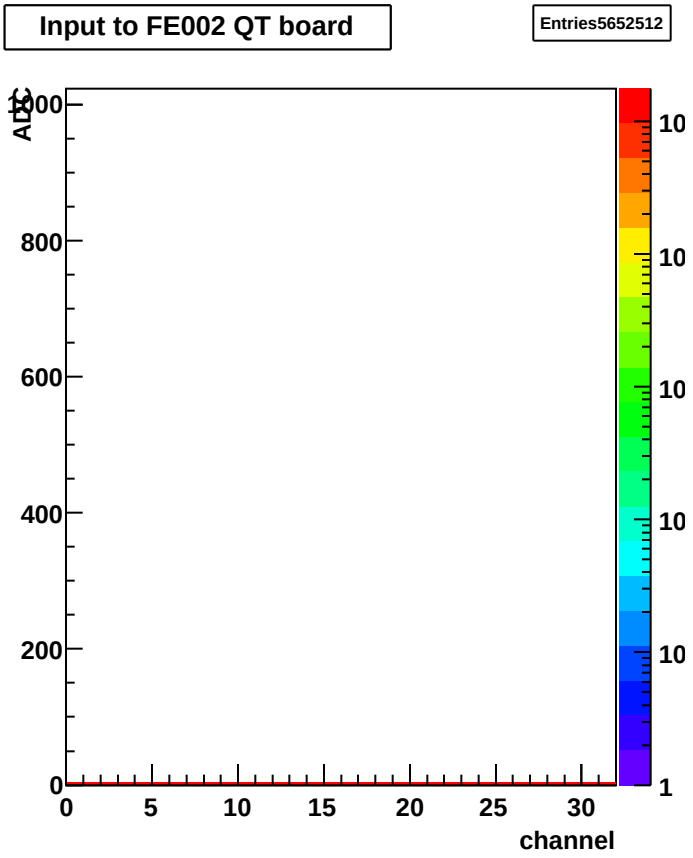

Input to FPD L2 DSM
Entries 24
CL bits - simulated
1
5
4
6
6
2
SMALL
LARGE-S
LARGE-N

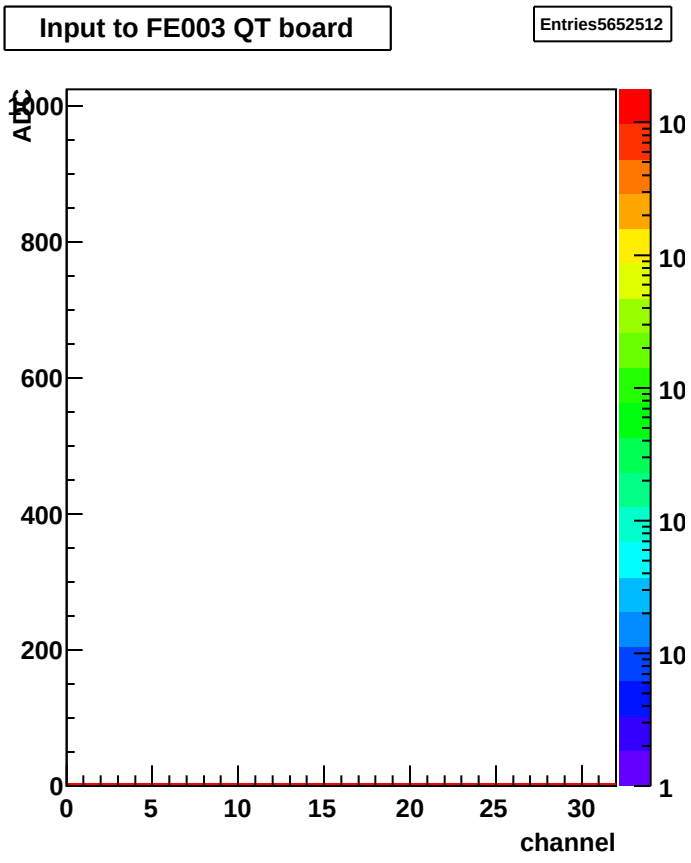

Input to FPD L2 DSM
Entries 529923
HT bits
2302
16228
6883
4954
11939
8887
4238
157446
151526
165520
SMALL
LARGE-S
LARGE-N

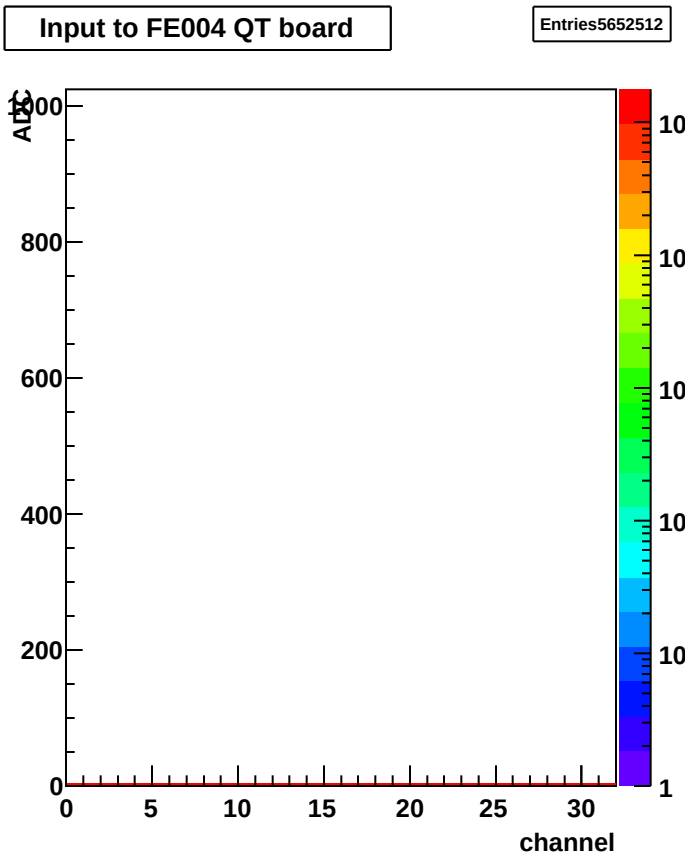

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

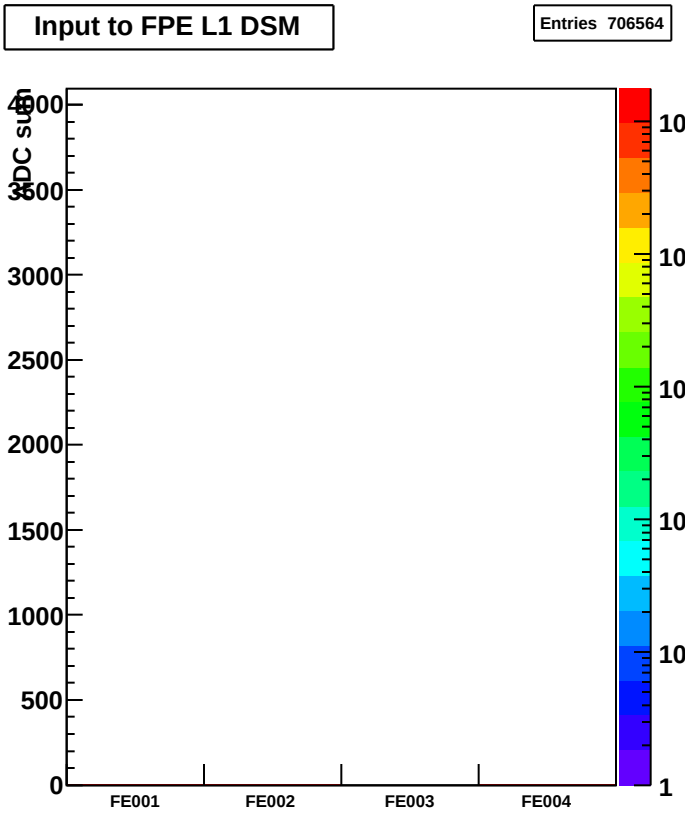

Input to FPD L2 DSM
Entries 1059846
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

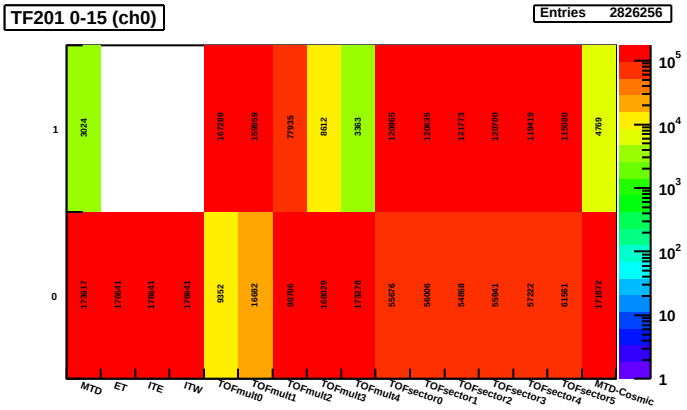

Input to FE001 QT board
Entries5652512
ADC
channel

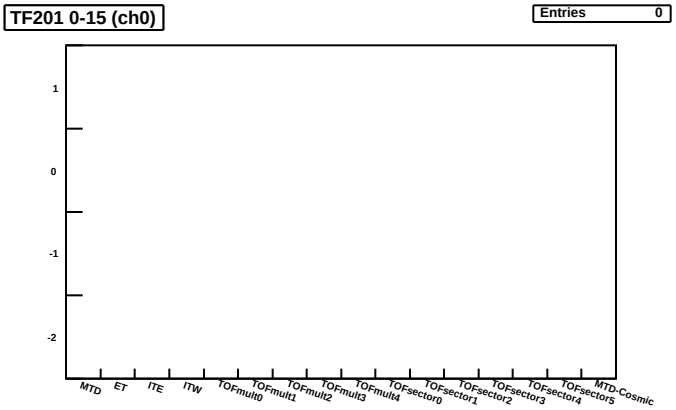

Input to FE002 QT board
Entries5652512
ADC
channel

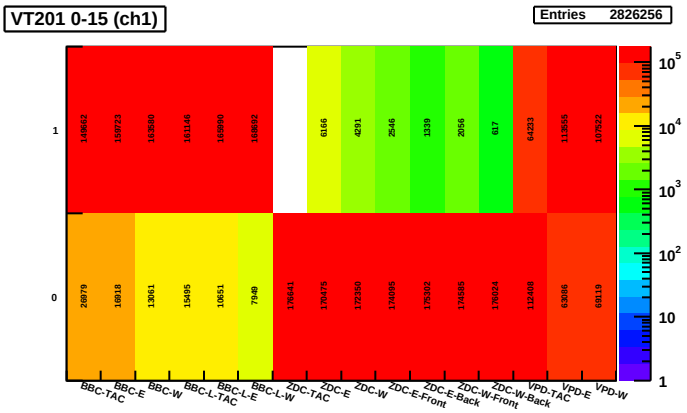

Input to FE003 QT board
Entries5652512
ADC
channel

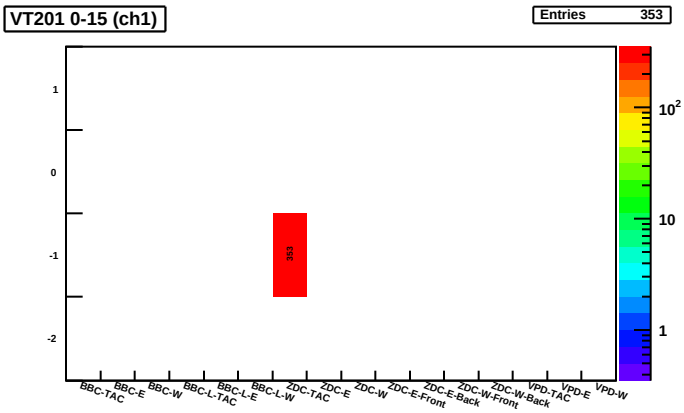

Input to FE004 QT board
Entries5652512
ADC
channel

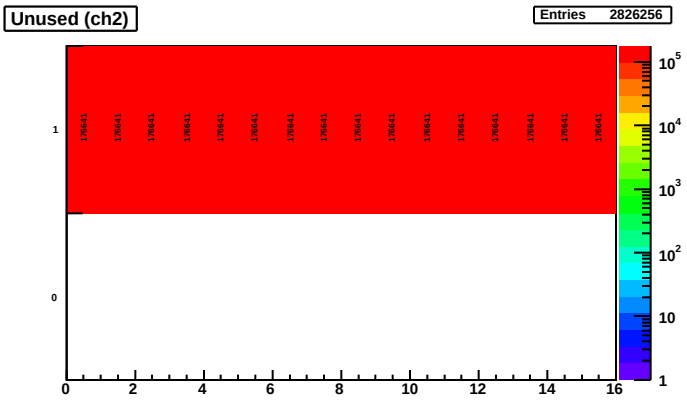

Input to FPE L1 DSM
Entries 706564
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

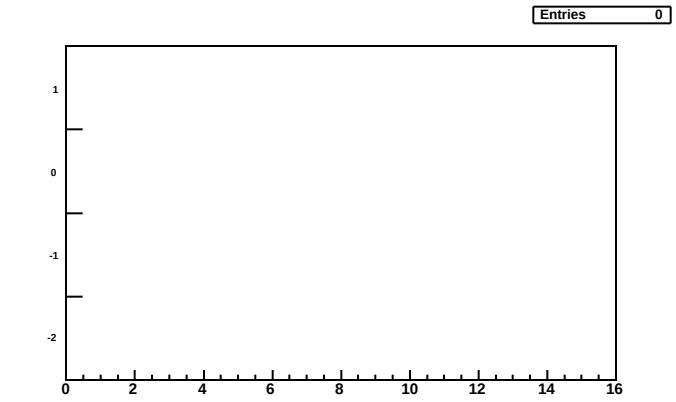

TF201 0-15 (ch0)
Entries 2826256
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

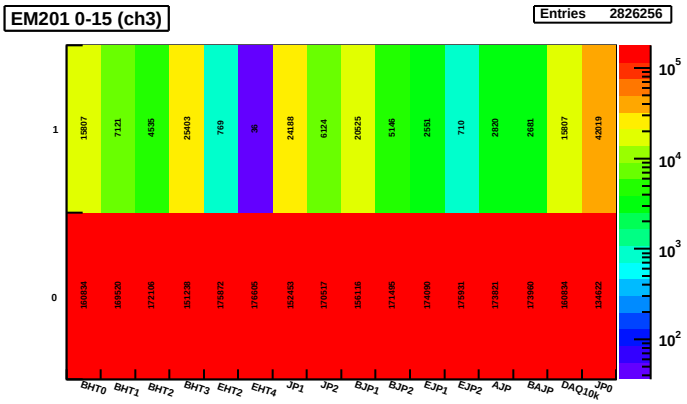

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 2826256
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 353
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 2826256


Entries 0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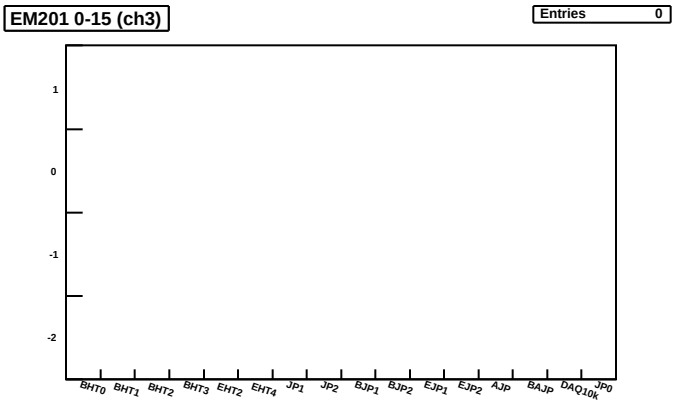
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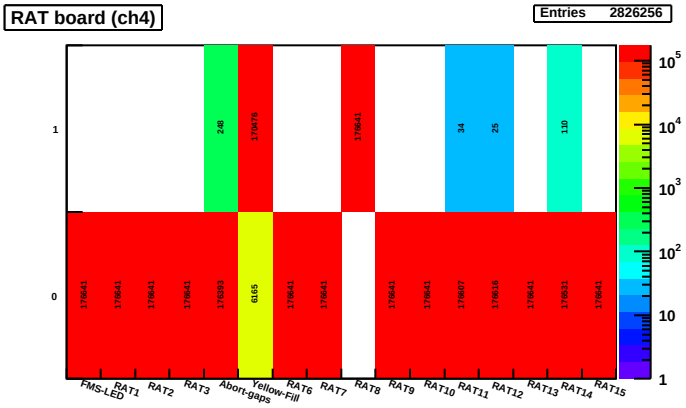
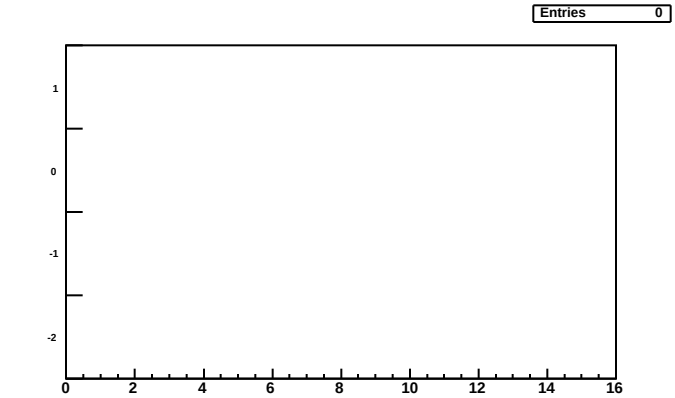
RAT board (ch4)





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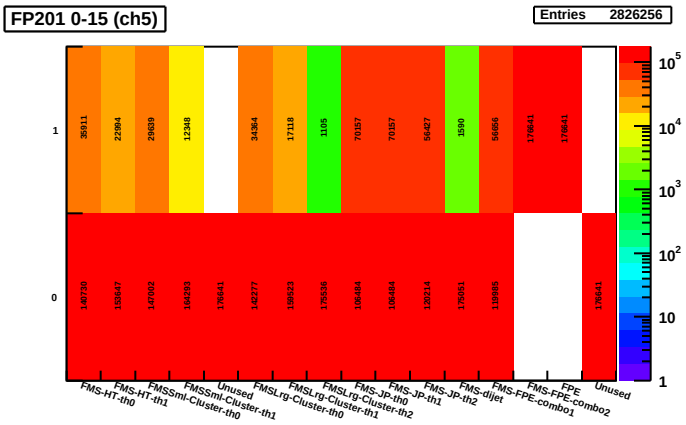

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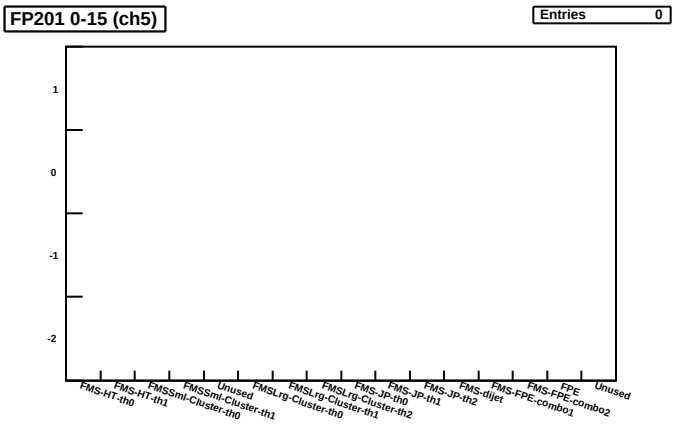

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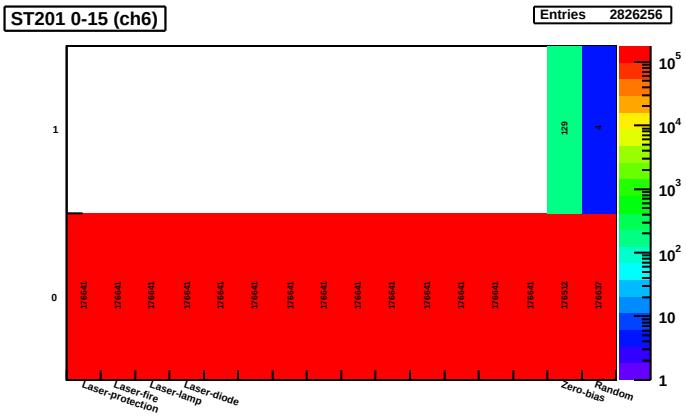

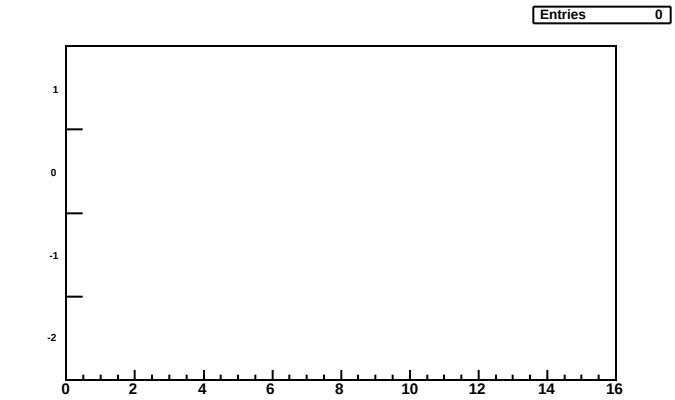
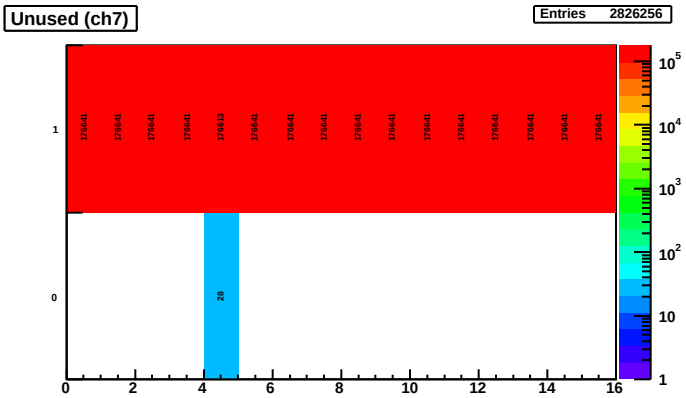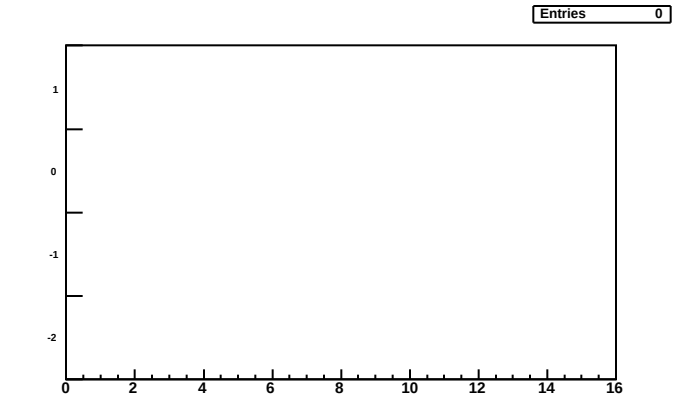
Unused (ch7)