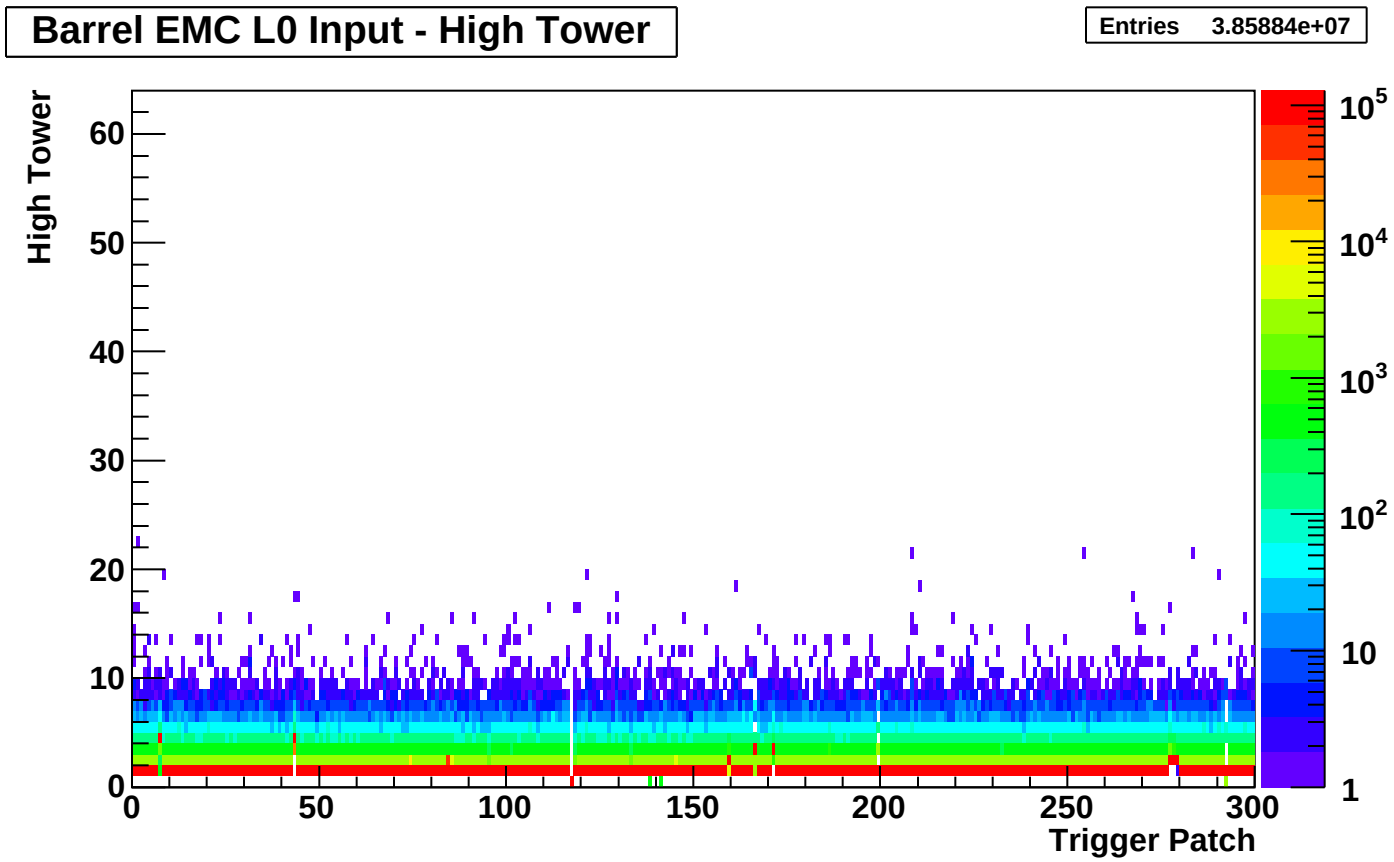
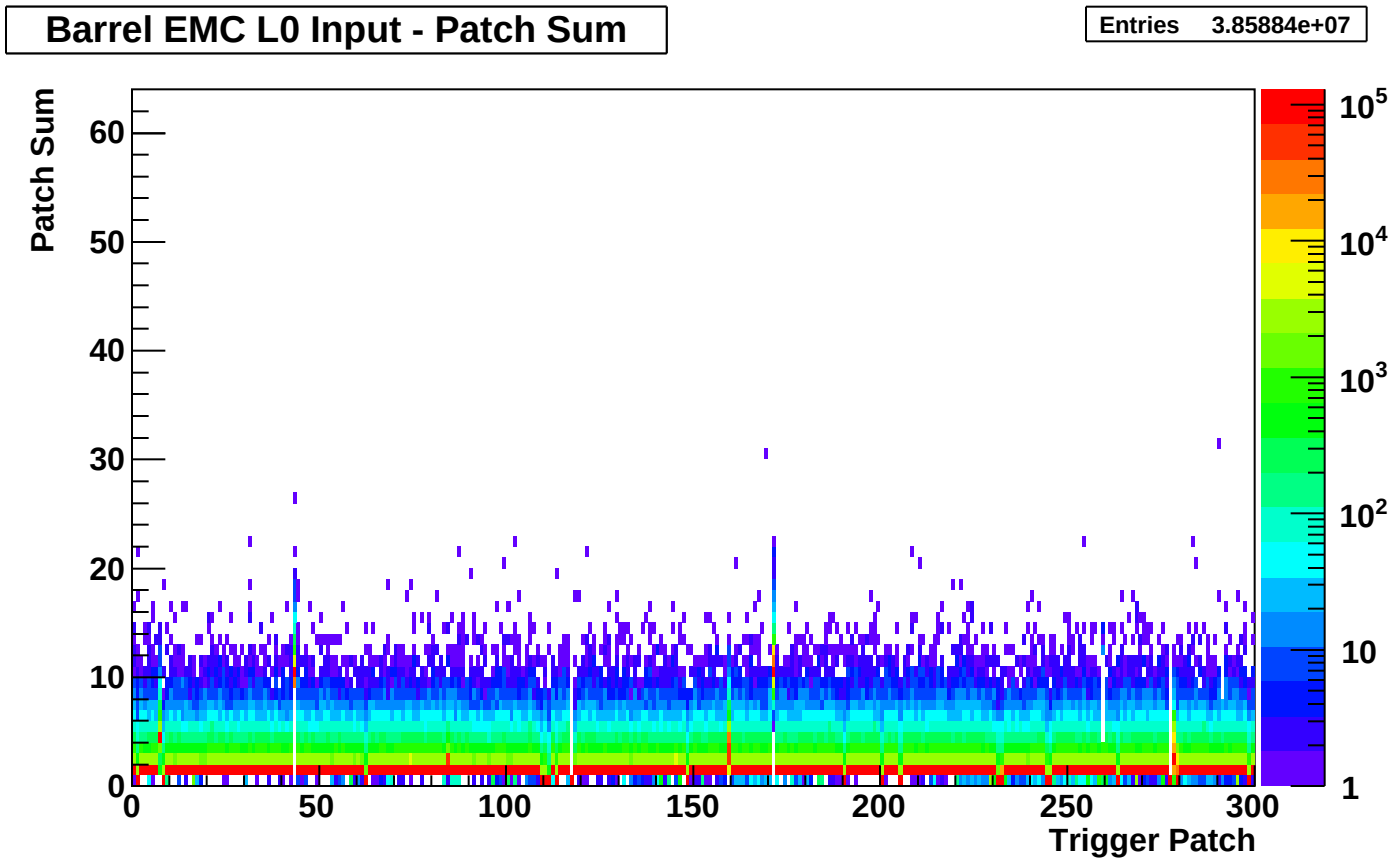
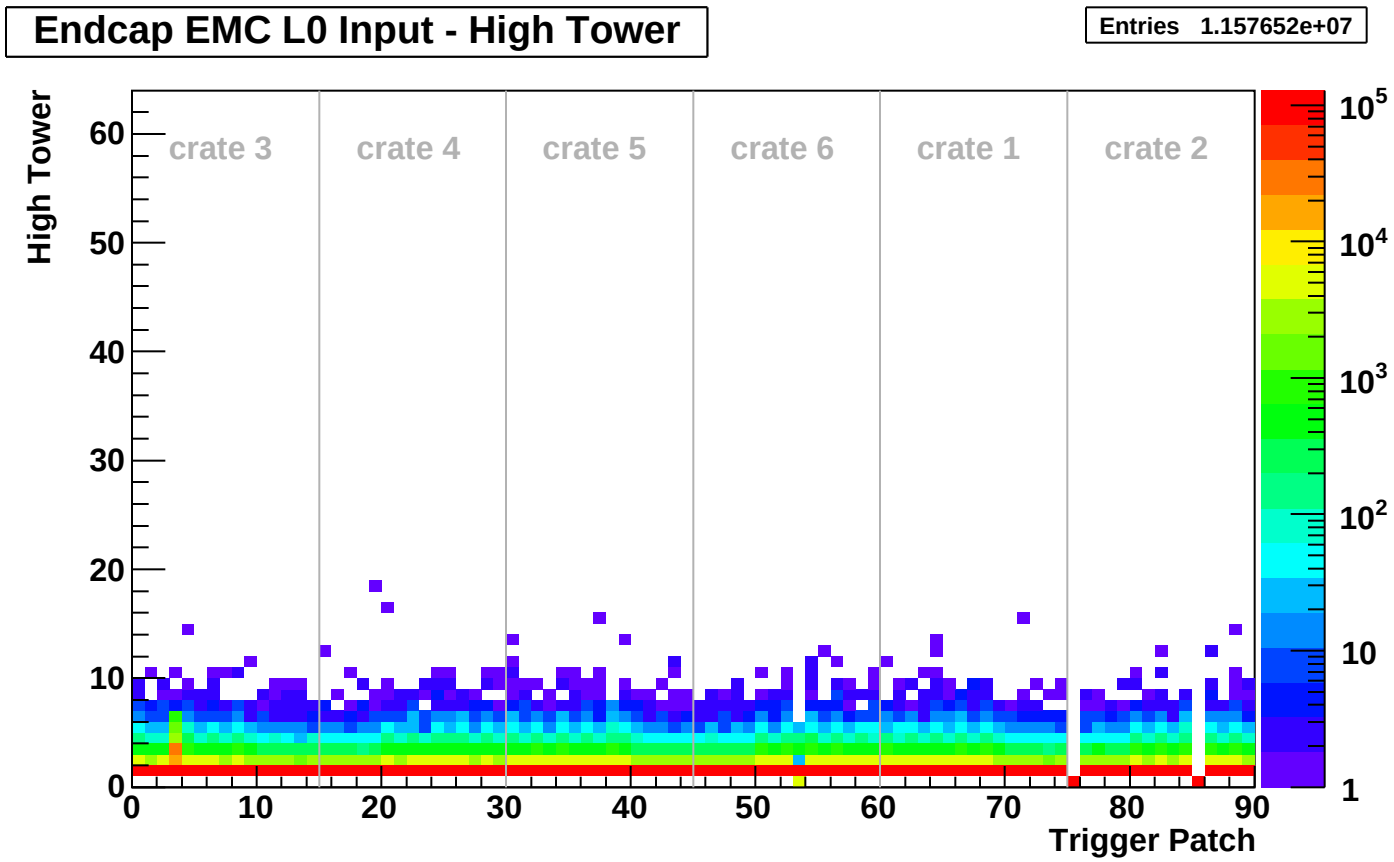
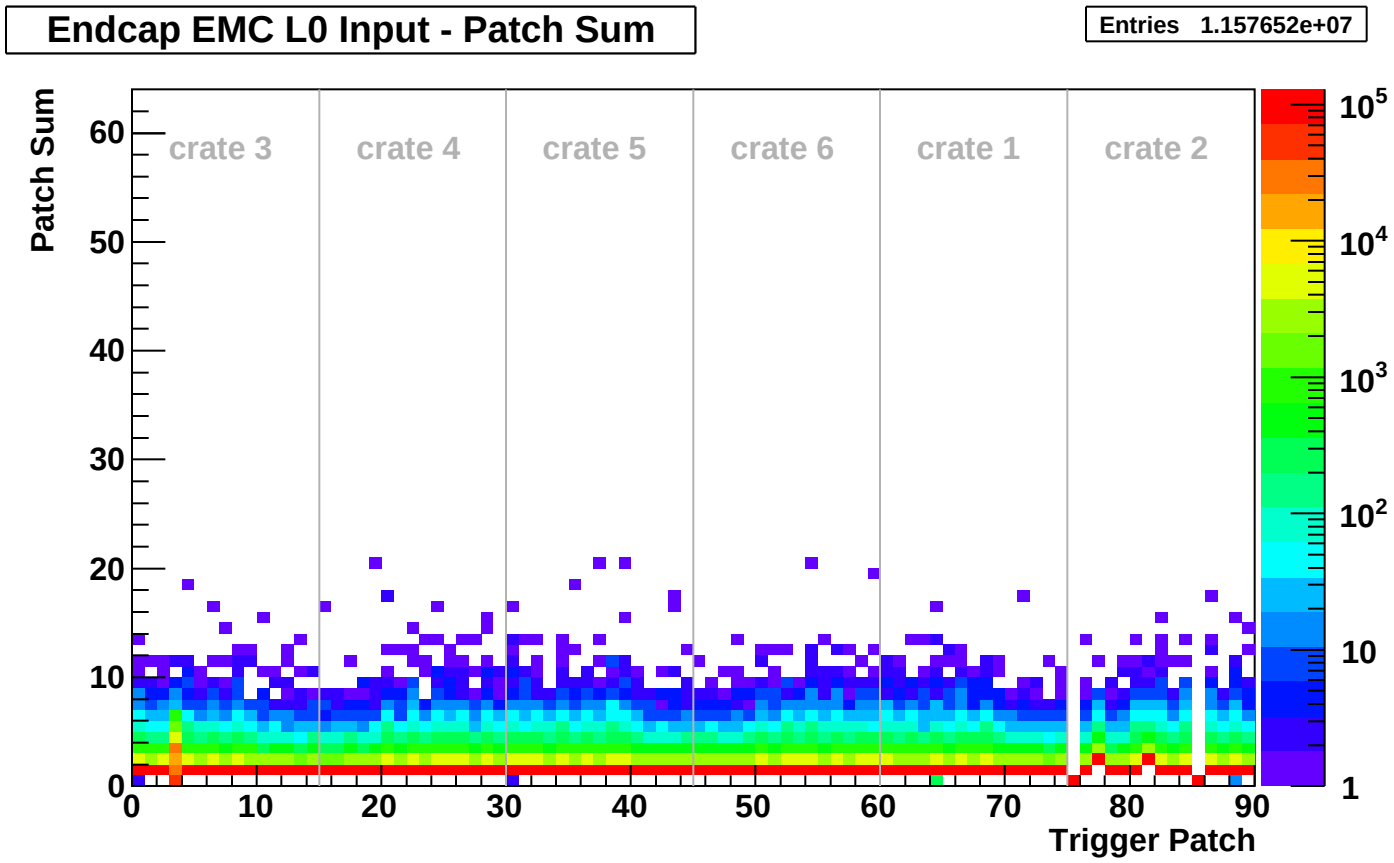
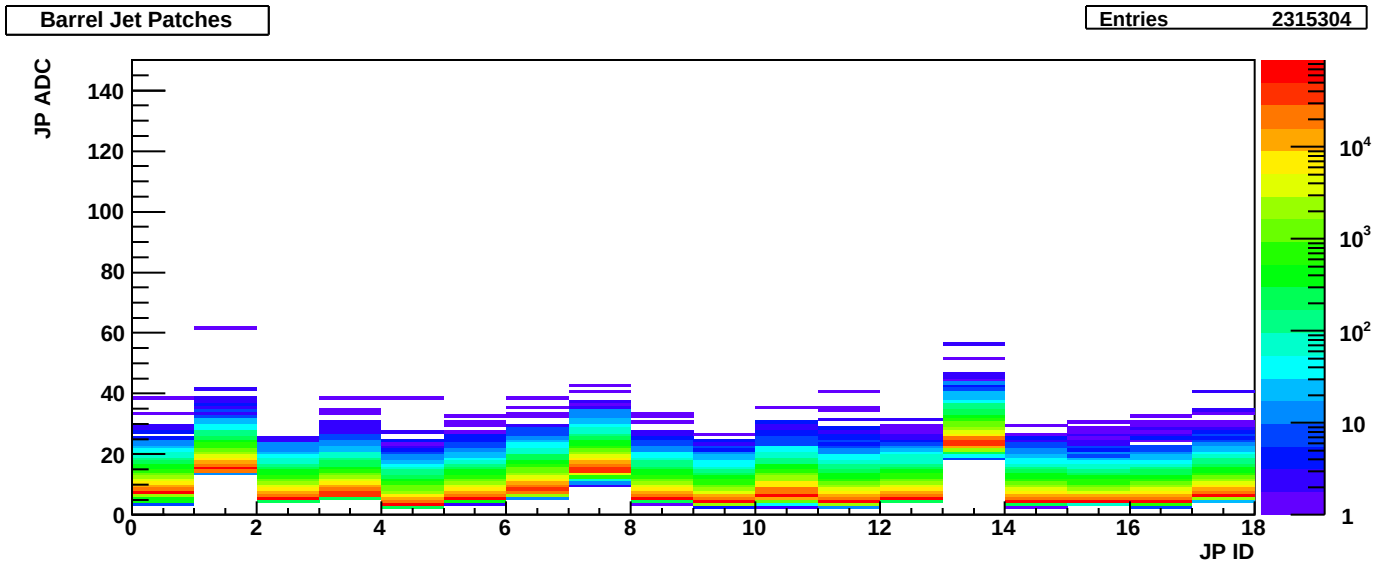
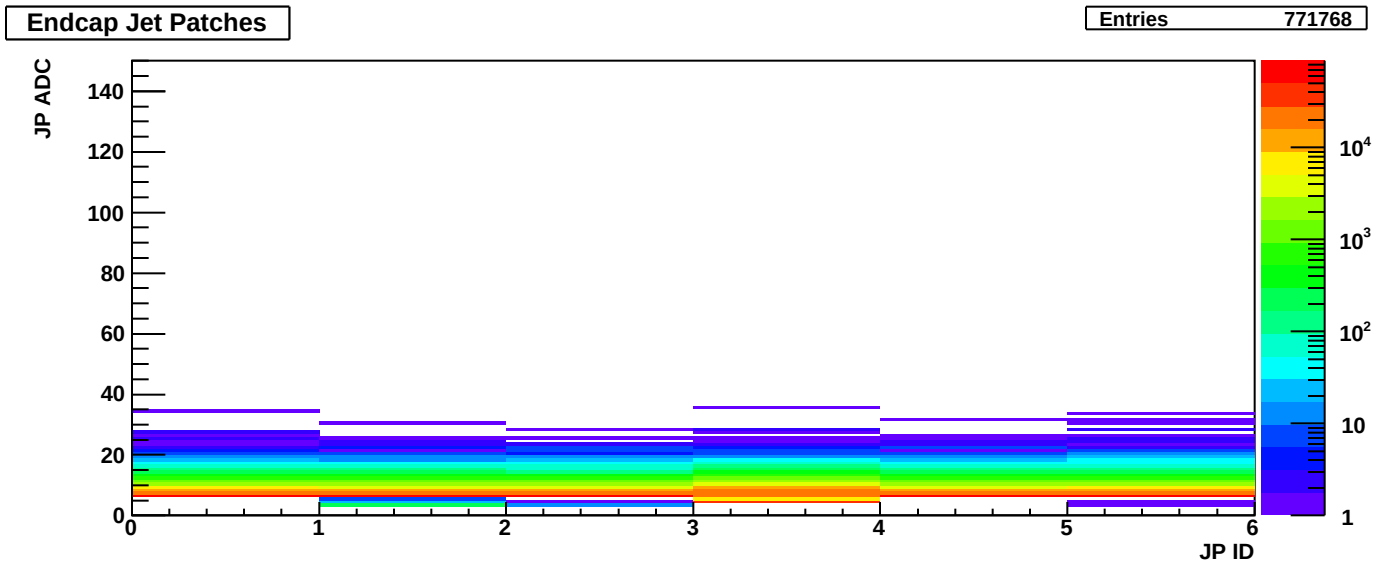

Barrel EMC L0 Input - High Tower
Entries 3.85884e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

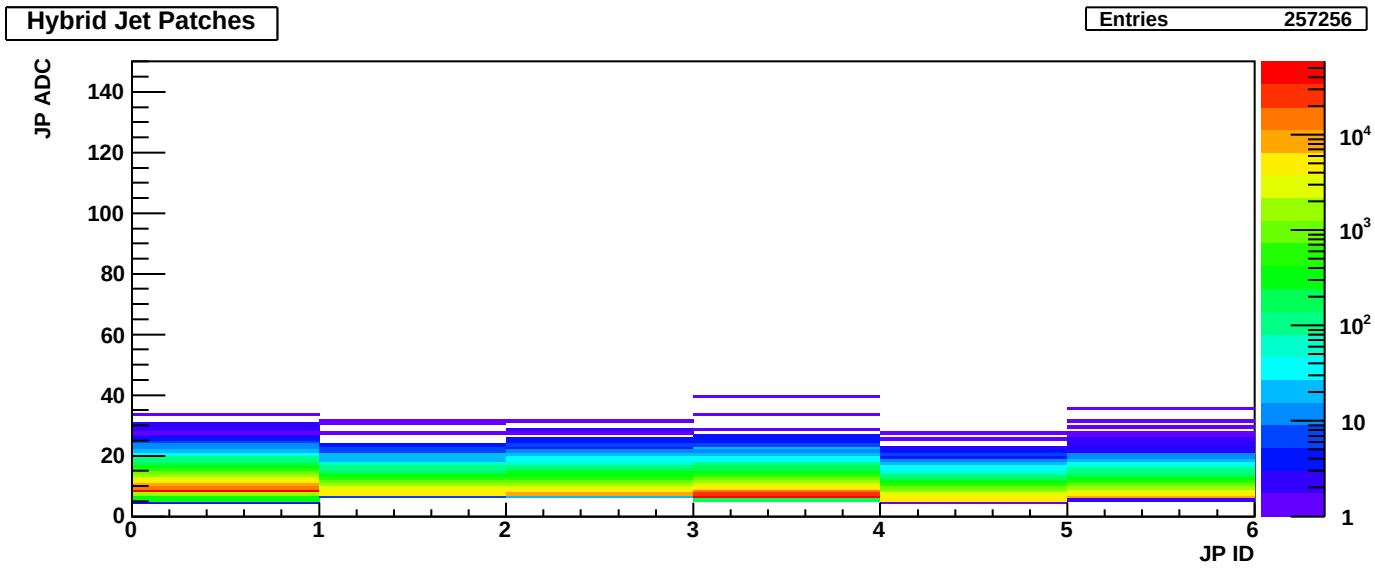

Barrel EMC L0 Input - Patch Sum
Entries 3.85884e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

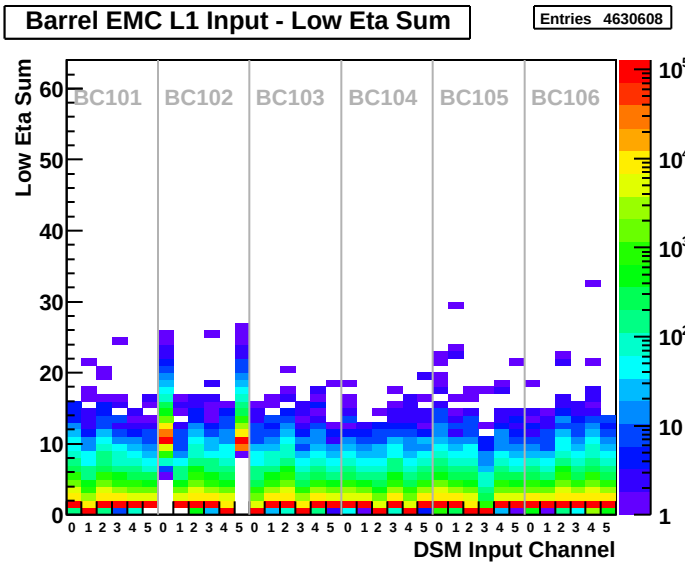

Endcap EMC L0 Input - High Tower
Entries 1.157652e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

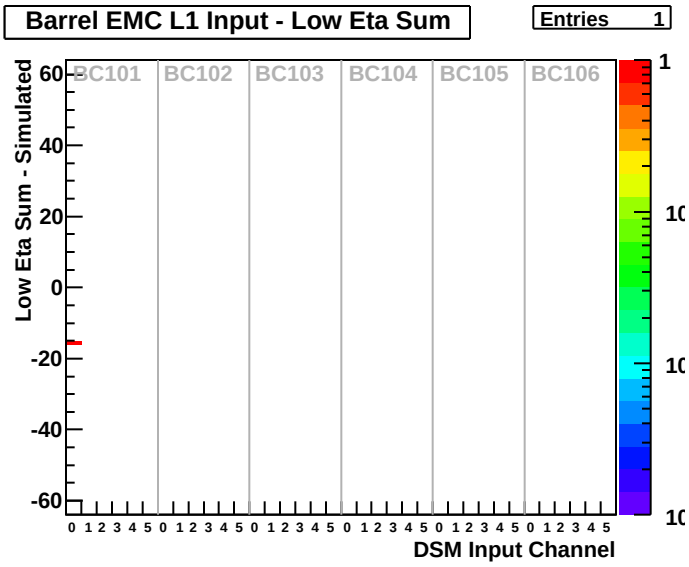

Endcap EMC L0 Input - Patch Sum
Entries 1.157652e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

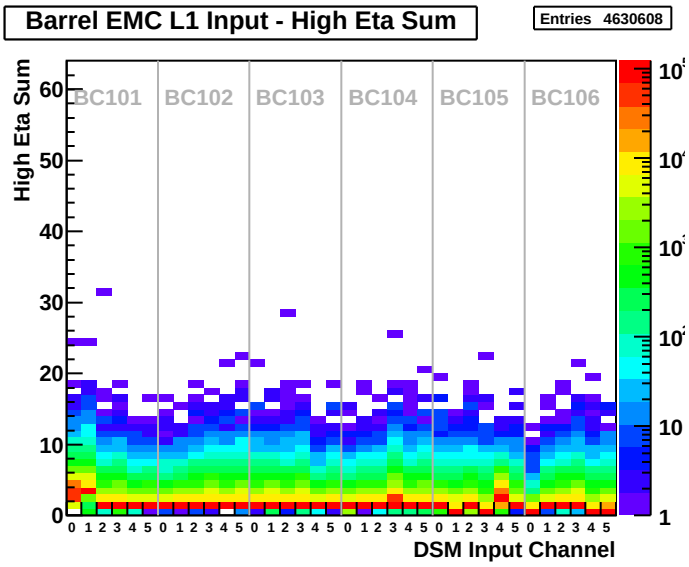

Barrel Jet Patches
Entries 2315304
JP ADC
JP ID

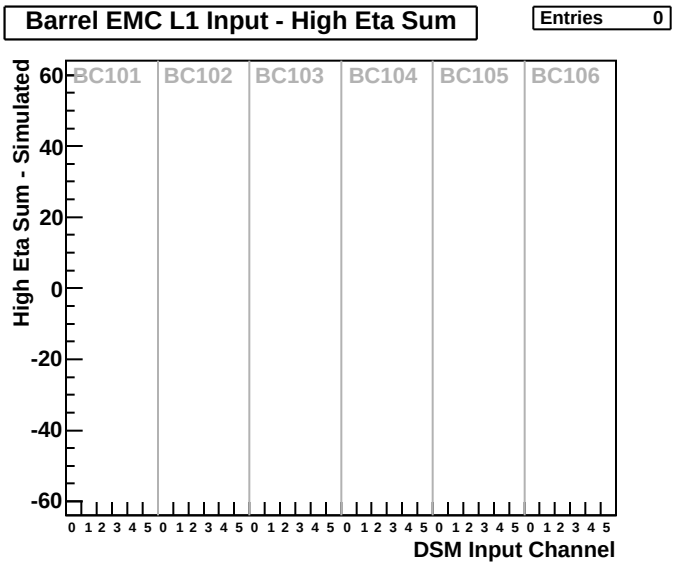

Endcap Jet Patches
Entries 771768
JP ADC
JP ID

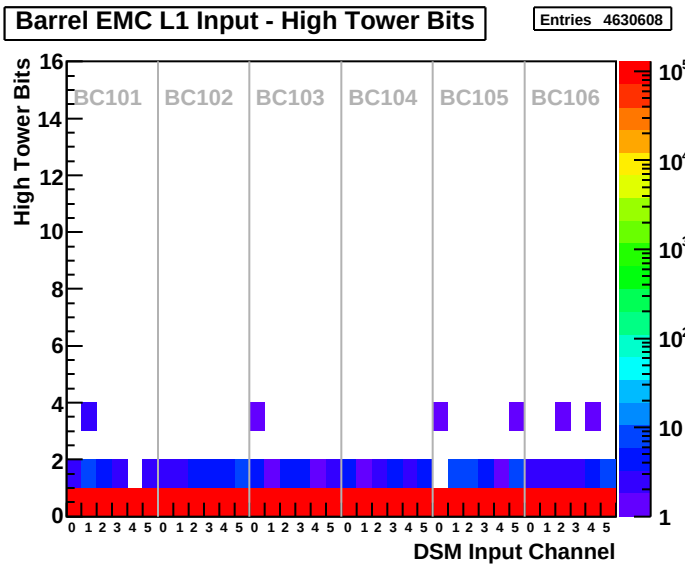

Hybrid Jet Patches
Entries 257256
JP ADC
JP ID

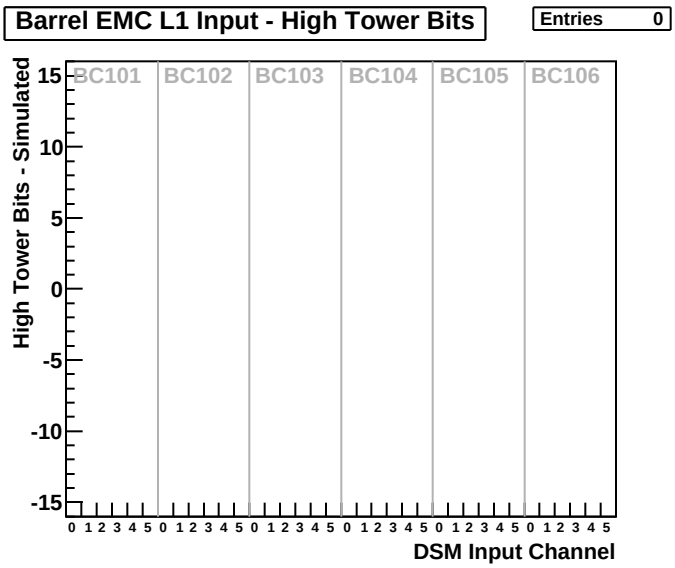

Barrel EMC L1 Input - Low Eta Sum
Entries 4630608
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

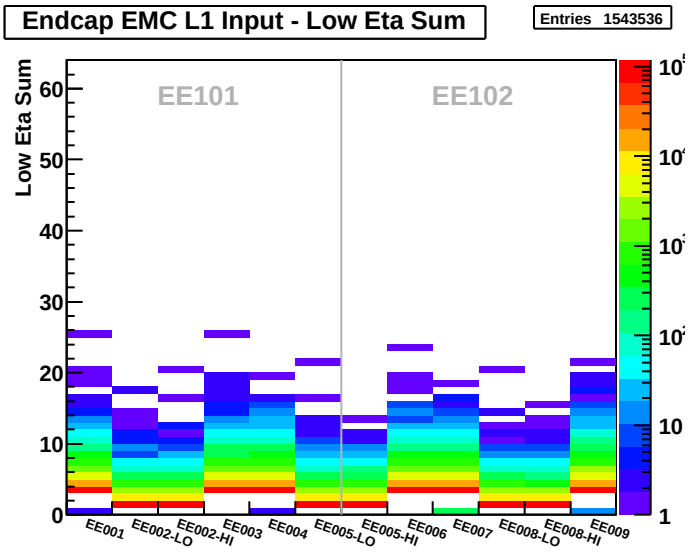

Barrel EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

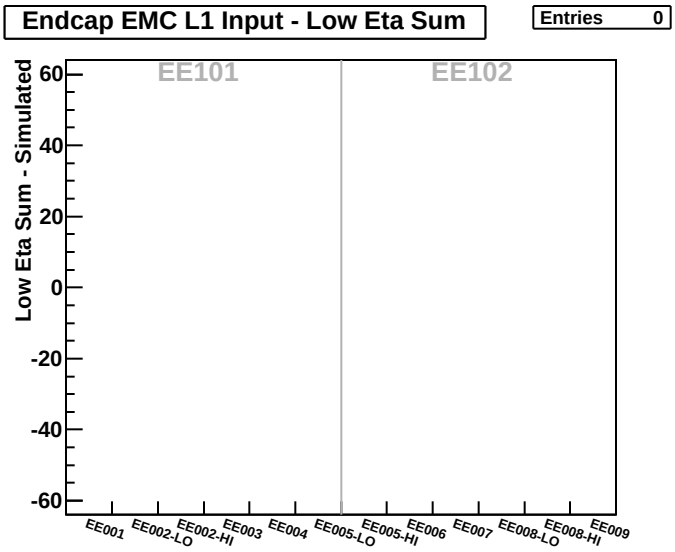

Barrel EMC L1 Input - High Eta Sum
Entries 4630608
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

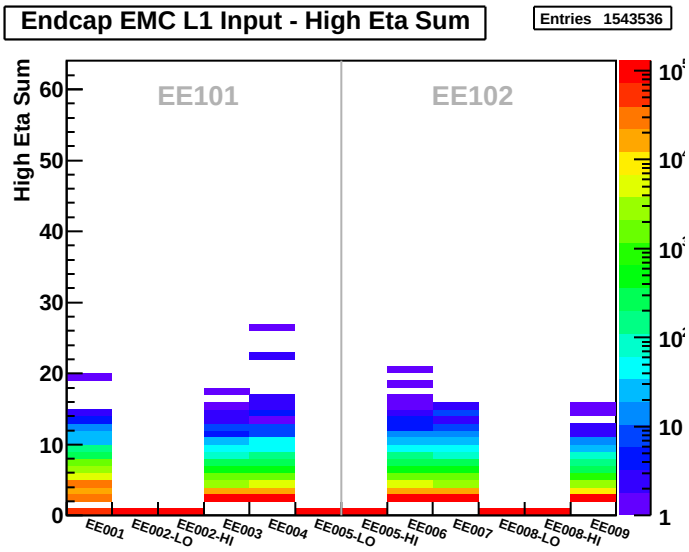

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

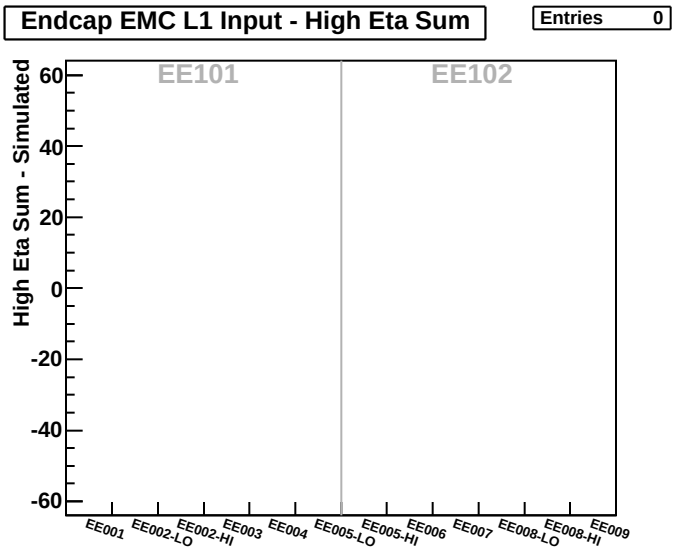

Barrel EMC L1 Input - High Tower Bits
Entries 4630608
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

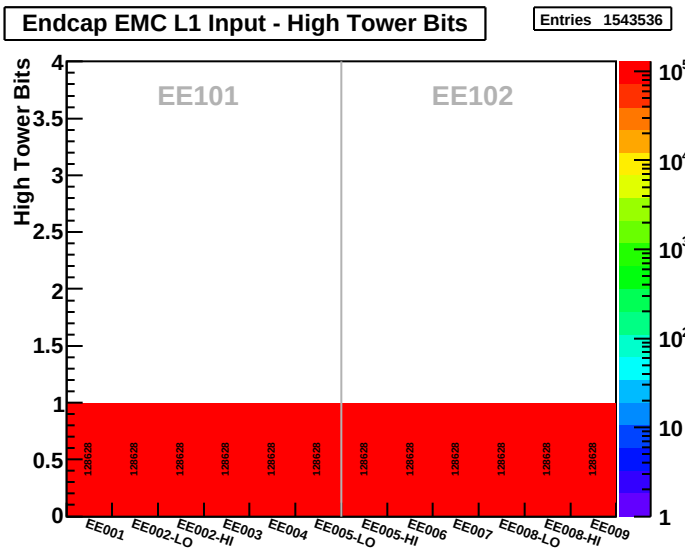

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

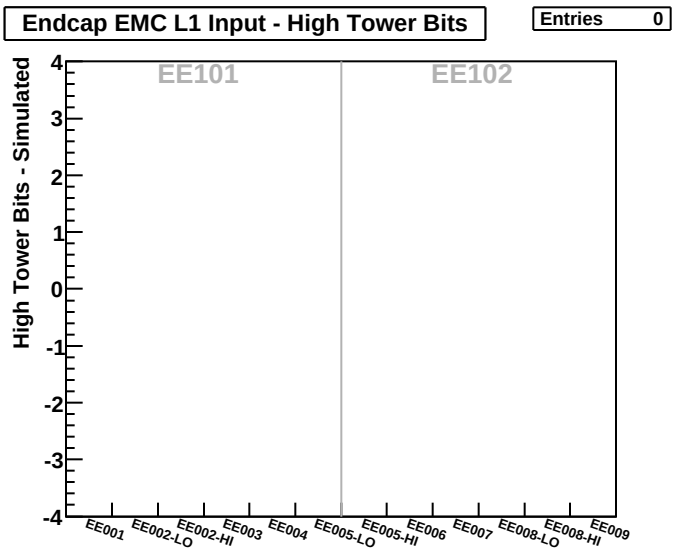

Endcap EMC L1 Input - Low Eta Sum
Entries 1543536
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

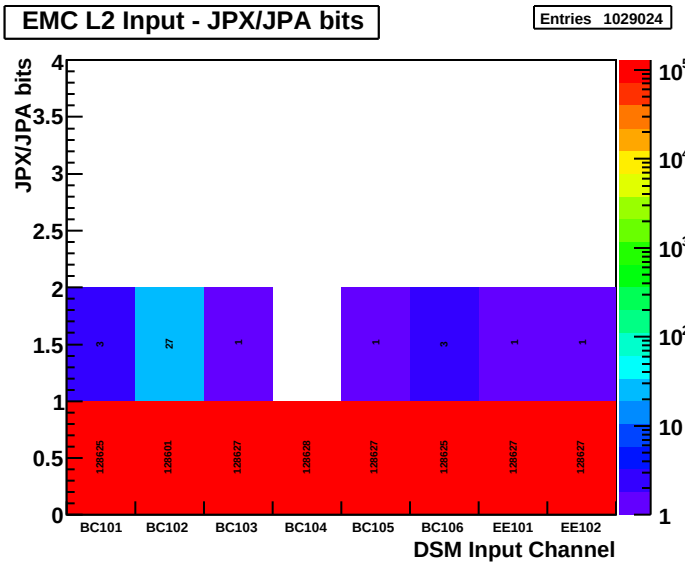

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

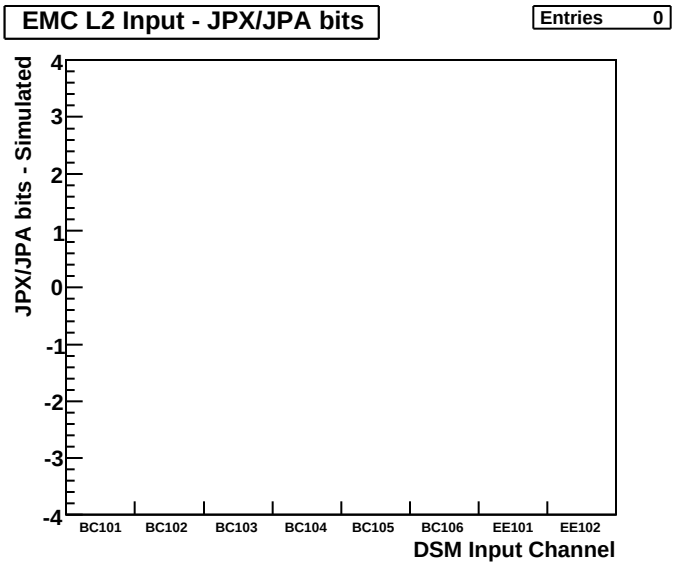

Endcap EMC L1 Input - High Eta Sum
Entries 1543536
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

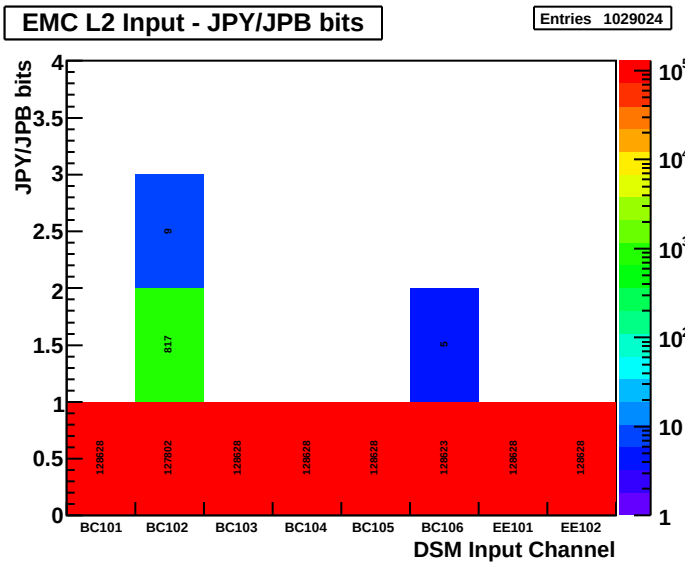

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

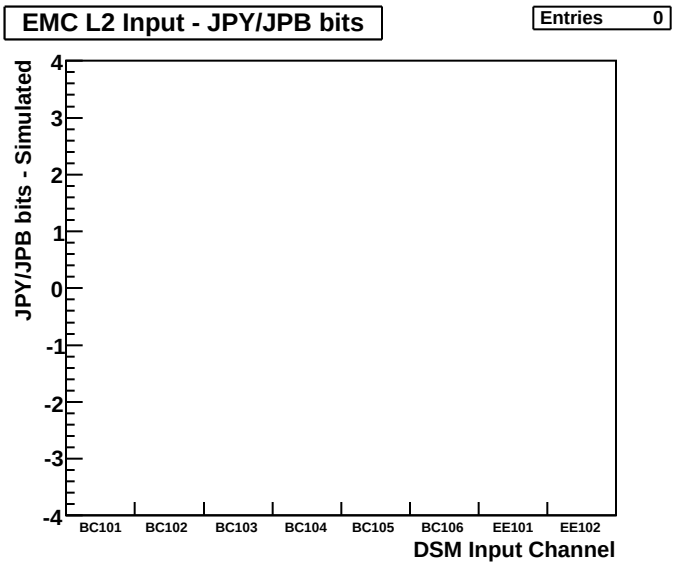

Endcap EMC L1 Input - High Tower Bits
Entries 1543536
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

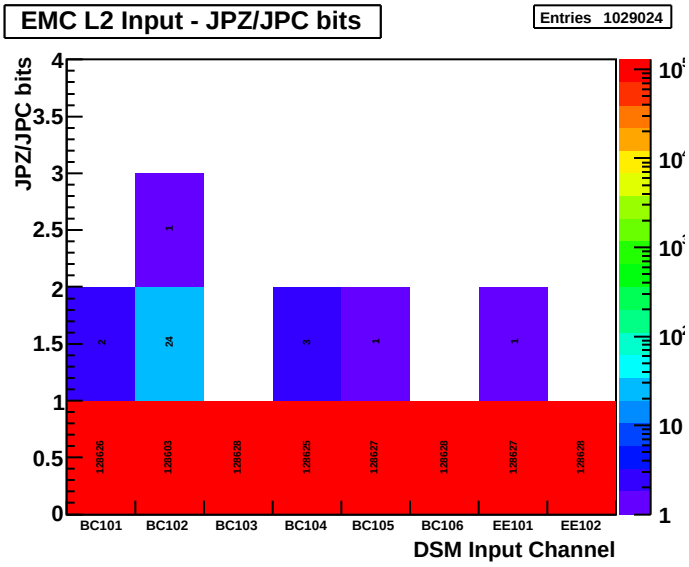

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

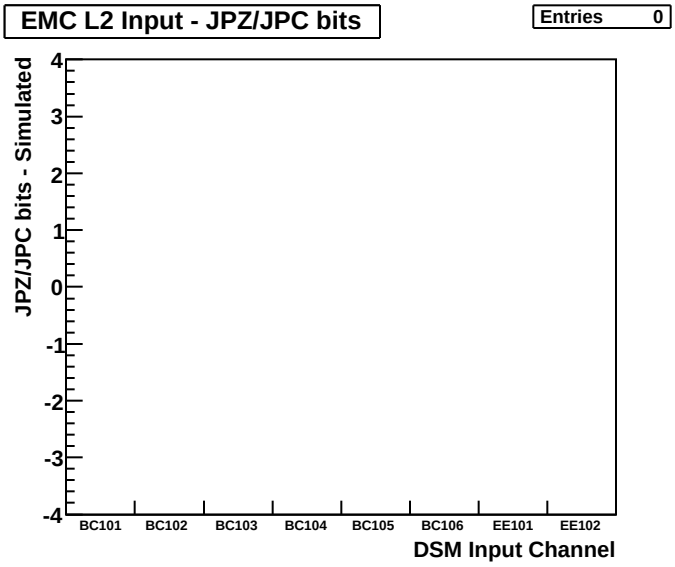

EMC L2 Input - JPX/JPA bits
Entries 1029024
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

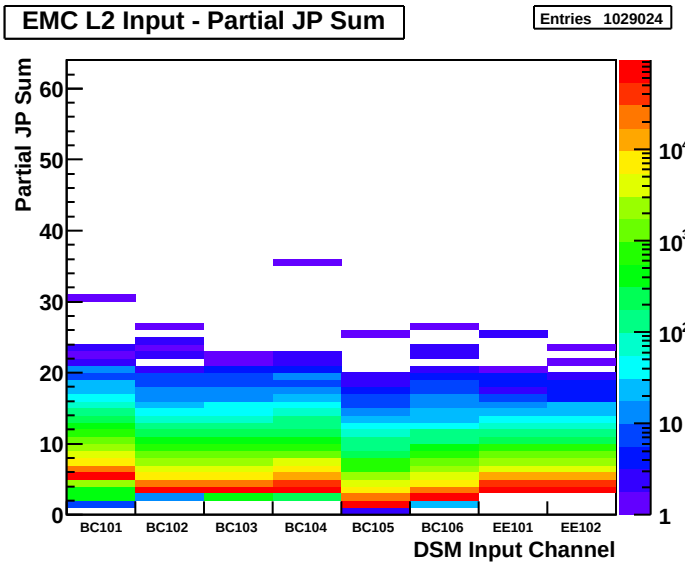

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

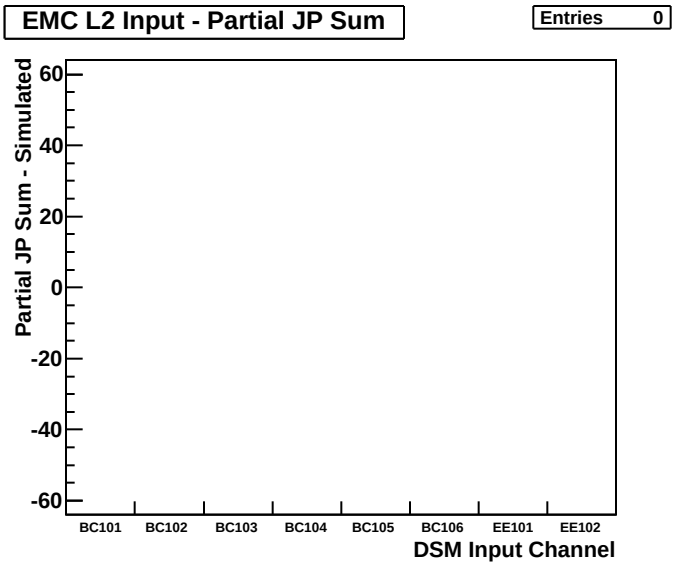

EMC L2 Input - JPY/JPB bits
Entries 1029024
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

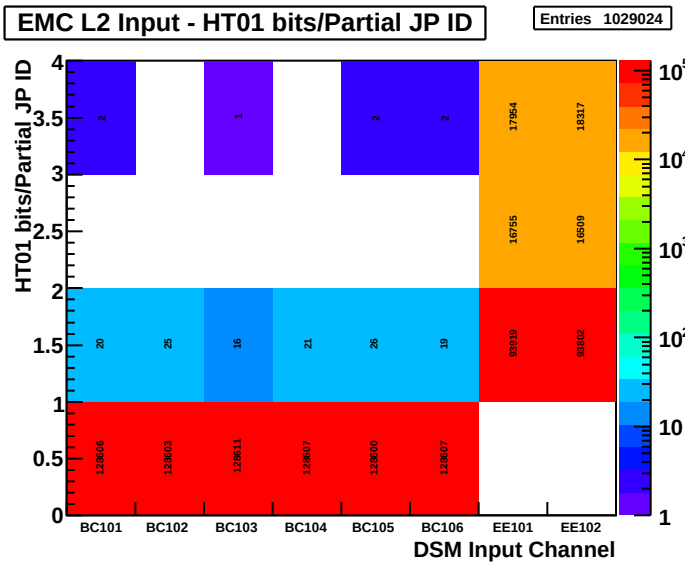

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

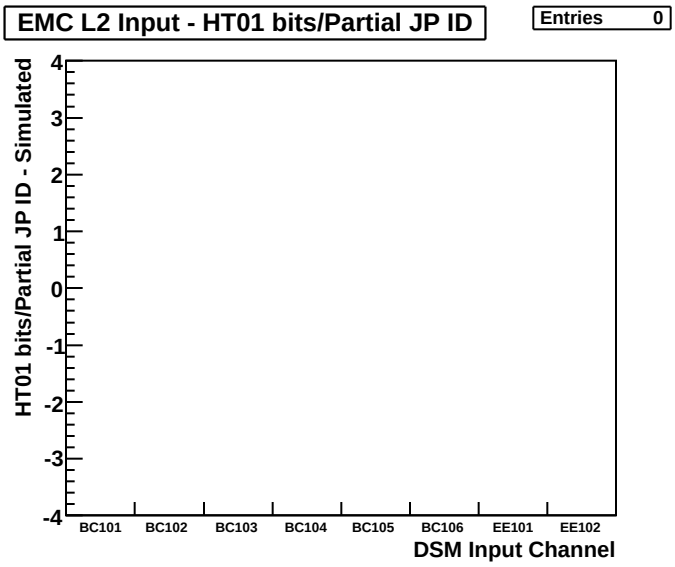

EMC L2 Input - JPZ/JPC bits
Entries 1029024
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

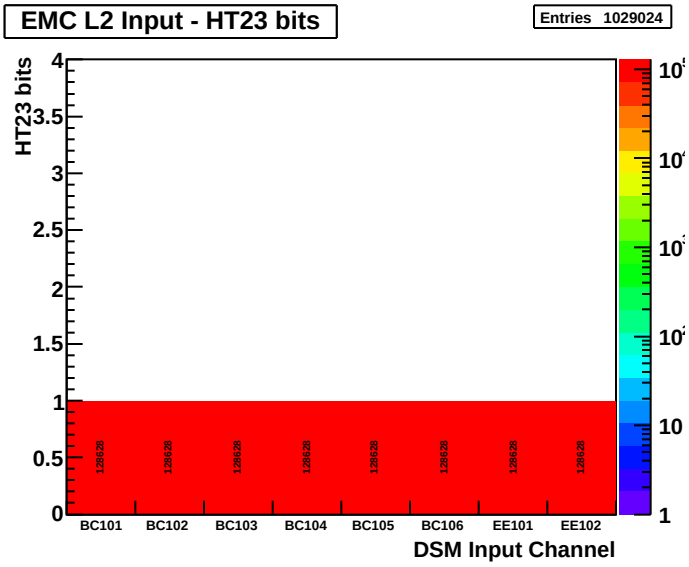

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

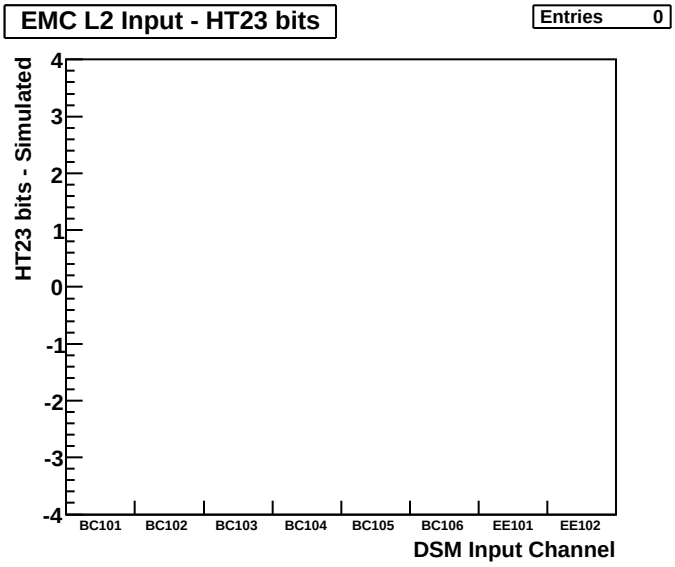

EMC L2 Input - Partial JP Sum
Entries 1029024
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

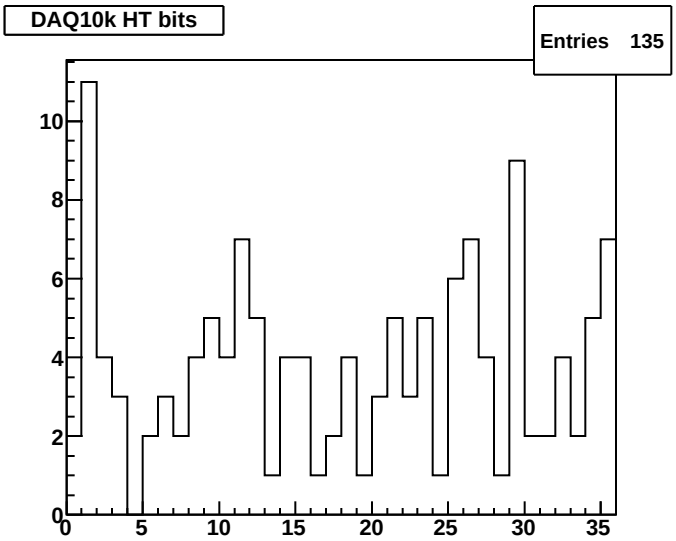

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

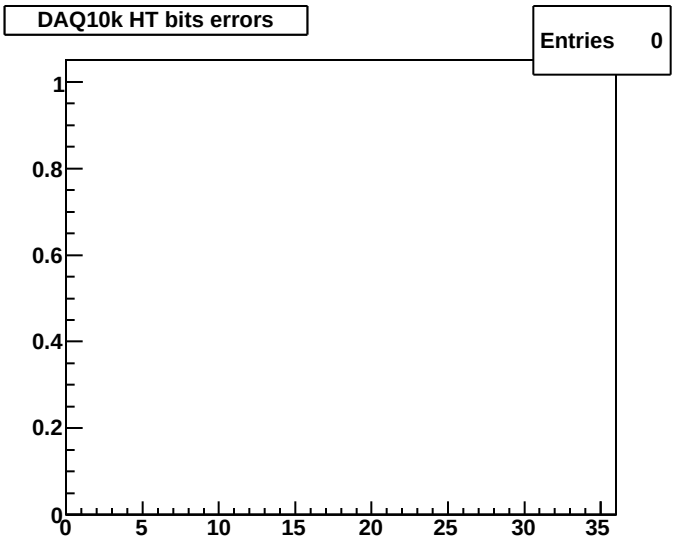

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1029024
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

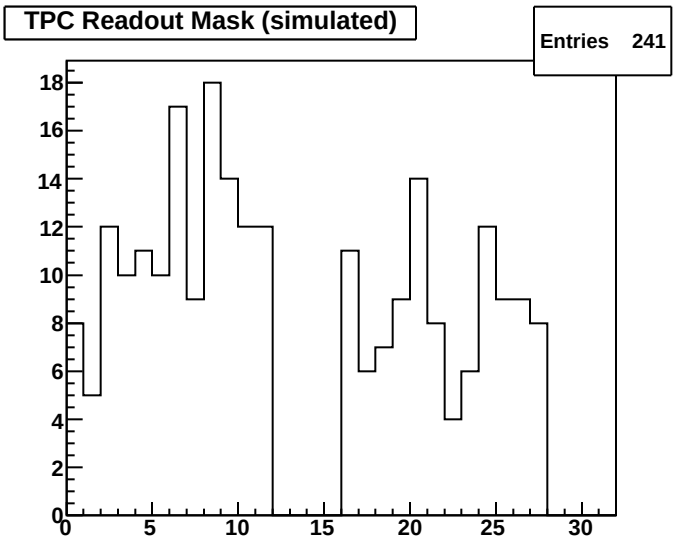

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

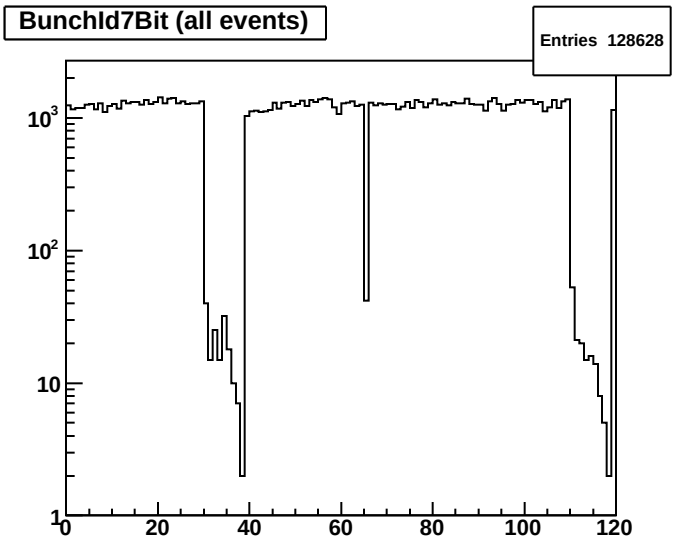

EMC L2 Input - HT23 bits
Entries 1029024
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

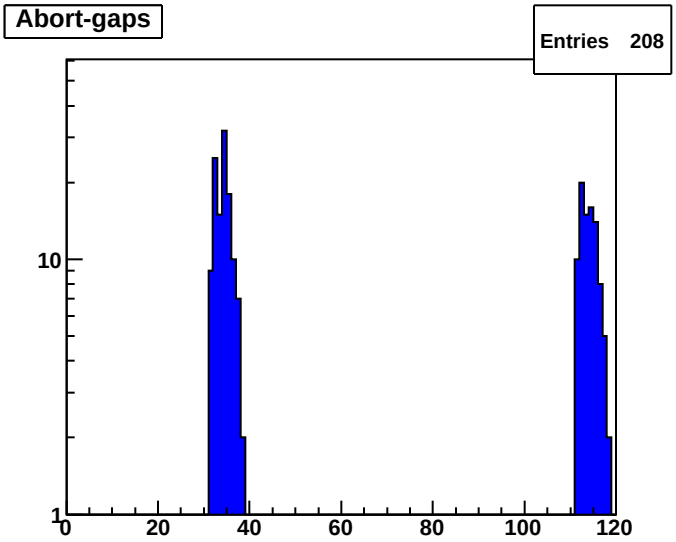

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

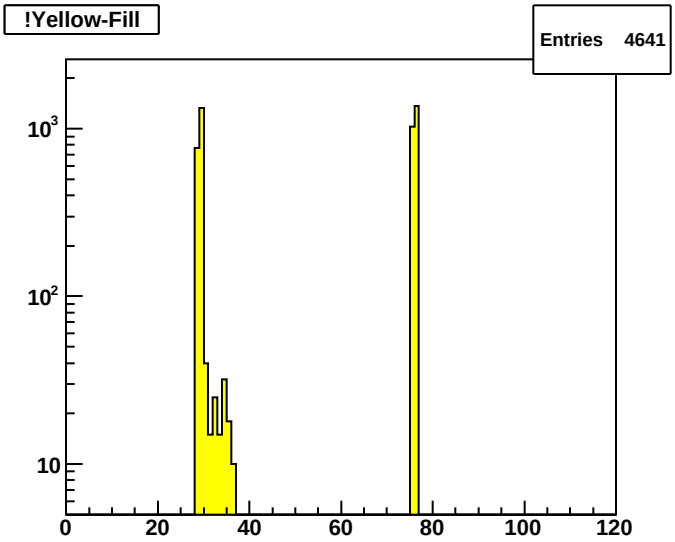

DAQ10k HT bits
Entries 135

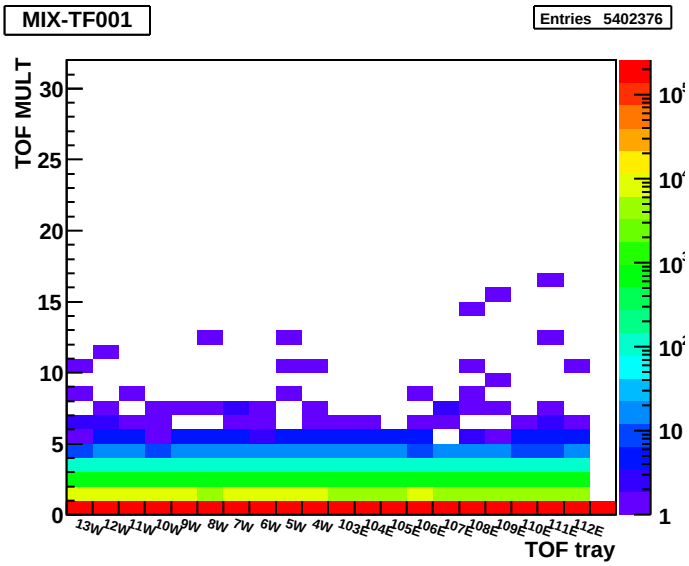

DAQ10k HT bits errors
Entries 0

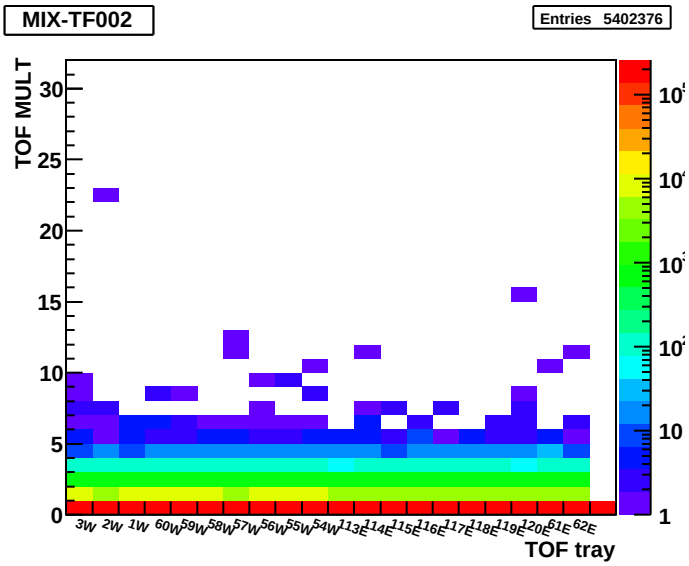

TPC Readout Mask (simulated)
Entries 241

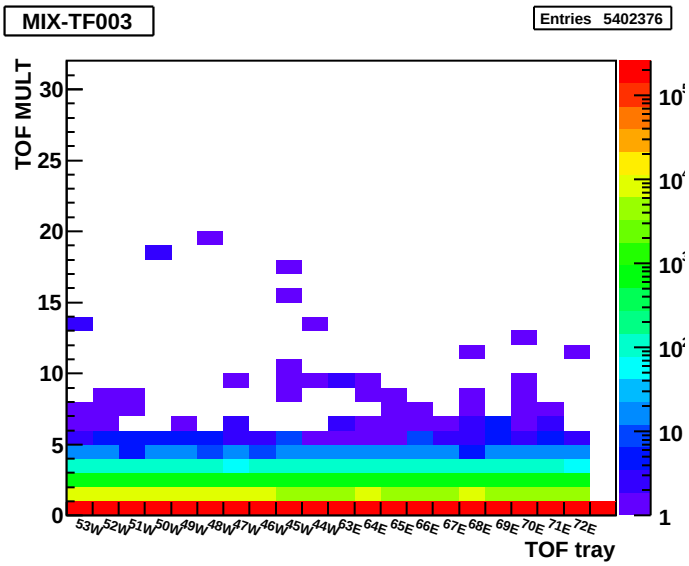

BunchId7Bit (all events)
Entries 128628

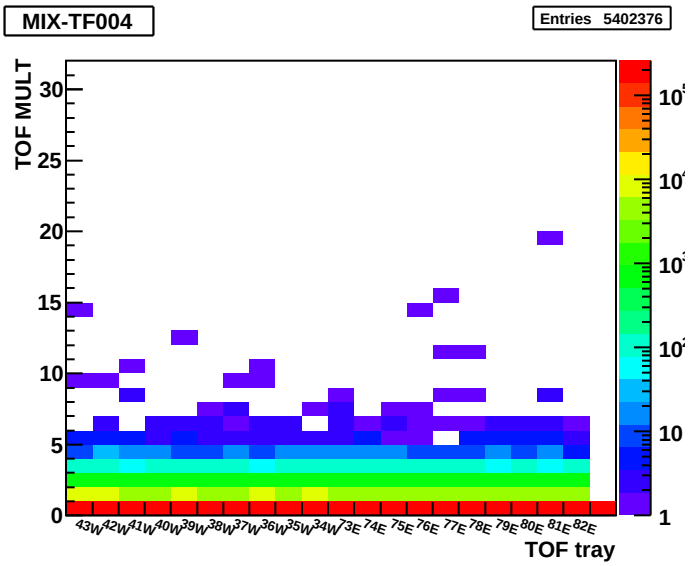

Abort-gaps
Entries 208

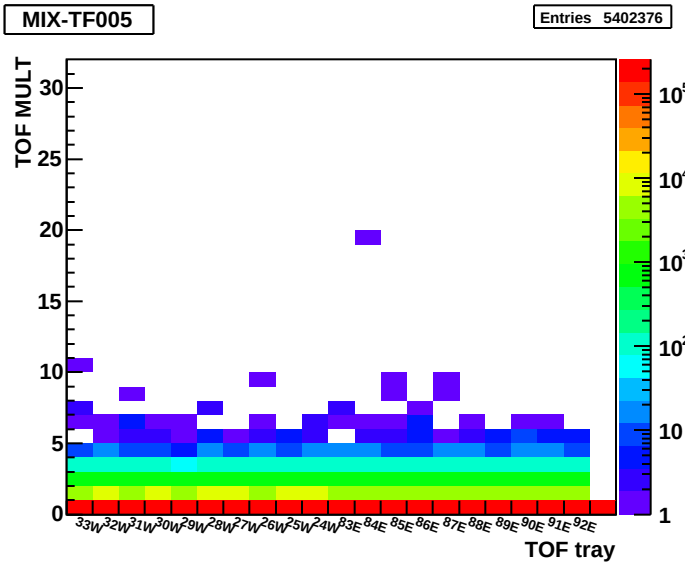

!Yellow-Fill
Entries 4641

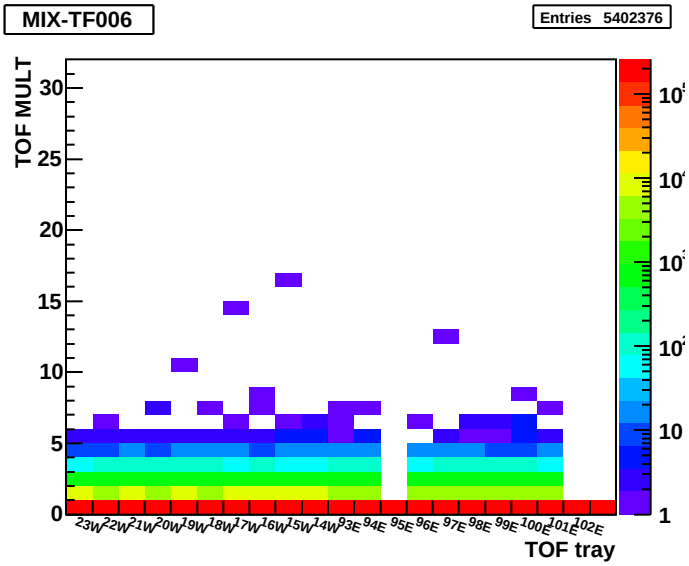

MIX-TF001
Entries 5402376
TOF MULT
TOF tray

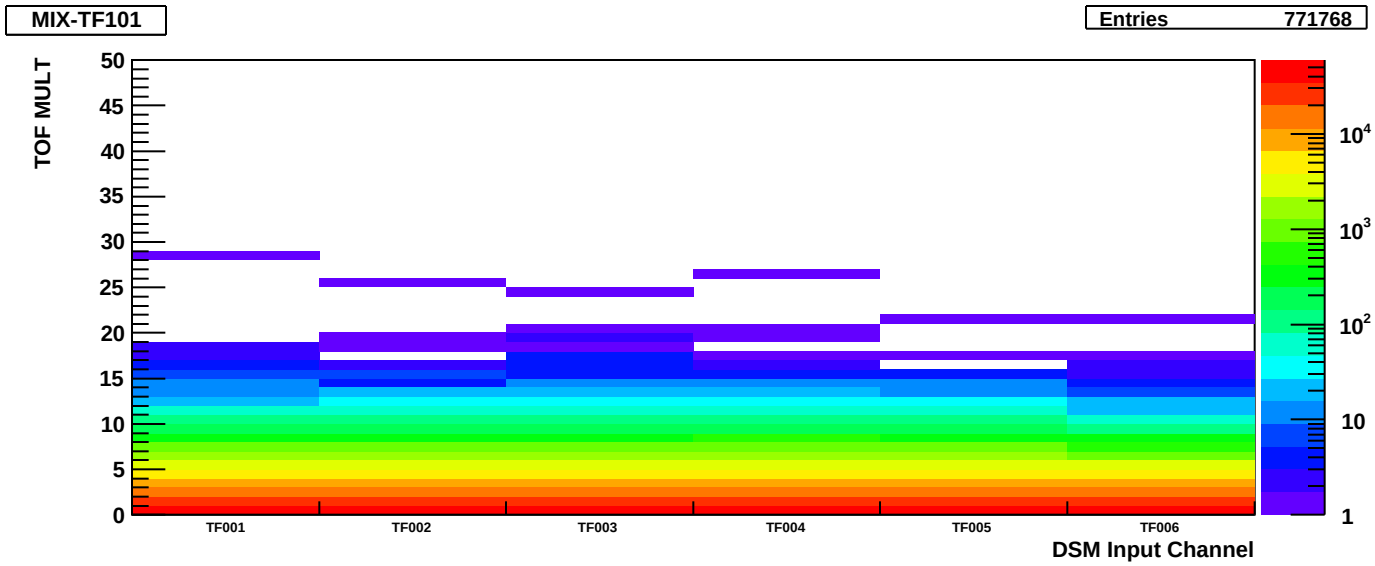

MIX-TF002
Entries 5402376
TOF MULT
TOF tray

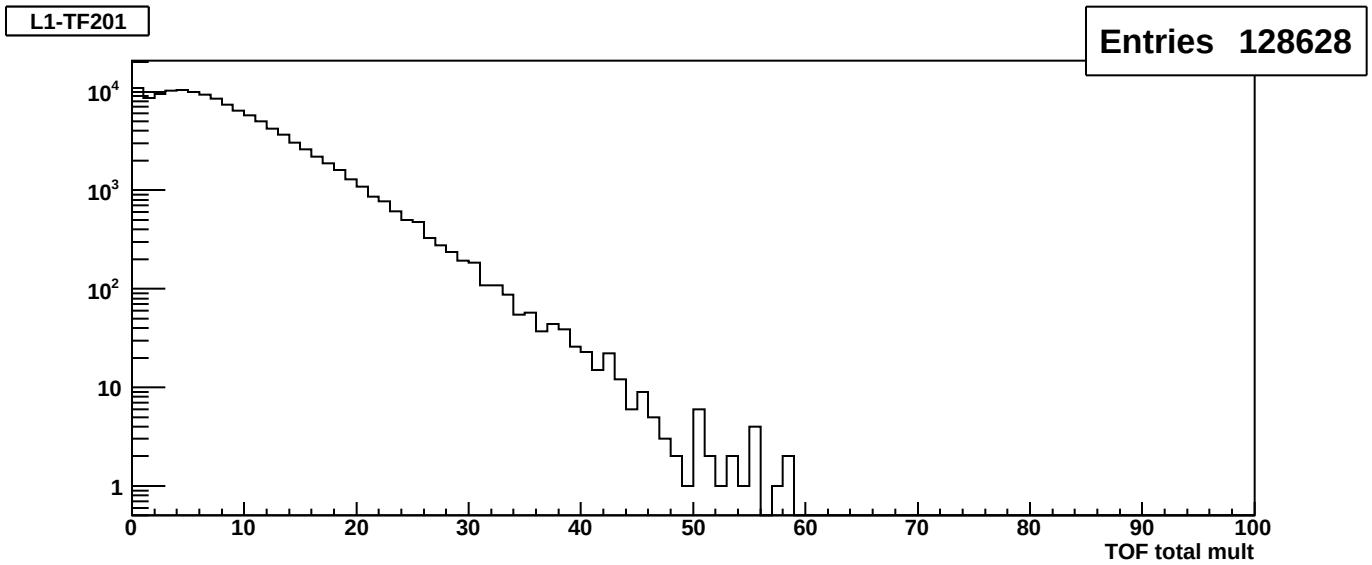

MIX-TF003
Entries 5402376
TOF MULT
TOF tray

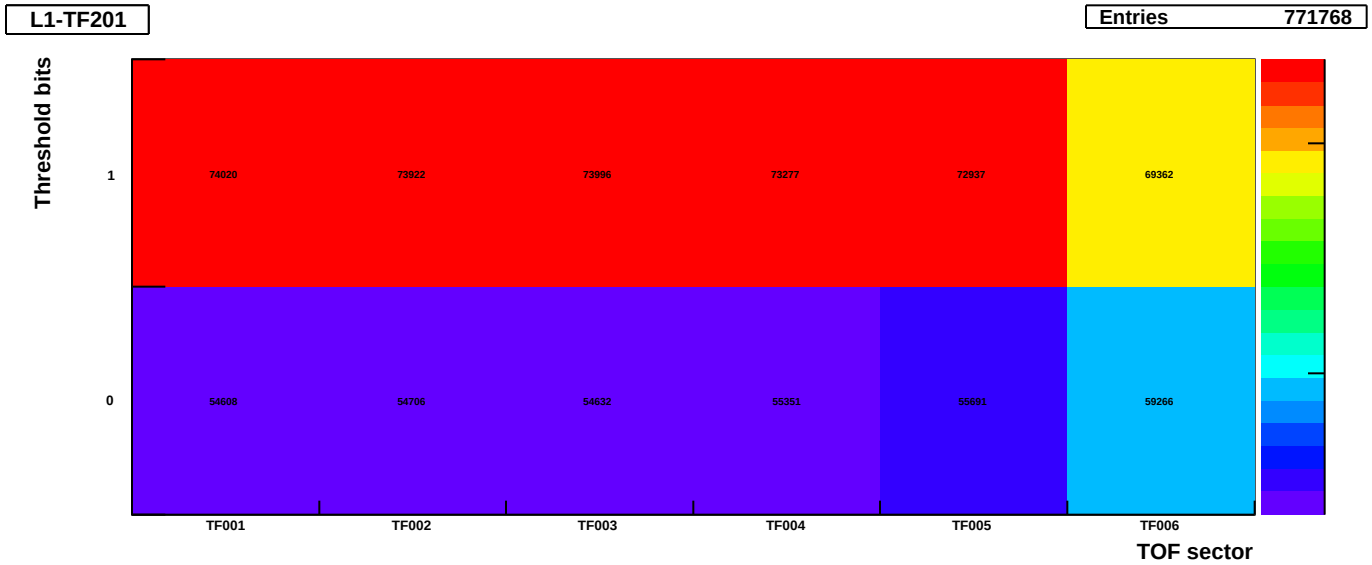

MIX-TF004
Entries 5402376
TOF MULT
TOF tray

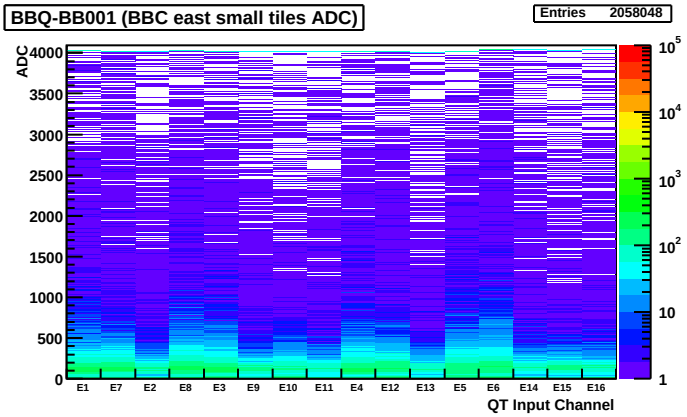

MIX-TF005
Entries 5402376
TOF MULT
TOF tray

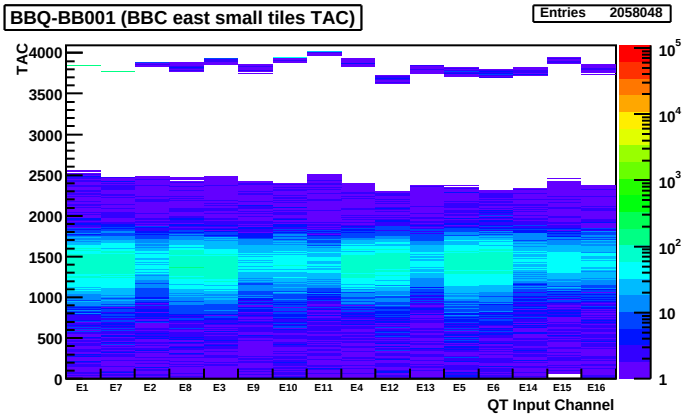

MIX-TF006
Entries 5402376
TOF MULT
TOF tray

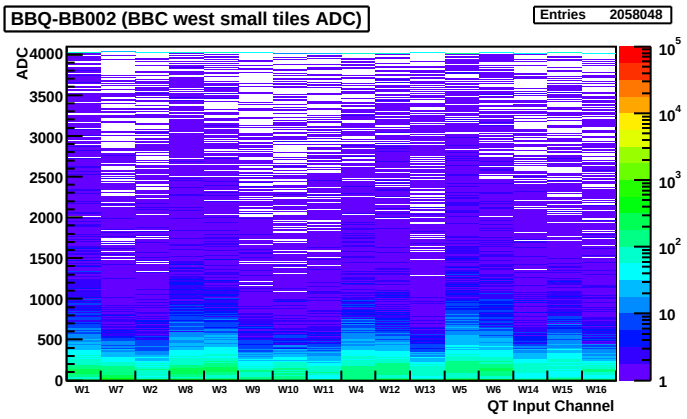

MIX-TF101
Entries 771768
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

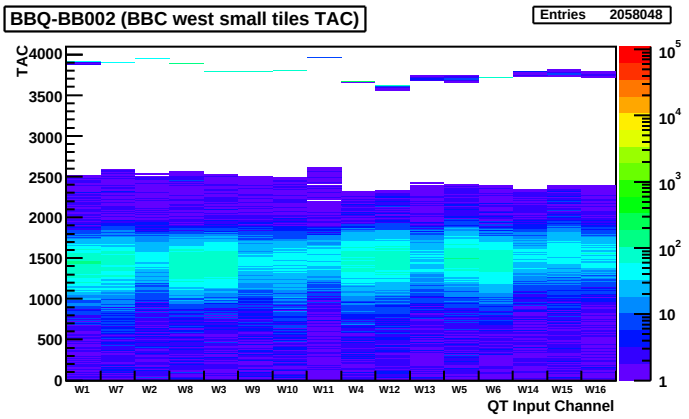

L1-TF201
Entries 128628
TOF total mult

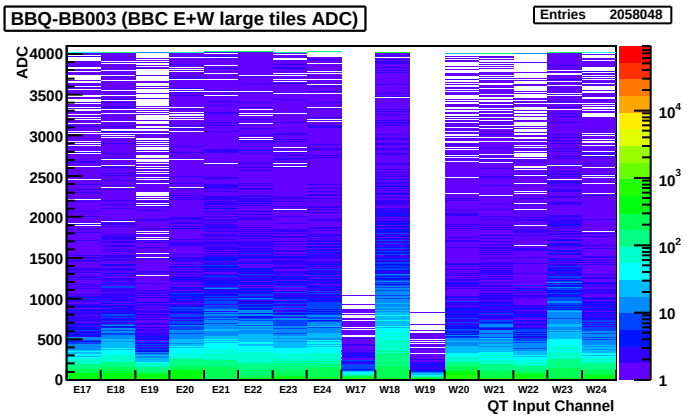

L1-TF201
Entries 771768
Threshold bits
74020
73922
73996
73277
72937
69362
54608
54706
54632
55351
55691
59266
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

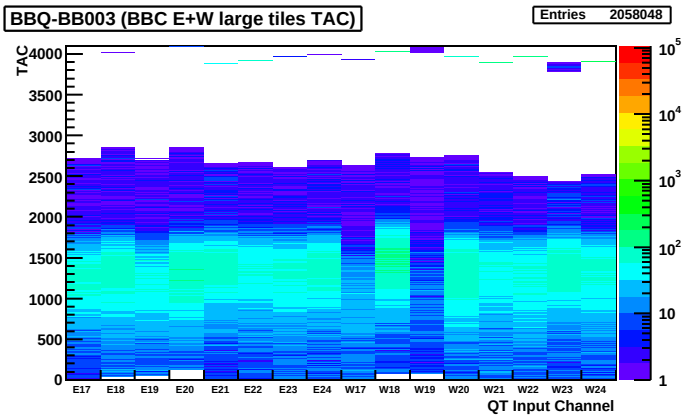

BBQ-BB001 (BBC east small tiles ADC)
Entries 2058048
ADC
QT Input Channel

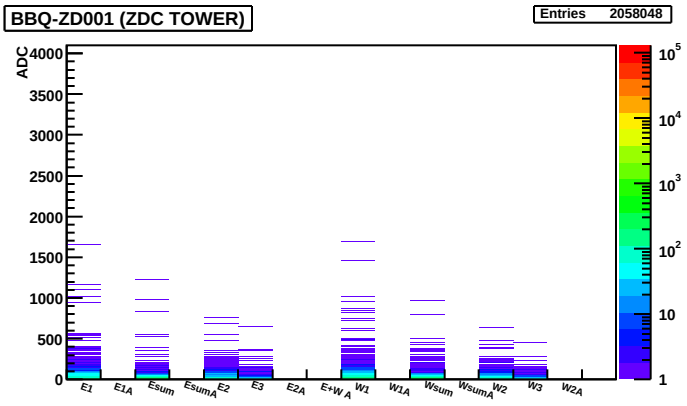

BBQ-BB001 (BBC east small tiles TAC)
Entries 2058048
TAC
QT Input Channel

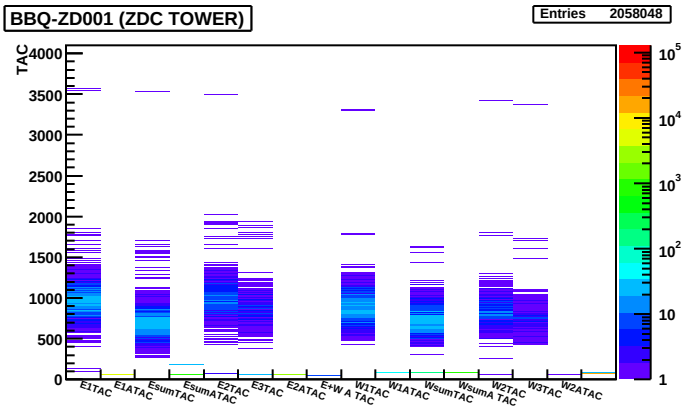

BBQ-BB002 (BBC west small tiles ADC)
Entries 2058048
ADC
QT Input Channel

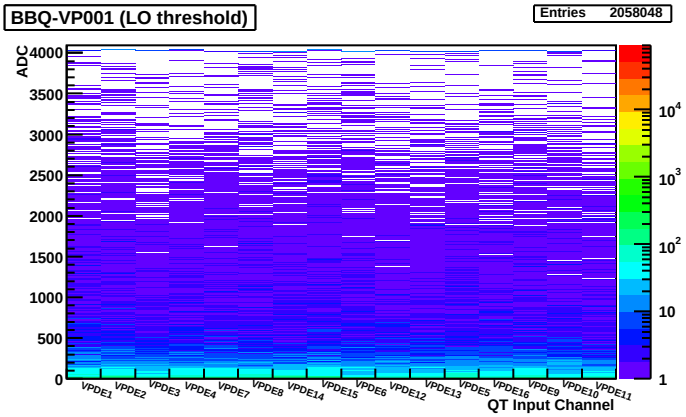

BBQ-BB002 (BBC west small tiles TAC)
Entries 2058048
TAC
QT Input Channel

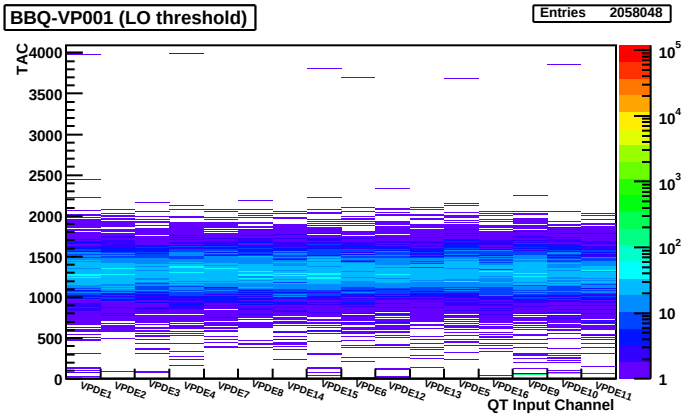

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2058048
ADC
QT Input Channel

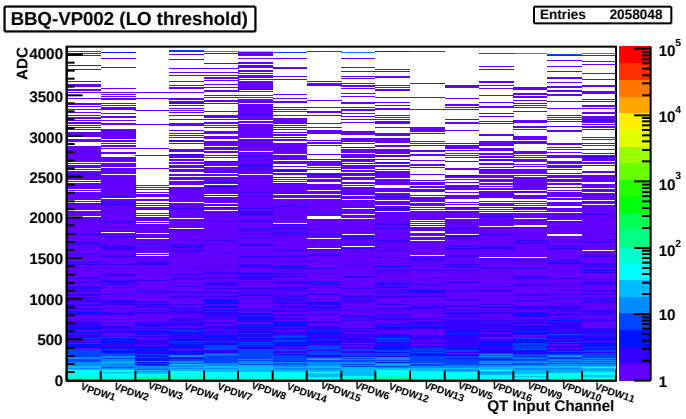

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2058048
TAC
QT Input Channel

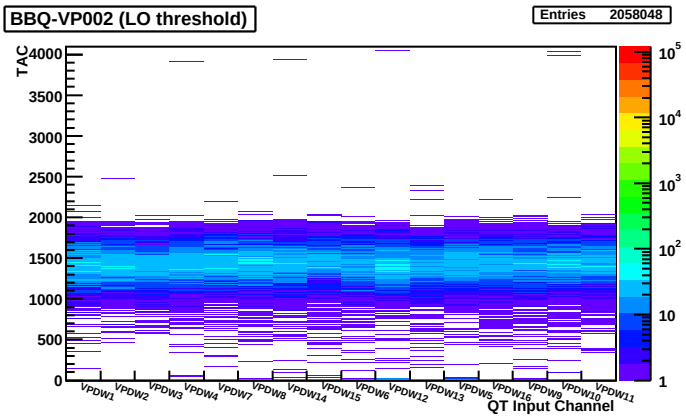

BBQ-ZD001 (ZDC TOWER)
Entries 2058048
ADC

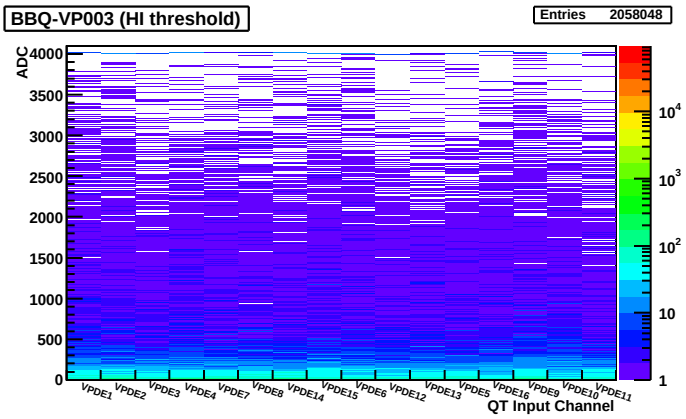

BBQ-ZD001 (ZDC TOWER)
Entries 2058048
TAC

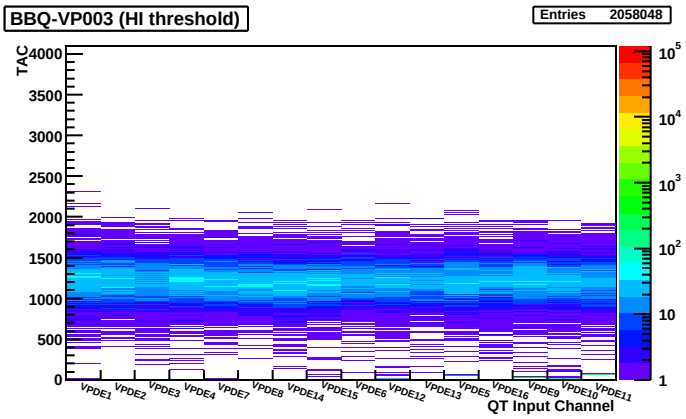

BBQ-VP001 (LO threshold)
Entries 2058048
ADC
QT Input Channel

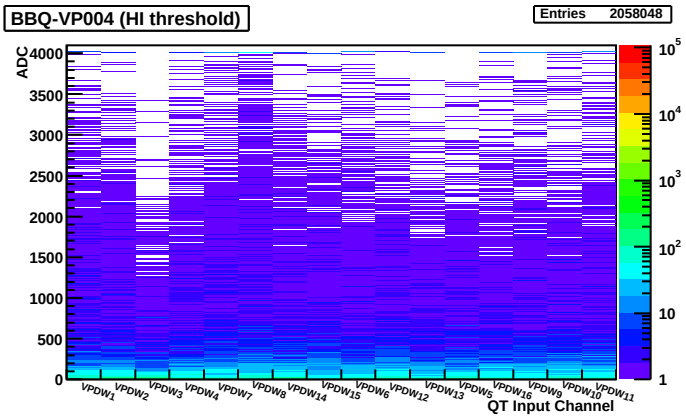

BBQ-VP001 (LO threshold)
Entries 2058048
TAC
QT Input Channel

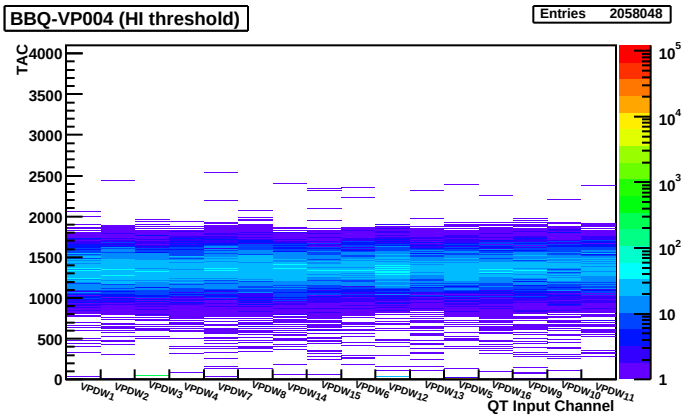

BBQ-VP002 (LO threshold)
Entries 2058048
ADC
QT Input Channel

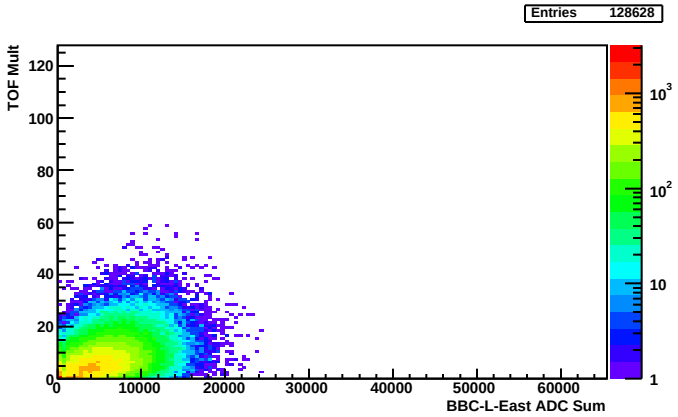

BBQ-VP002 (LO threshold)
Entries 2058048
TAC
QT Input Channel

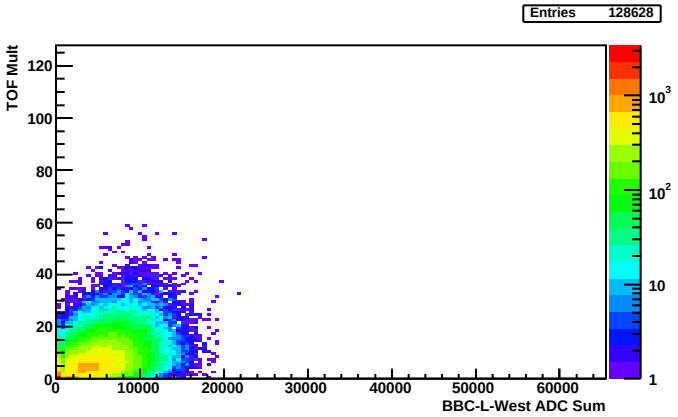

BBQ-VP003 (HI threshold)
Entries 2058048
ADC
QT Input Channel

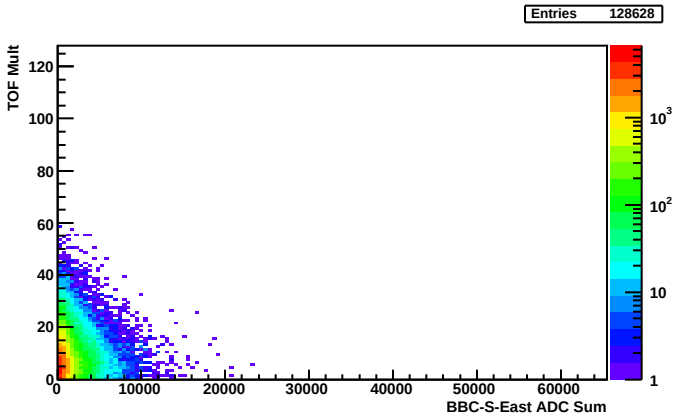

BBQ-VP003 (HI threshold)
Entries 2058048
TAC
QT Input Channel

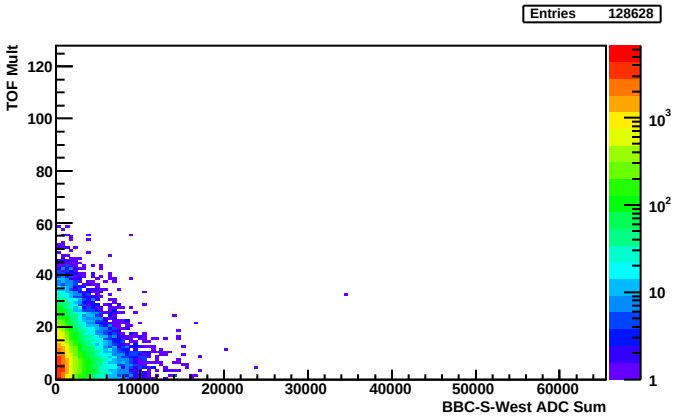

BBQ-VP004 (HI threshold)
Entries 2058048
ADC
QT Input Channel

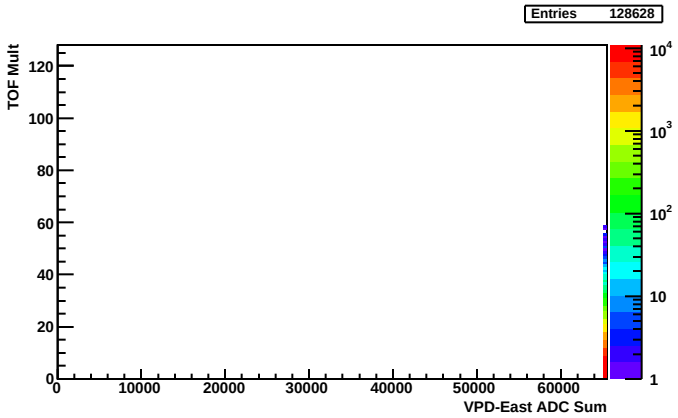

BBQ-VP004 (HI threshold)
Entries 2058048
TAC
QT Input Channel

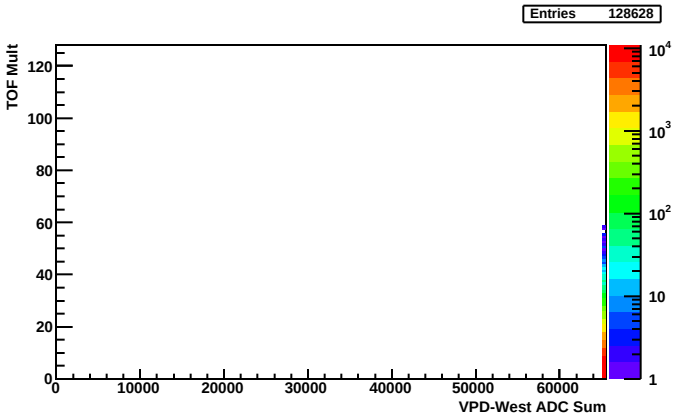

Entries 128628
TOF Mult
BBC-L-East ADC Sum

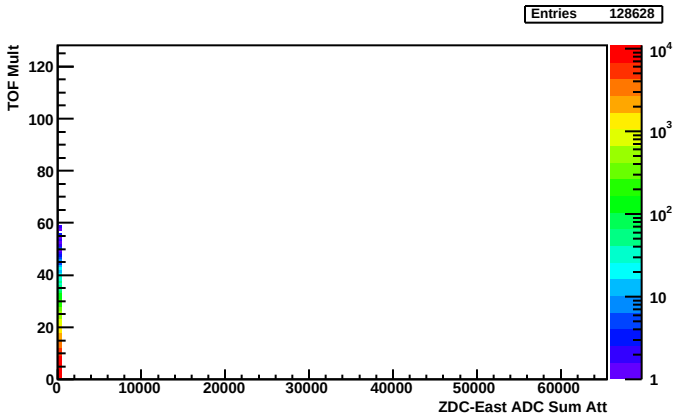

Entries 128628
TOF Mult
BBC-L-West ADC Sum

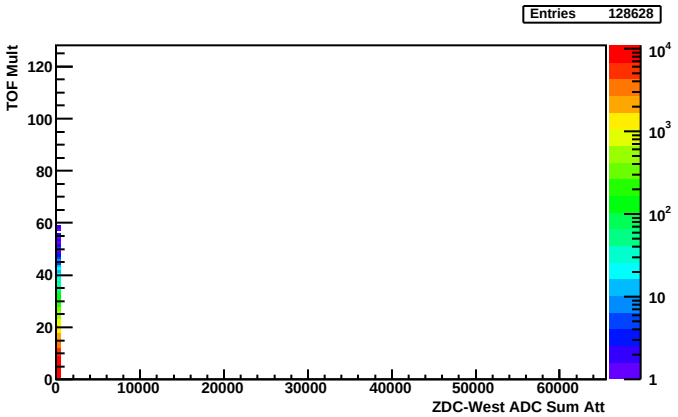

Entries 128628
TOF Mult
BBC-S-East ADC Sum

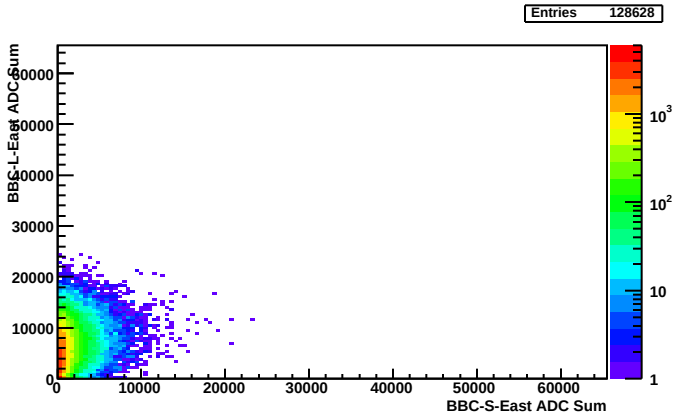

Entries 128628
TOF Mult
BBC-S-West ADC Sum

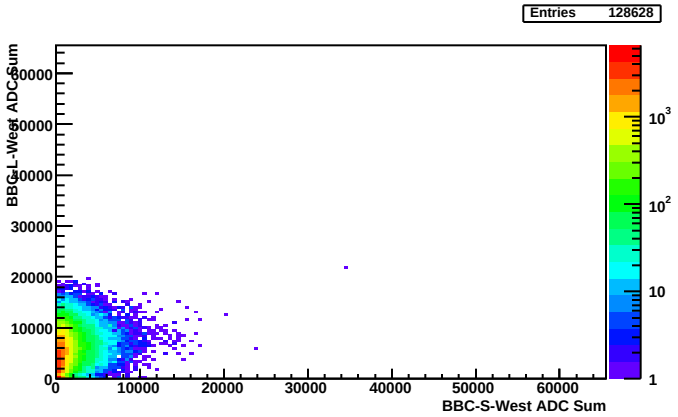

Entries 128628
TOF Mult
VPD-East ADC Sum

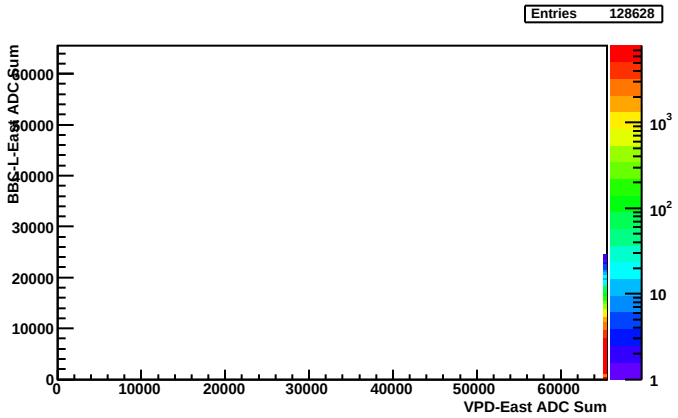

Entries 128628
TOF Mult
VPD-West ADC Sum

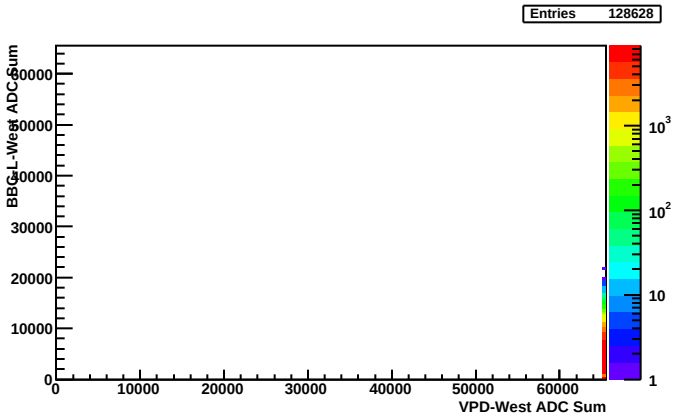

Entries 128628
TOF Mult
ZDC-East ADC Sum Att

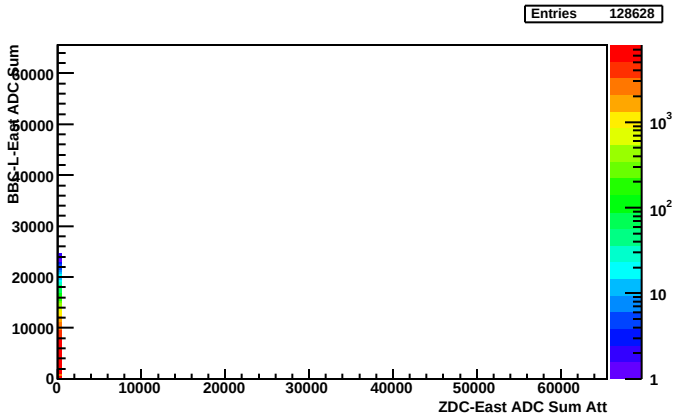

Entries 128628
TOF Mult
ZDC-West ADC Sum Att


Entries 128628
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 128628
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 128628
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 128628
BBC-L-West ADC Sum
VPD-West ADC Sum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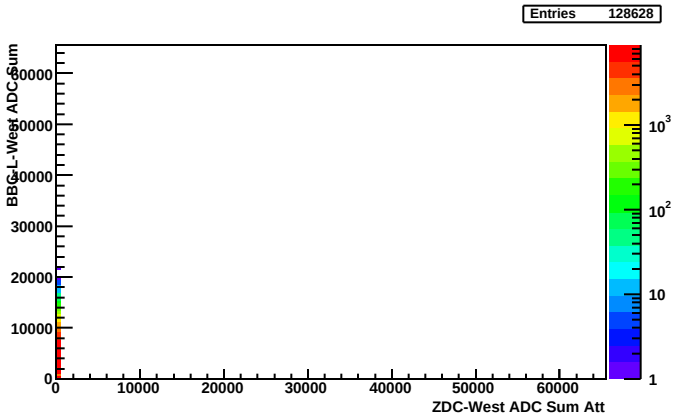
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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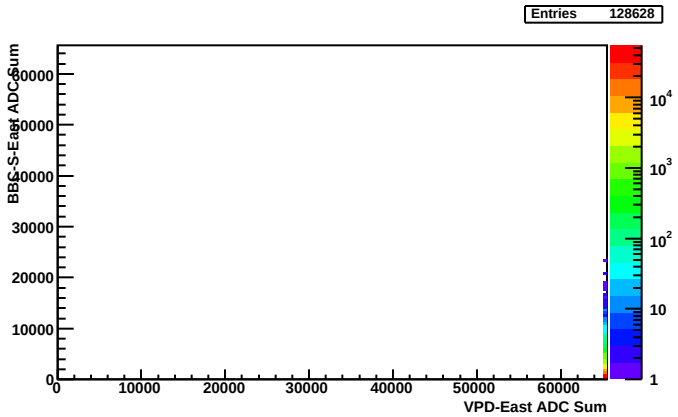
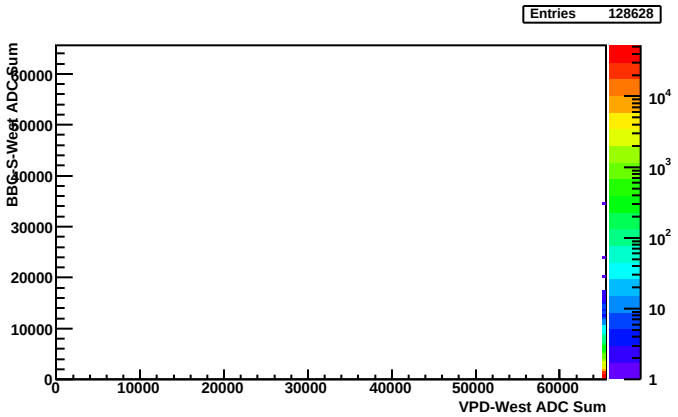
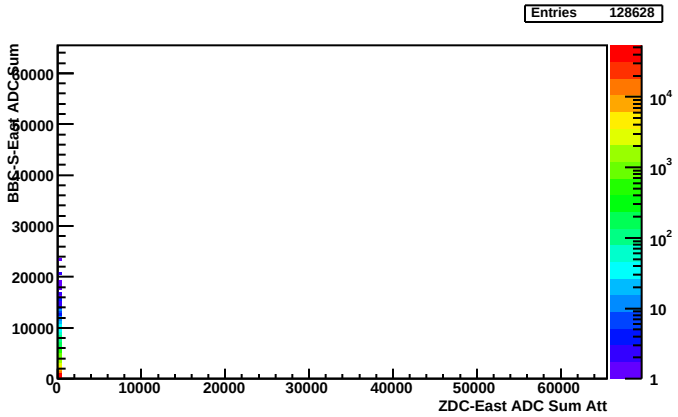
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 128628
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 128628
BBC-S-West ADC Sum
VPD-West ADC Sum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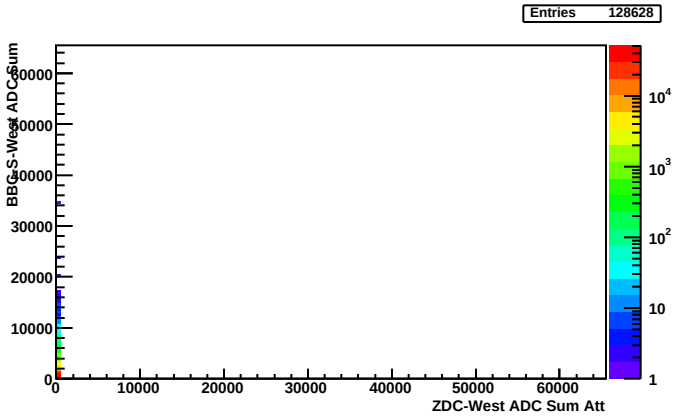
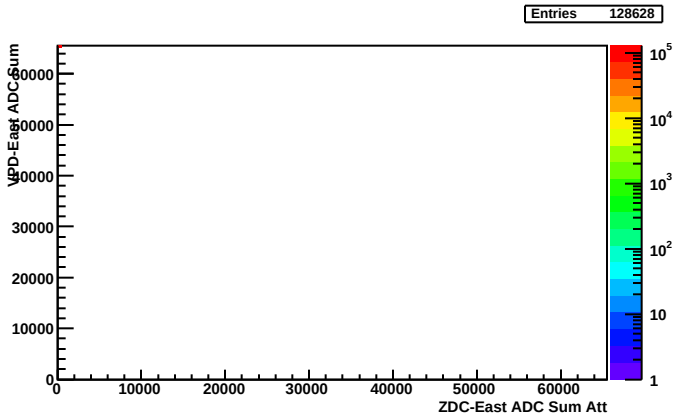
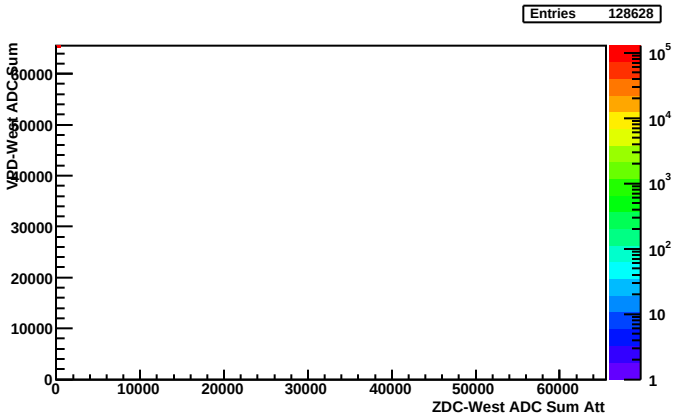
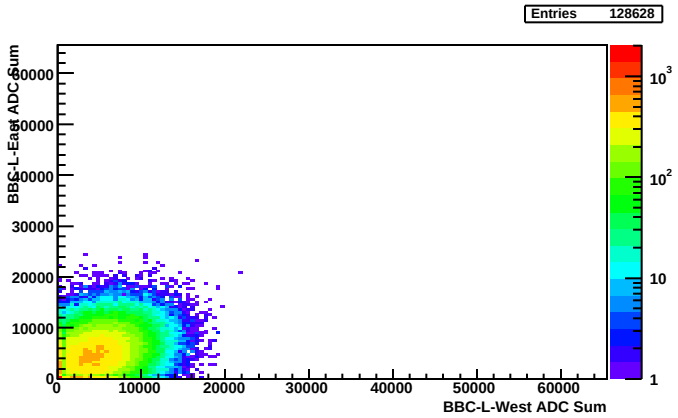
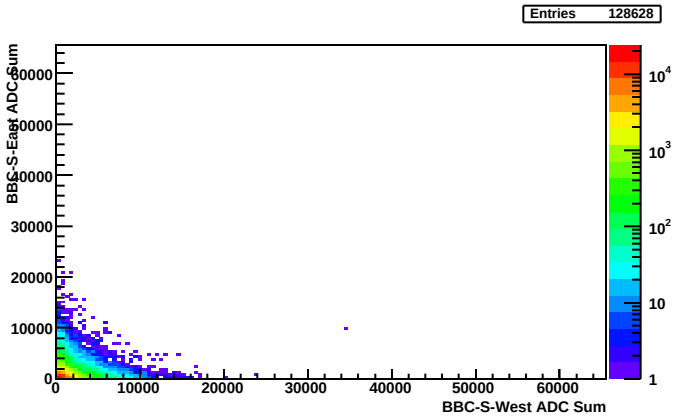
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

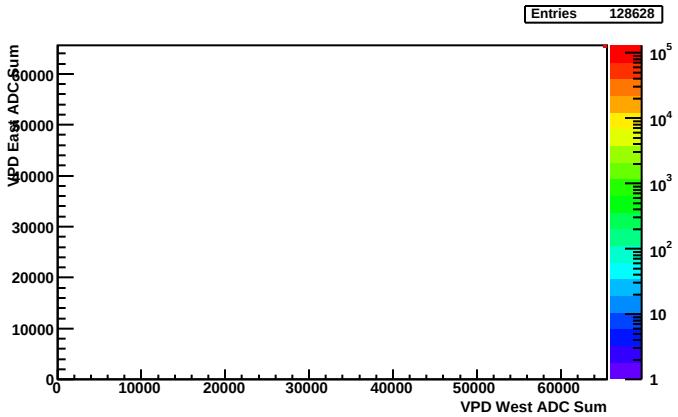

Entries 128628
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

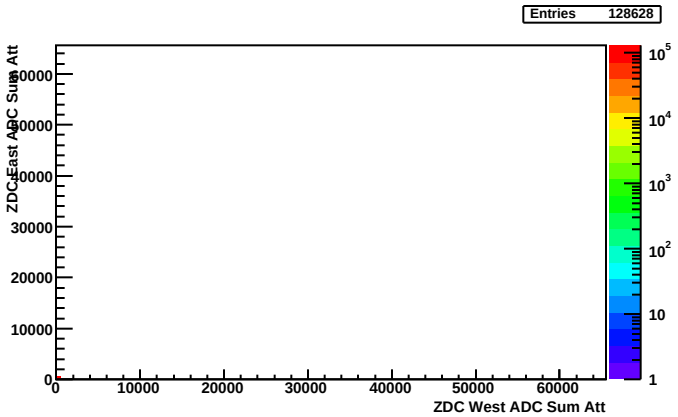

Entries 128628
VPD-East ADC Sum
ZDC-East ADC Sum Att

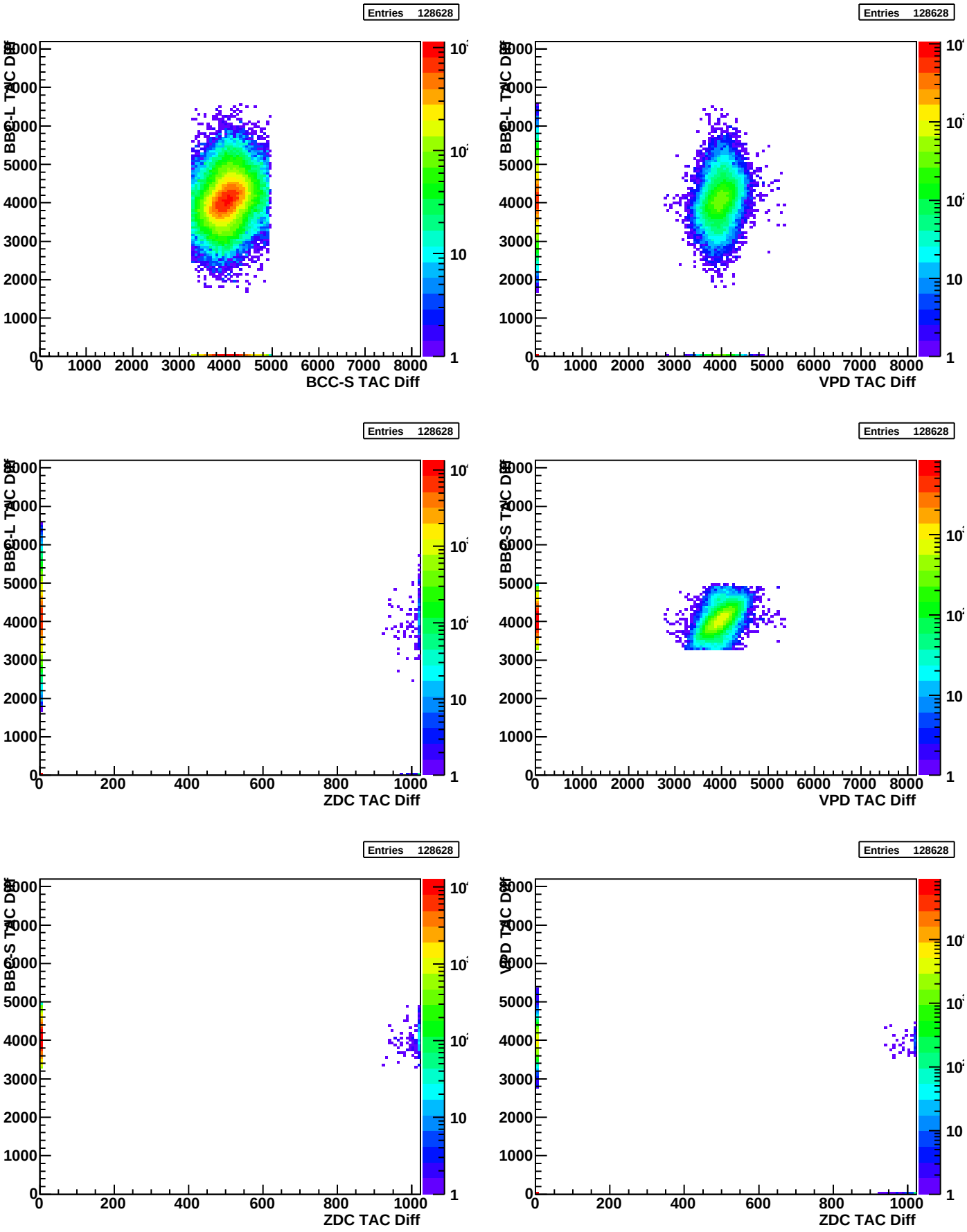

Entries 128628
VPD-West ADC Sum
ZDC-West ADC Sum Att

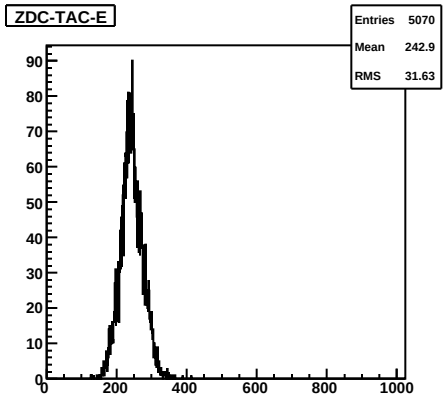

Entries 128628
BBC-L-East ADC Sum
BBC-L-West ADC Sum

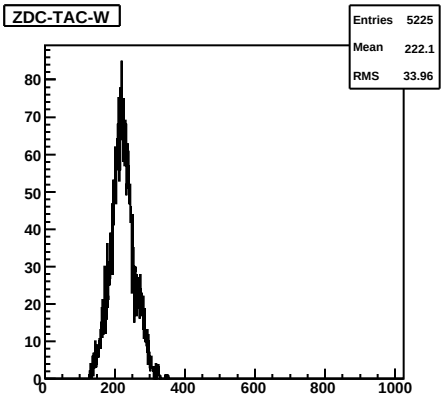

Entries 128628
BBC-S-East ADC Sum
BBC-S-West ADC Sum

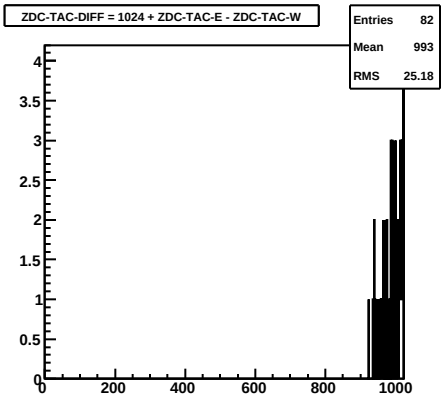

Entries 128628
VPD East ADC Sum
VPD West ADC Sum

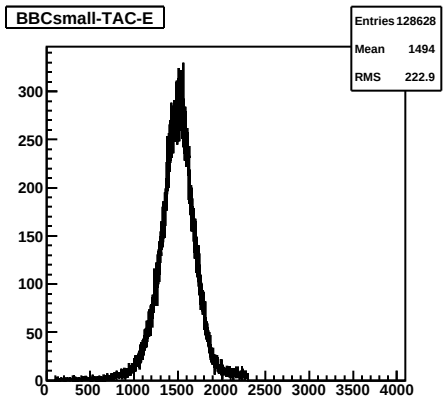

Entries 128628
ZDC East ADC Sum Att
ZDC West ADC Sum Att

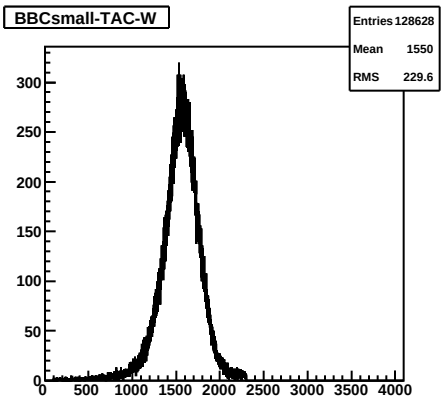

Entries 128628
BBC-L TAC Diff
BCC-S TAC Diff
Entries 128628
BBC-L TAC Diff
VPD TAC Diff
Entries 128628
BBC-L TAC Diff
ZDC TAC Diff
Entries 128628
BBC-S TAC Diff
VPD TAC Diff
Entries 128628
BBC-S TAC Diff
ZDC TAC Diff
Entries 128628
VPD TAC Diff
ZDC TAC Diff

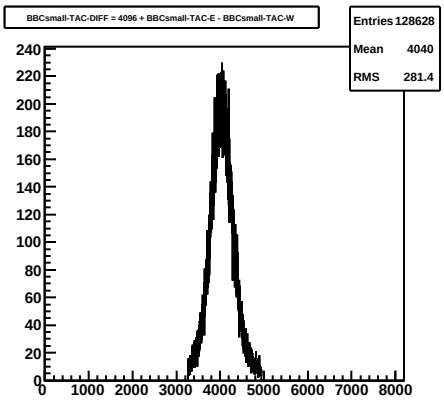

ZDC-TAC-E
Entries 5070
Mean 242.9
RMS 31.63

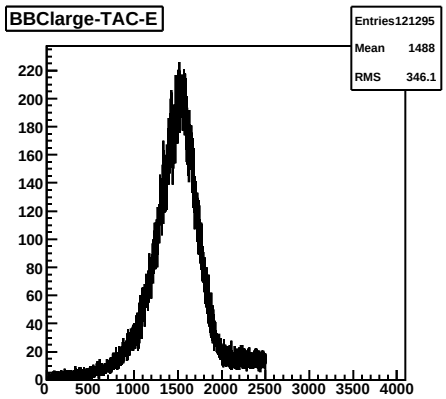

ZDC-TAC-W
Entries 5225
Mean 222.1
RMS 33.96

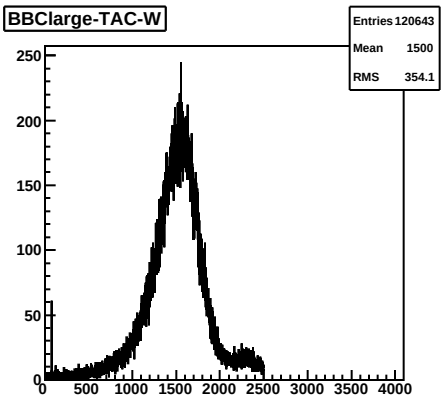

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 82
Mean 993
RMS 25.18

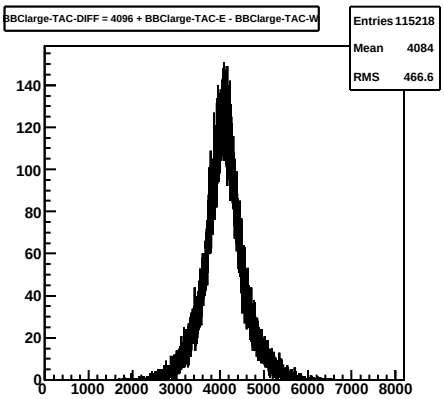

BBCsmall-TAC-E
Entries 128628
Mean 1494
RMS 222.9

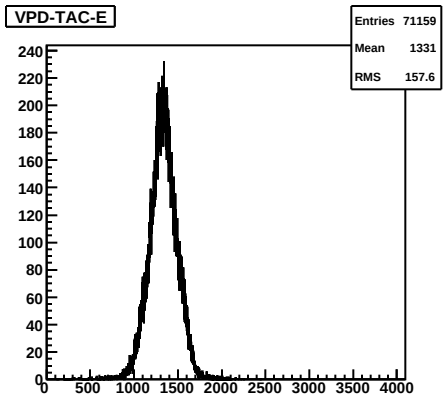

BBCsmall-TAC-W
Entries 128628
Mean 1550
RMS 229.6

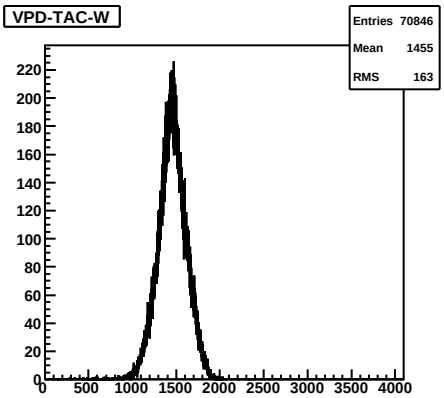

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 128628
Mean 4040
RMS 281.4

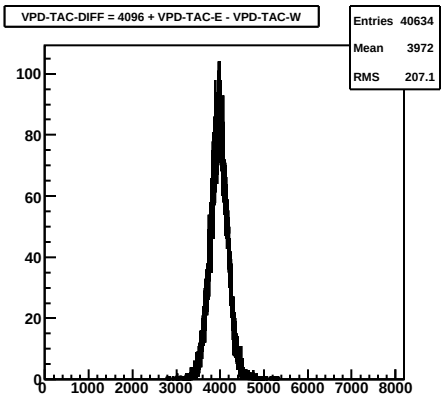

BBClarge-TAC-E
Entries 121295
Mean 1488
RMS 346.1

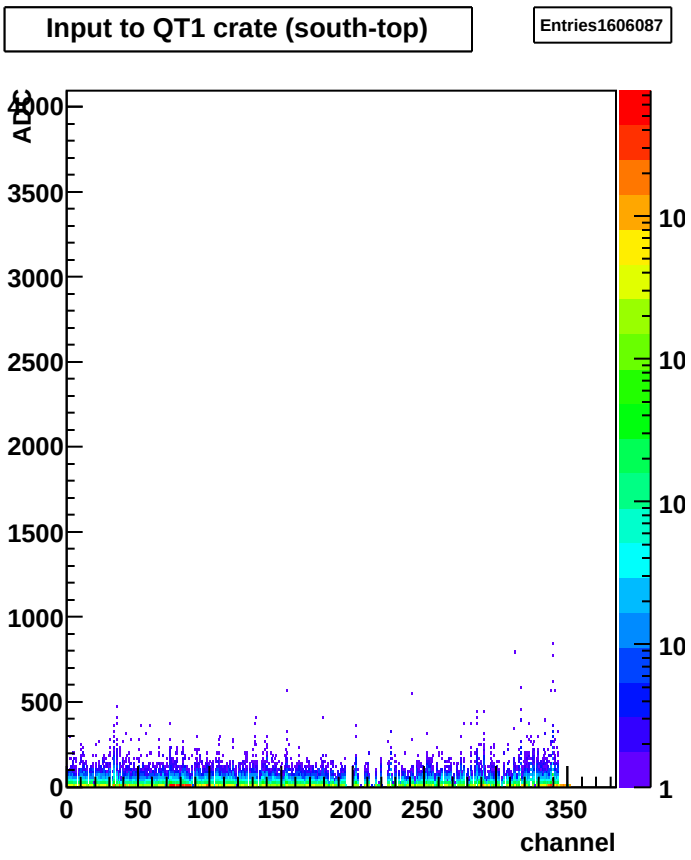

BBClarge-TAC-W
Entries 120643
Mean 1500
RMS 354.1

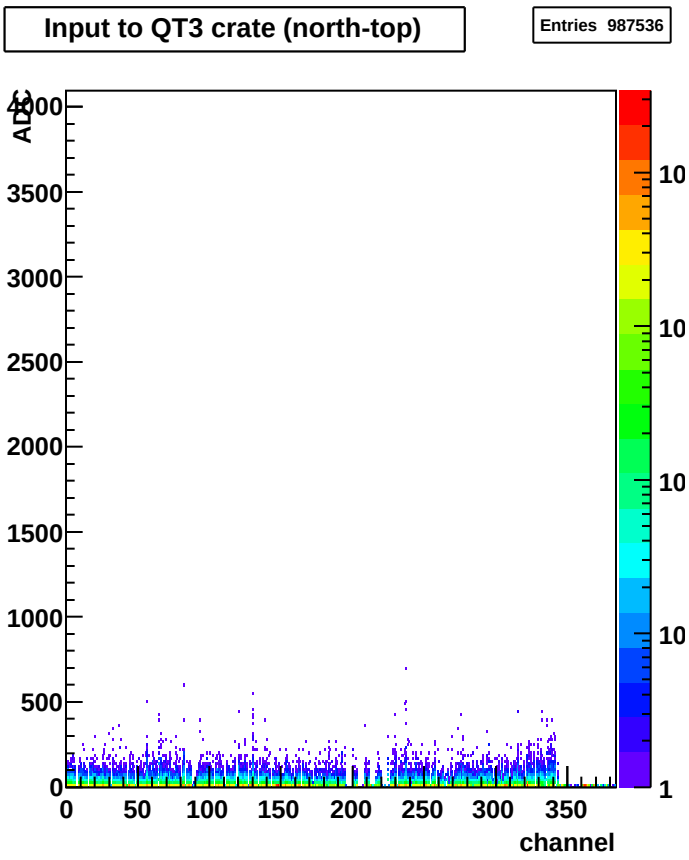

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 115218
Mean 4084
RMS 466.6

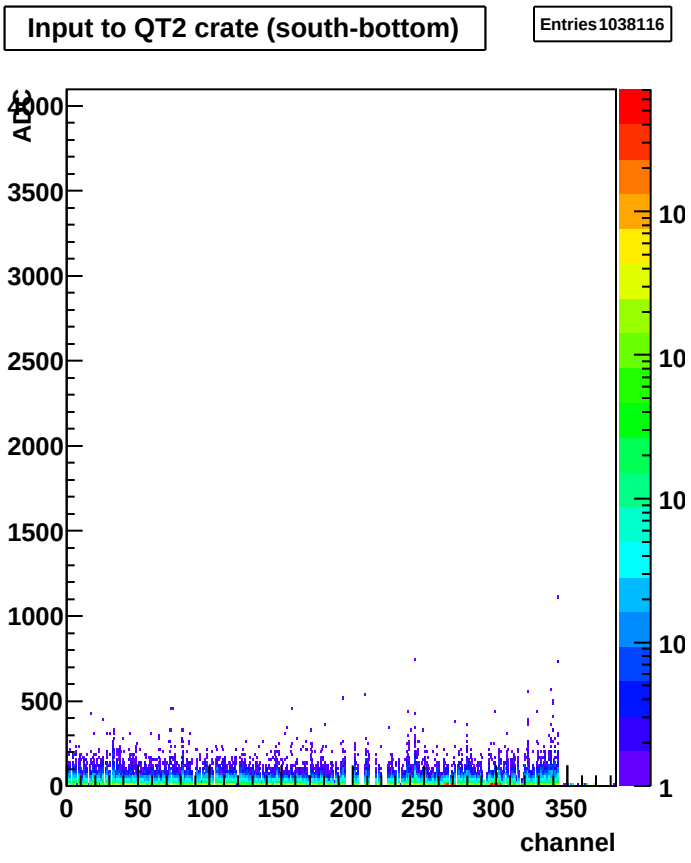

VPD-TAC-E
Entries 71159
Mean 1331
RMS 157.6

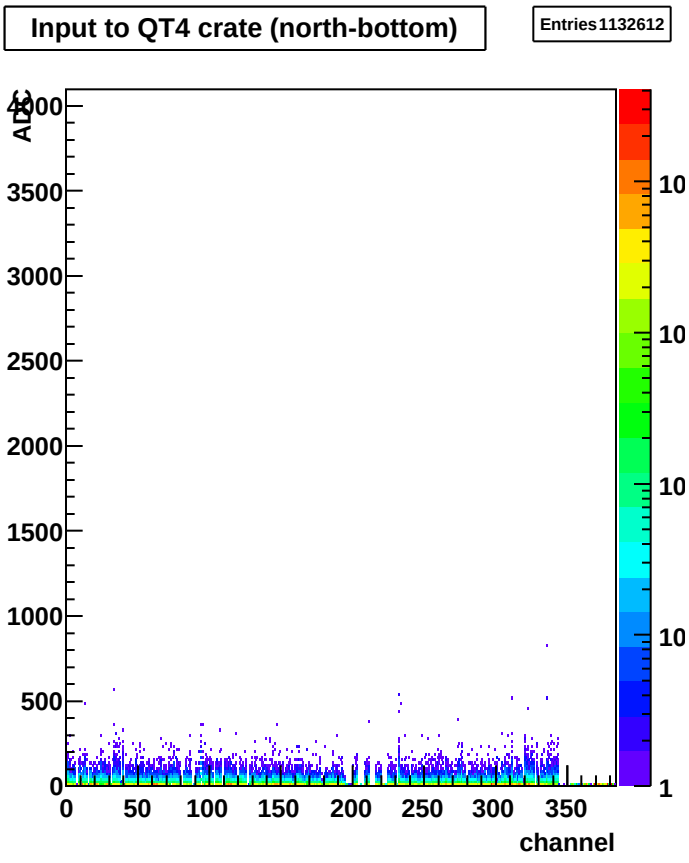

VPD-TAC-W
Entries 70846
Mean 1455
RMS 163

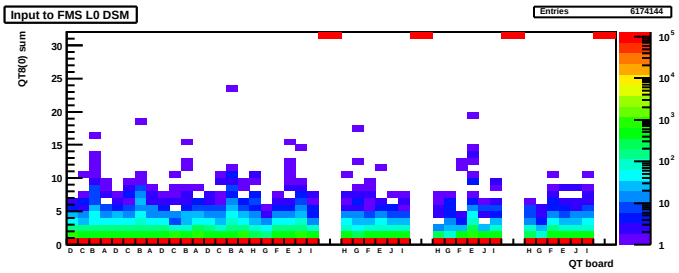

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 40634
Mean 3972
RMS 207.1

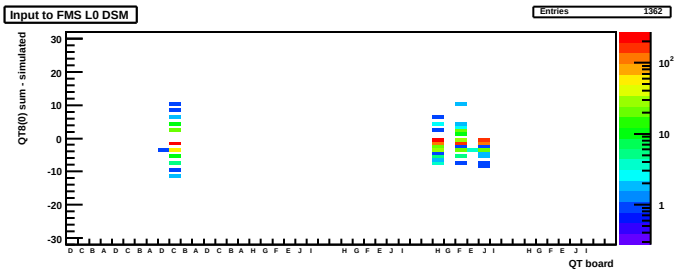

Input to QT1 crate (south-top)
Entries1606087
ADC
channel

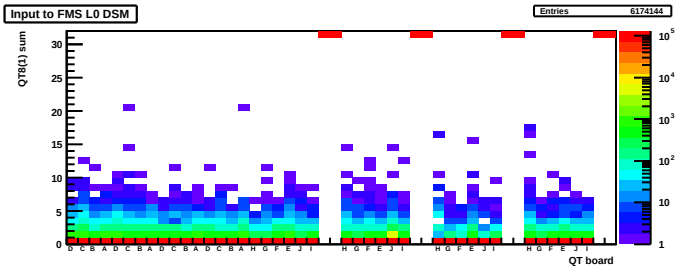

Input to QT3 crate (north-top)
Entries 987536
ADC
channel

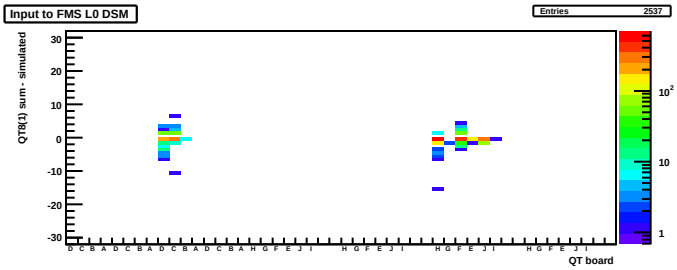

Input to QT2 crate (south-bottom)
Entries1038116
ADC
channel

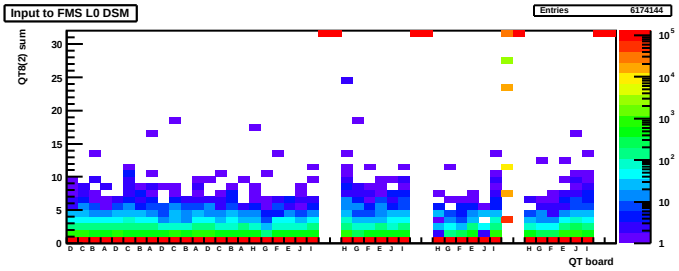

Input to QT4 crate (north-bottom)
Entries1132612
ADC
channel

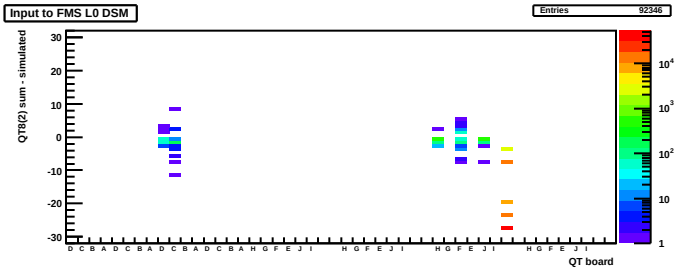

Input to FMS L0 DSM
Entries 6174144
QT8(0) sum
QT board

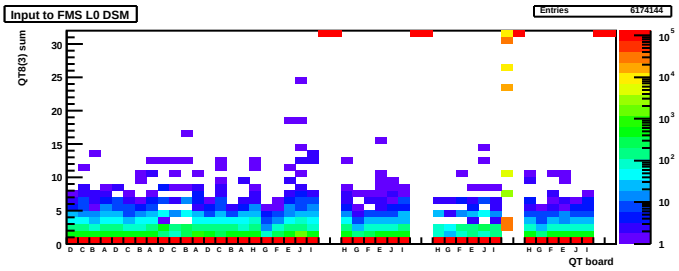

Input to FMS L0 DSM
Entries 1382
QT8(0) sum - simulated
QT board

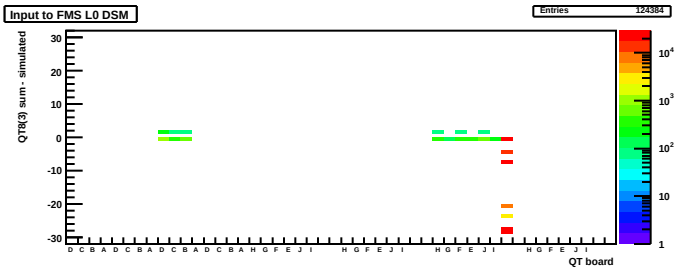

Input to FMS L0 DSM
Entries 6174144
QT8(1) sum
QT board

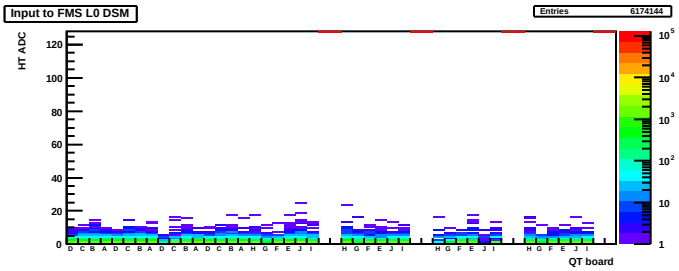

Input to FMS L0 DSM
Entries 2537
QT8(1) sum - simulated
QT board

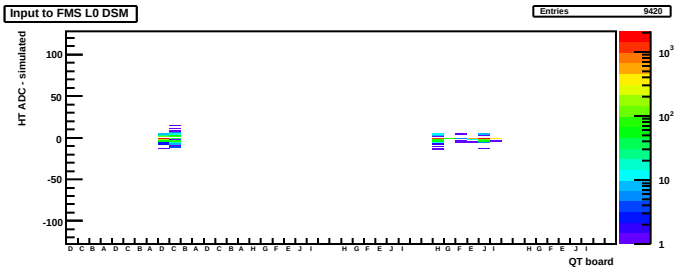

Input to FMS L0 DSM
Entries 6174144
QT8(2) sum
QT board

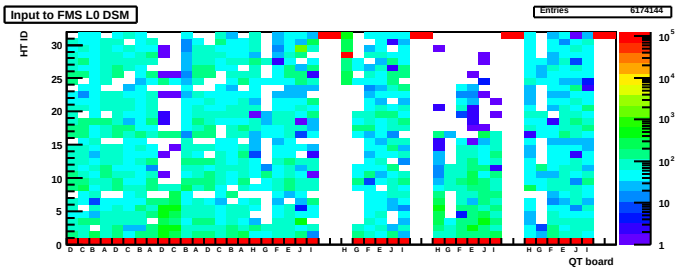

Input to FMS L0 DSM
Entries 92346
QT8(2) sum - simulated
QT board

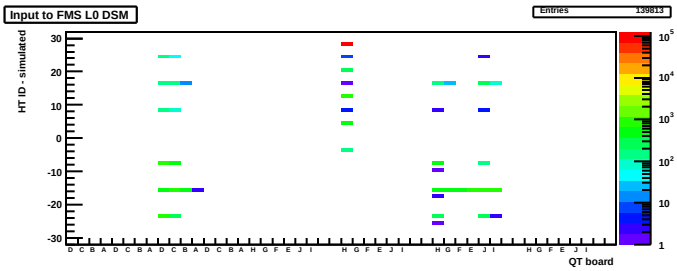

Input to FMS L0 DSM
Entries 6174144
QT8(3) sum
QT board

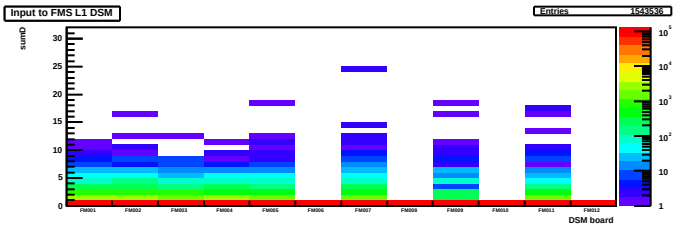

Input to FMS L0 DSM
Entries 124384
QT8(3) sum - simulated
QT board

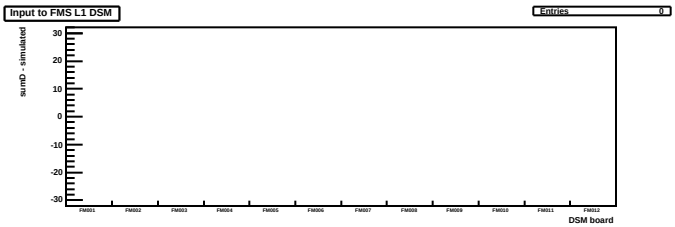

Input to FMS L0 DSM
Entries 6174144
HT ADC
QT board

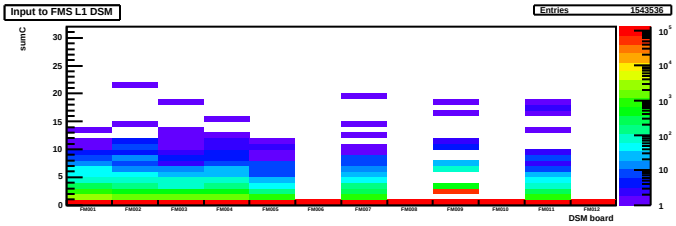

Input to FMS L0 DSM
Entries 9420
HT ADC - simulated
QT board

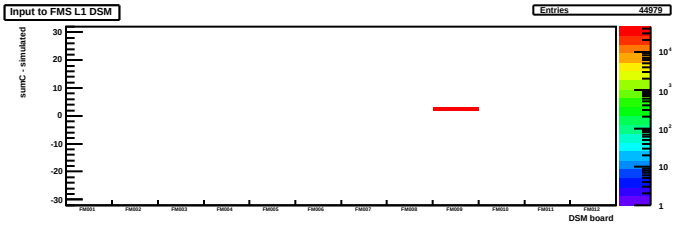

Input to FMS L0 DSM
Entries 6174144
HT ID
QT board

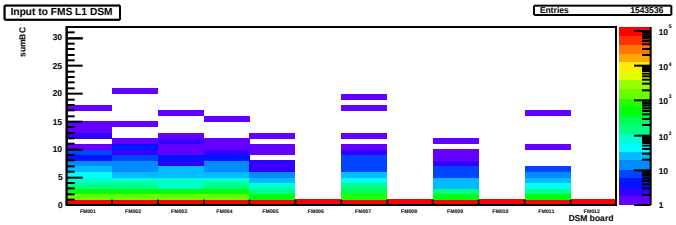

Input to FMS L0 DSM
Entries 139813
HT ID - simulated
QT board

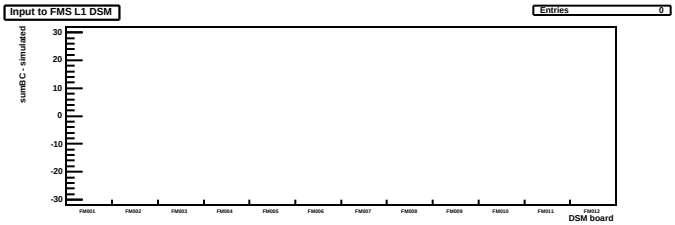

Input to FMS L1 DSM
Entries 1543536
sumD
DSM board

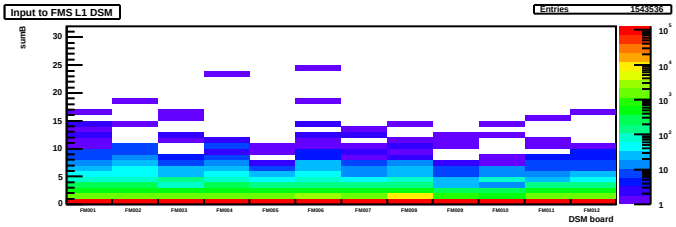

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

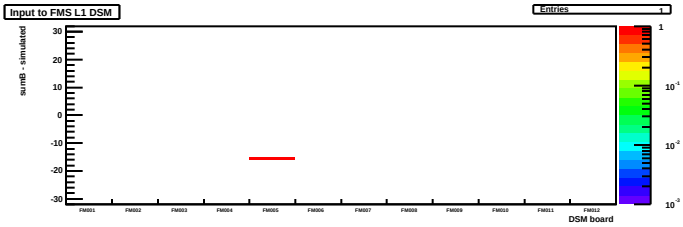

Input to FMS L1 DSM
Entries 1543536
sumC
DSM board

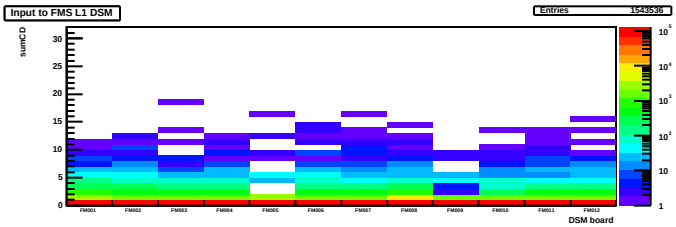

Input to FMS L1 DSM
Entries 44979
sumC - simulated
DSM board

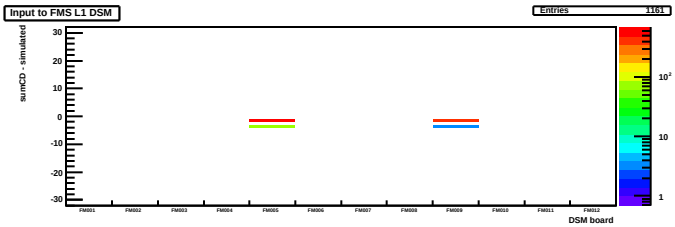

Input to FMS L1 DSM
Entries 1543536
sumBC
DSM board

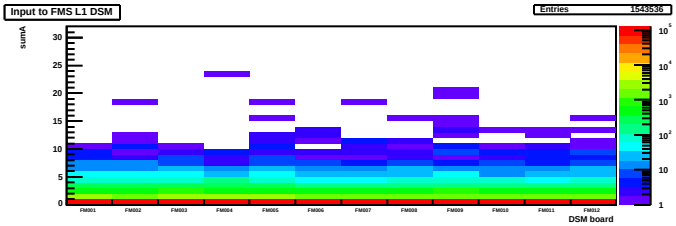

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

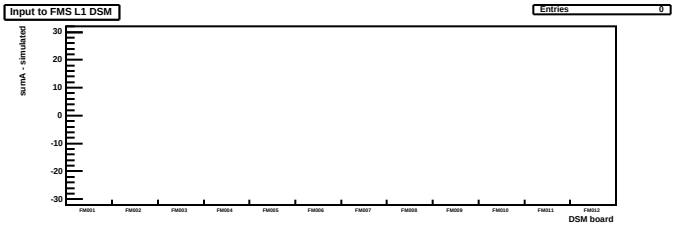

Input to FMS L1 DSM
Entries 1543536
sumB
DSM board

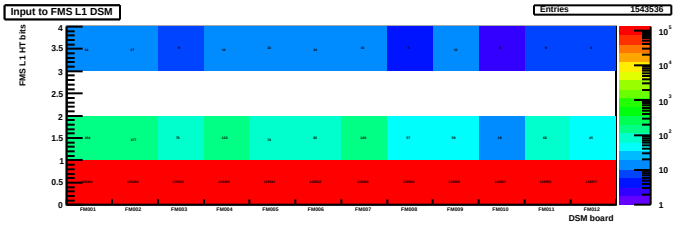

Input to FMS L1 DSM
Entries 1
sumB - simulated
DSM board

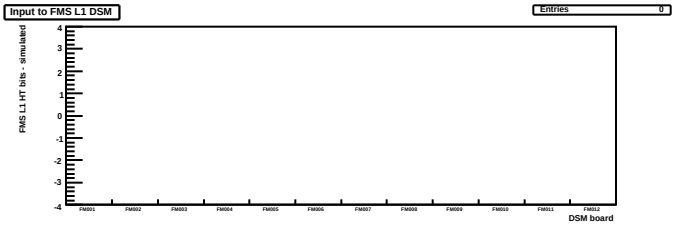

Input to FMS L1 DSM
Entries 1543536
sumCD
DSM board

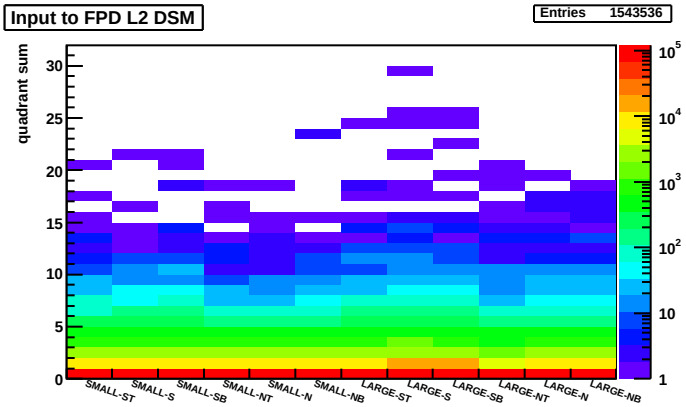

Input to FMS L1 DSM
Entries 1161
sumCD - simulated
DSM board

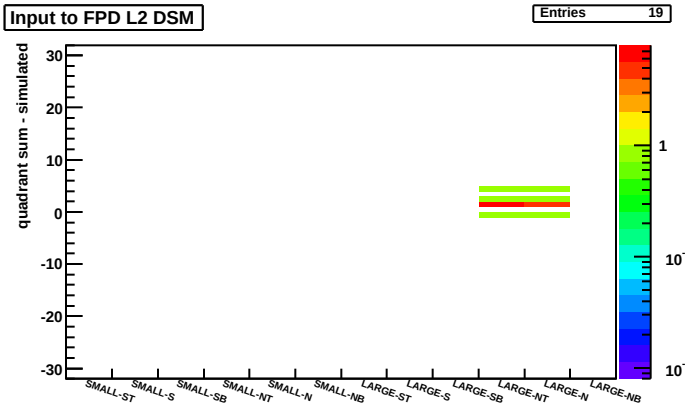

Input to FMS L1 DSM
Entries 1543536
sumA
DSM board

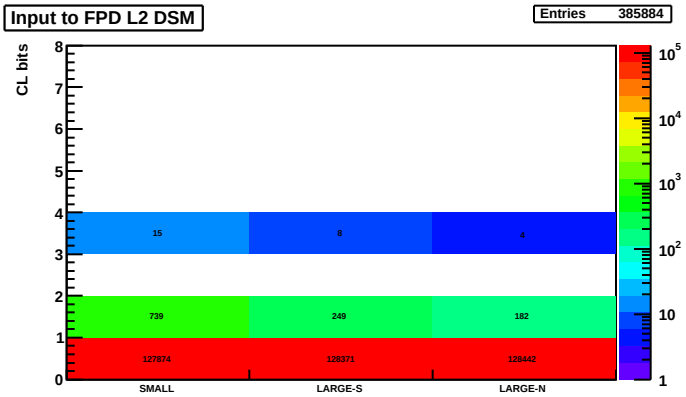

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

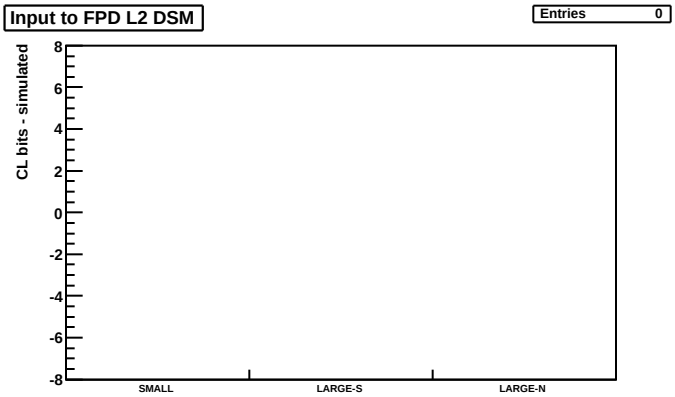

Input to FMS L1 DSM
Entries 1543536
FMS L1 HT bits
DSM board

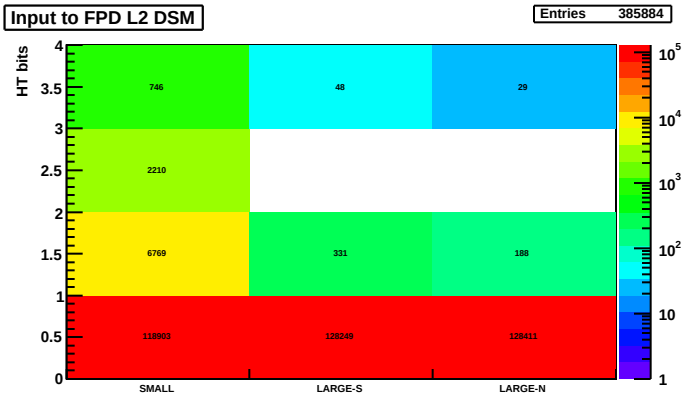

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

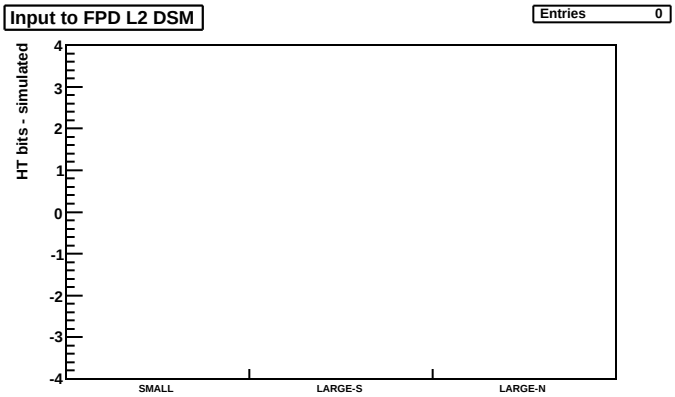

Input to FPD L2 DSM
Entries 1543536
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

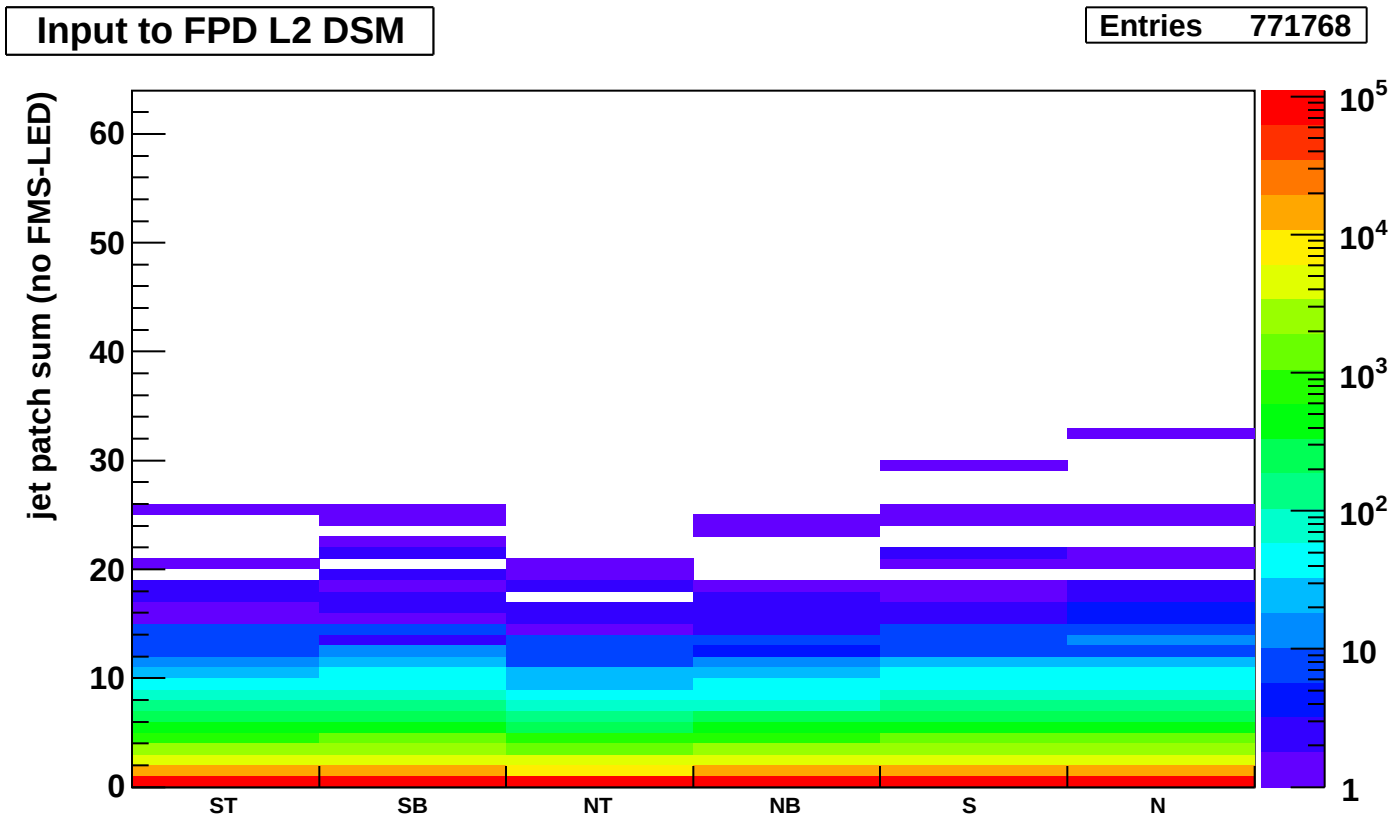

Input to FPD L2 DSM
Entries 19
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

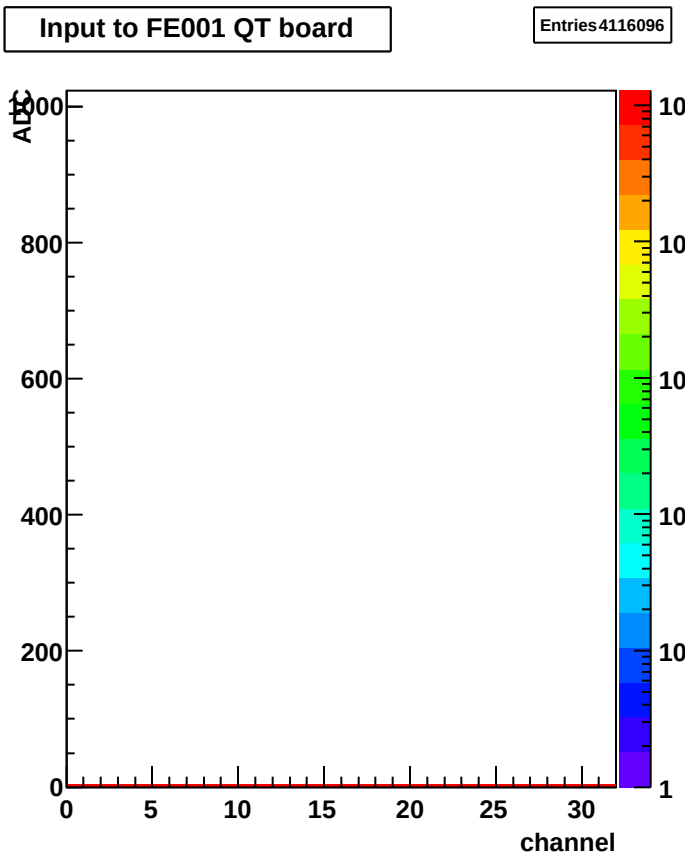

Input to FPD L2 DSM
Entries 385884
CL bits
15
8
4
739
249
182
127874
128371
128442
SMALL
LARGE-S
LARGE-N

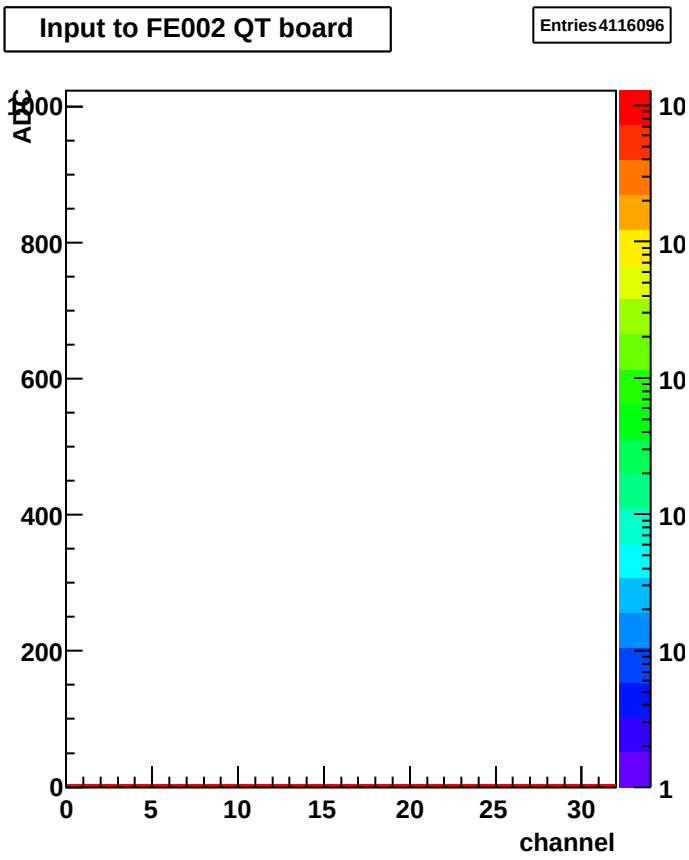

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

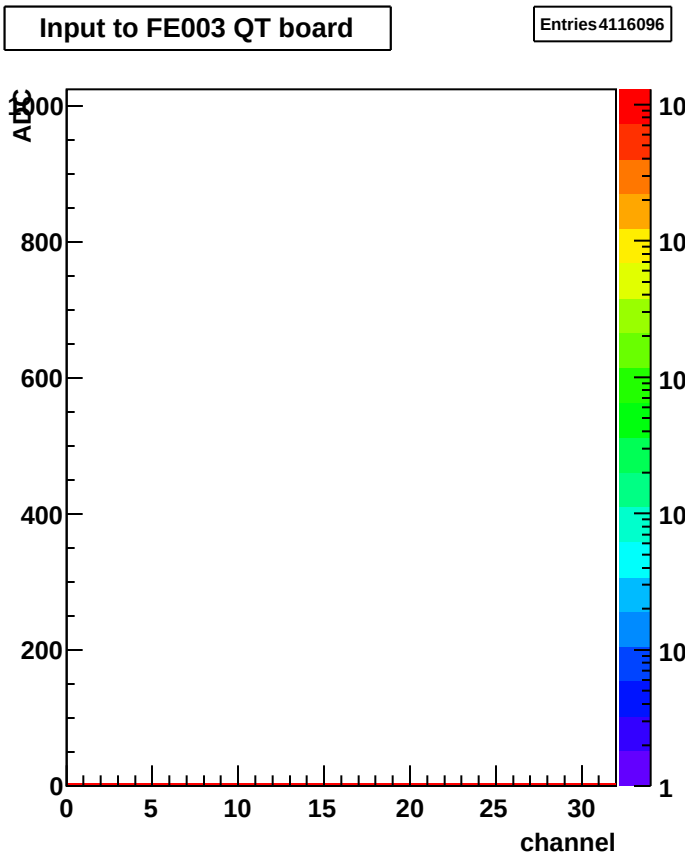

Input to FPD L2 DSM
Entries 385884
HT bits
746
48
29
2210
6769
331
188
118903
128249
128411
SMALL
LARGE-S
LARGE-N

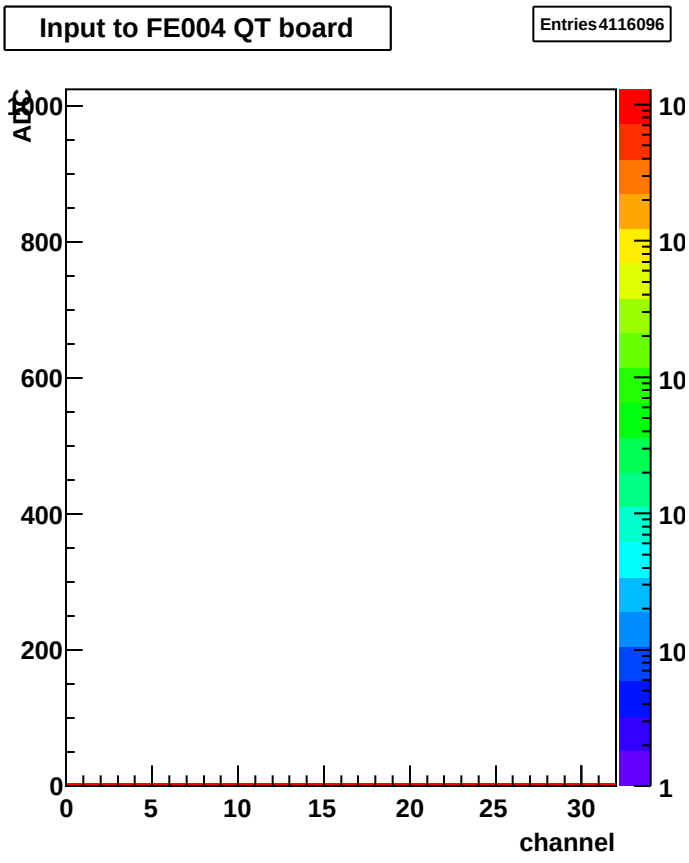

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

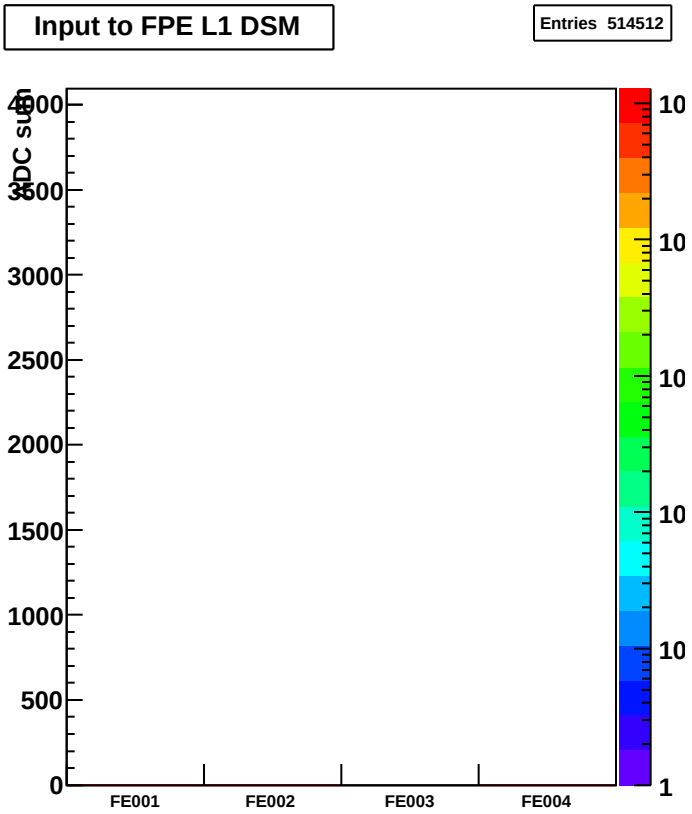

Input to FPD L2 DSM
Entries 771768
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

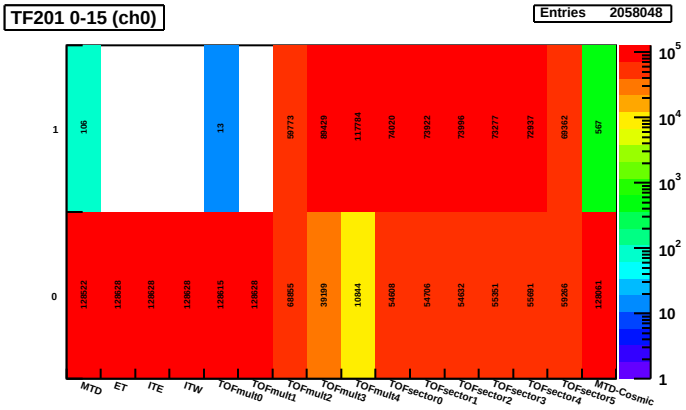

Input to FE001 QT board
Entries 4116096
ADC
channel

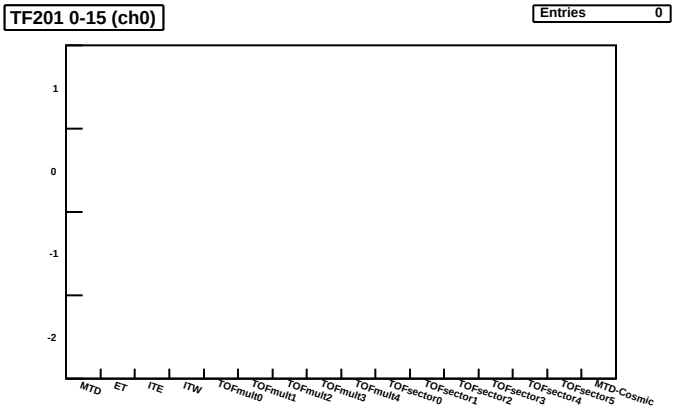

Input to FE002 QT board
Entries 4116096
ADC
channel

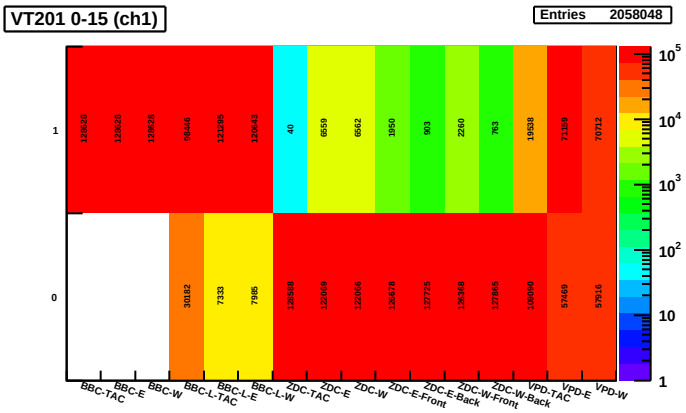

Input to FE003 QT board
Entries 4116096
ADC
channel

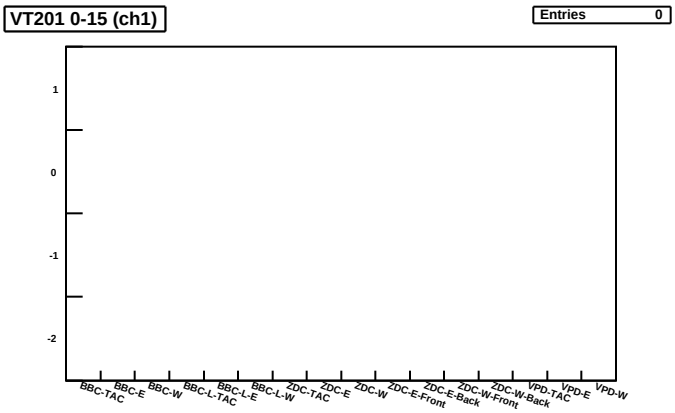

Input to FE004 QT board
Entries 4116096
ADC
channel

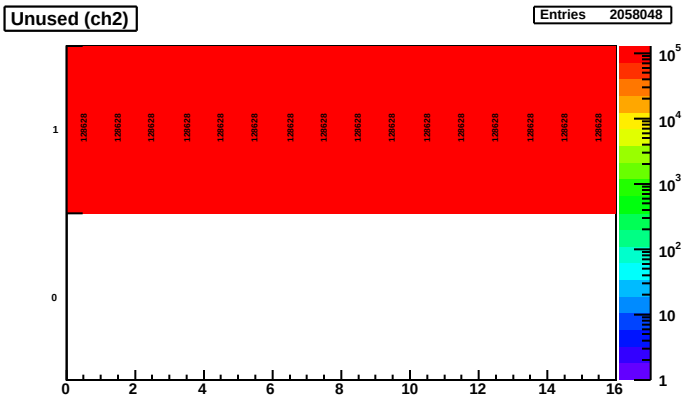

Input to FPE L1 DSM
Entries 514512
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

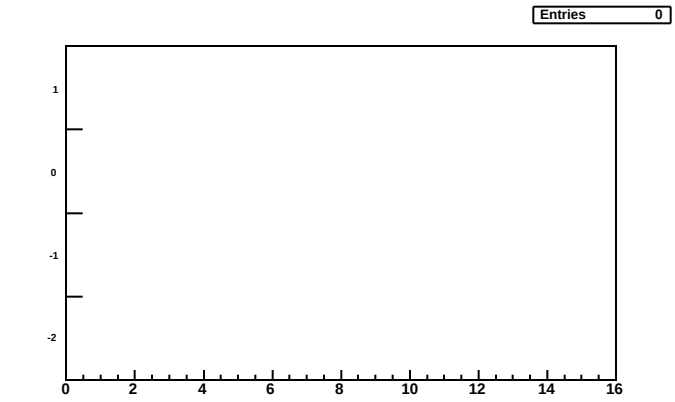

TF201 0-15 (ch0)
Entries 2058048
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

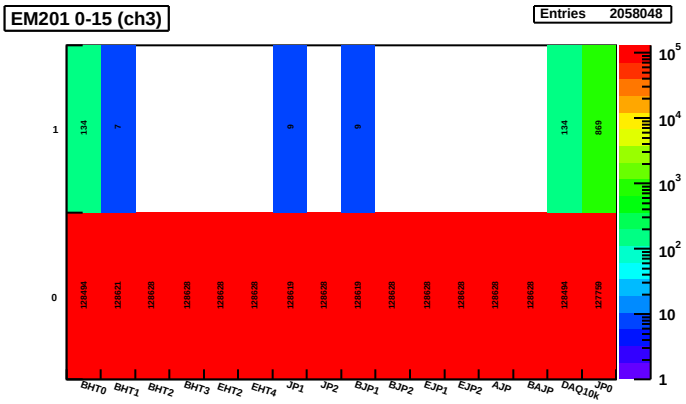

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

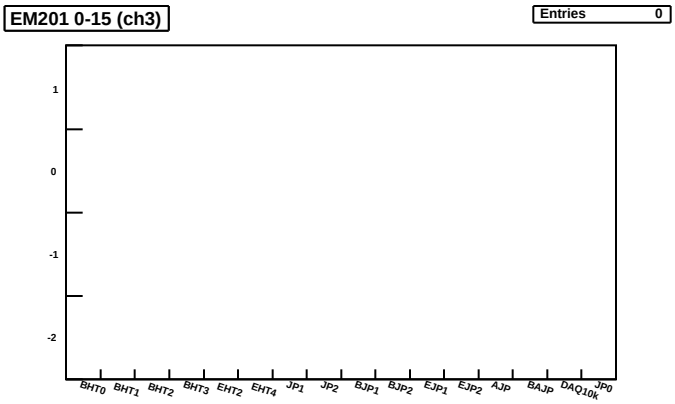

VT201 0-15 (ch1)
Entries 2058048
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 2058048


Entries 0


EM201 0-15 (ch3)
Entries 2058048
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0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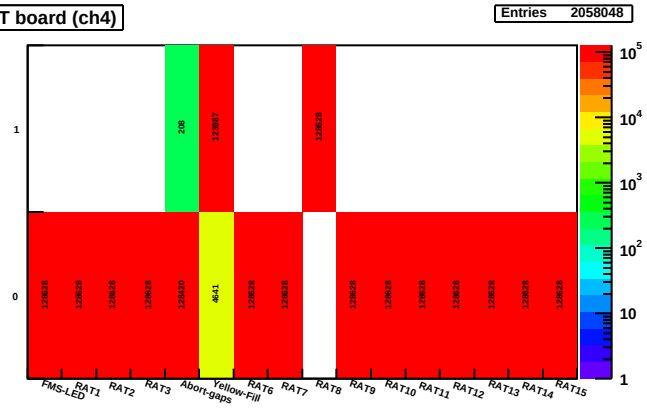

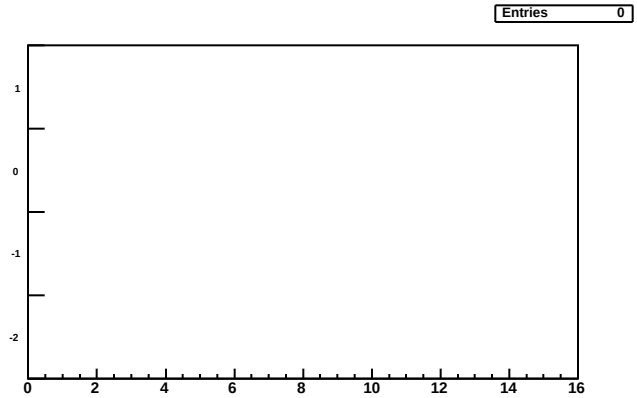

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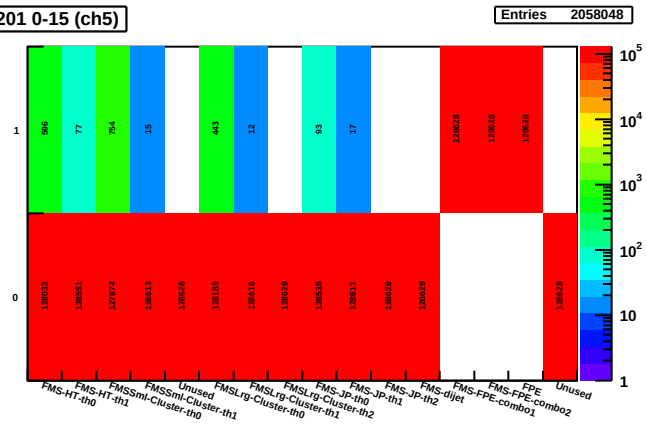

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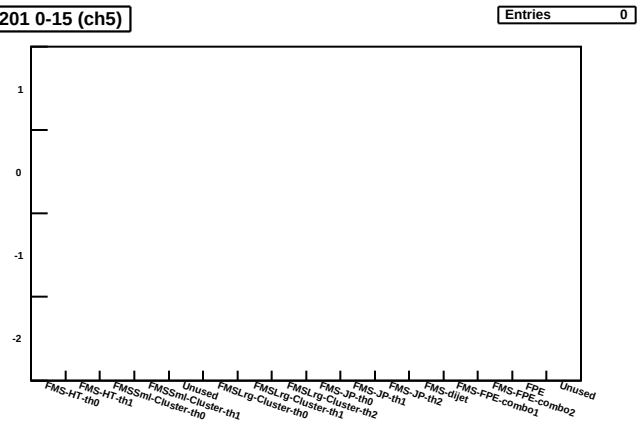

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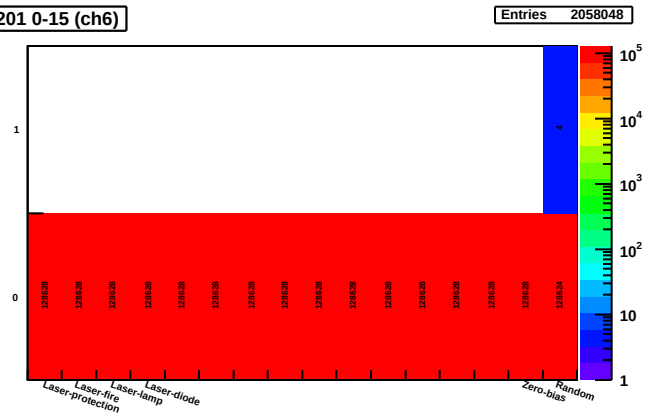

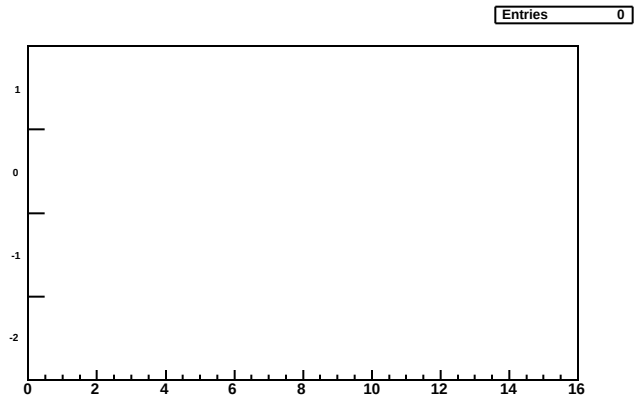
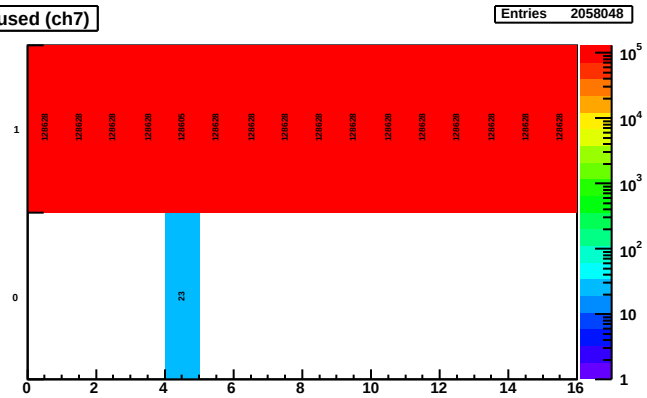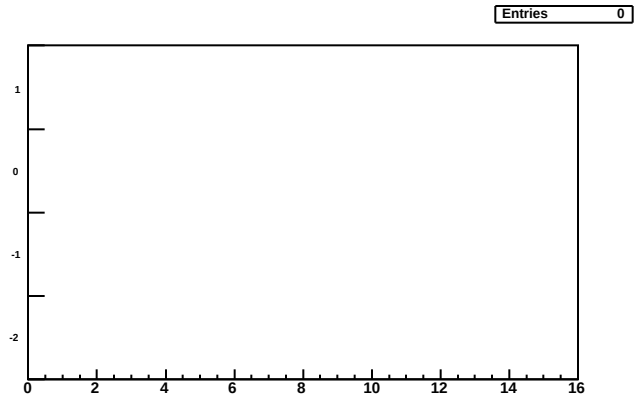
Unused (ch7)