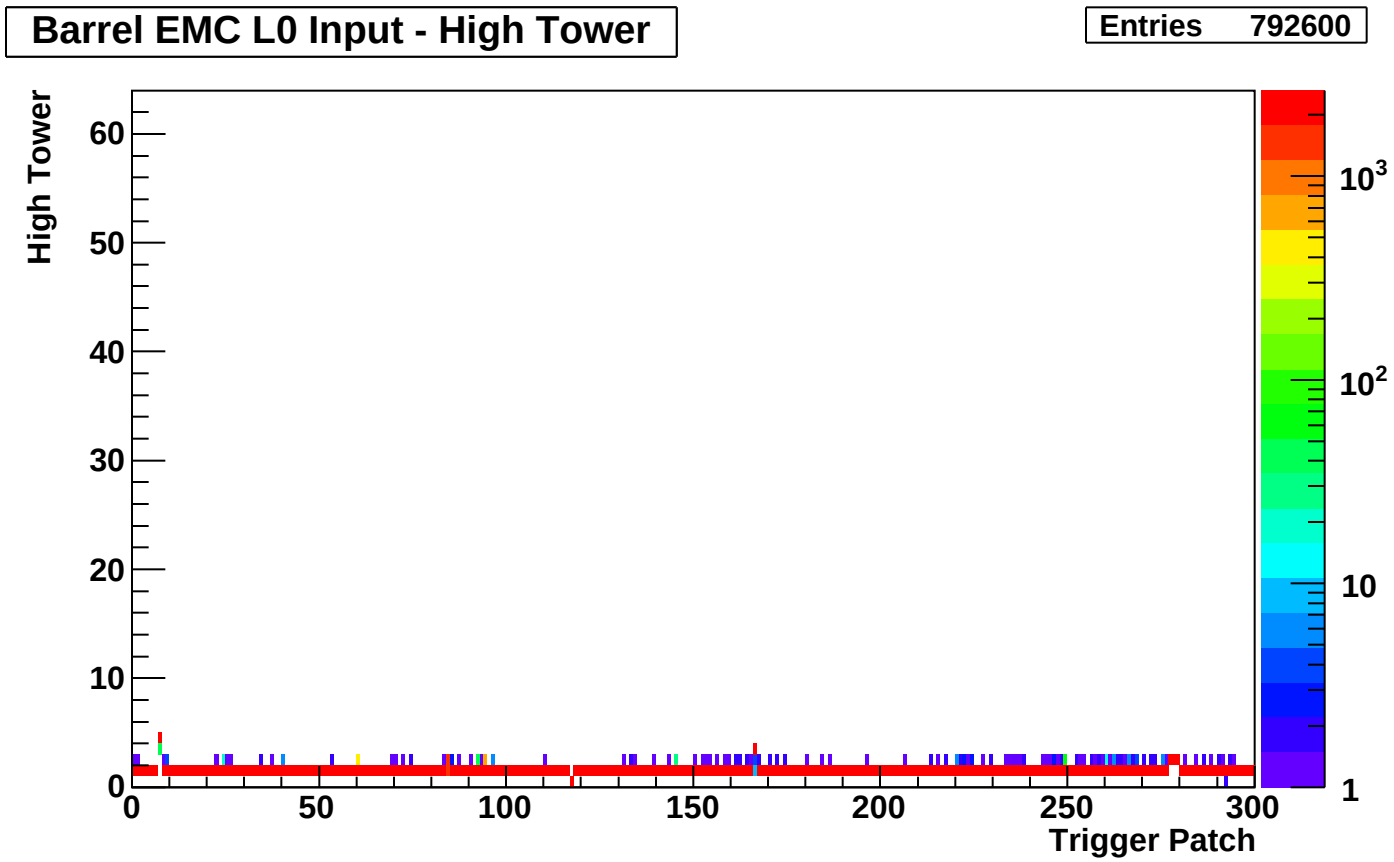
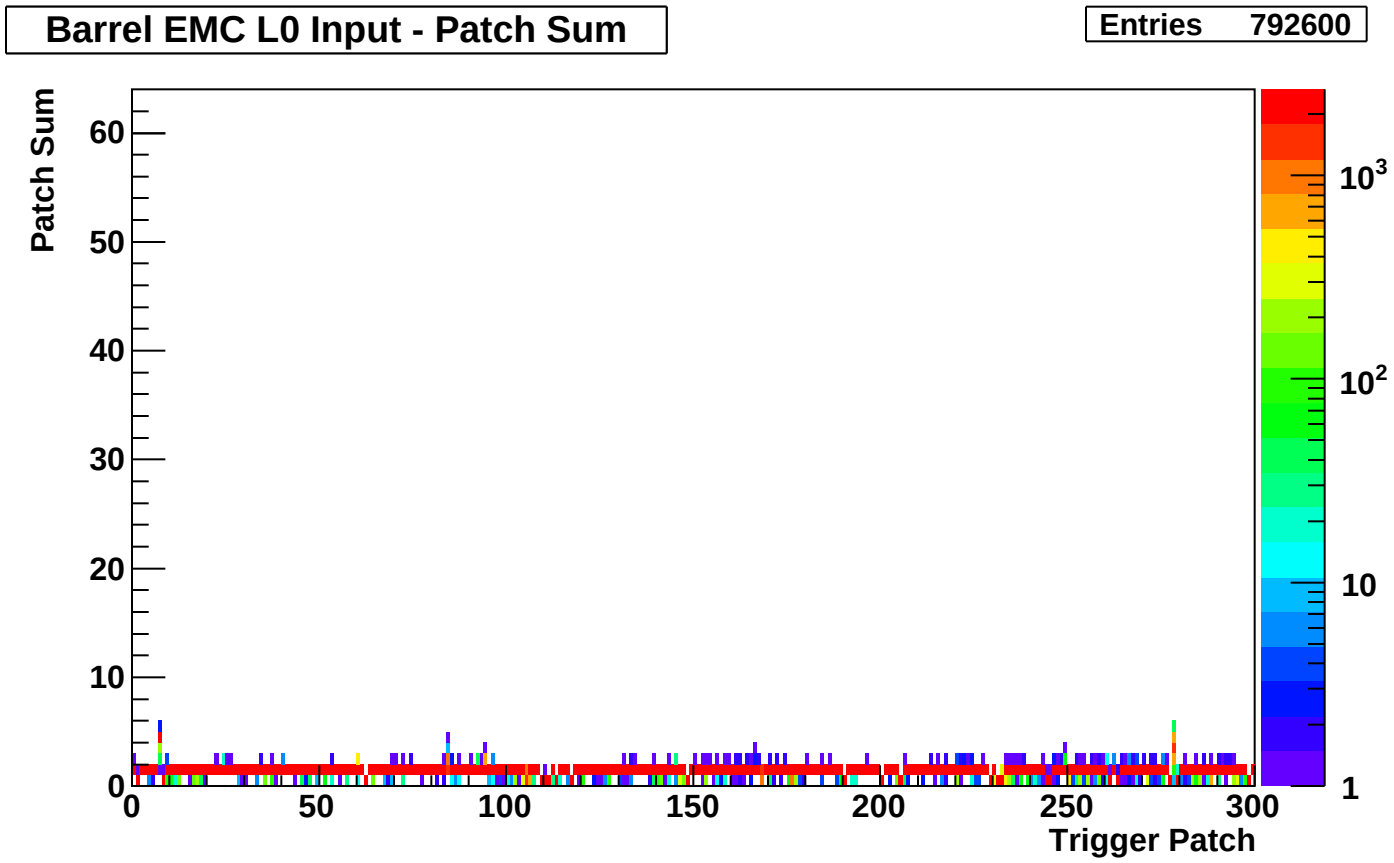
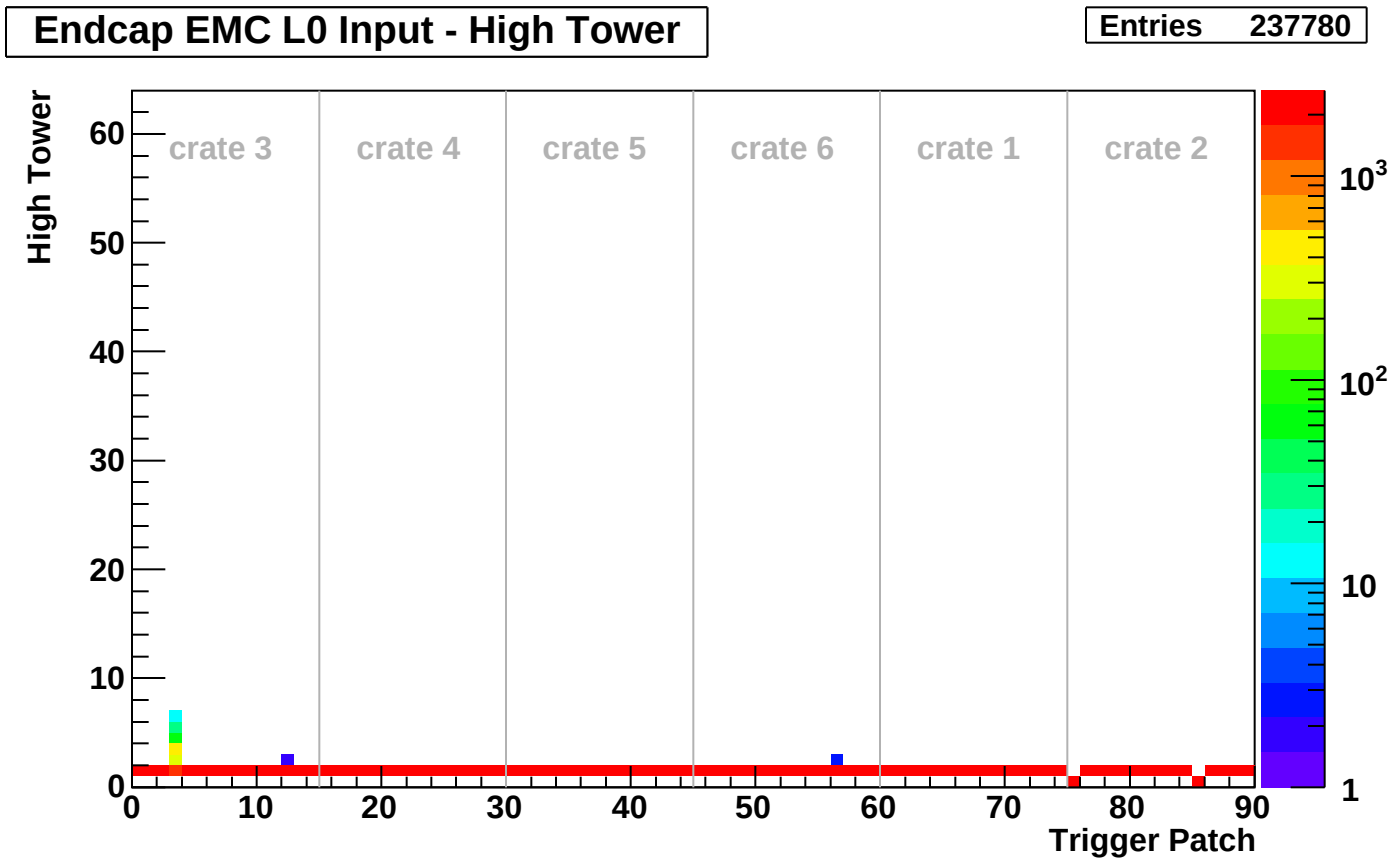
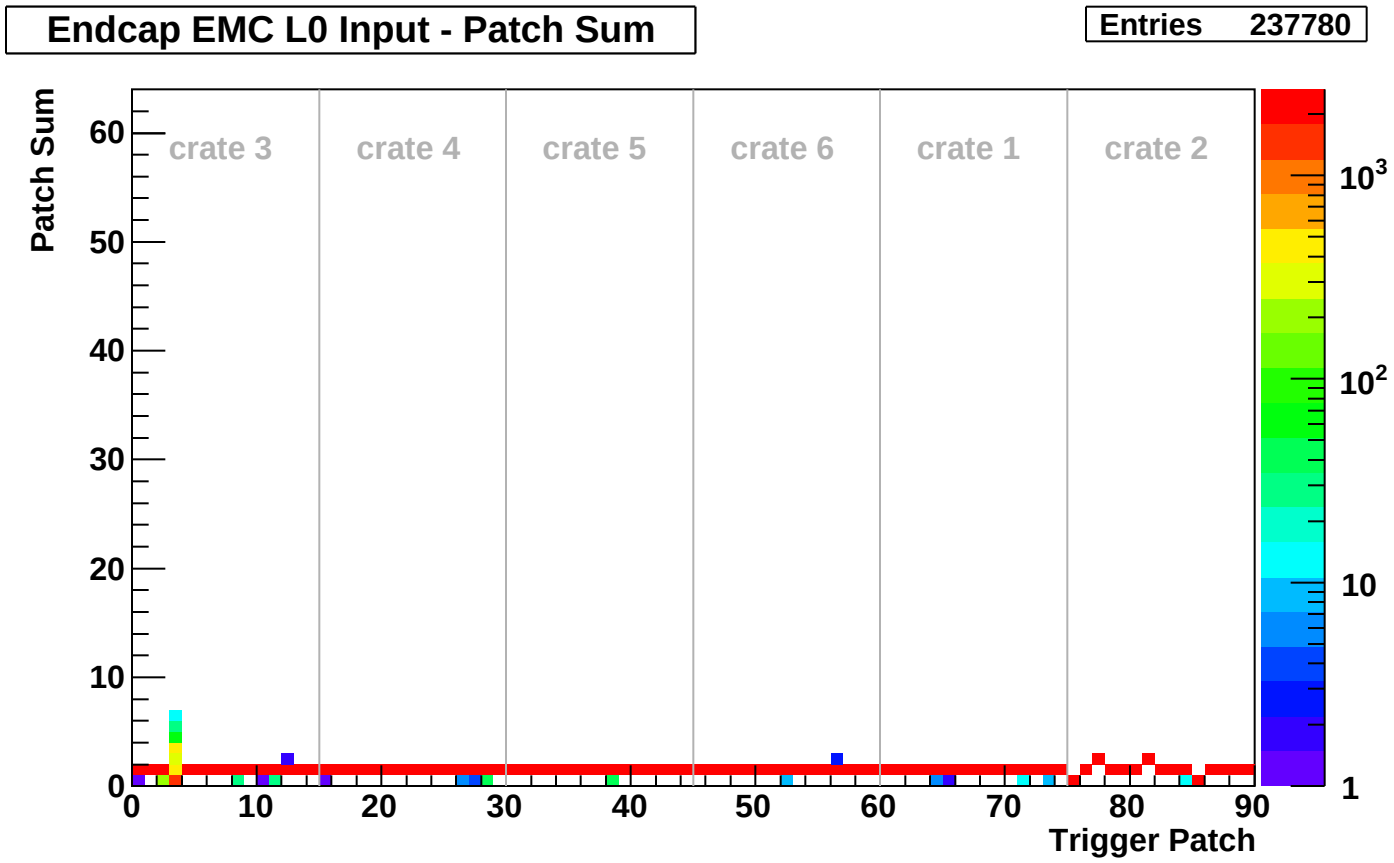
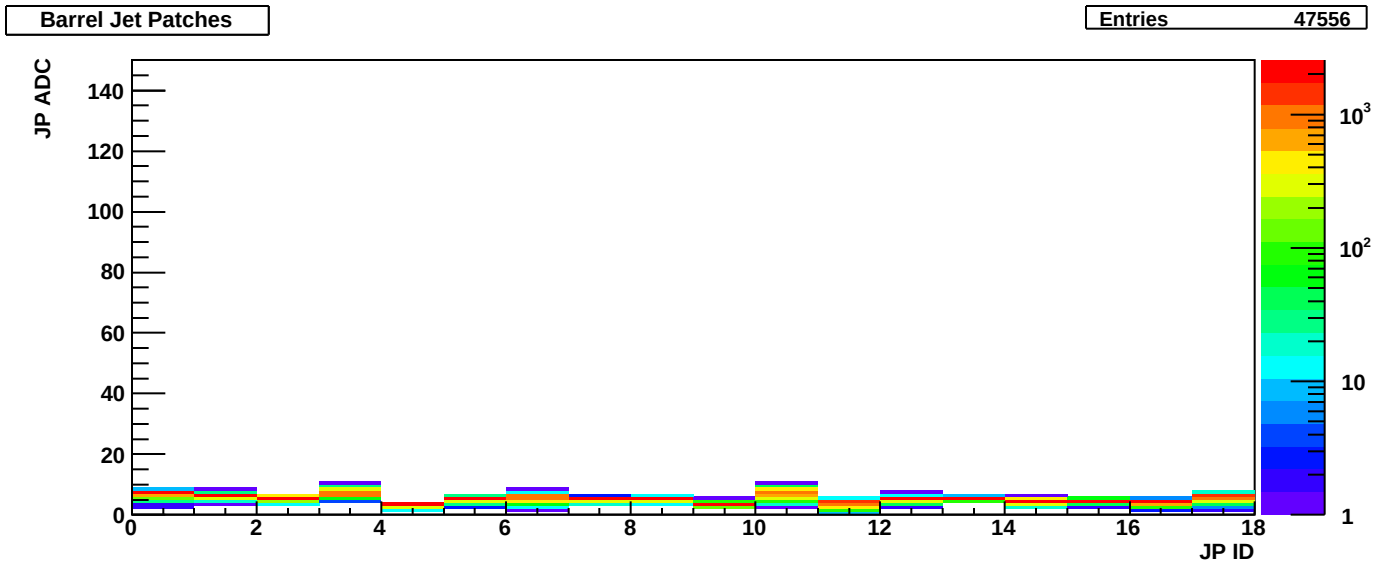
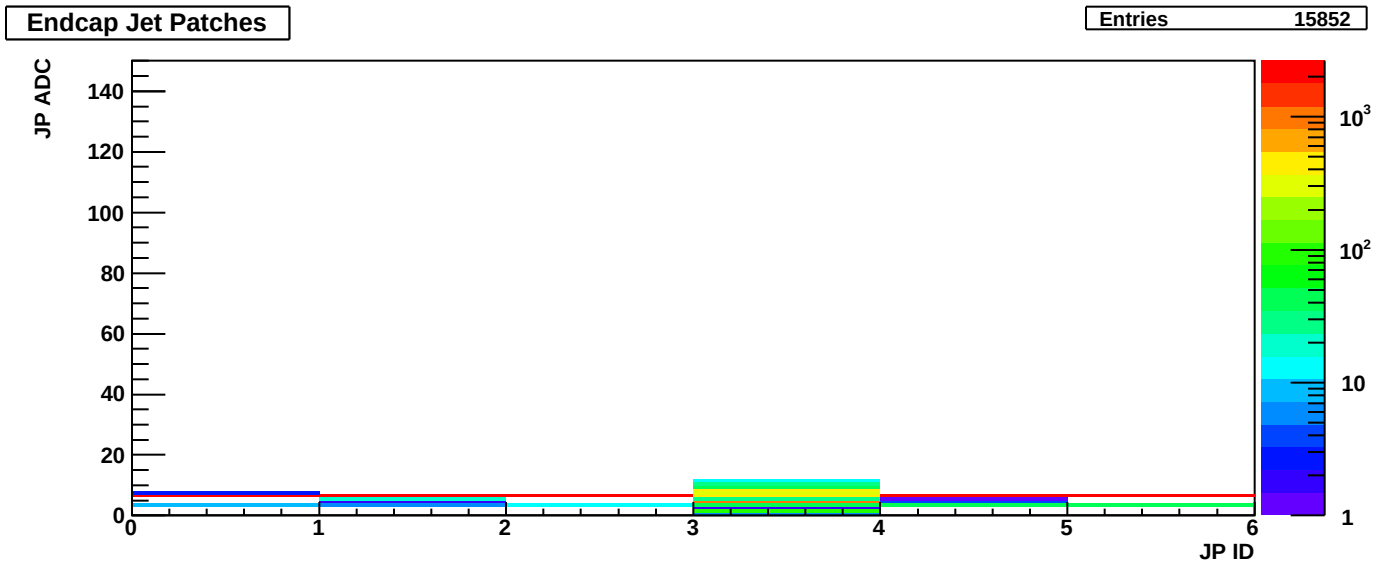

Barrel EMC L0 Input - High Tower
Entries 792600
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$

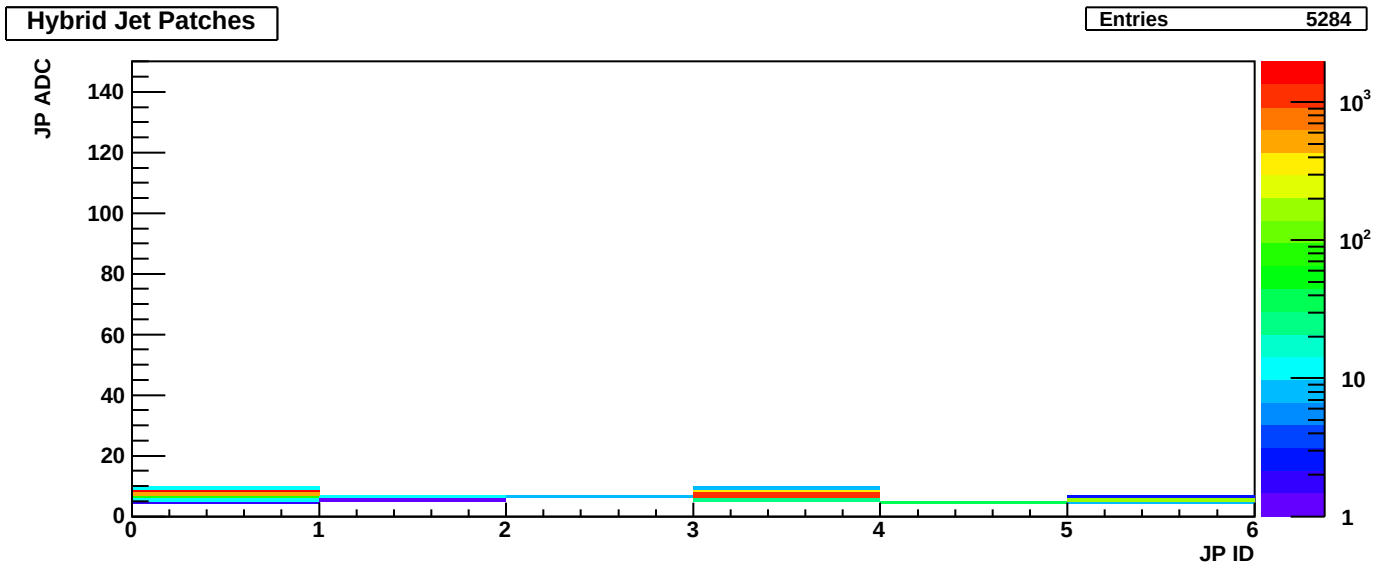

Barrel EMC L0 Input - Patch Sum
Entries 792600
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$

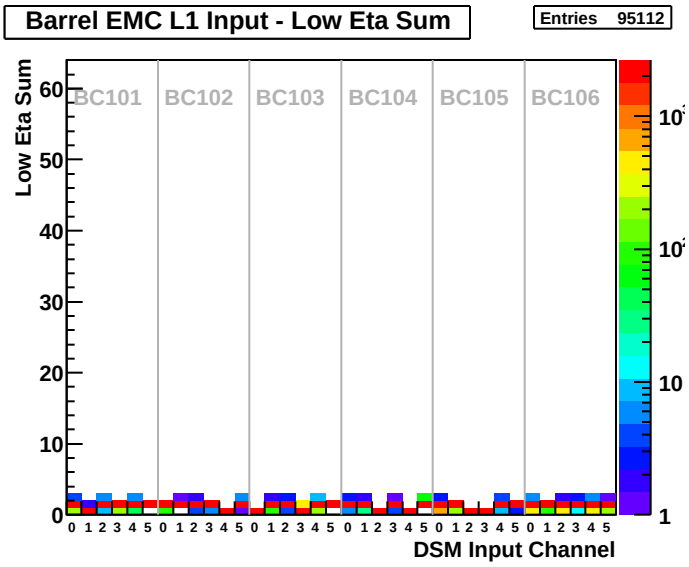

Endcap EMC L0 Input - High Tower
Entries 237780
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
$10^2$
$10^3$

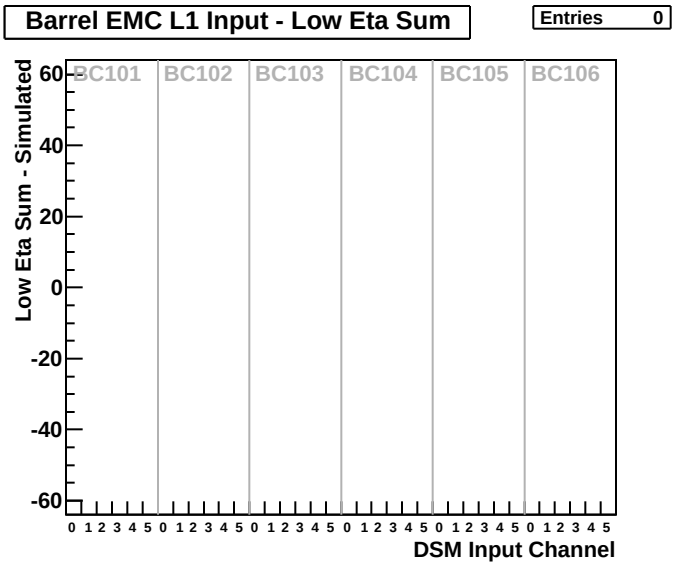

Endcap EMC L0 Input - Patch Sum
Entries 237780
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
$10^2$
$10^3$

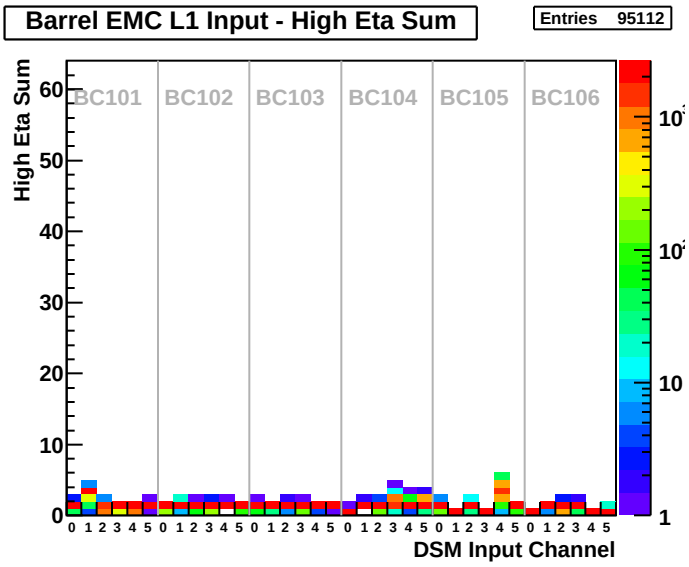

Barrel Jet Patches
Entries 47556
JP ADC
JP ID

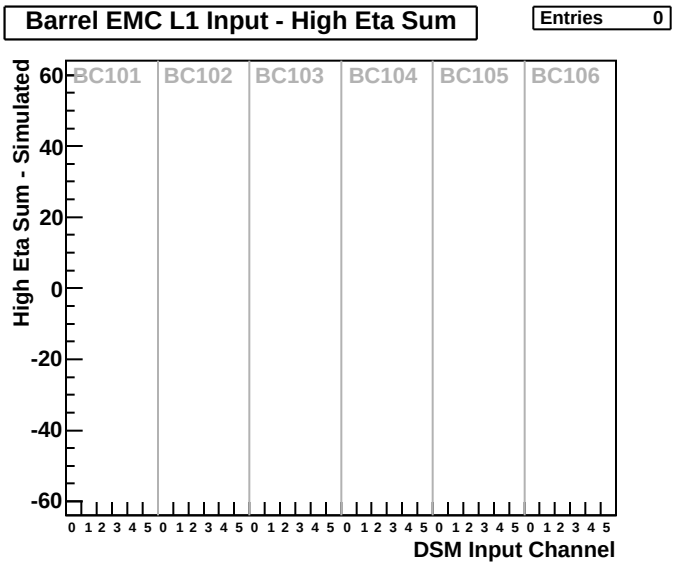

Endcap Jet Patches
Entries 15852
JP ADC
JP ID

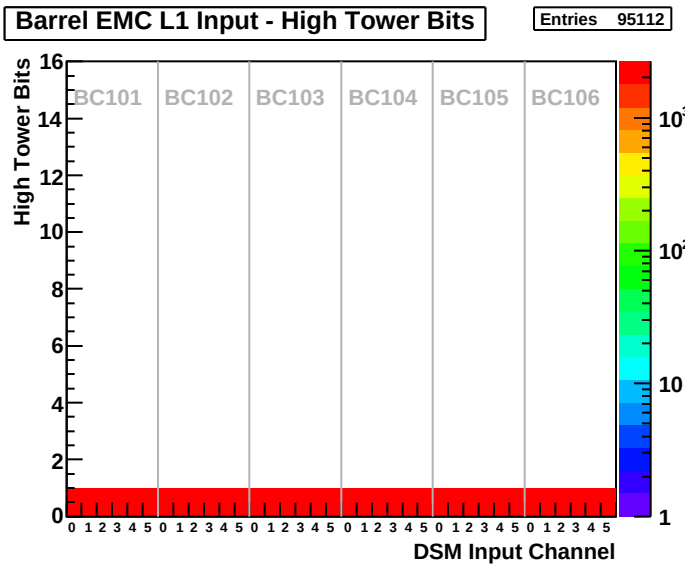

Hybrid Jet Patches
Entries 5284
JP ADC
JP ID

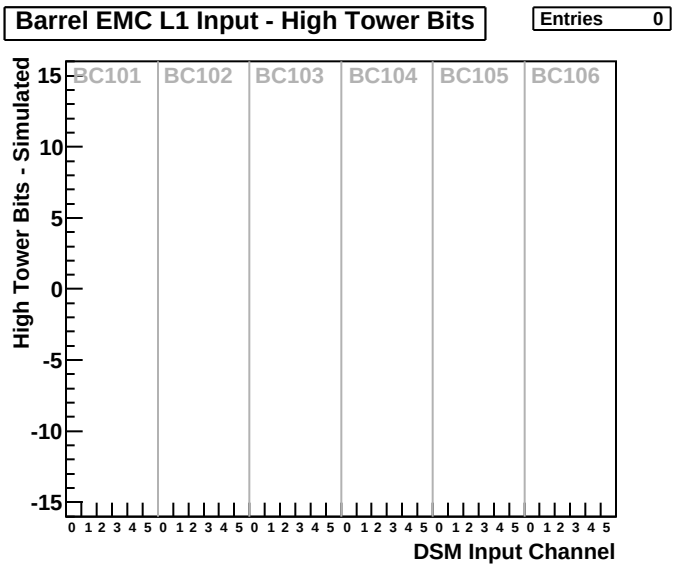

Barrel EMC L1 Input - Low Eta Sum
Entries 95112
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

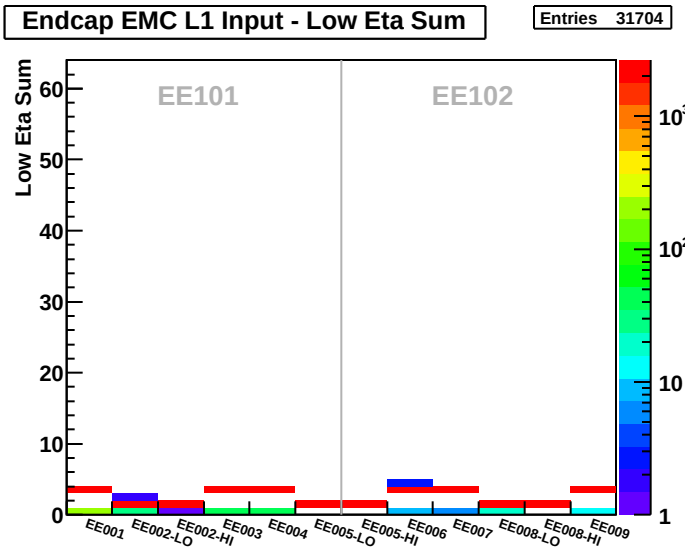

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

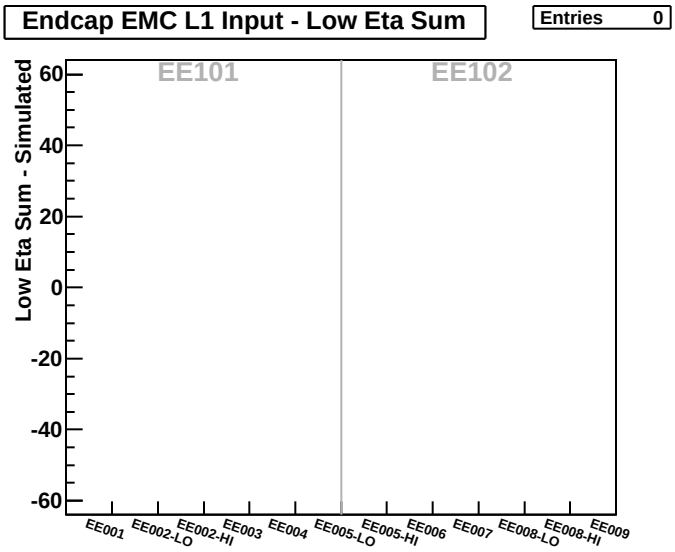

Barrel EMC L1 Input - High Eta Sum
Entries 95112
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

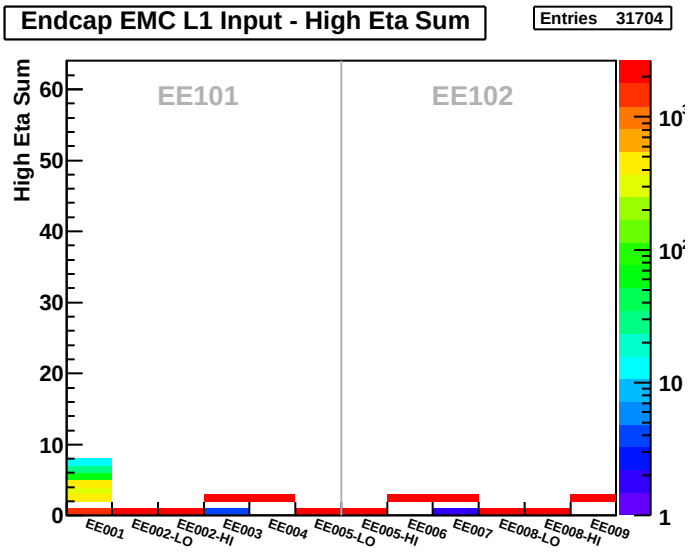

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

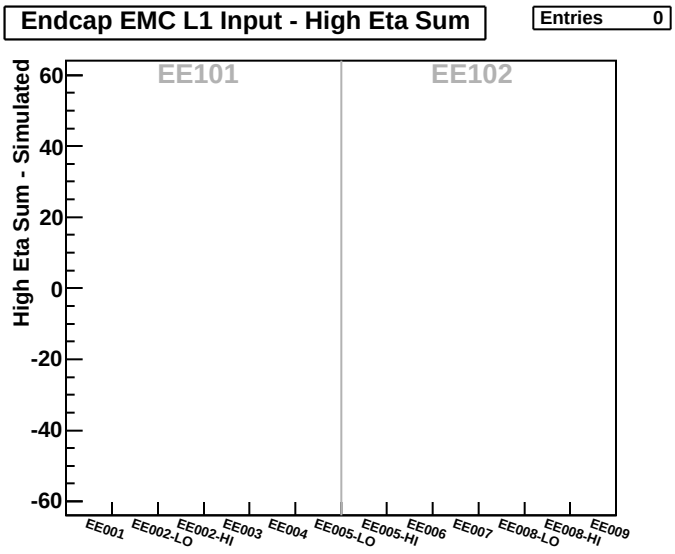

Barrel EMC L1 Input - High Tower Bits
Entries 95112
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

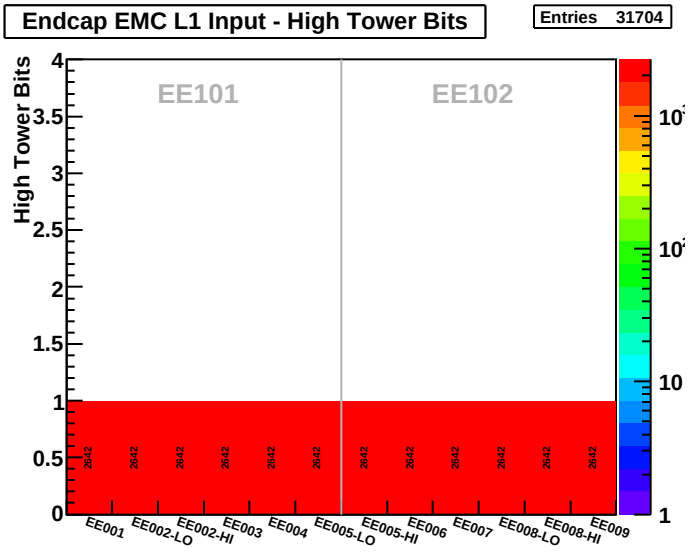

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

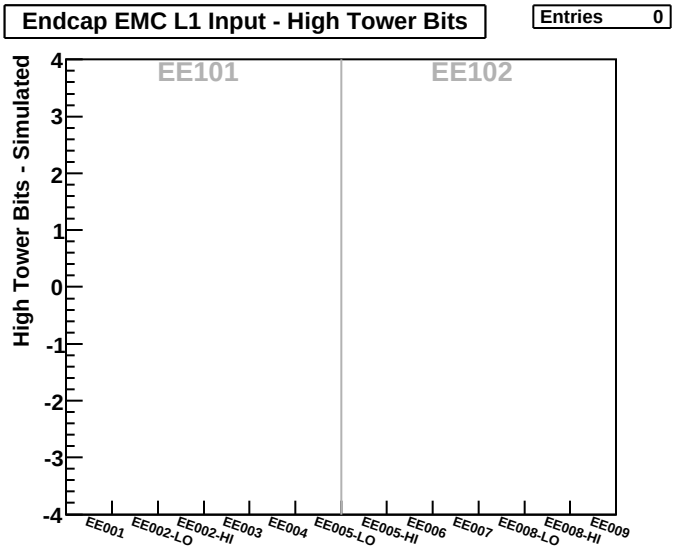

Endcap EMC L1 Input - Low Eta Sum
Entries 31704
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

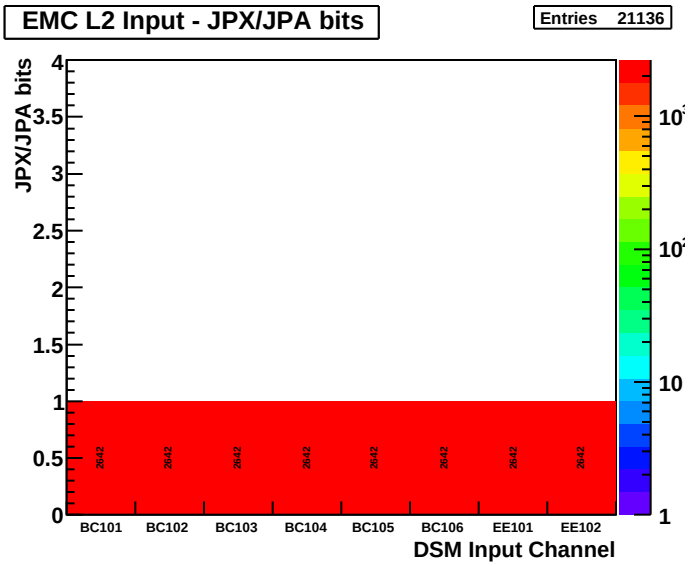

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

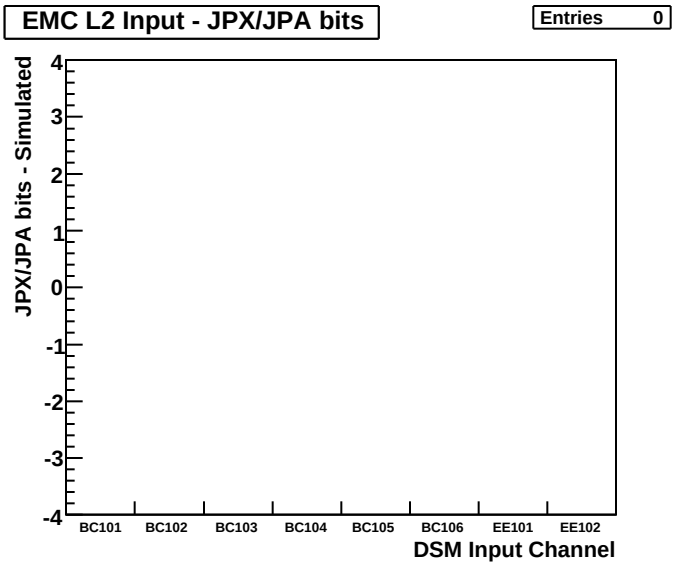

Endcap EMC L1 Input - High Eta Sum
Entries 31704
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

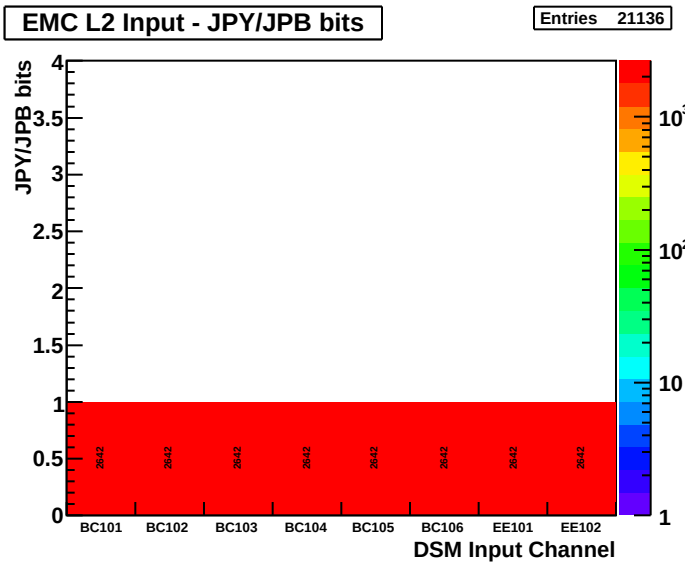

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

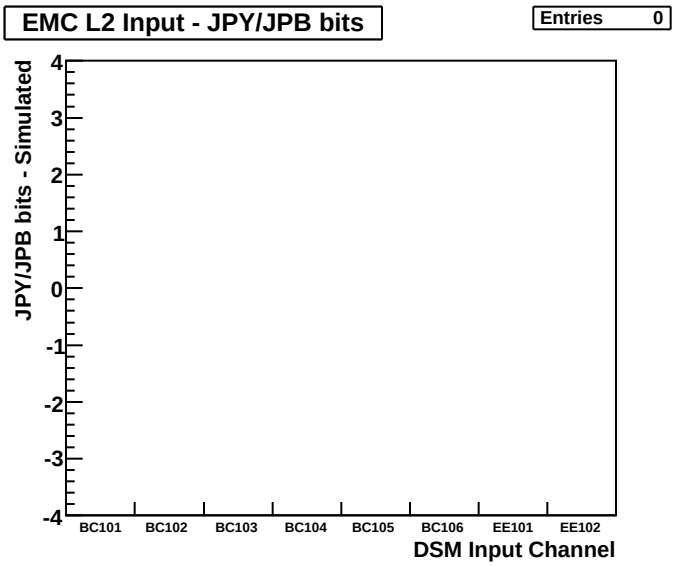

Endcap EMC L1 Input - High Tower Bits
Entries 31704
High Tower Bits
EE101
EE102
2642
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

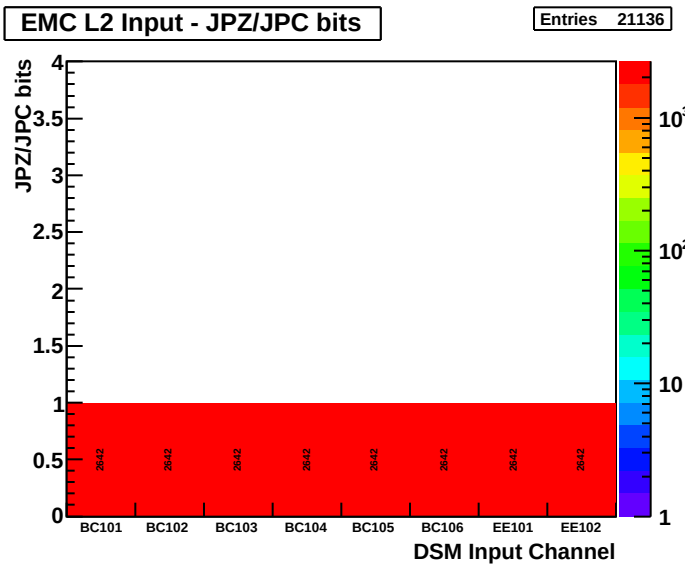

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

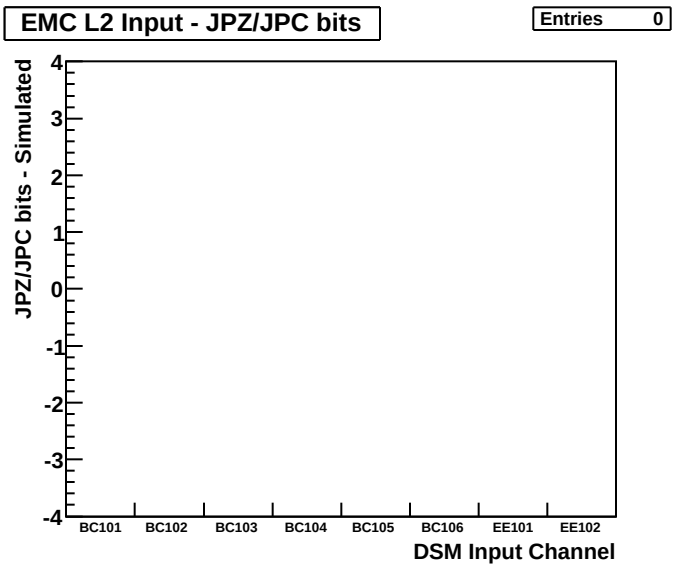

EMC L2 Input - JPX/JPA bits
Entries 21136
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

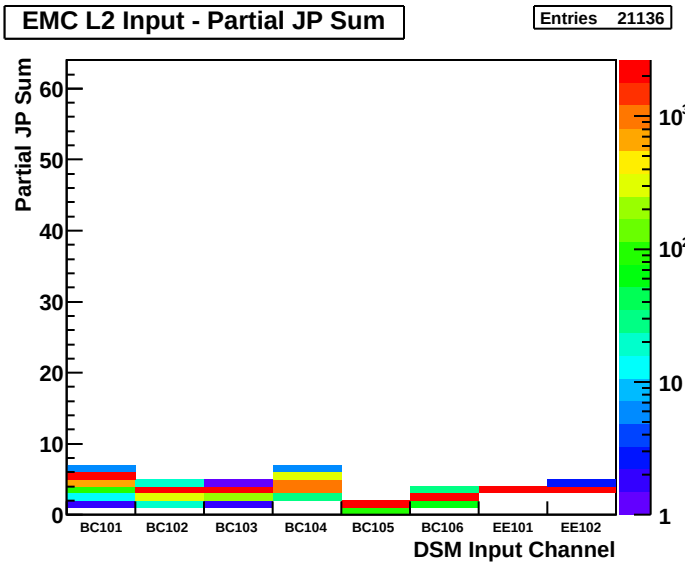

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

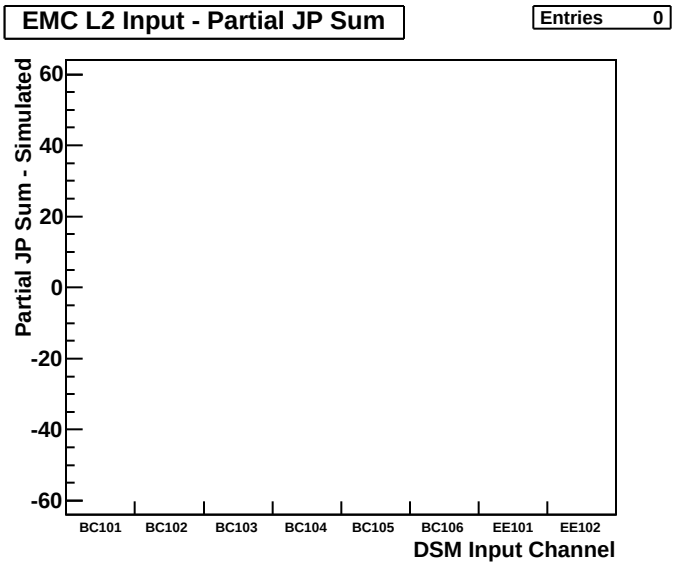

EMC L2 Input - JPY/JPB bits
Entries 21136
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

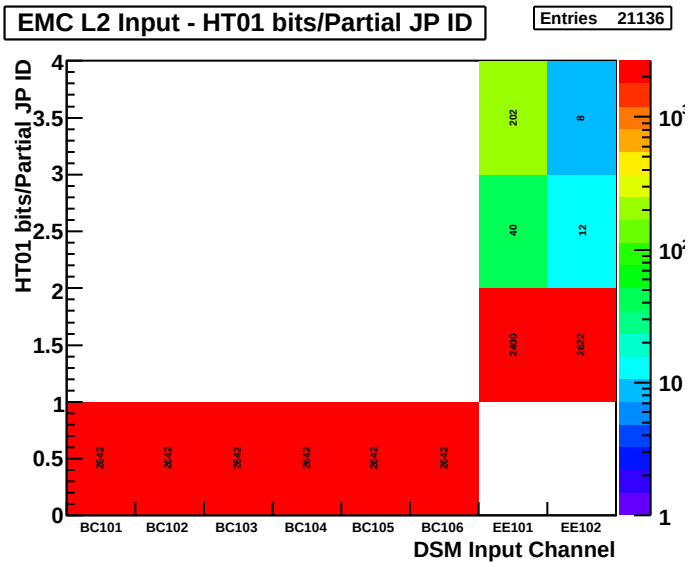

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

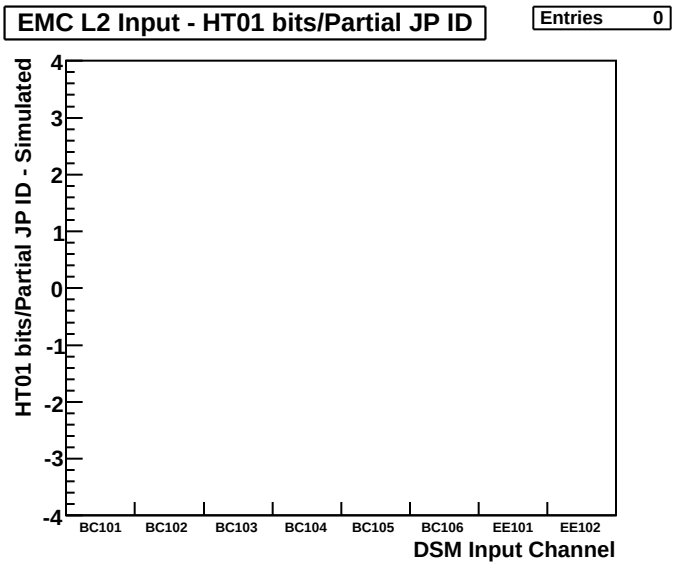

EMC L2 Input - JPZ/JPC bits
Entries 21136
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

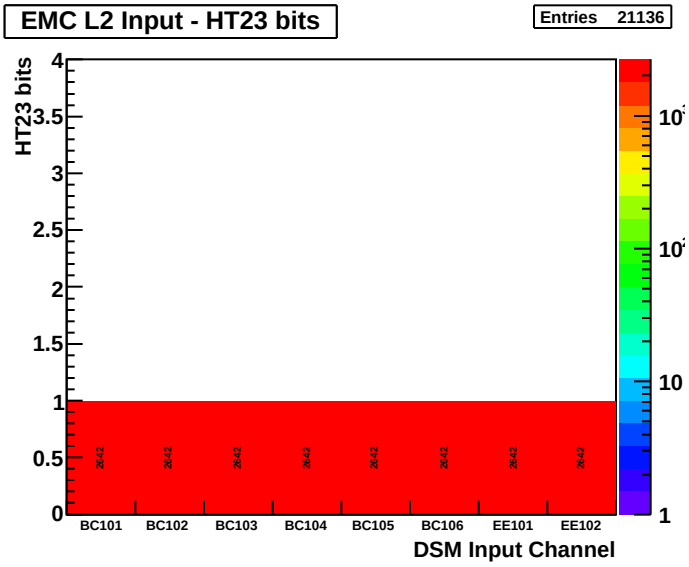

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

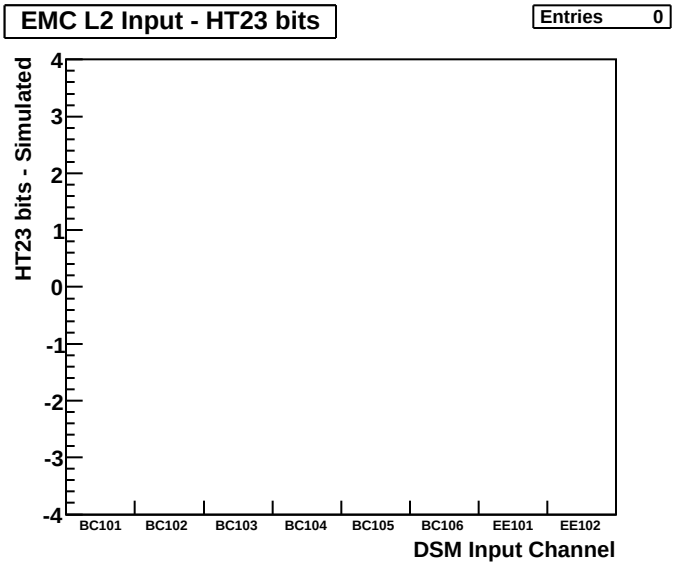

EMC L2 Input - Partial JP Sum
Entries 21136
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

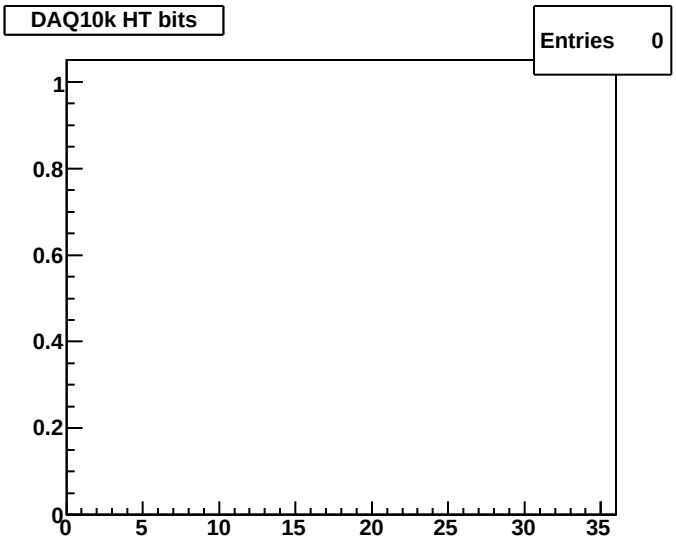

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

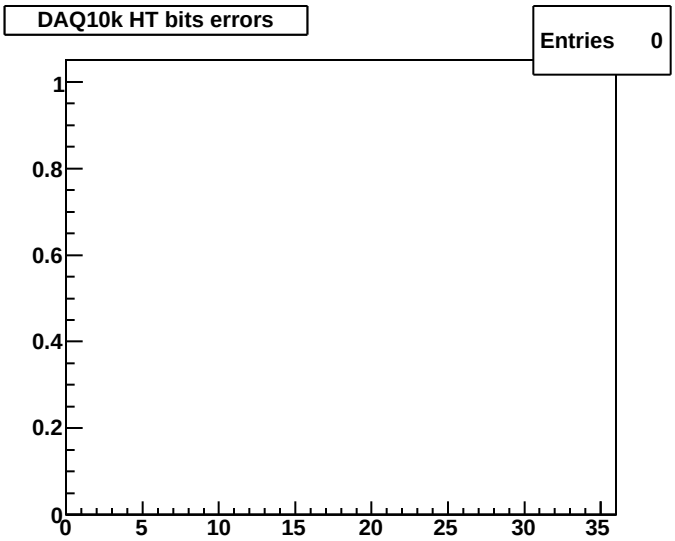

EMC L2 Input - HT01 bits/Partial JP ID
Entries 21136
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

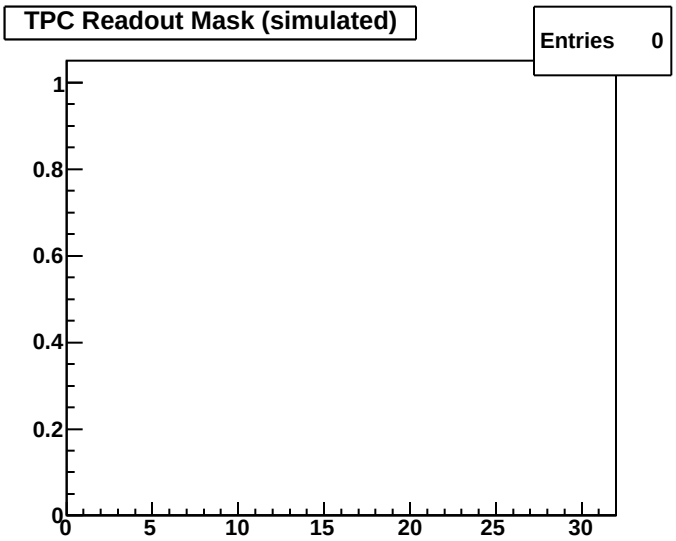

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

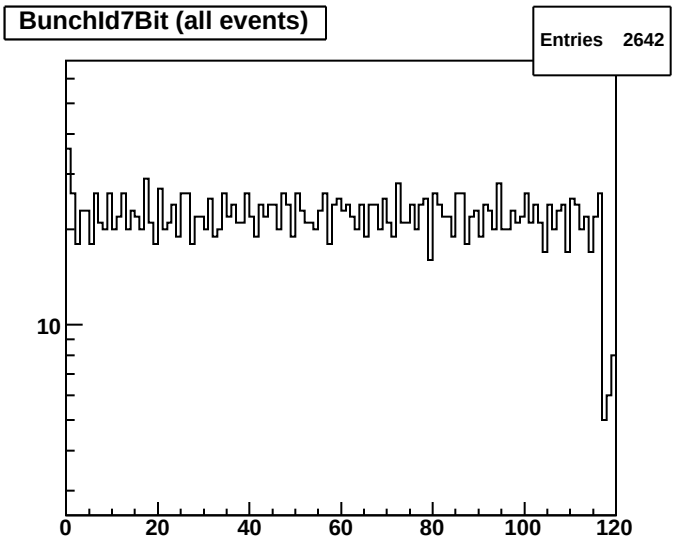

EMC L2 Input - HT23 bits
Entries 21136
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

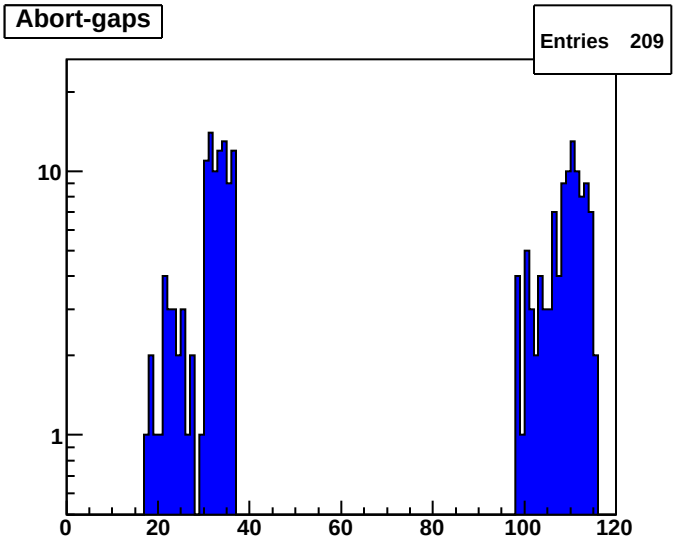

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

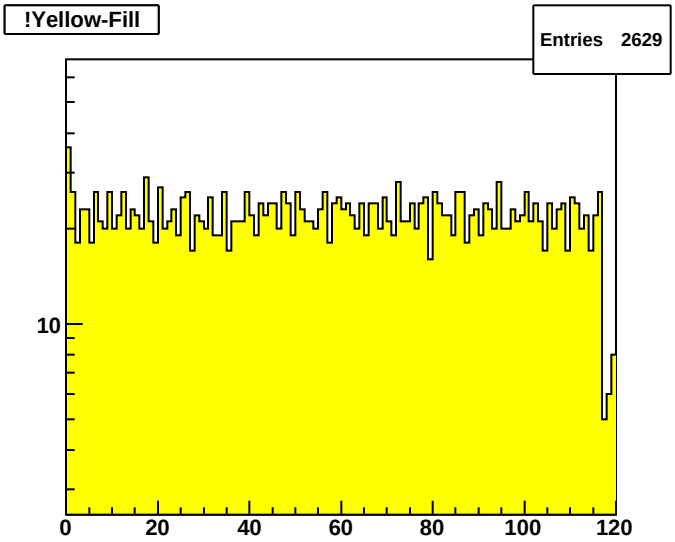

DAQ10k HT bits
Entries 0

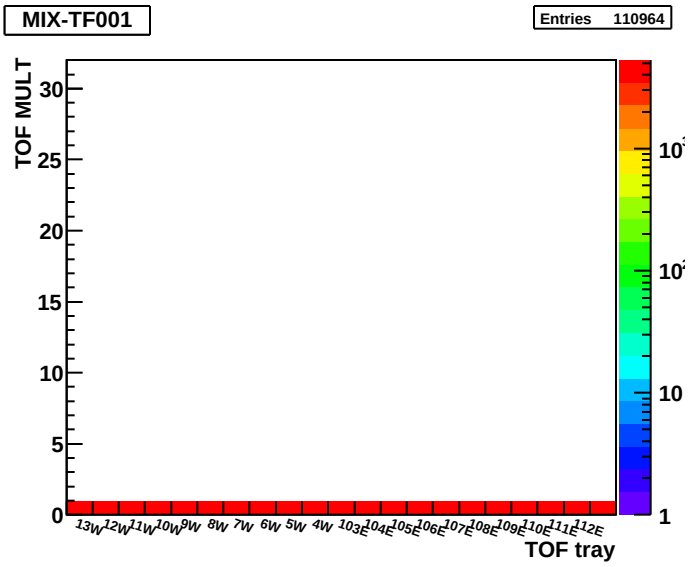

DAQ10k HT bits errors
Entries 0

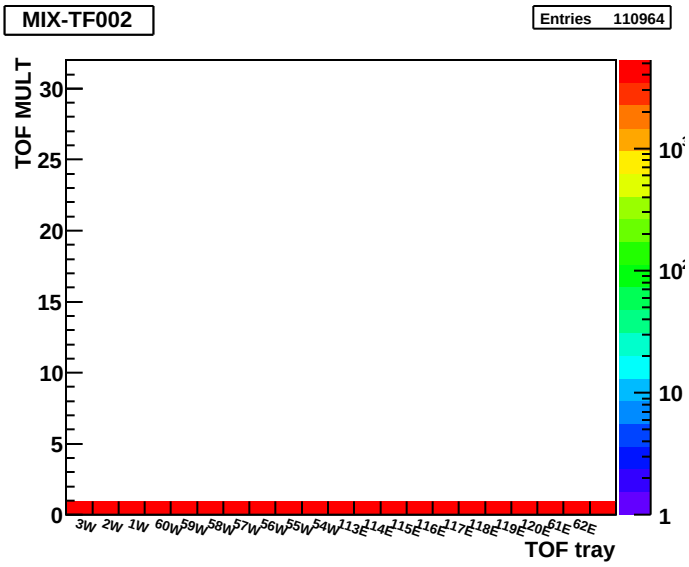

TPC Readout Mask (simulated)
Entries 0

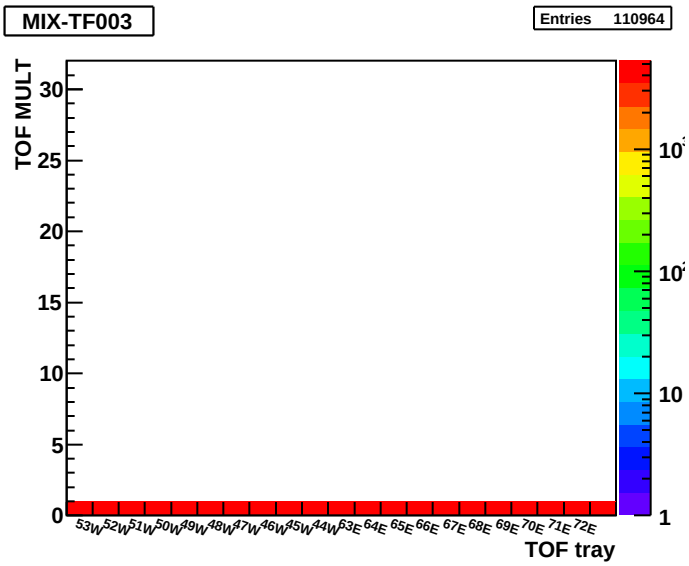

BunchId7Bit (all events)
Entries 2642

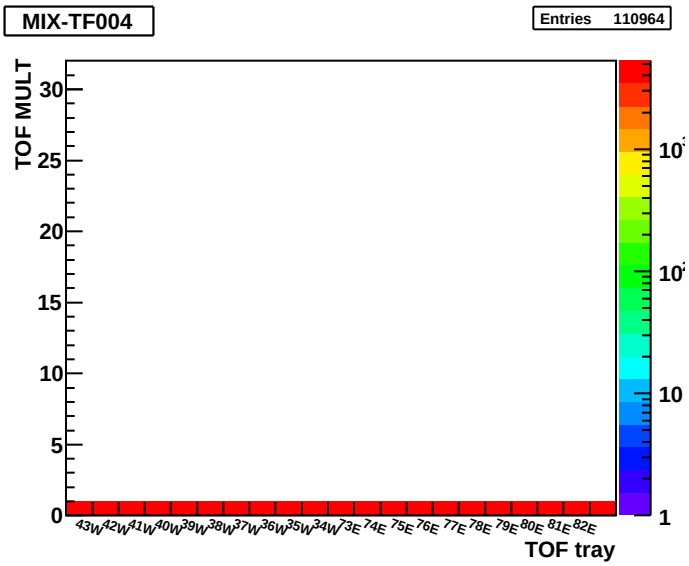

Abort-gaps
Entries 209

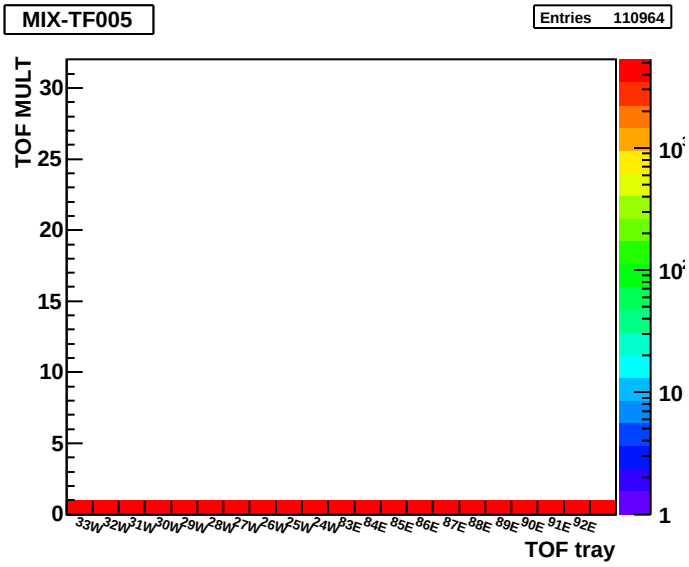

!Yellow-Fill
Entries 2629

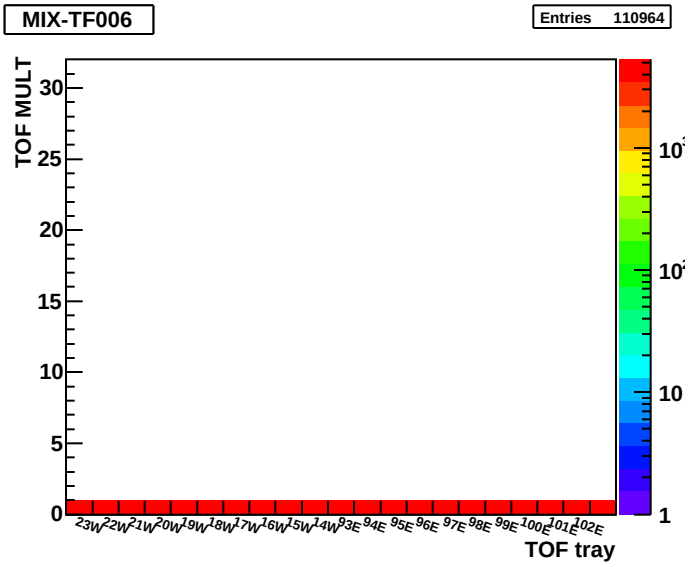

MIX-TF001
Entries 110964
TOF MULT
TOF tray

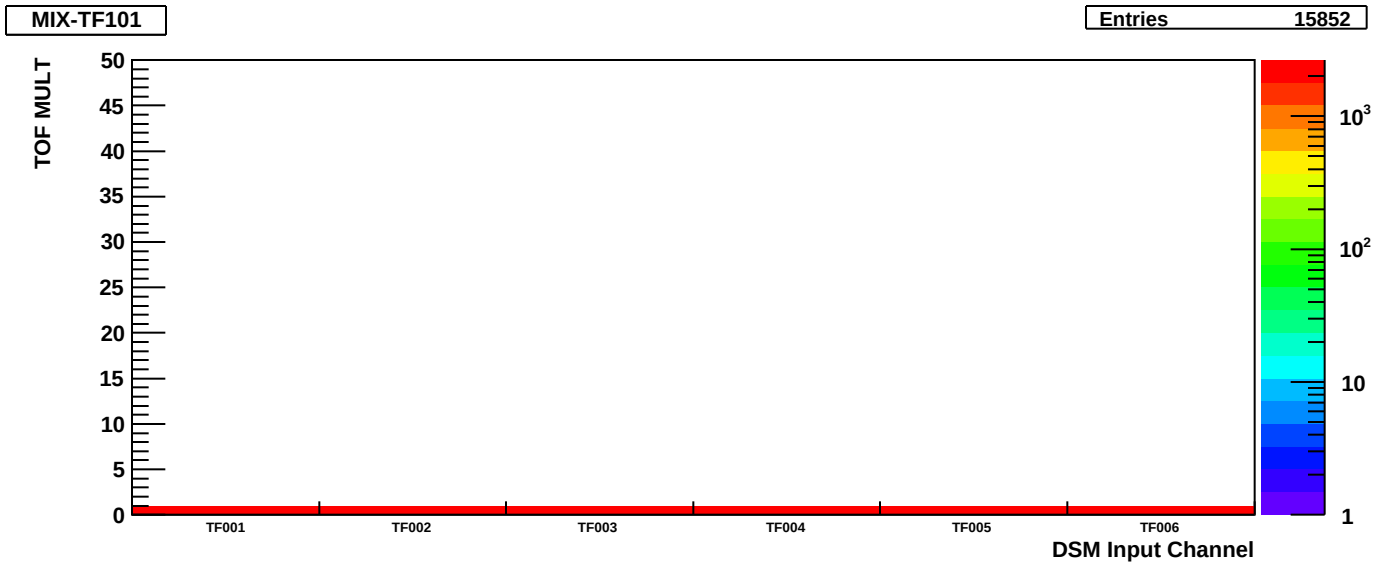

MIX-TF002
Entries 110964
TOF MULT
TOF tray

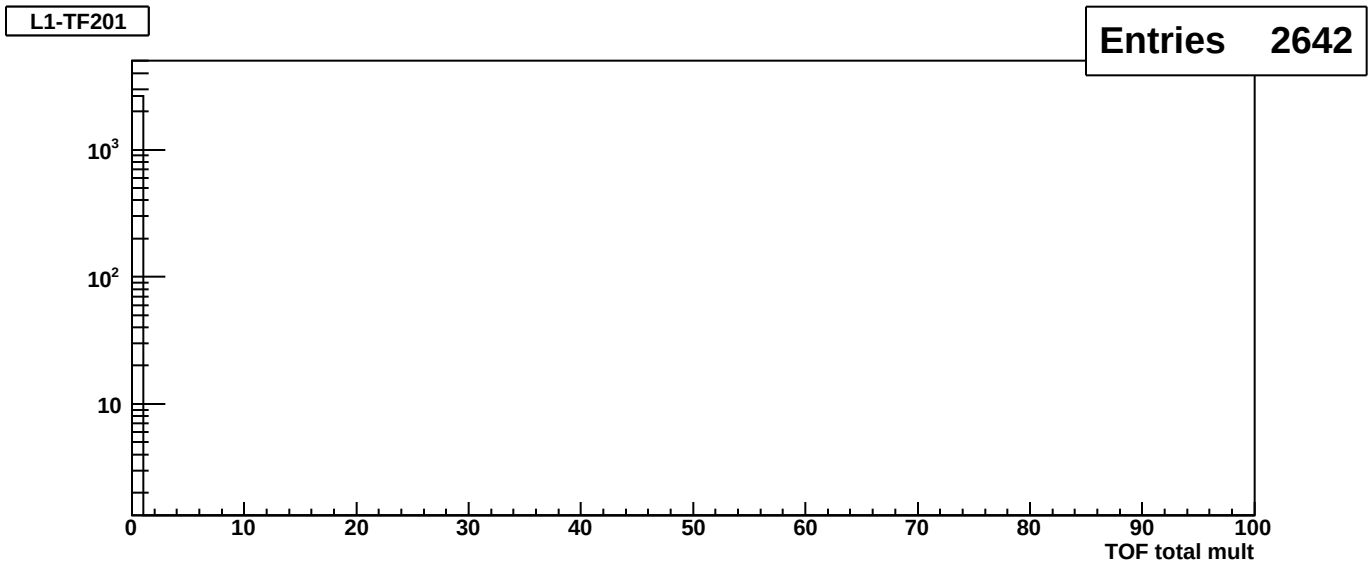

MIX-TF003
Entries 110964
TOF MULT
TOF tray

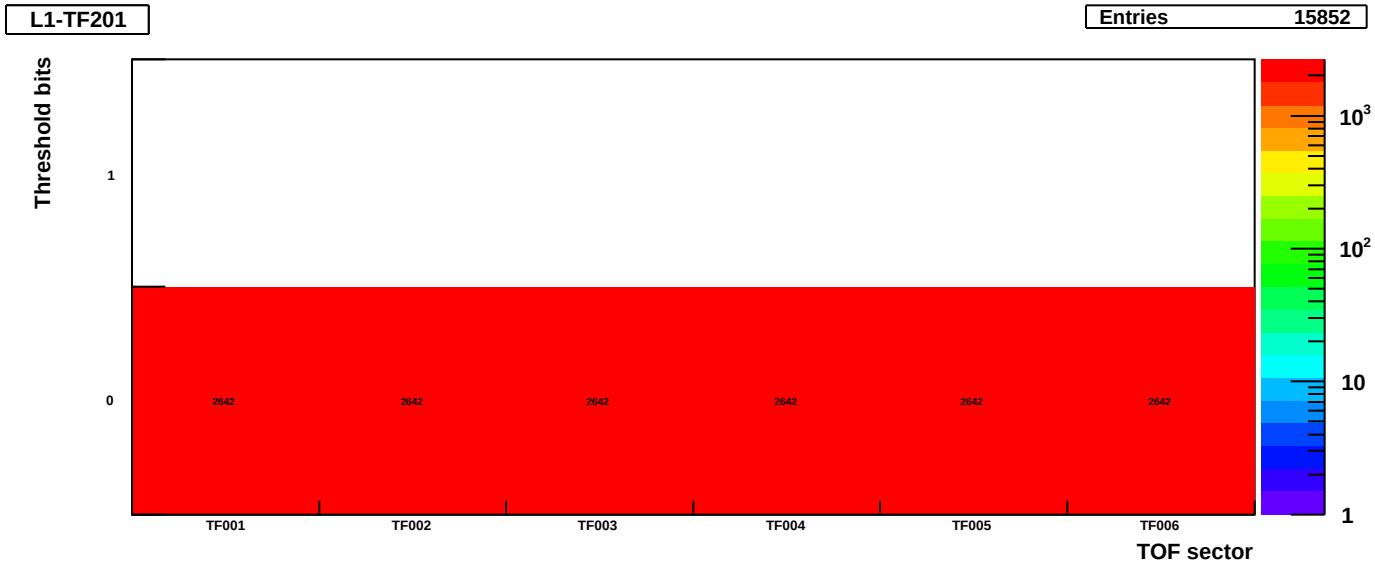

MIX-TF004
Entries 110964
TOF MULT
TOF tray

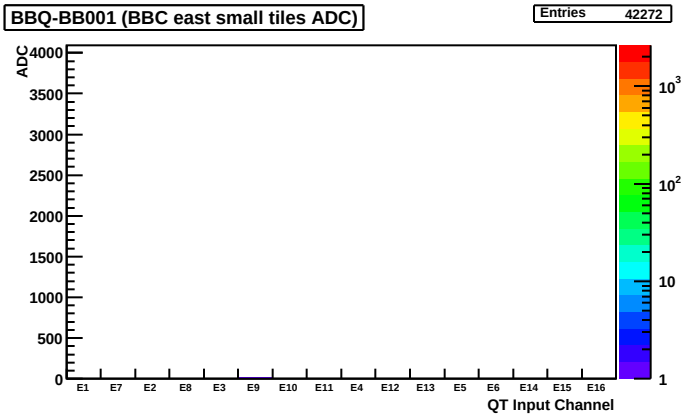

MIX-TF005
Entries 110964
TOF MULT
TOF tray

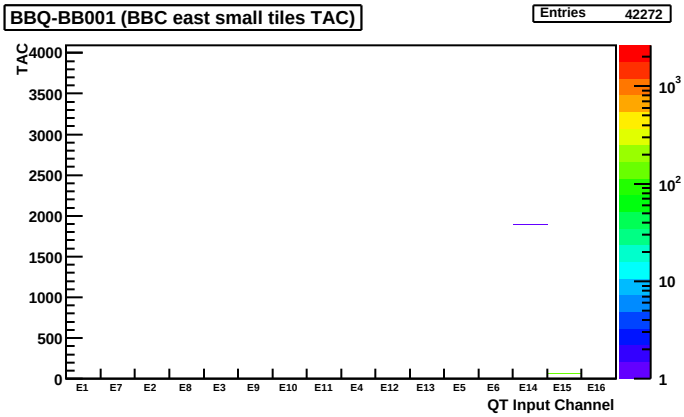

MIX-TF006
Entries 110964
TOF MULT
TOF tray

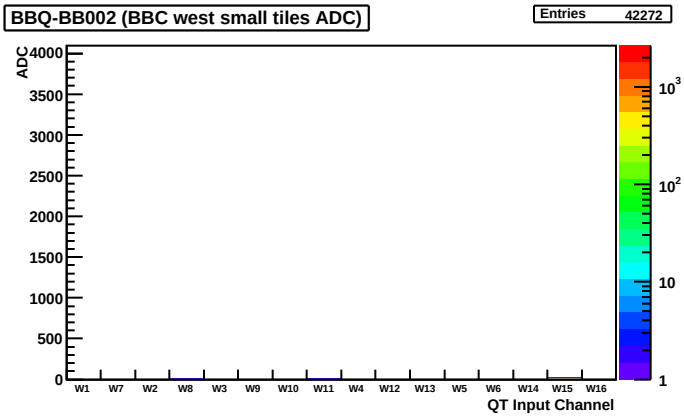

MIX-TF101
Entries 15852
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^3$
$10^2$
10
1

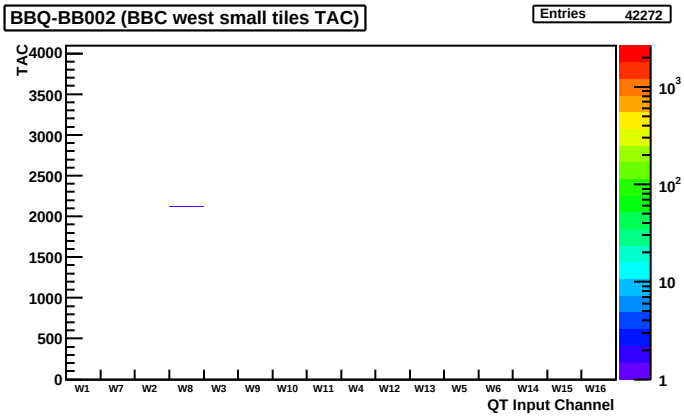

L1-TF201
Entries 2642
$10^3$
$10^2$
10
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

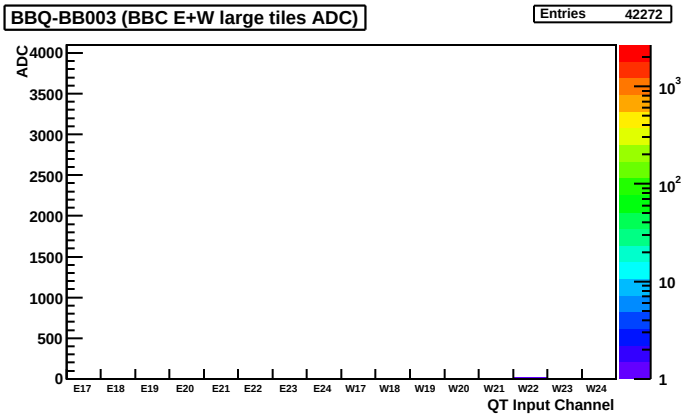

L1-TF201
Entries 15852
Threshold bits
1
0
2642
2642
2642
2642
2642
2642
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
$10^3$
$10^2$
10
1

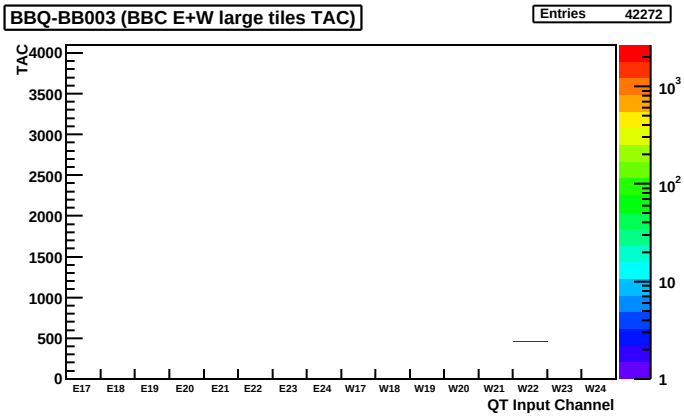

BBQ-BB001 (BBC east small tiles ADC)
Entries 42272
ADC
QT Input Channel

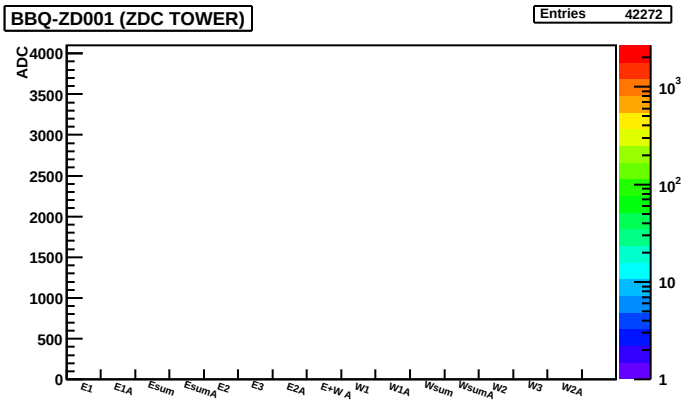

BBQ-BB001 (BBC east small tiles TAC)
Entries 42272
TAC
QT Input Channel

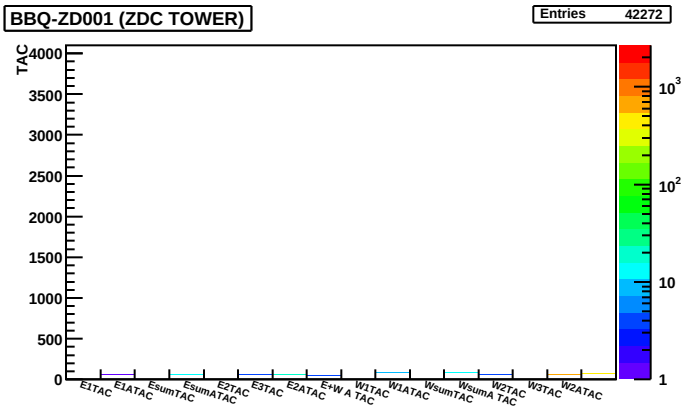

BBQ-BB002 (BBC west small tiles ADC)
Entries 42272
ADC
QT Input Channel

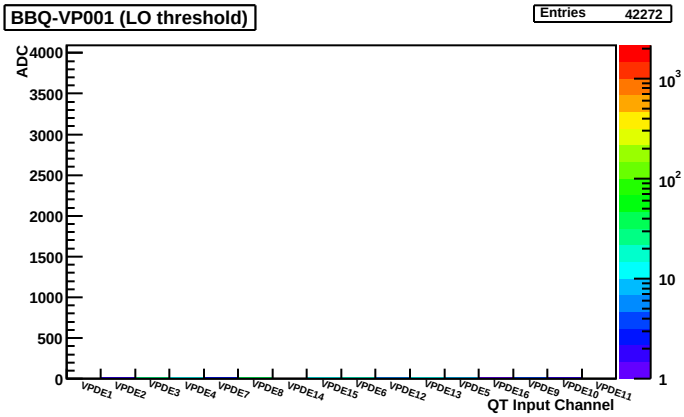

BBQ-BB002 (BBC west small tiles TAC)
Entries 42272
TAC
QT Input Channel

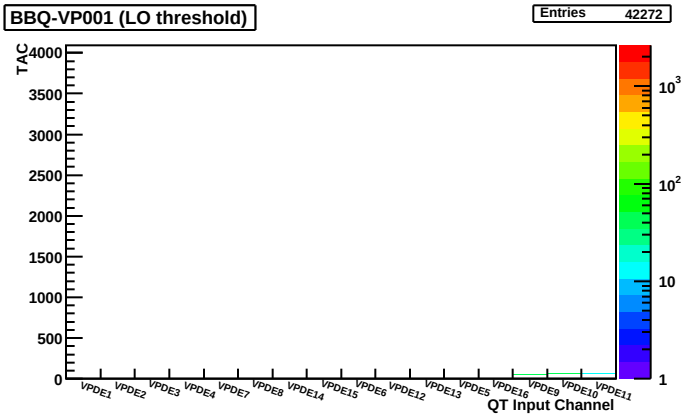

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 42272
ADC
QT Input Channel

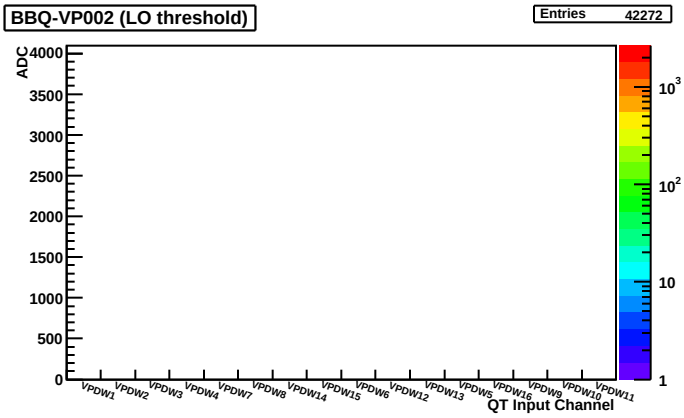

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 42272
TAC
QT Input Channel

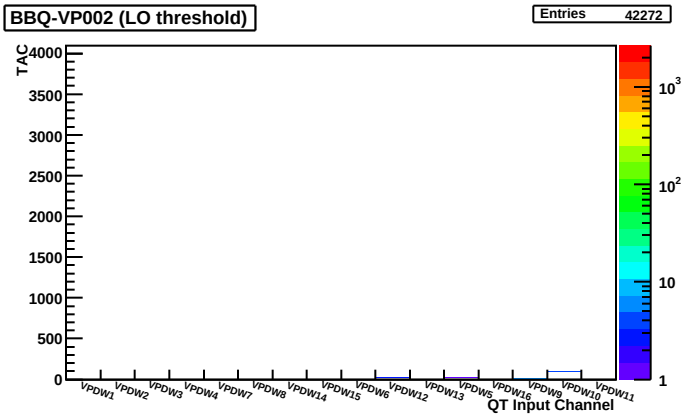

BBQ-ZD001 (ZDC TOWER)
Entries 42272
ADC

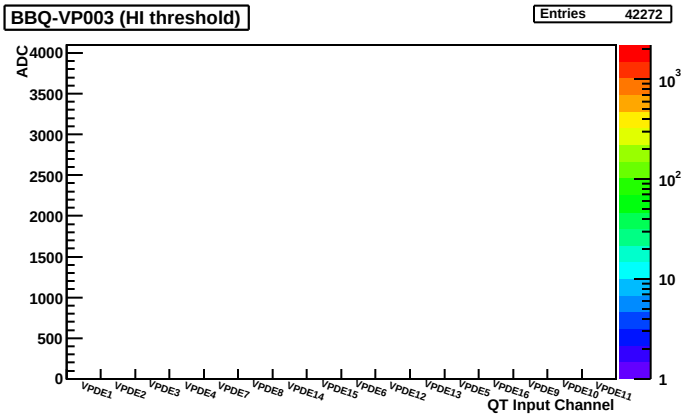

BBQ-ZD001 (ZDC TOWER)
Entries 42272
TAC

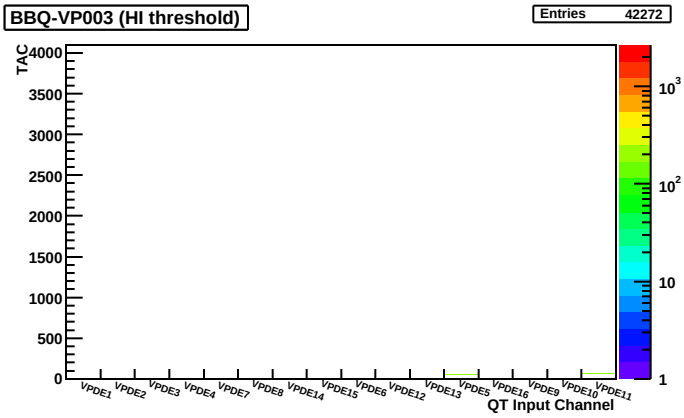

BBQ-VP001 (LO threshold)
Entries 42272
ADC
QT Input Channel

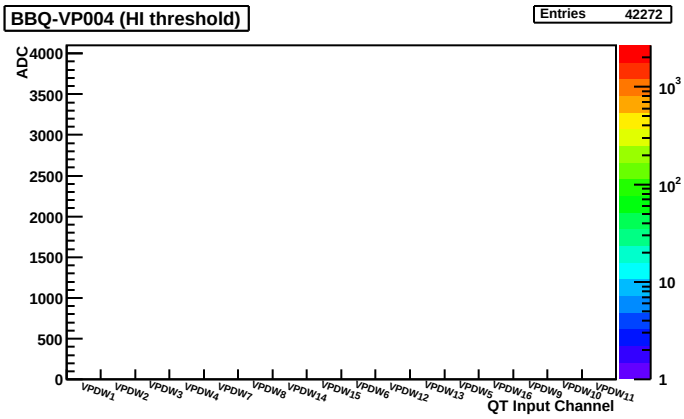

BBQ-VP001 (LO threshold)
Entries 42272
TAC
QT Input Channel

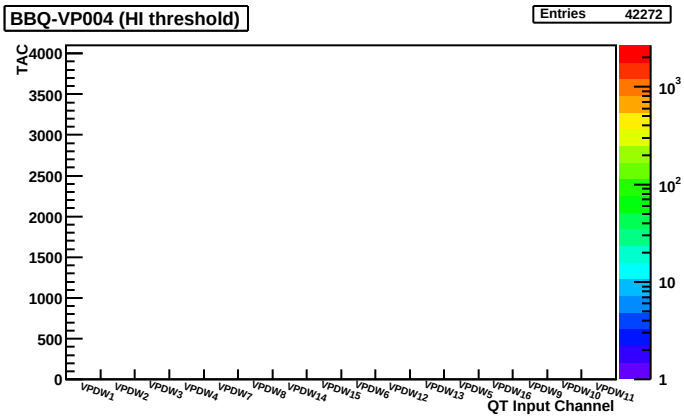

BBQ-VP002 (LO threshold)
Entries 42272
ADC
QT Input Channel

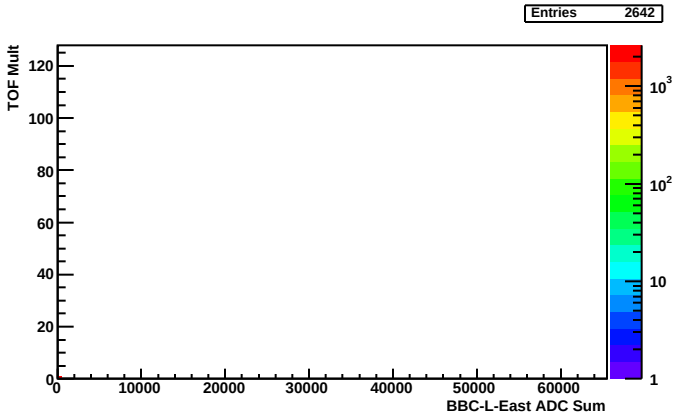

BBQ-VP002 (LO threshold)
Entries 42272
TAC
QT Input Channel

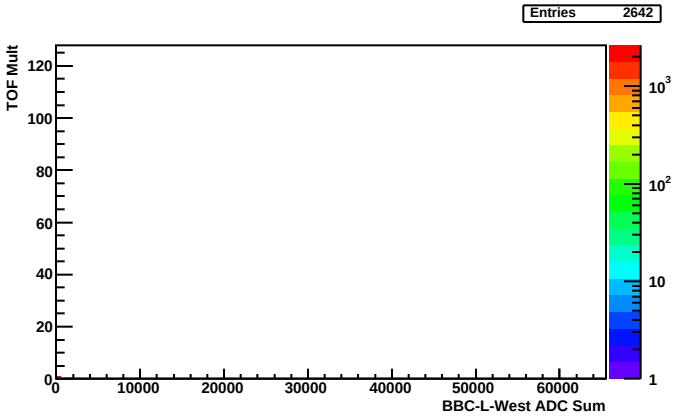

BBQ-VP003 (HI threshold)
Entries 42272
ADC
QT Input Channel

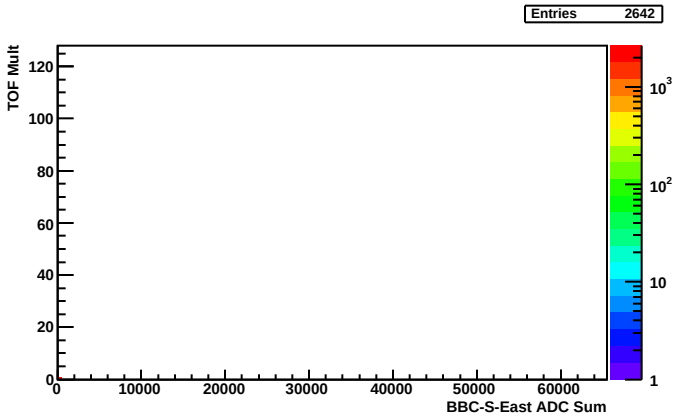

BBQ-VP003 (HI threshold)
Entries 42272
TAC
QT Input Channel

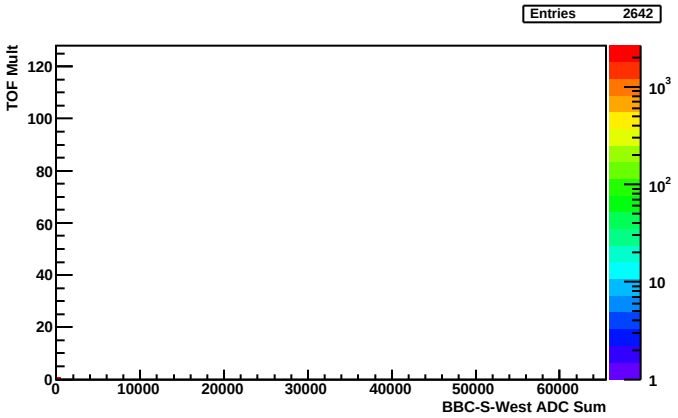

BBQ-VP004 (HI threshold)
Entries 42272
ADC
QT Input Channel

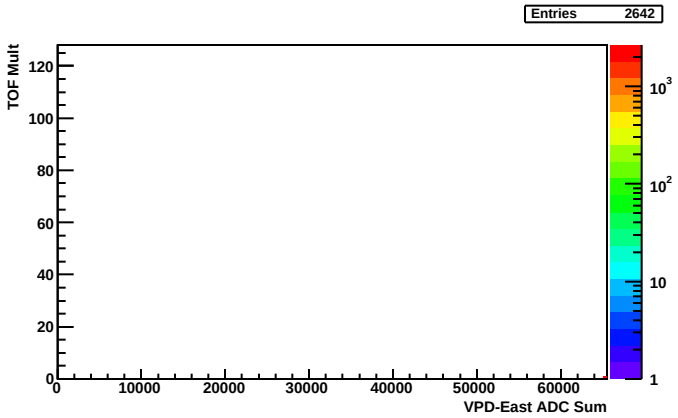

BBQ-VP004 (HI threshold)
Entries 42272
TAC
QT Input Channel

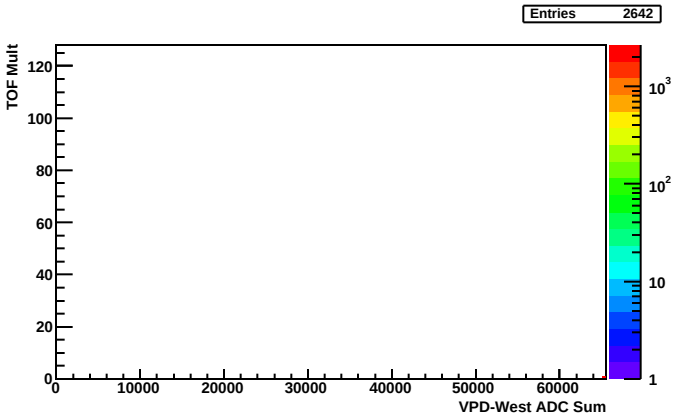

Entries 2642
TOF Mult
BBC-L-East ADC Sum

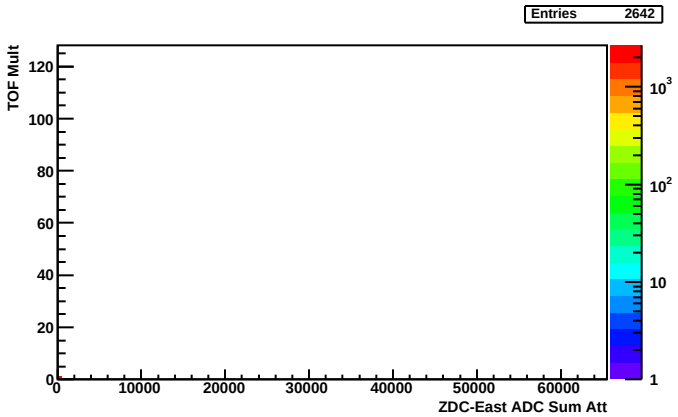

Entries 2642
TOF Mult
BBC-L-West ADC Sum

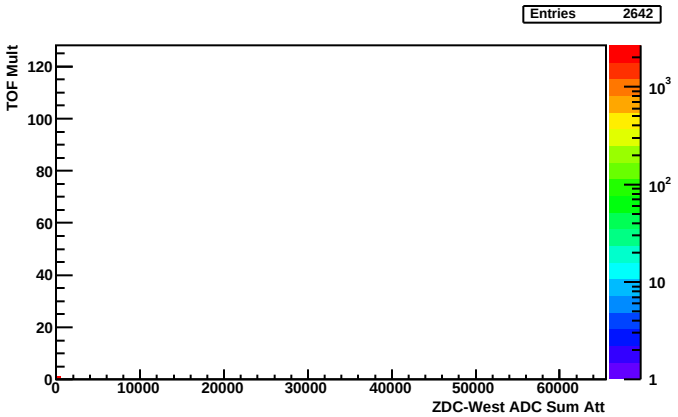

Entries 2642
TOF Mult
BBC-S-East ADC Sum

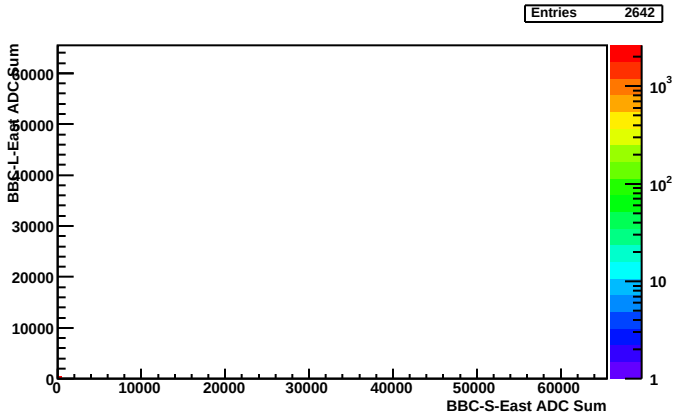

Entries 2642
TOF Mult
BBC-S-West ADC Sum

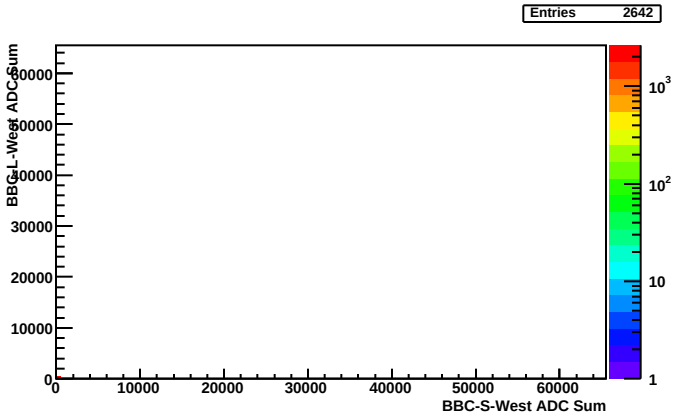

Entries 2642
TOF Mult
VPD-East ADC Sum

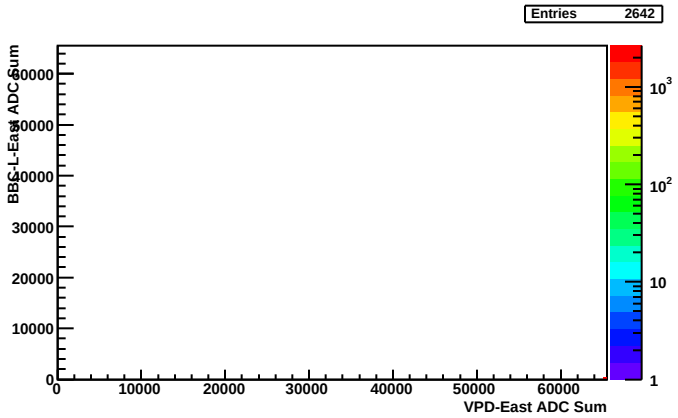

Entries 2642
TOF Mult
VPD-West ADC Sum

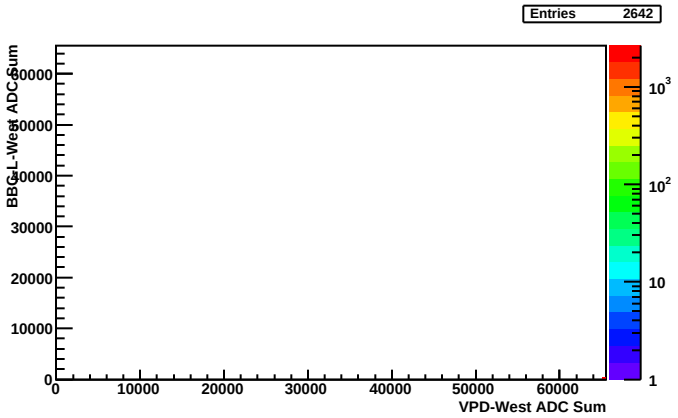

Entries 2642
TOF Mult
ZDC-East ADC Sum Att

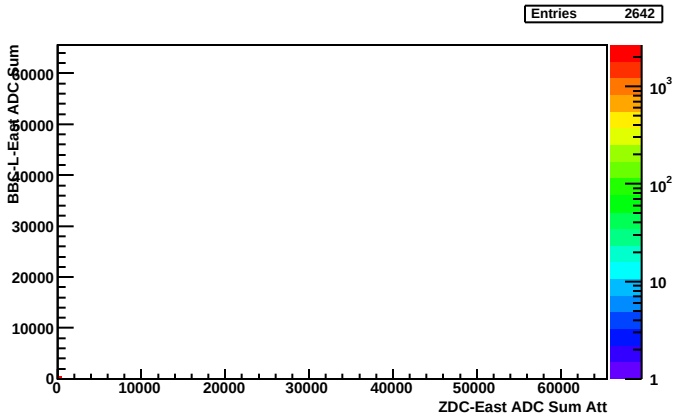

Entries 2642
TOF Mult
ZDC-West ADC Sum Att

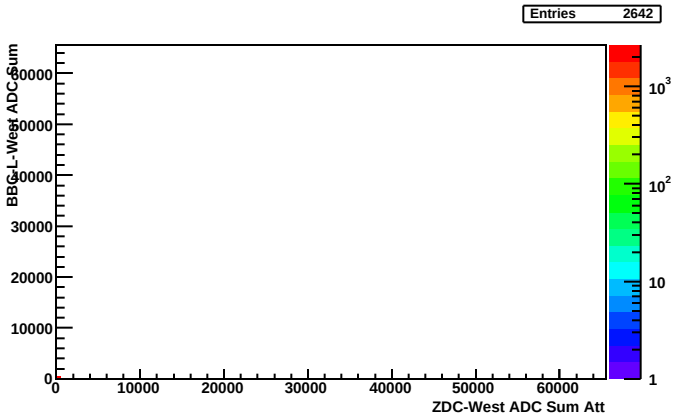

Entries 2642
BBC-L-East ADC Sum
BBC-S-East ADC Sum

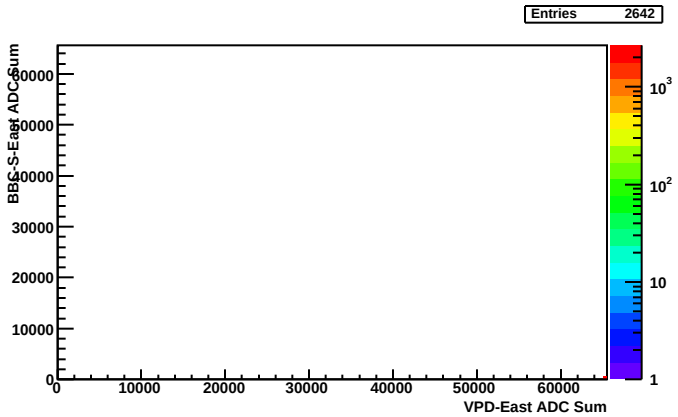

Entries 2642
BBC-L-West ADC Sum
BBC-S-West ADC Sum

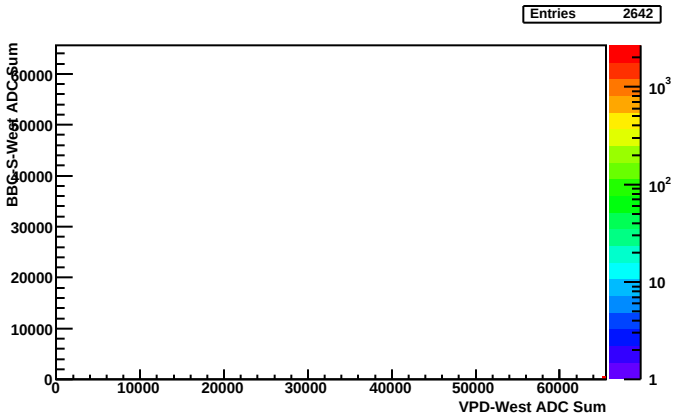

Entries 2642
BBC-L-East ADC Sum
VPD-East ADC Sum

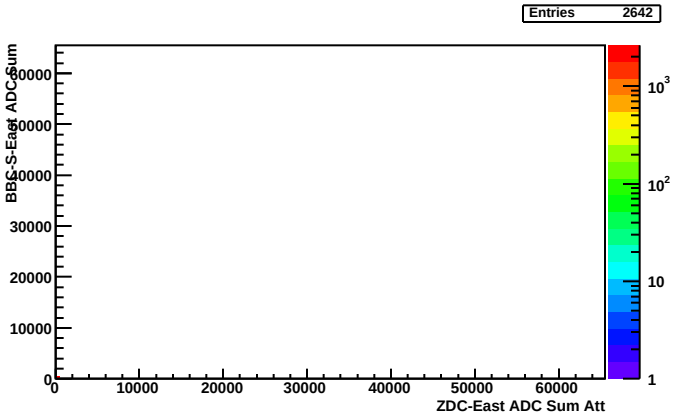

Entries 2642
BBC-L-West ADC Sum
VPD-West ADC Sum

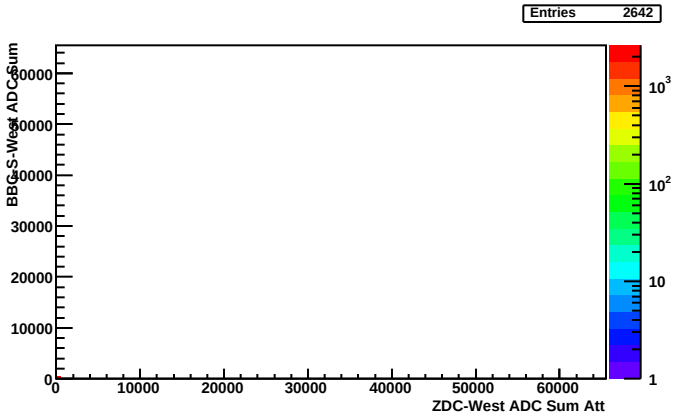

Entries 2642
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

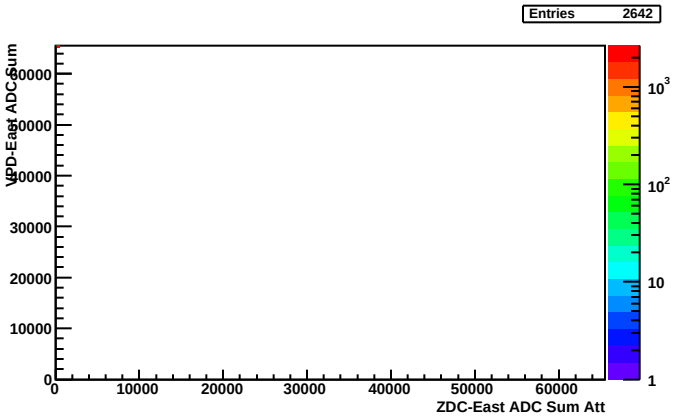

Entries 2642
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

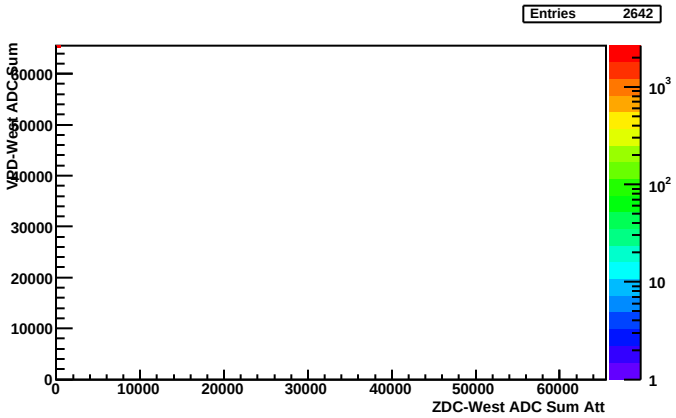

Entries 2642
BBC-S-East ADC Sum
VPD-East ADC Sum

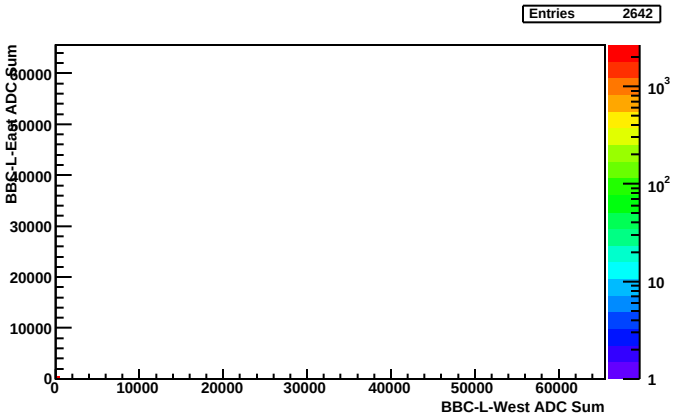

Entries 2642
BBC-S-West ADC Sum
VPD-West ADC Sum

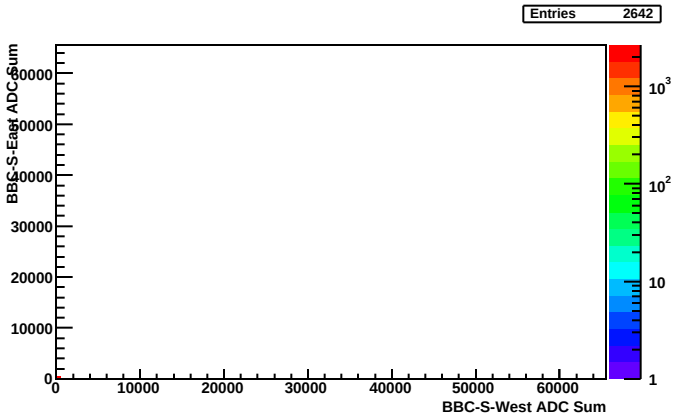

Entries 2642
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

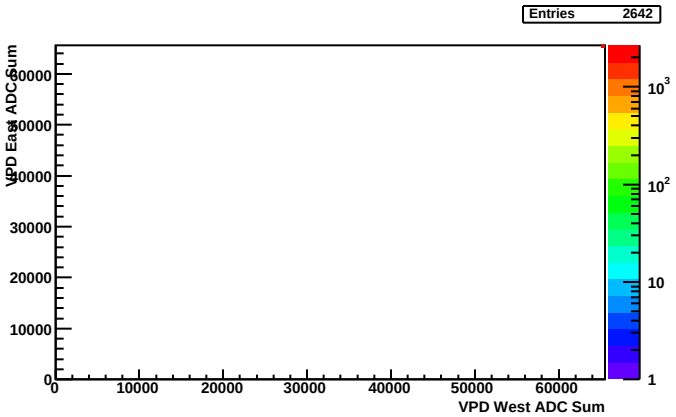

Entries 2642
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

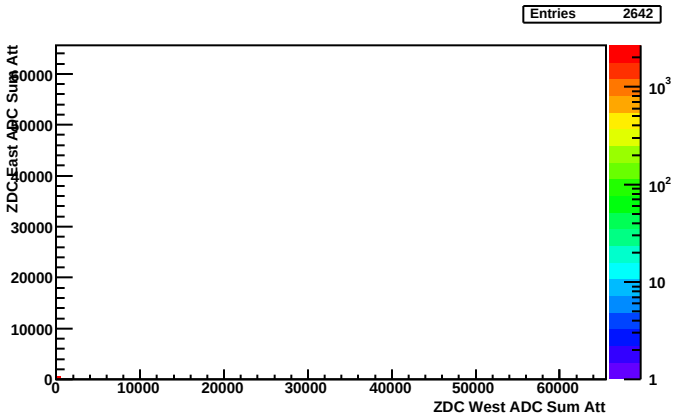

Entries 2642
VPD-East ADC Sum
ZDC-East ADC Sum Att

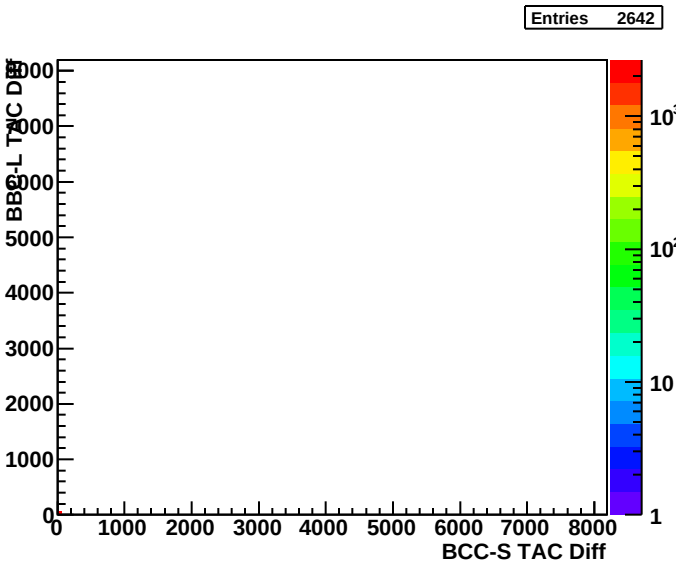

Entries 2642
VPD-West ADC Sum
ZDC-West ADC Sum Att

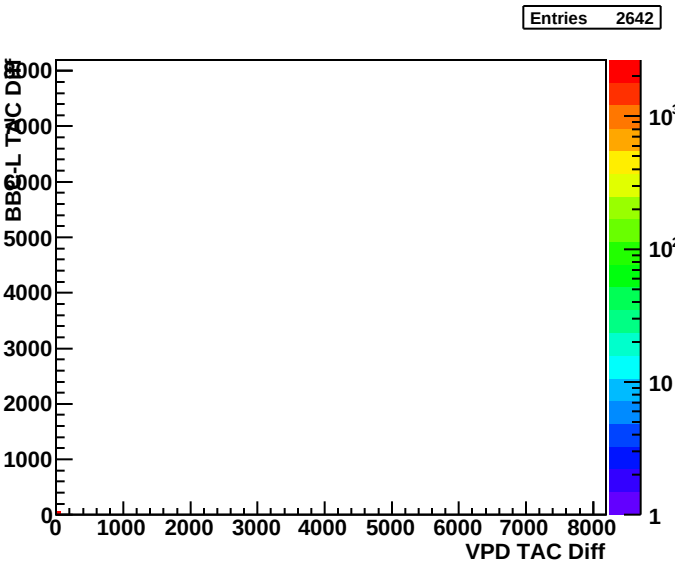

Entries 2642
BBC-L-East ADC Sum
BBC-L-West ADC Sum

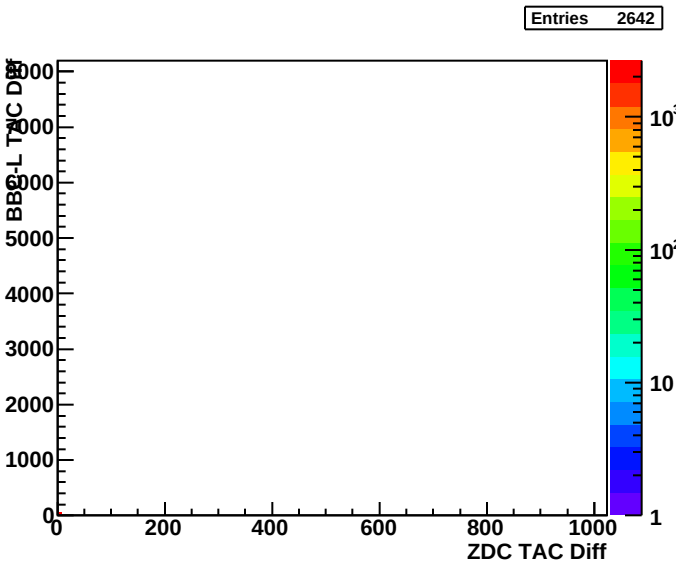

Entries 2642
BBC-S-East ADC Sum
BBC-S-West ADC Sum

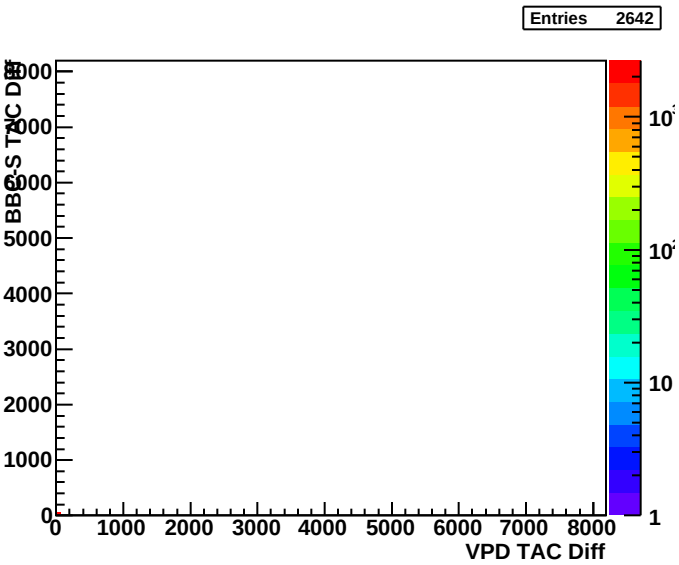

Entries 2642
VPD East ADC Sum
VPD West ADC Sum

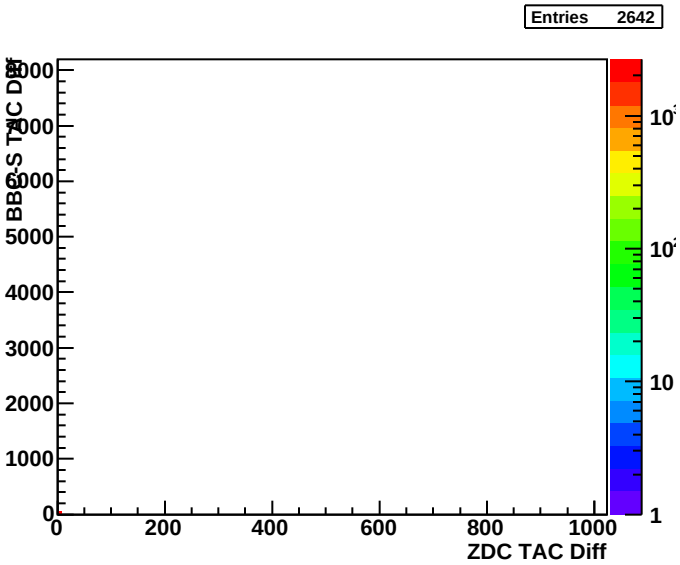

Entries 2642
ZDC East ADC Sum Att
ZDC West ADC Sum Att

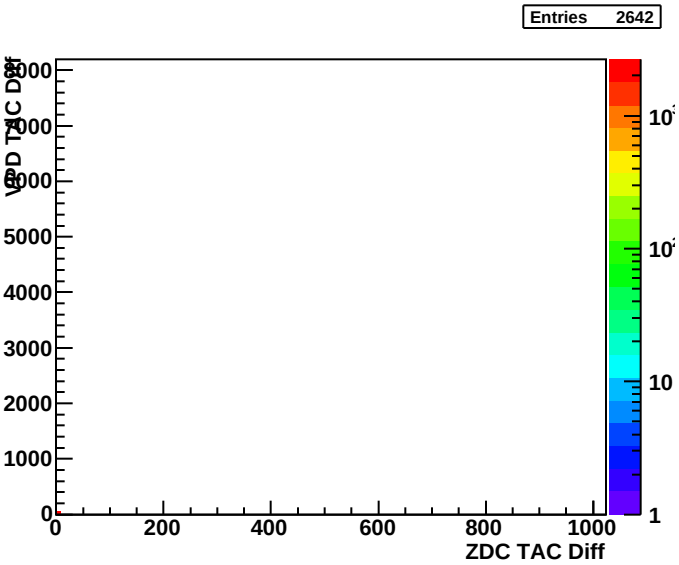

Entries 2642
BBC-L TAC Diff
BCC-S TAC Diff

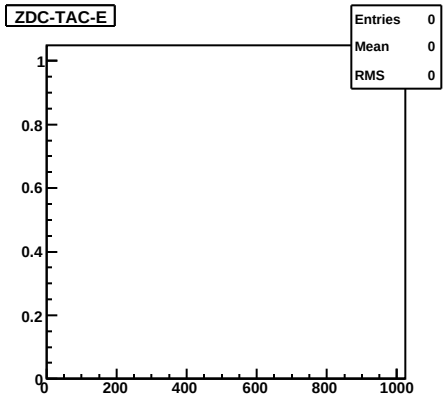

Entries 2642
BBC-L TAC Diff
VPD TAC Diff

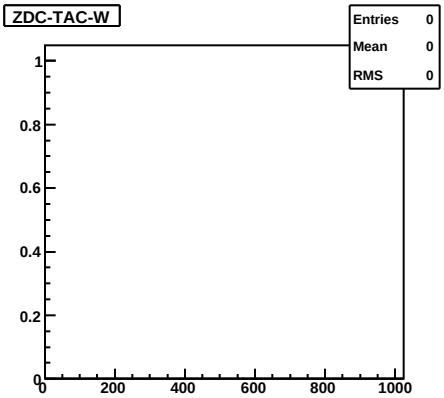

Entries 2642
BBC-L TAC Diff
ZDC TAC Diff

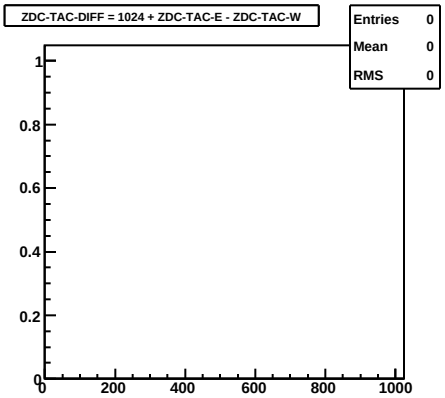

Entries 2642
BBC-S TAC Diff
VPD TAC Diff

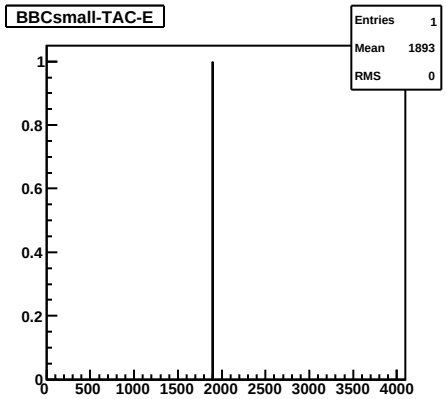

Entries 2642
BBC-S TAC Diff
ZDC TAC Diff

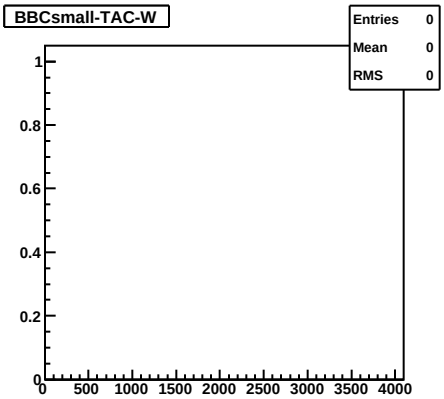

Entries 2642
VPD TAC Diff
ZDC TAC Diff

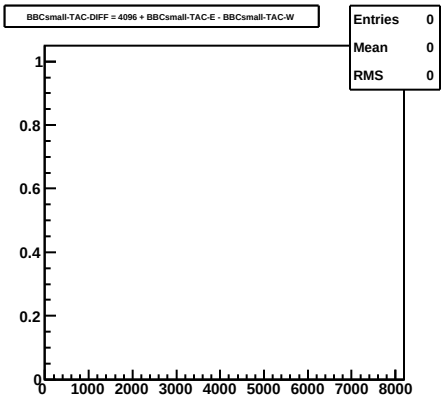

ZDC-TAC-E
Entries 0
Mean 0
RMS 0

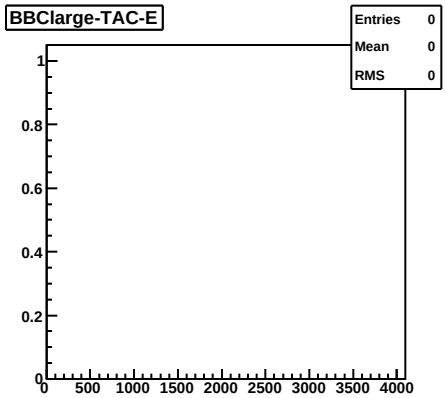

ZDC-TAC-W
Entries 0
Mean 0
RMS 0

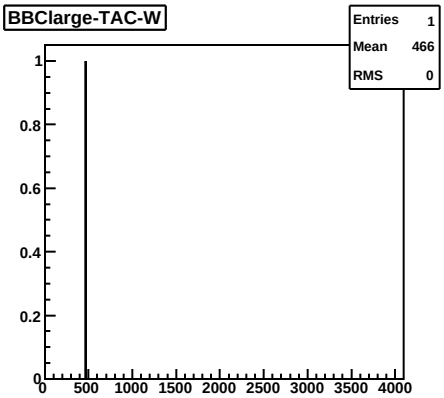

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 0
Mean 0
RMS 0

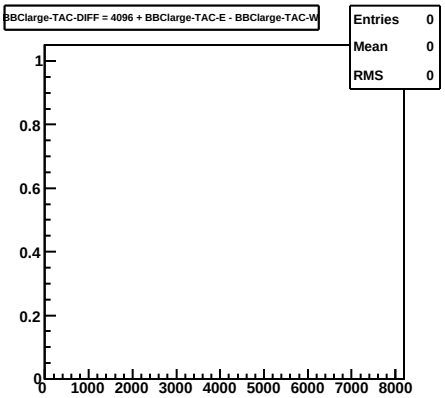

BBCsmall-TAC-E
Entries 1
Mean 1893
RMS 0

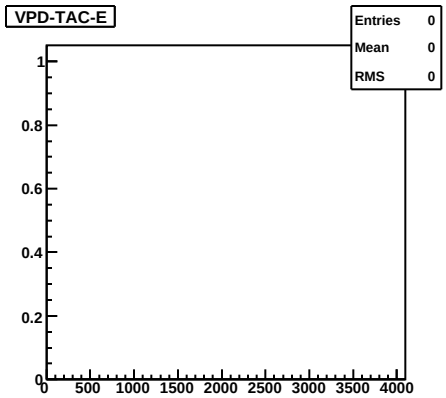

BBCsmall-TAC-W
Entries 0
Mean 0
RMS 0

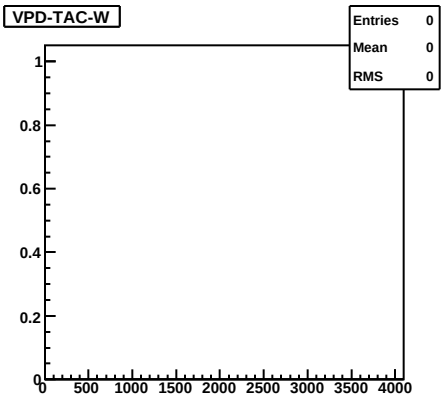

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 0
Mean 0
RMS 0

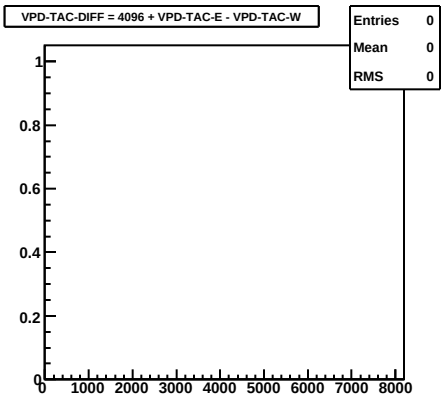

BBClarge-TAC-E
Entries 0
Mean 0
RMS 0

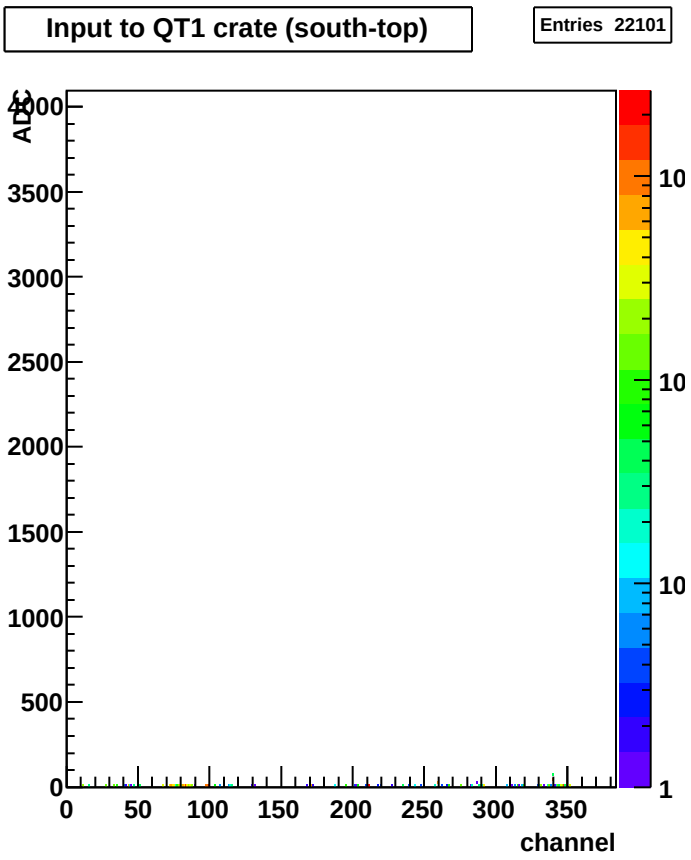

BBClarge-TAC-W
Entries 1
Mean 466
RMS 0

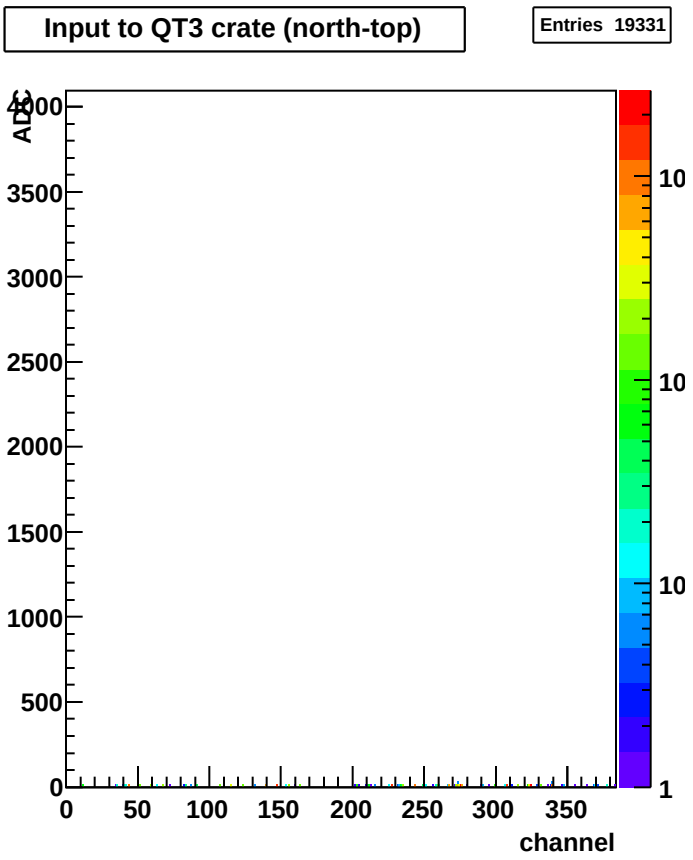

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 0
Mean 0
RMS 0

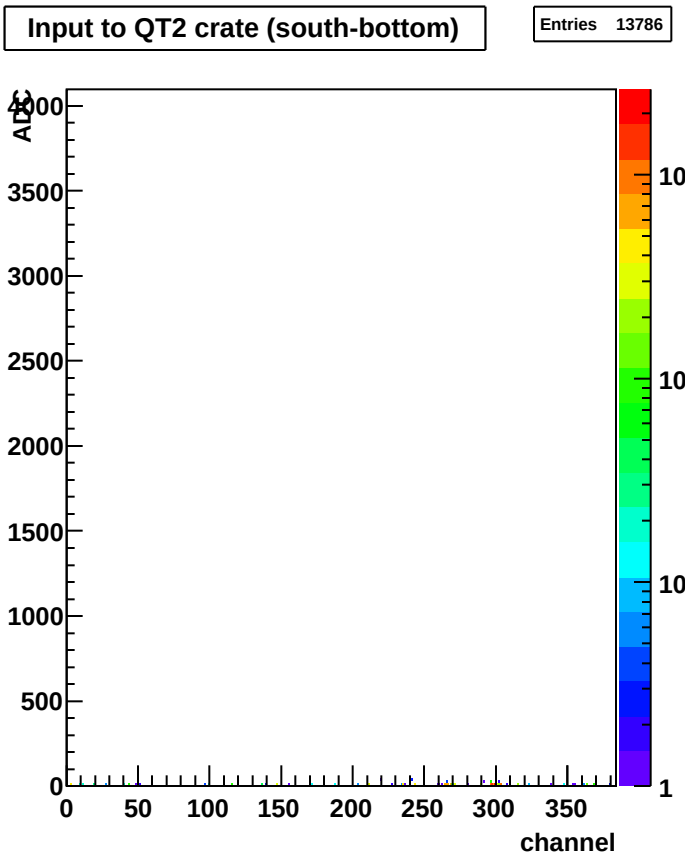

VPD-TAC-E
Entries 0
Mean 0
RMS 0

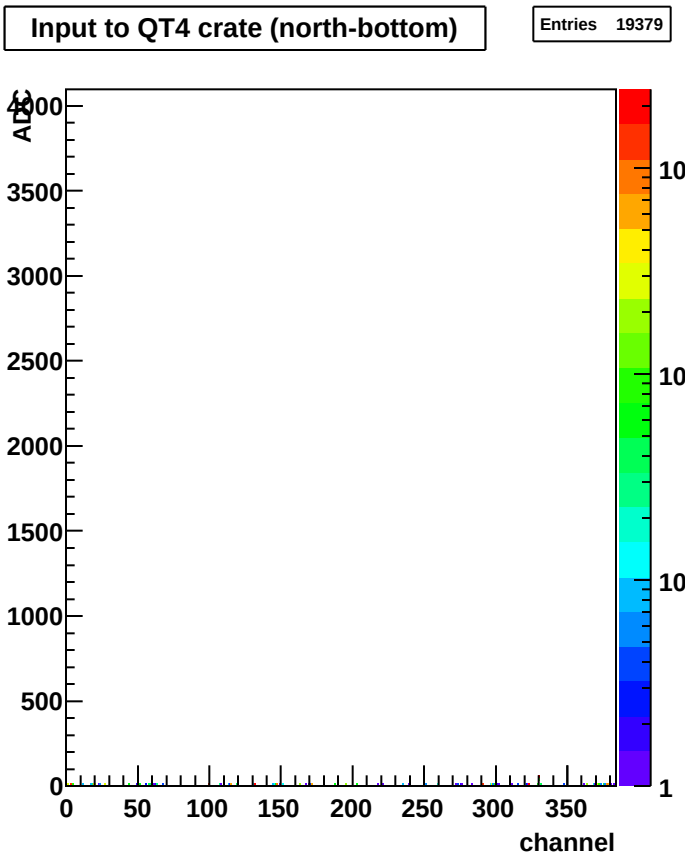

VPD-TAC-W
Entries 0
Mean 0
RMS 0

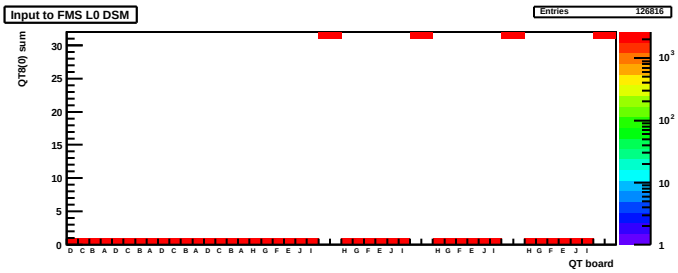

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 0
Mean 0
RMS 0

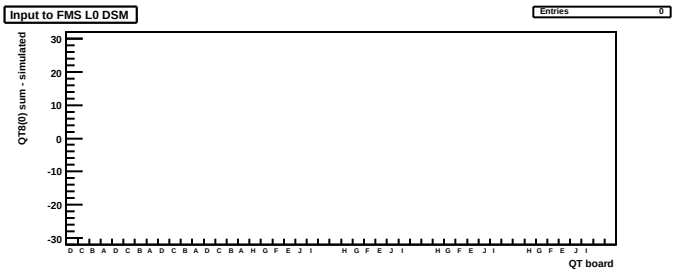

Input to QT1 crate (south-top)
Entries 22101
ADC
channel

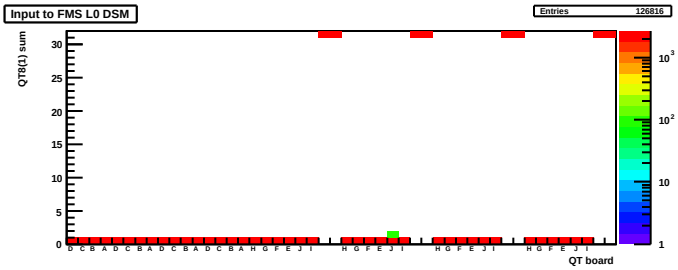

Input to QT3 crate (north-top)
Entries 19331
ADC
channel

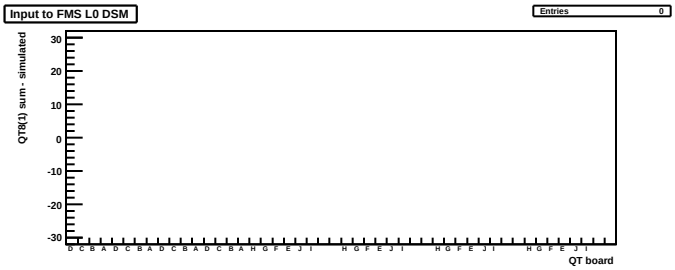

Input to QT2 crate (south-bottom)
Entries 13786
ADC
channel

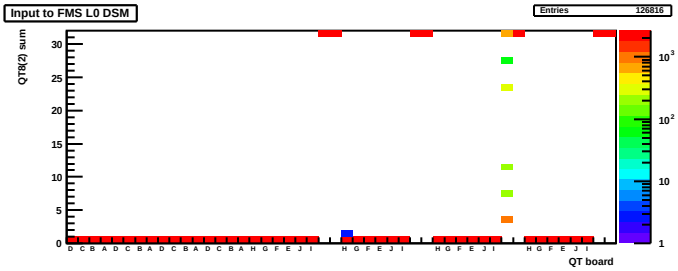

Input to QT4 crate (north-bottom)
Entries 19379
ADC
channel

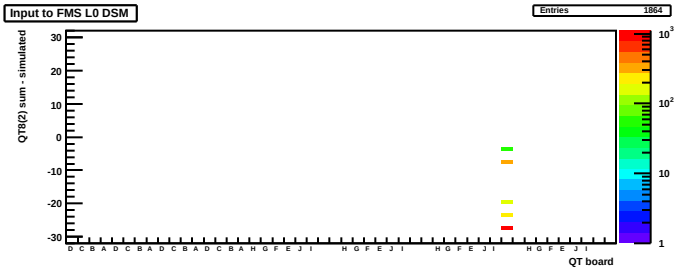

Input to FMS L0 DSM
Entries 126816
QT8(0) sum
QT board

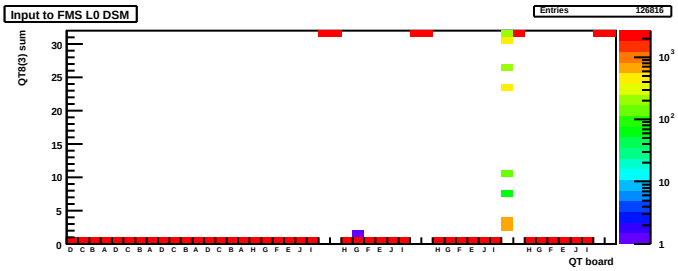

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

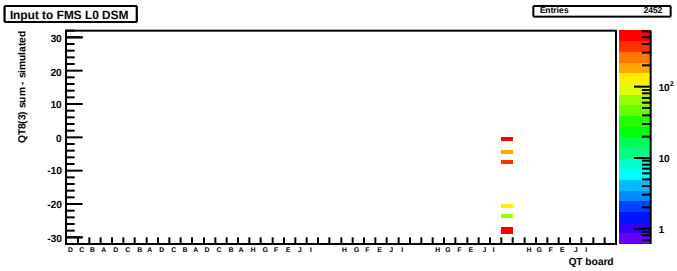

Input to FMS L0 DSM
Entries 126816
QT8(1) sum
QT board

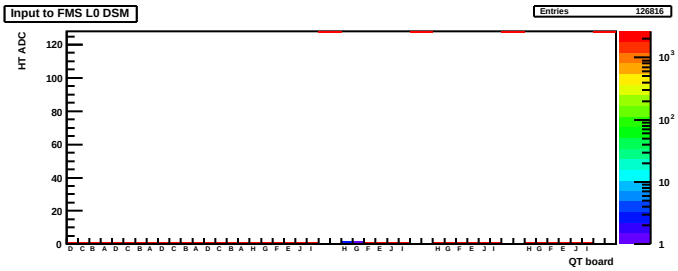

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

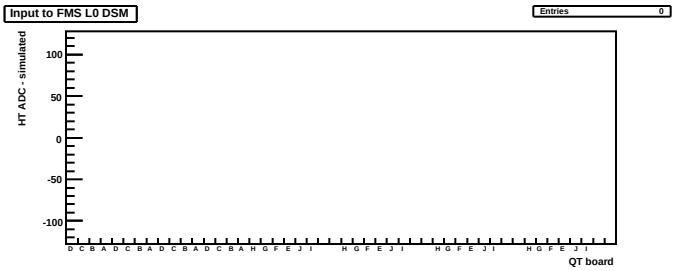

Input to FMS L0 DSM
Entries 126816
QT8(2) sum
QT board

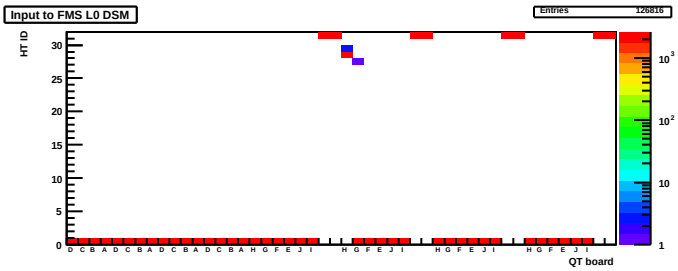

Input to FMS L0 DSM
Entries 1864
QT8(2) sum - simulated
QT board

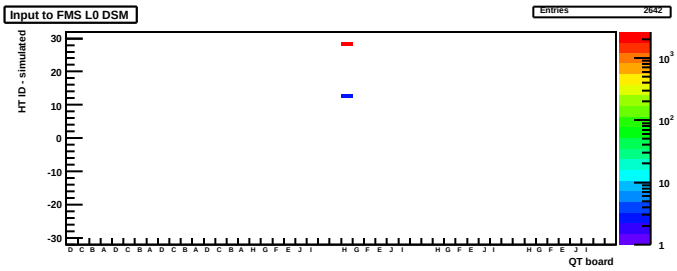

Input to FMS L0 DSM
Entries 126816
QT8(3) sum
QT board

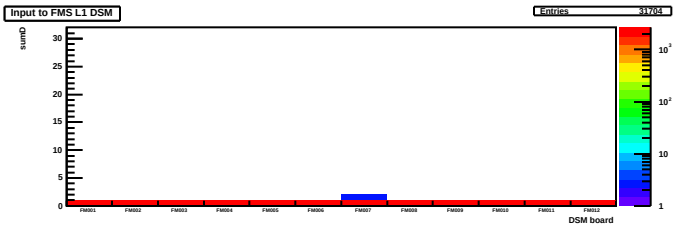

Input to FMS L0 DSM
Entries 2452
QT8(3) sum - simulated
QT board

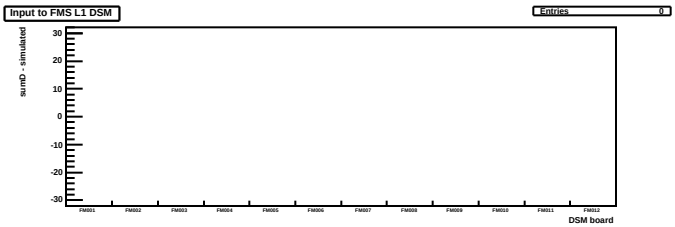

Input to FMS L0 DSM
Entries 126816
HT ADC
QT board

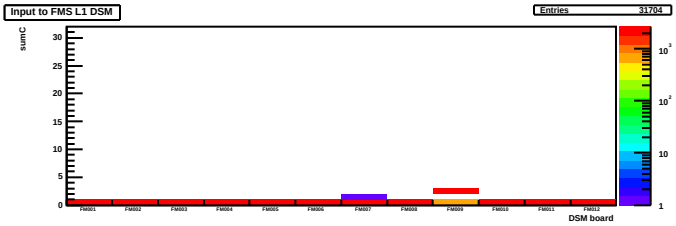

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

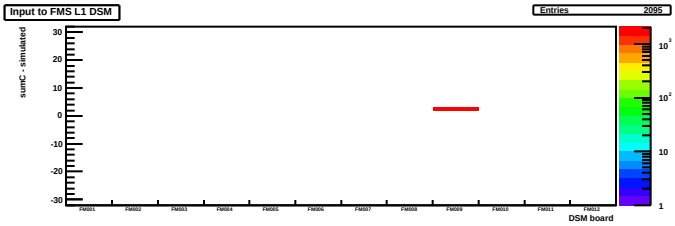

Input to FMS L0 DSM
Entries 126816
HT ID
QT board

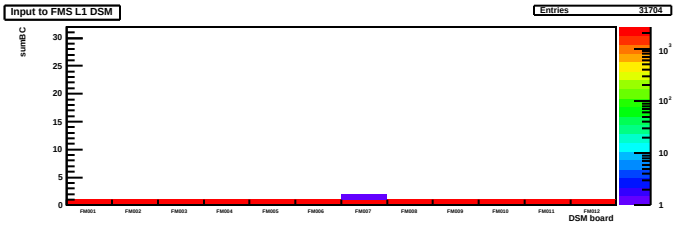

Input to FMS L0 DSM
Entries 2642
HT ID - simulated
QT board

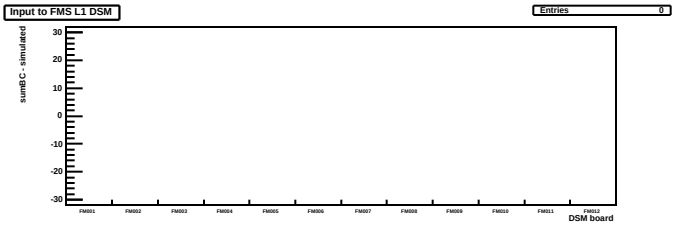

Input to FMS L1 DSM
Entries 31704
sumD
DSM board

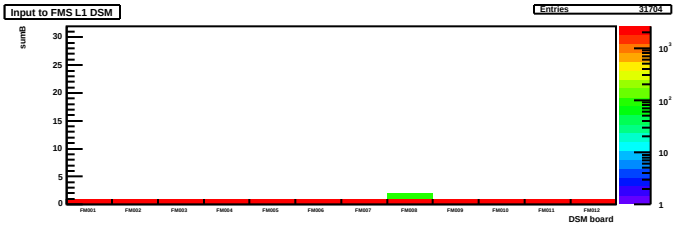

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

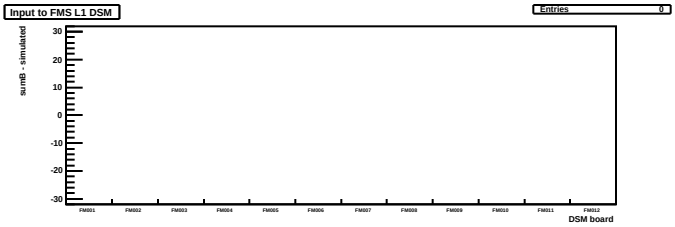

Input to FMS L1 DSM
Entries 31704
sumC
DSM board

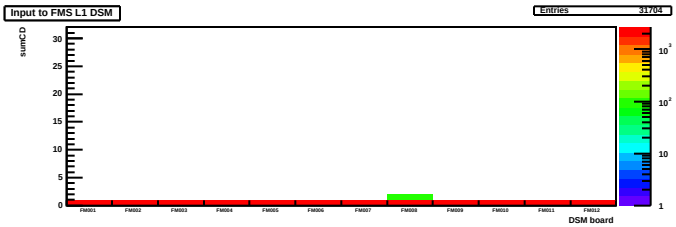

Input to FMS L1 DSM
Entries 2095
sumC - simulated
DSM board

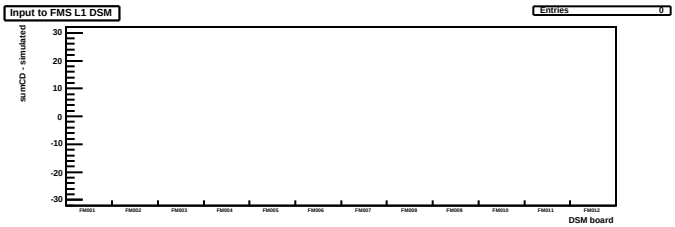

Input to FMS L1 DSM
Entries 31704
sumBC
DSM board

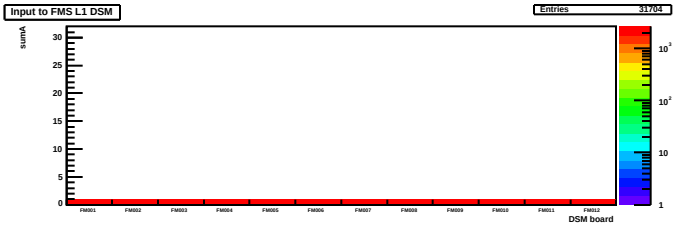

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

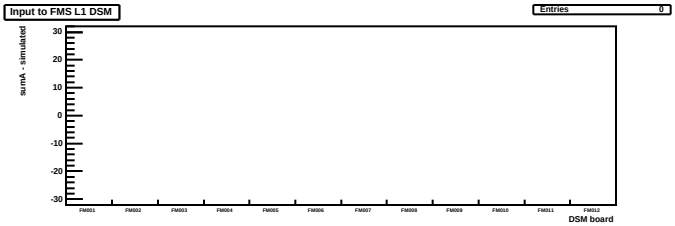

Input to FMS L1 DSM
Entries 31704
sumB
DSM board

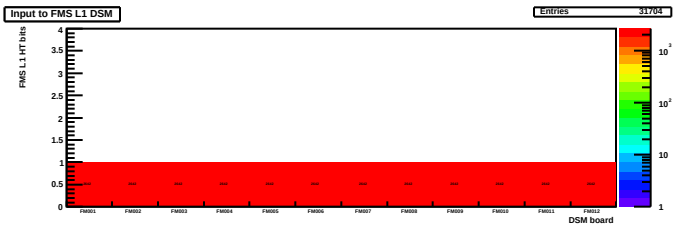

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

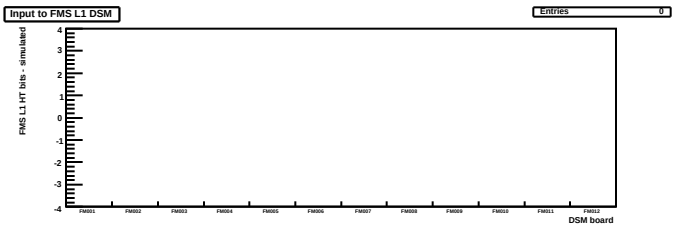

Input to FMS L1 DSM
Entries 31704
sumCD
DSM board

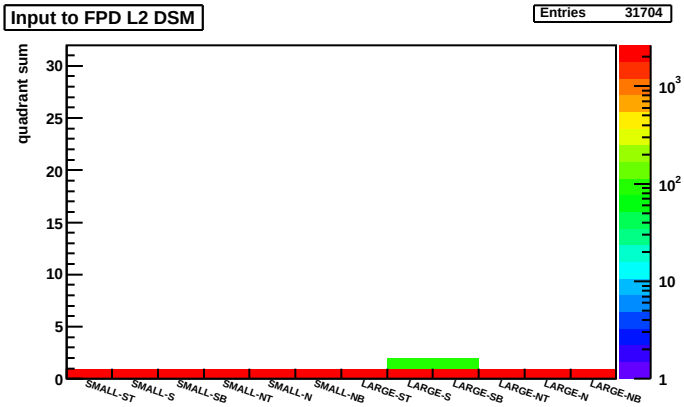

Input to FMS L1 DSM
Entries 0
sumCD - simulated
DSM board

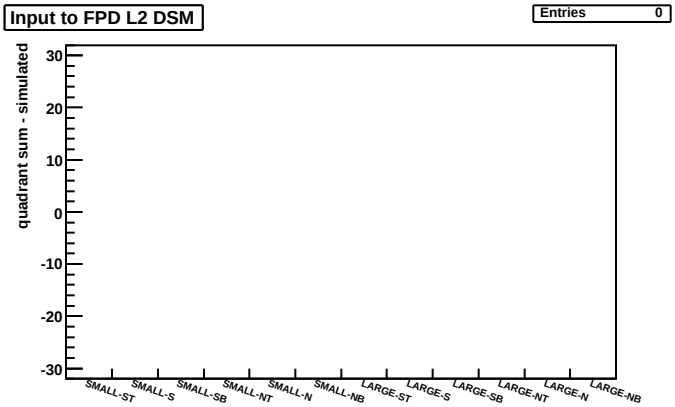

Input to FMS L1 DSM
Entries 31704
sumA
DSM board

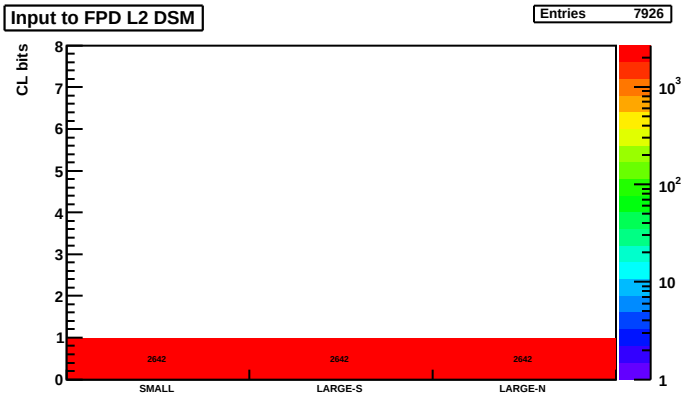

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

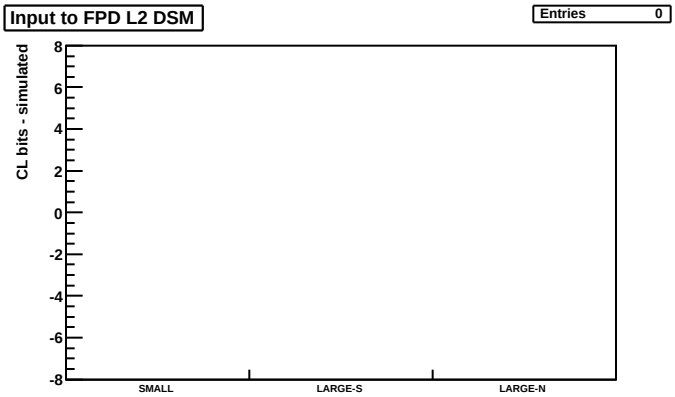

Input to FMS L1 DSM
Entries 31704
FMS L1 HT bits
DSM board

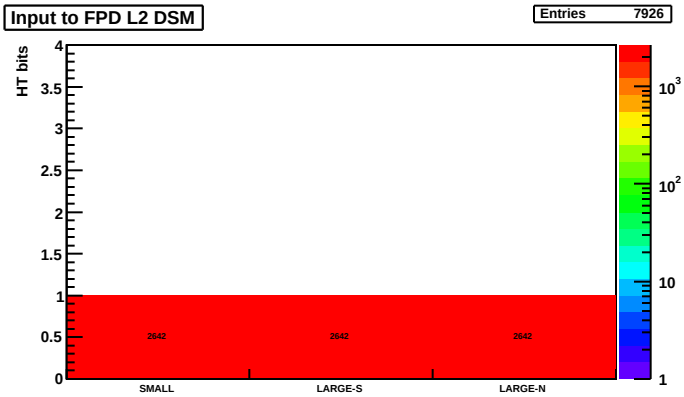

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

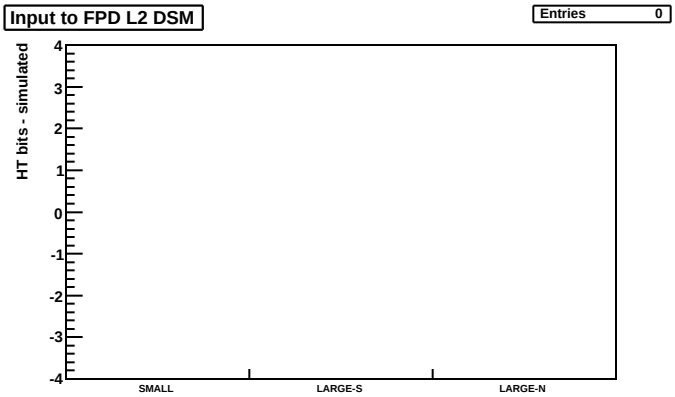

Input to FPD L2 DSM
Entries 31704
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

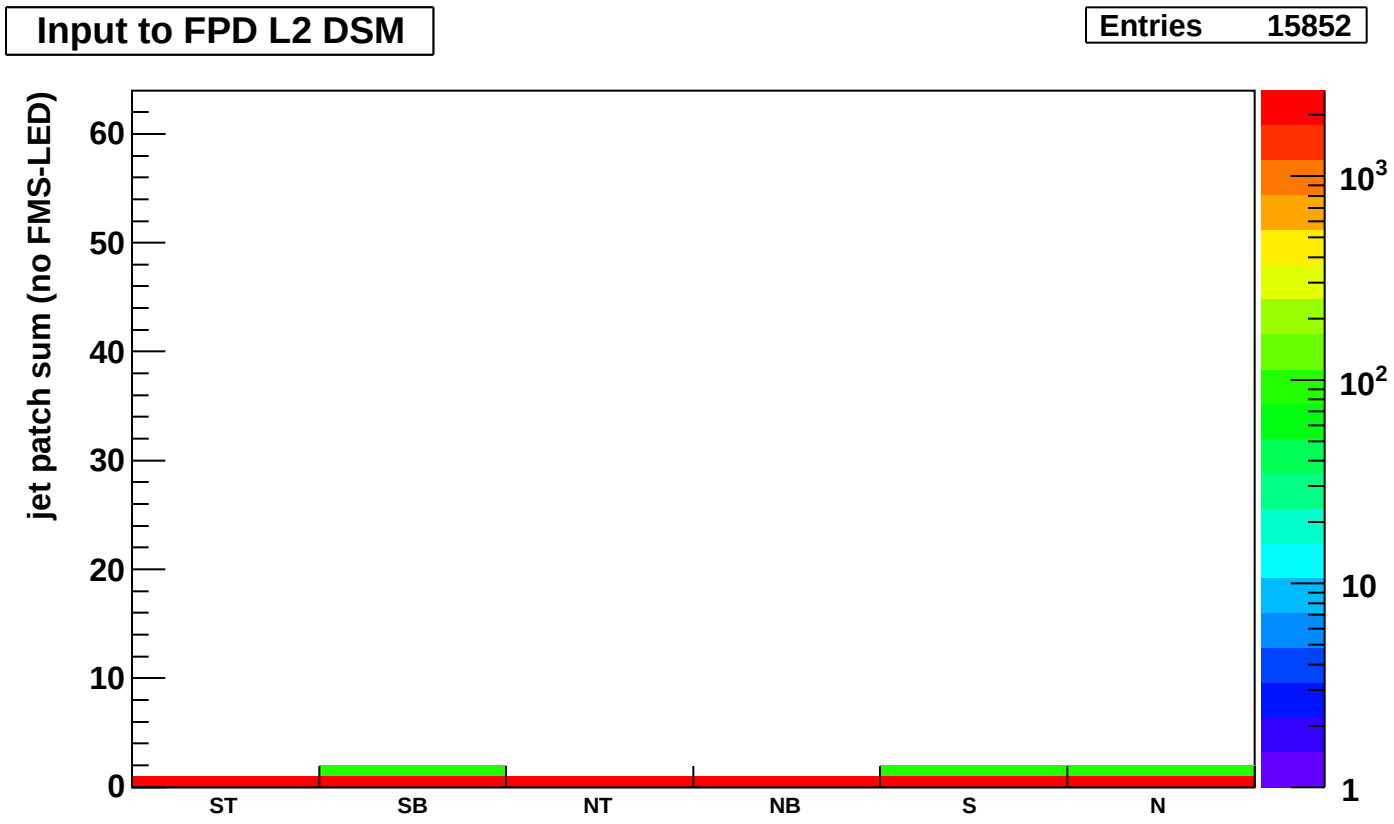

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

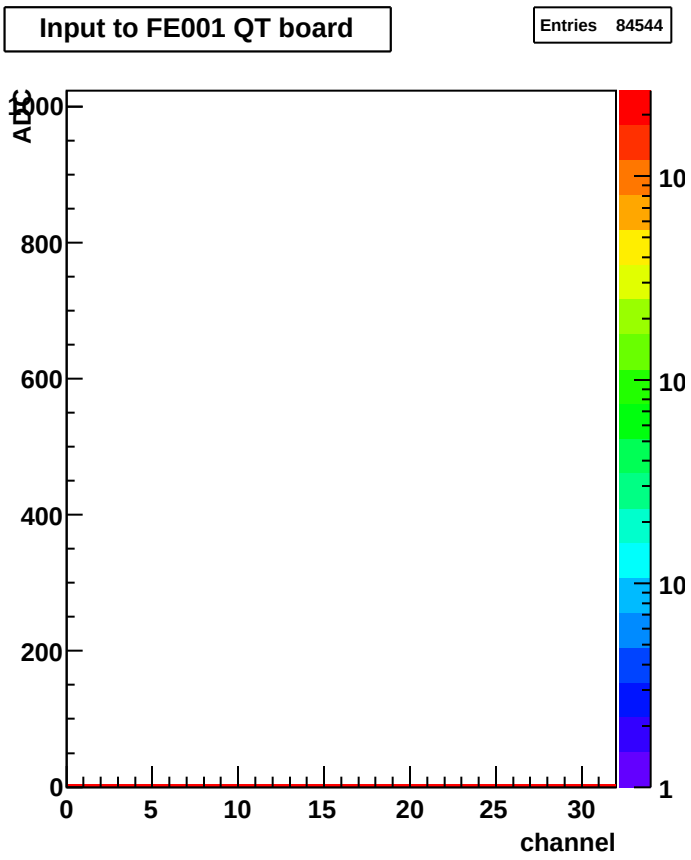

Input to FPD L2 DSM
Entries 7926
CL bits
2642
2642
2642
SMALL
LARGE-S
LARGE-N

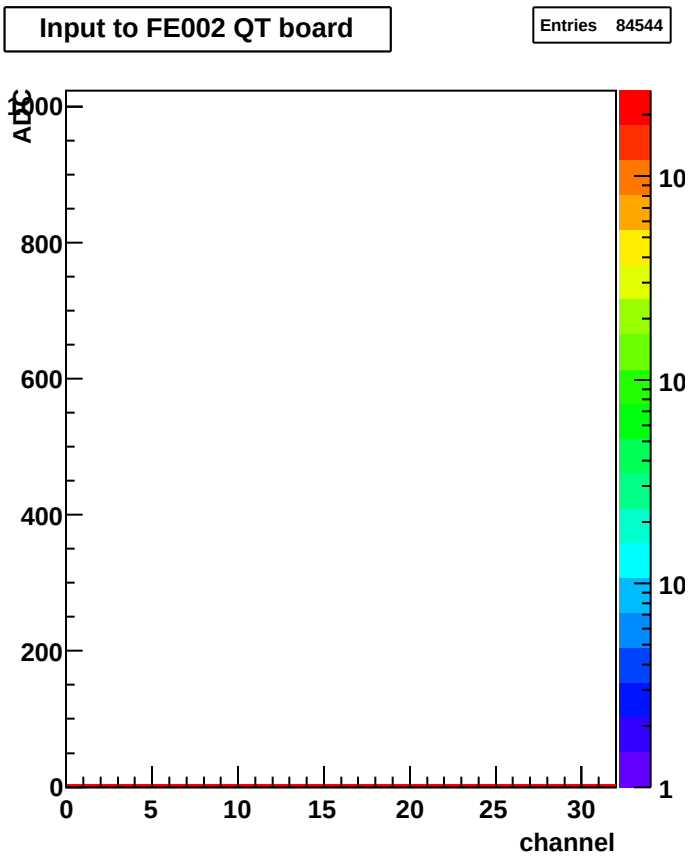

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

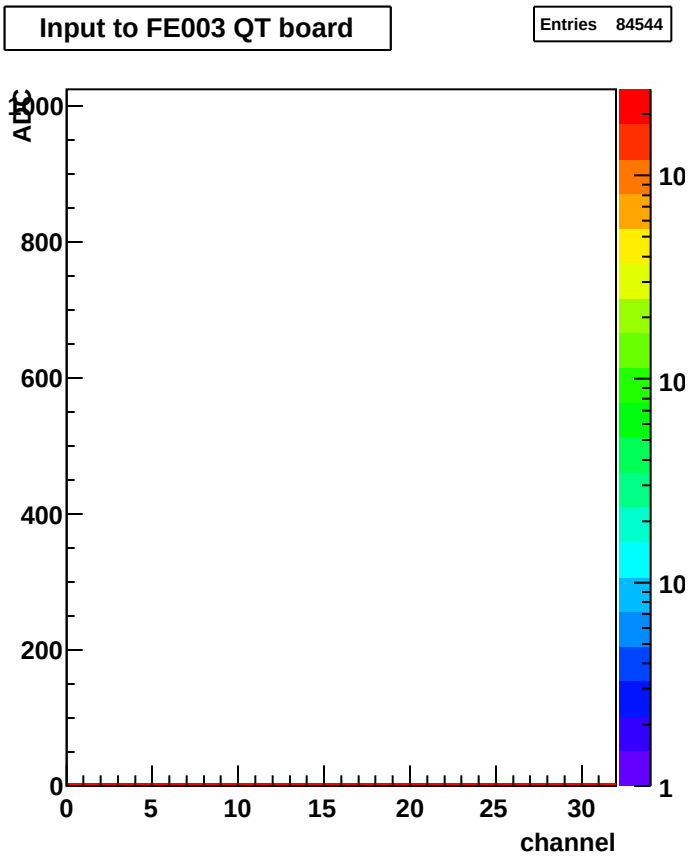

Input to FPD L2 DSM
Entries 7926
HT bits
2642
2642
2642
SMALL
LARGE-S
LARGE-N

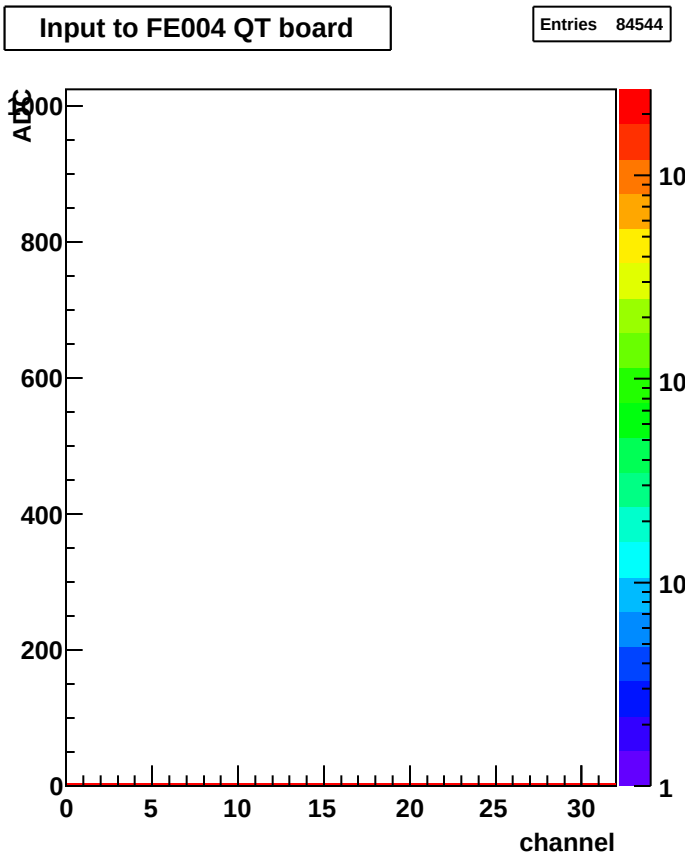

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

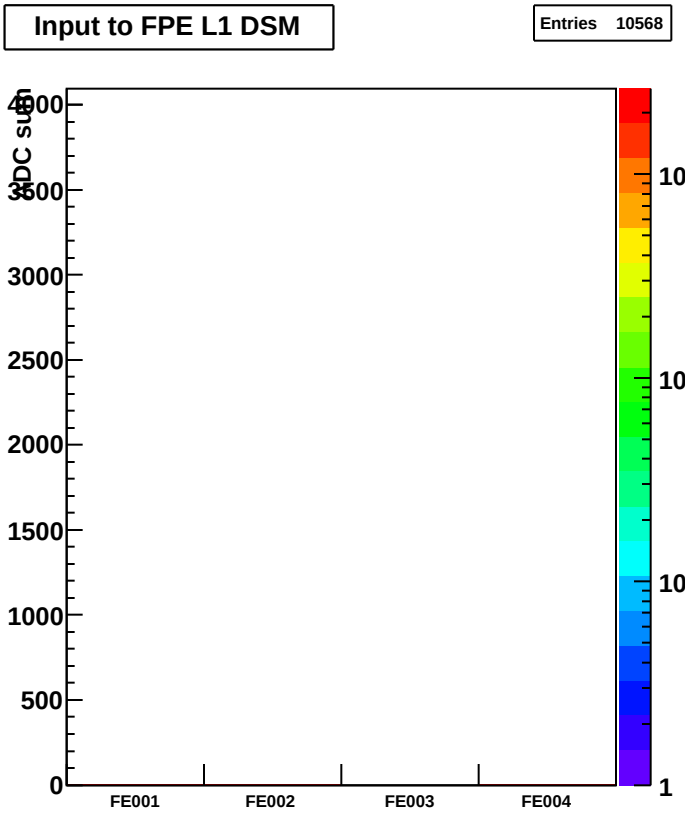

Input to FPD L2 DSM
Entries 15852
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
$10^2$
$10^3$

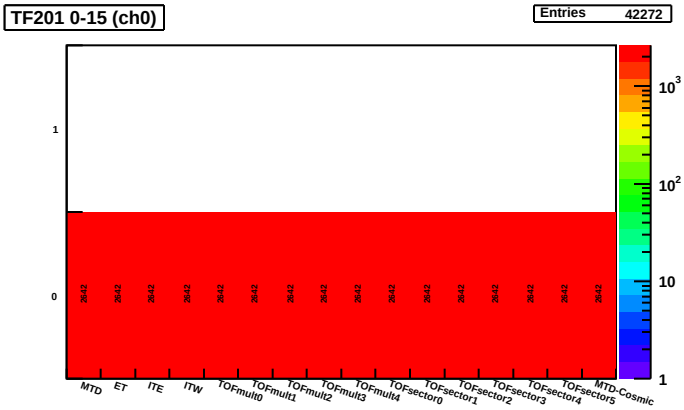

Input to FE001 QT board
Entries 84544
ADC
channel

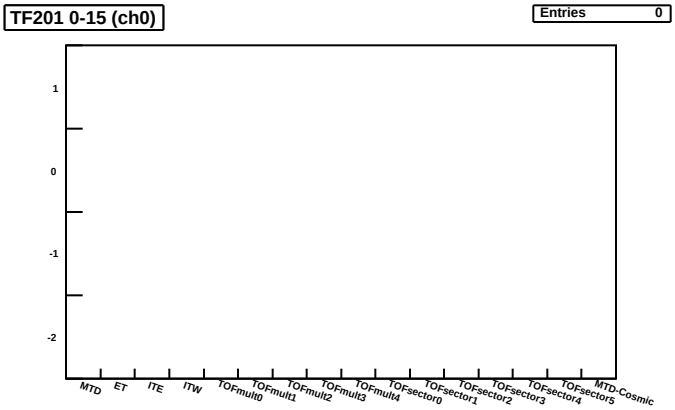

Input to FE002 QT board
Entries 84544
ADC
channel

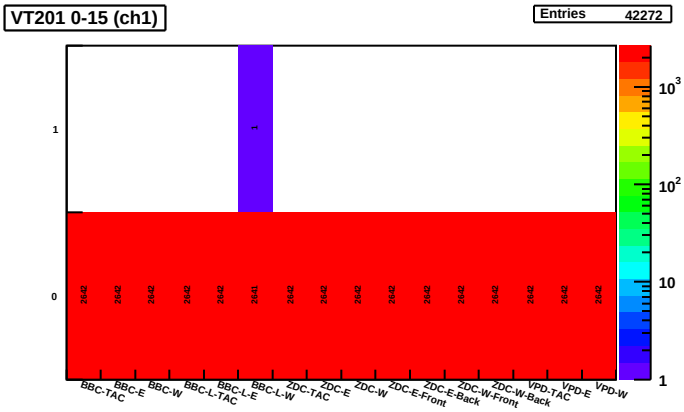

Input to FE003 QT board
Entries 84544
ADC
channel

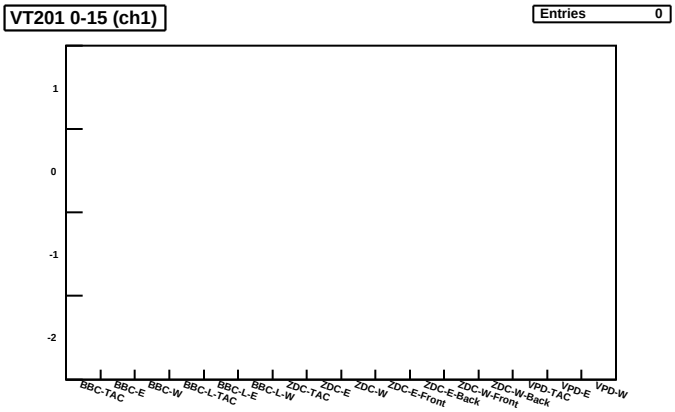

Input to FE004 QT board
Entries 84544
ADC
channel

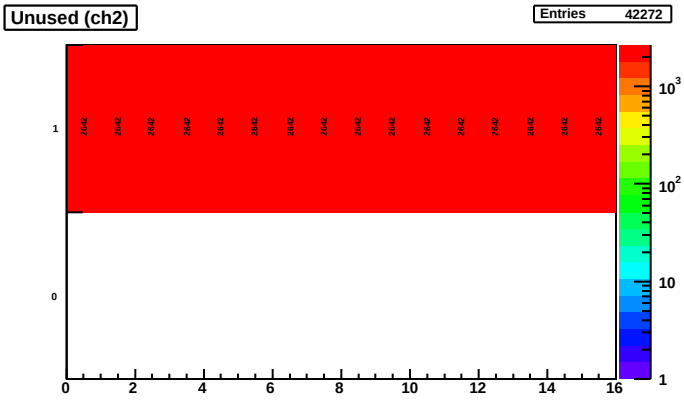

Input to FPE L1 DSM
Entries 10568
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
1

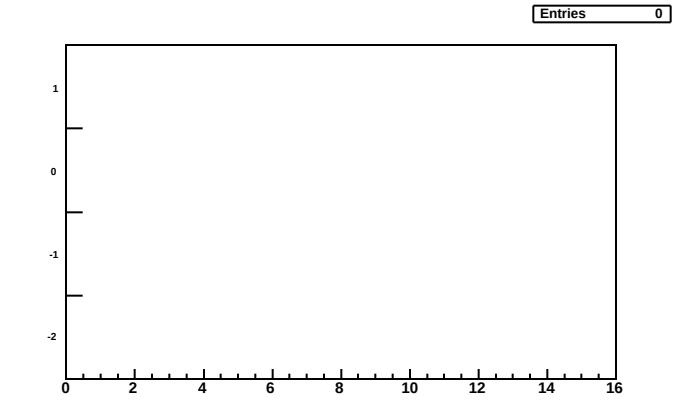

TF201 0-15 (ch0)
Entries 42272
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

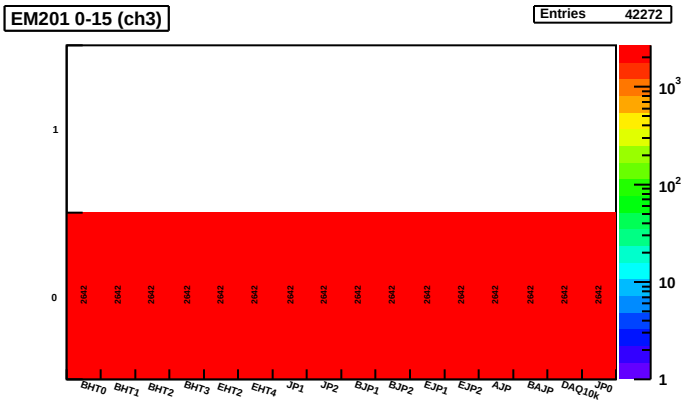

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

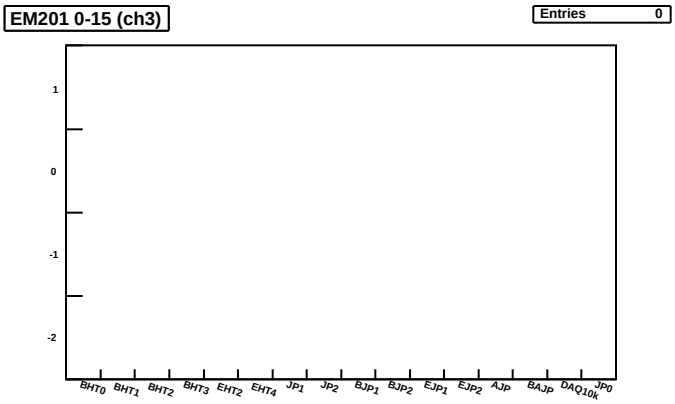

VT201 0-15 (ch1)
Entries 42272
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

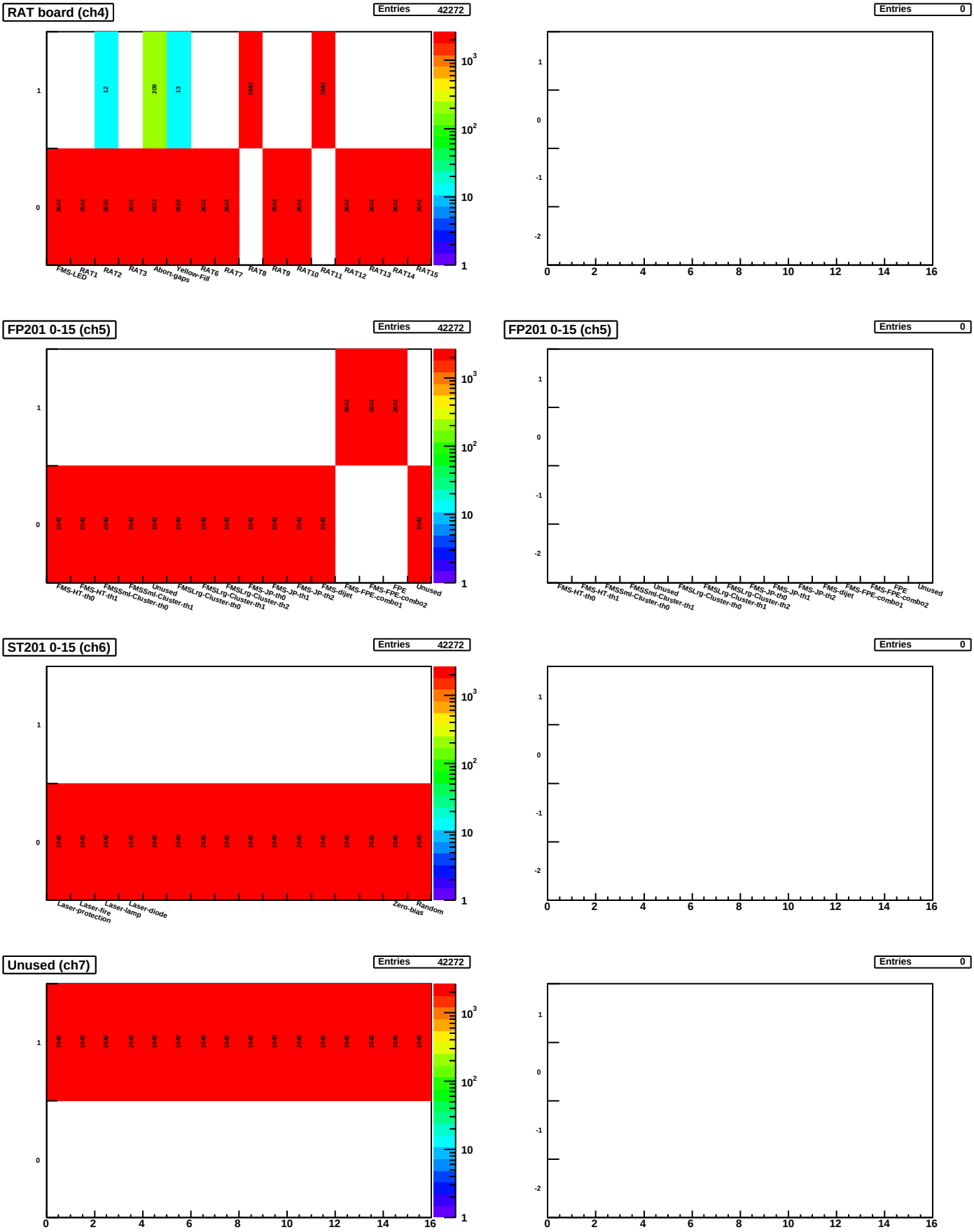

VT201 0-15 (ch1)
Entries 0
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 42272


Entries 0


EM201 0-15 (ch3)
Entries 42272
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0


EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0


RAT board (ch4)
Entries 42272
Entries 0
FMS-LED
RAT1
RAT2
RAT3
Abort-gaps
Yellow-Fill
RAT6
RAT7
RAT8
RAT9
RAT10
RAT11
RAT12
RAT13
RAT14
RAT15
FP201 0-15 (ch5)
Entries 42272
Entries 0
FMS-HT-th0
FMS-HT-th1
FMSSml-Cluster-th0
FMSSml-Cluster-th1
Unused
FMSLrg-Cluster-th0
FMSLrg-Cluster-th1
FMSLrg-Cluster-th2
FMS-JP-th0
FMS-JP-th1
FMS-JP-th2
FMS-diJet
FMS-FPE-combo1
FMS-FPE-combo2
FPE
Unused
ST201 0-15 (ch6)
Entries 42272
Entries 0
Laser-protection
Laser-fire
Laser-lamp
Laser-diode
Zero-bias
Random
Unused (ch7)
Entries 42272
Entries 0