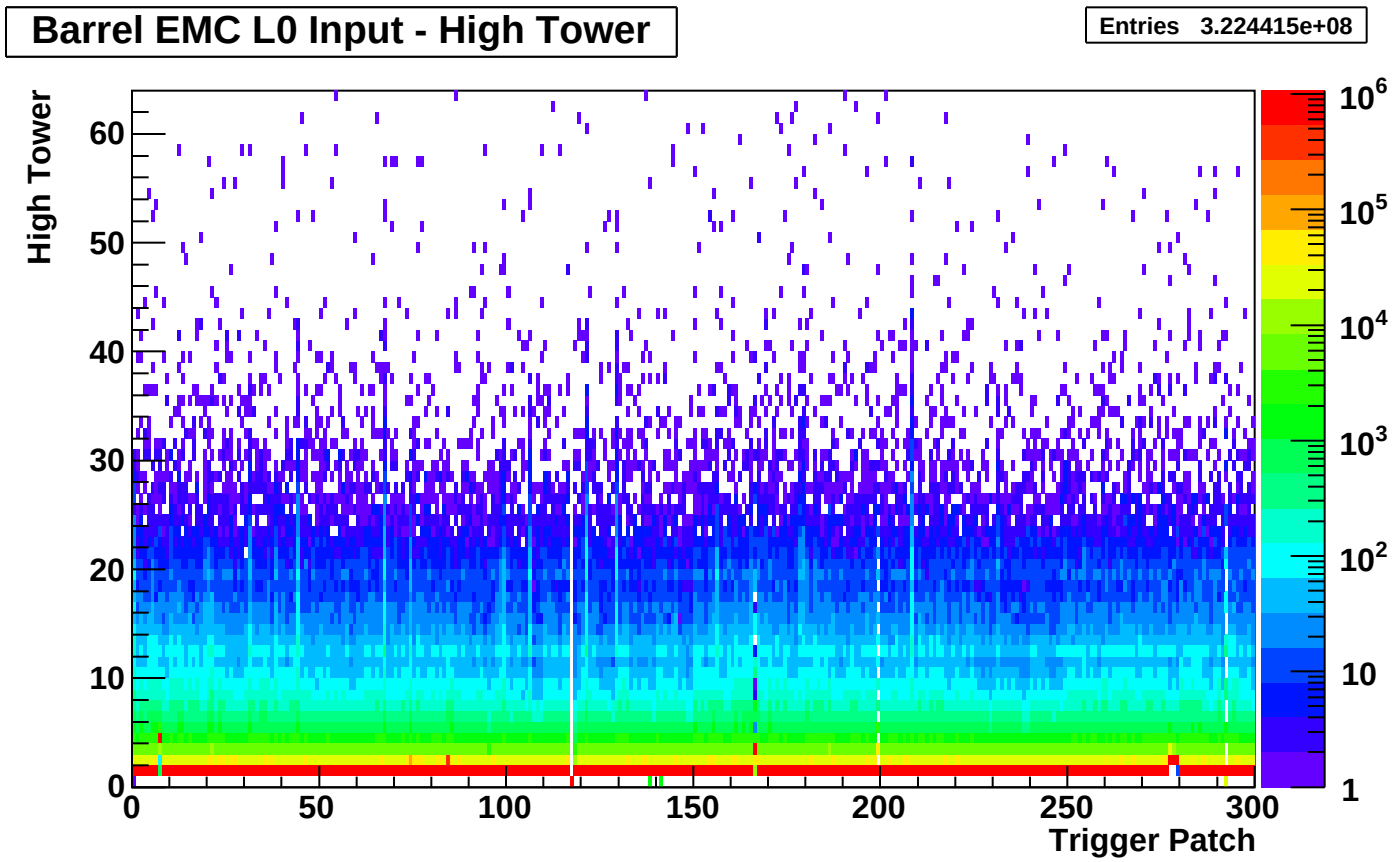
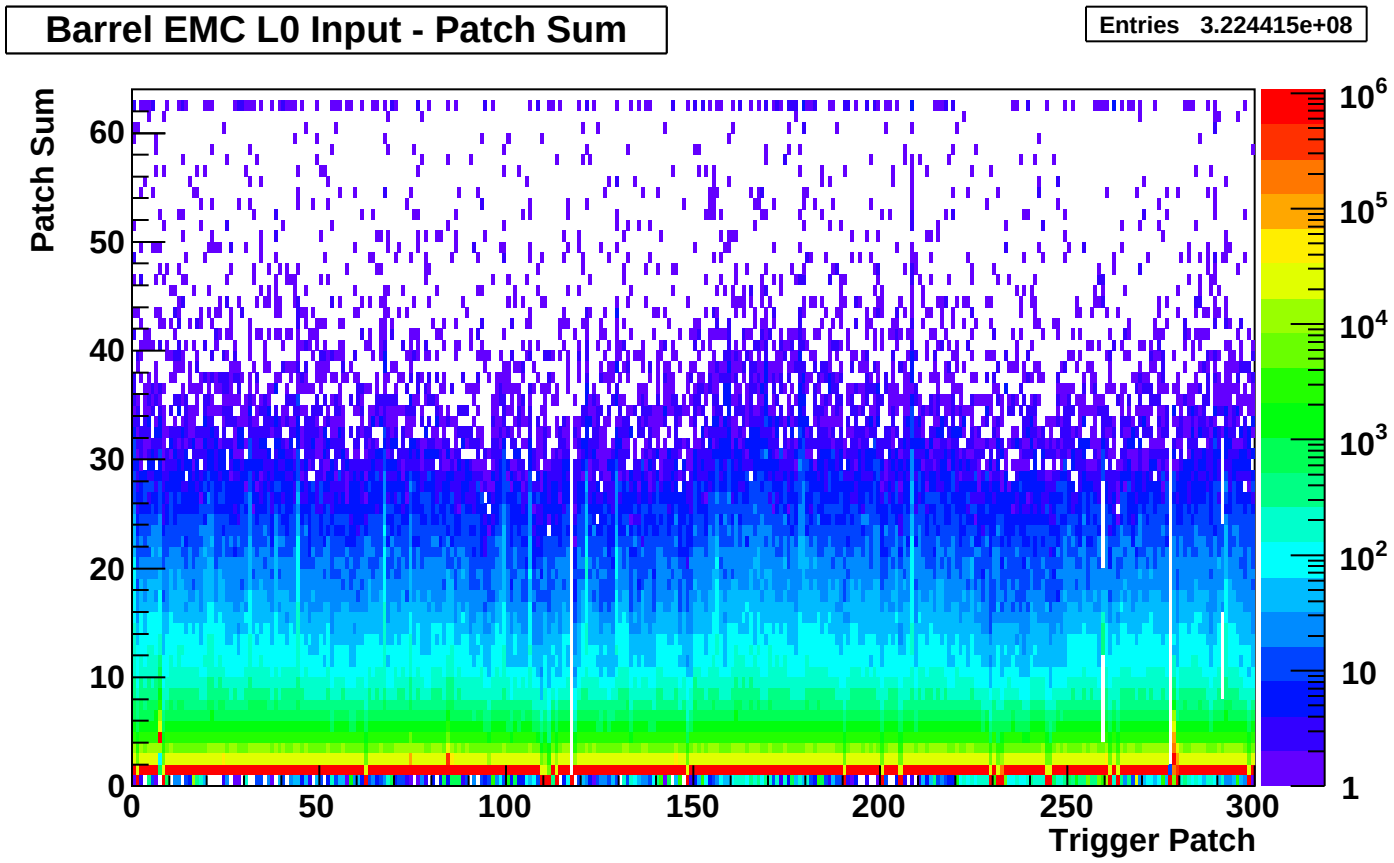
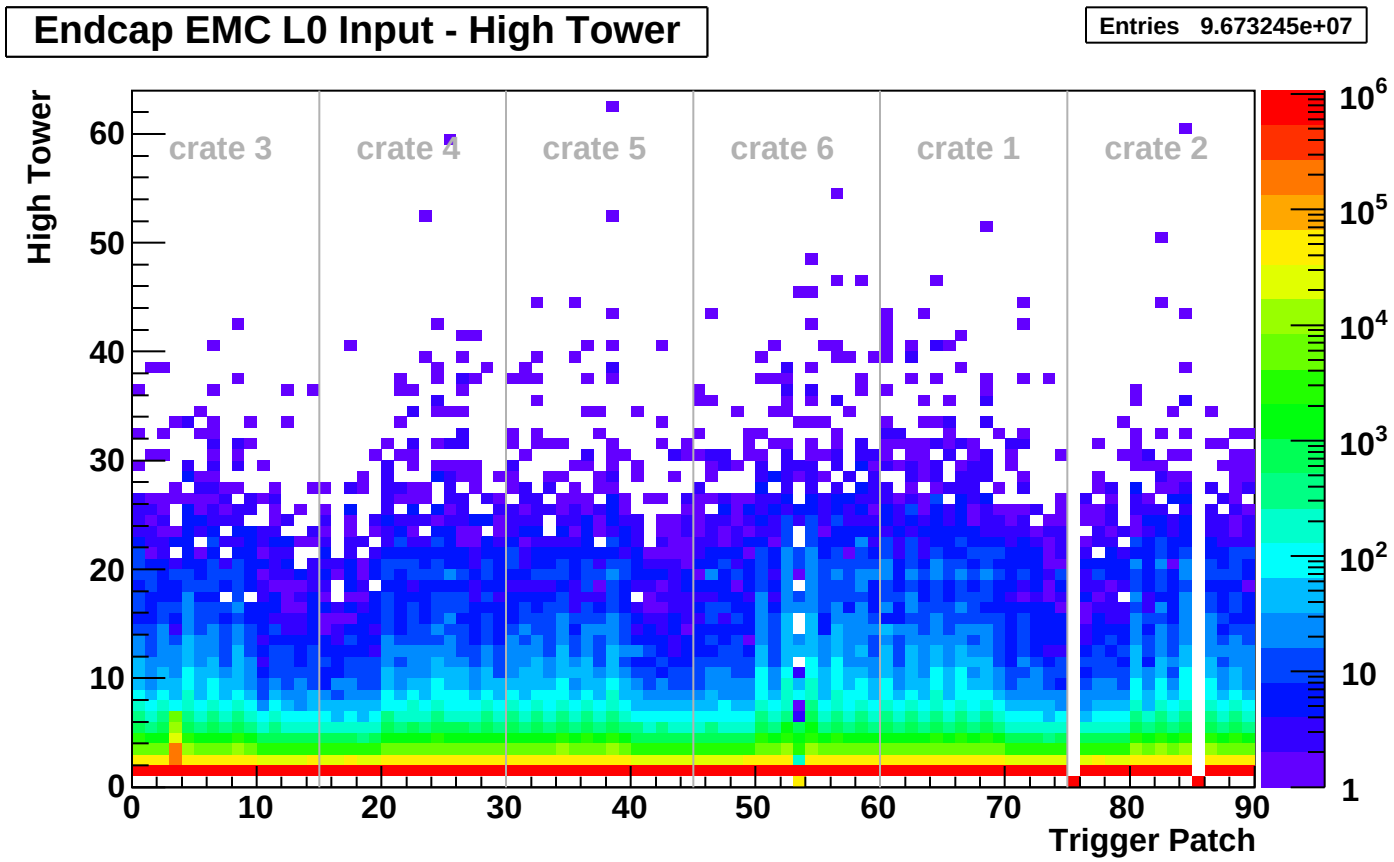
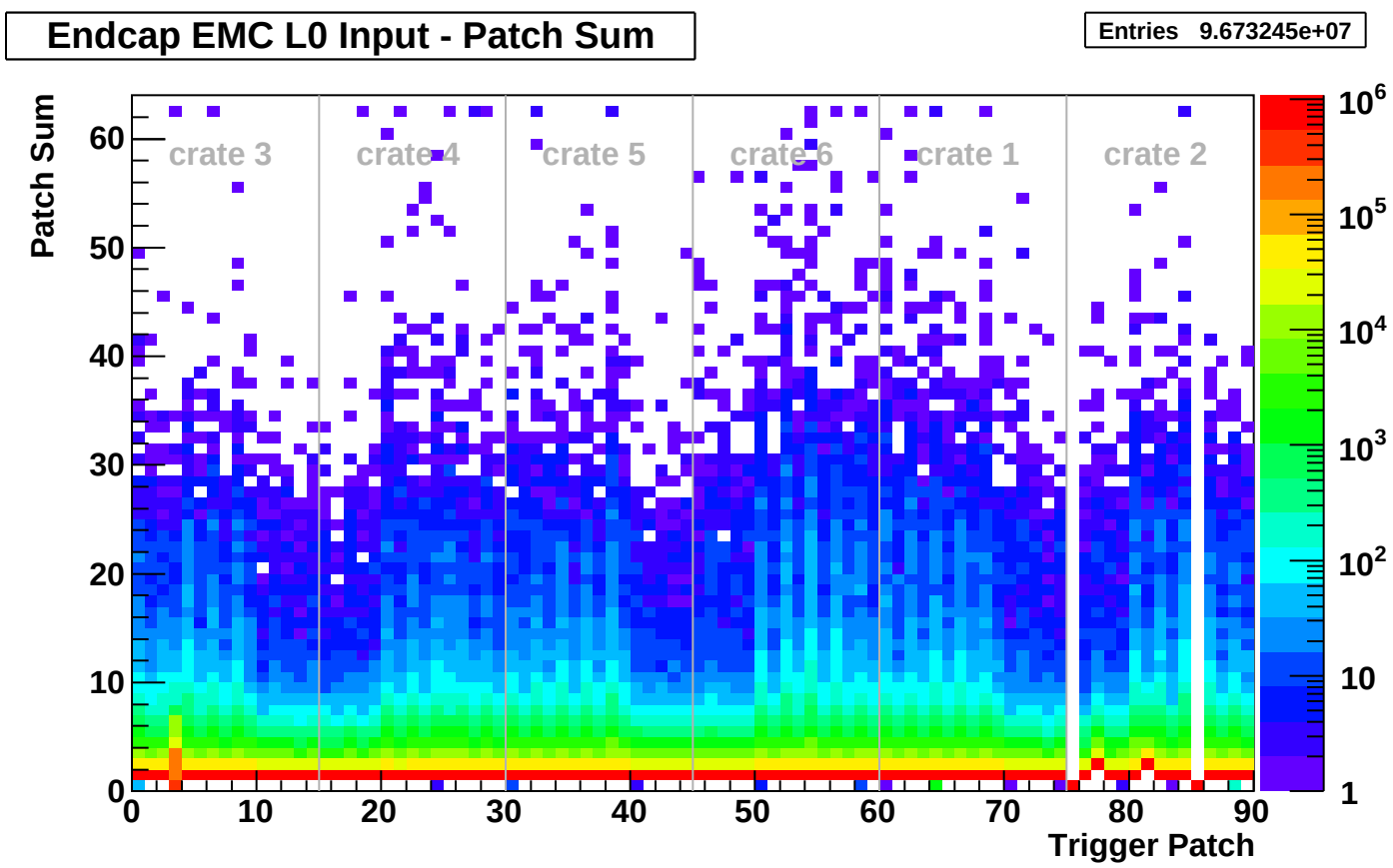
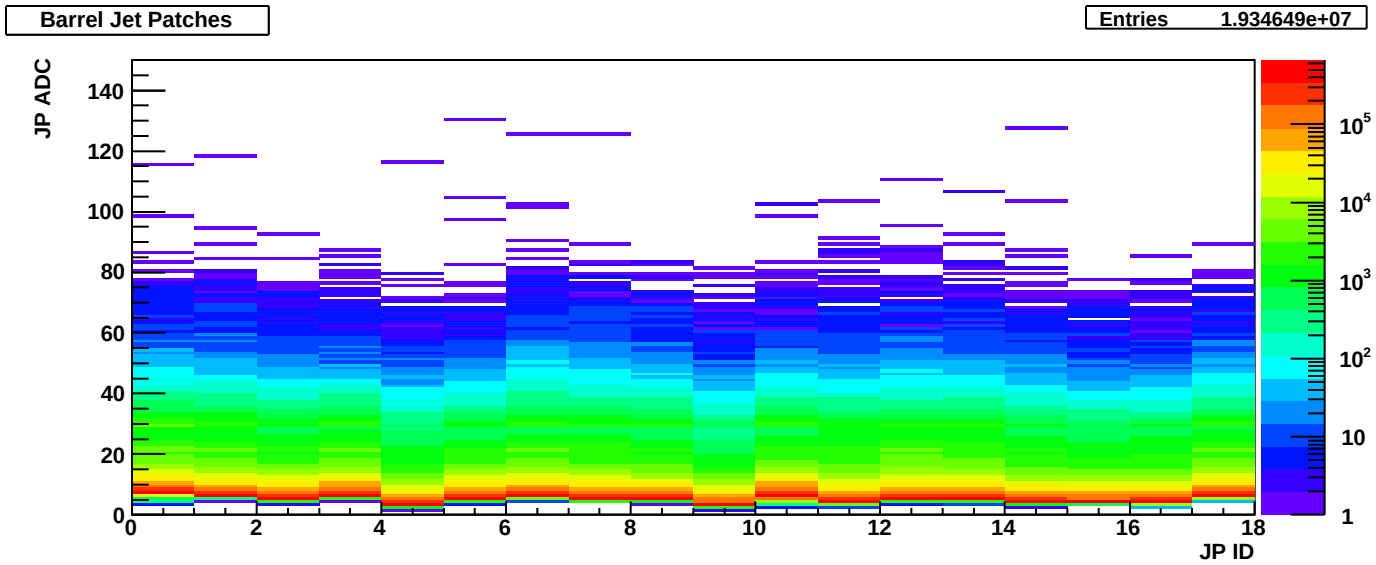
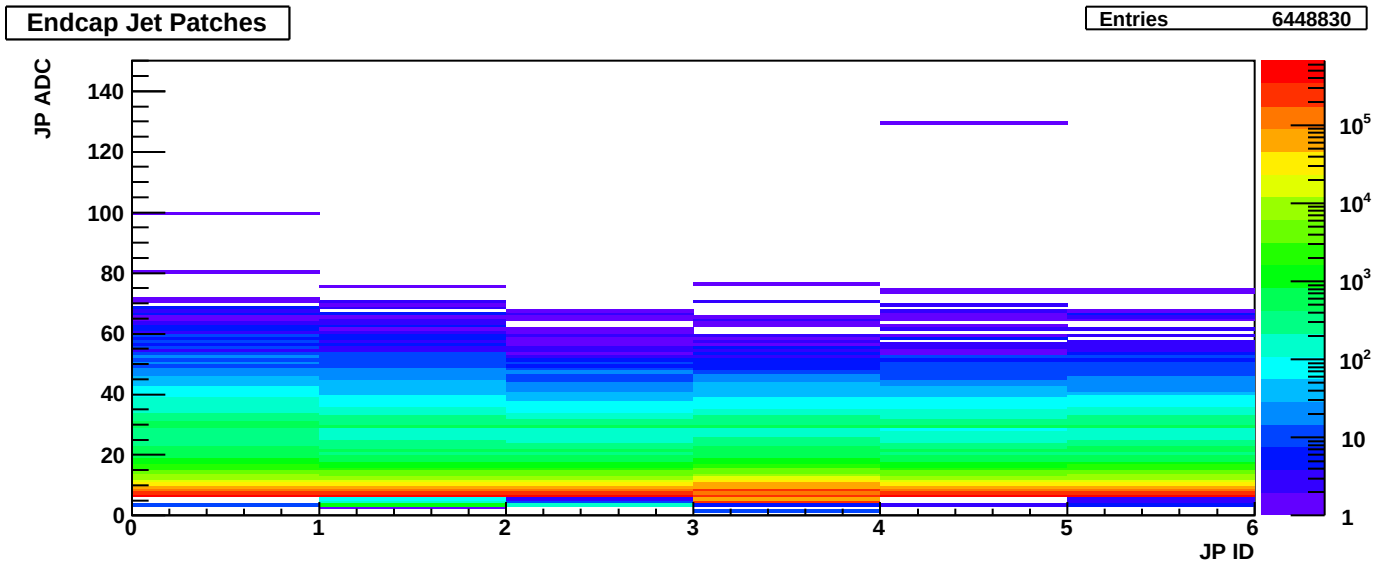

Barrel EMC L0 Input - High Tower
Entries 3.224415e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

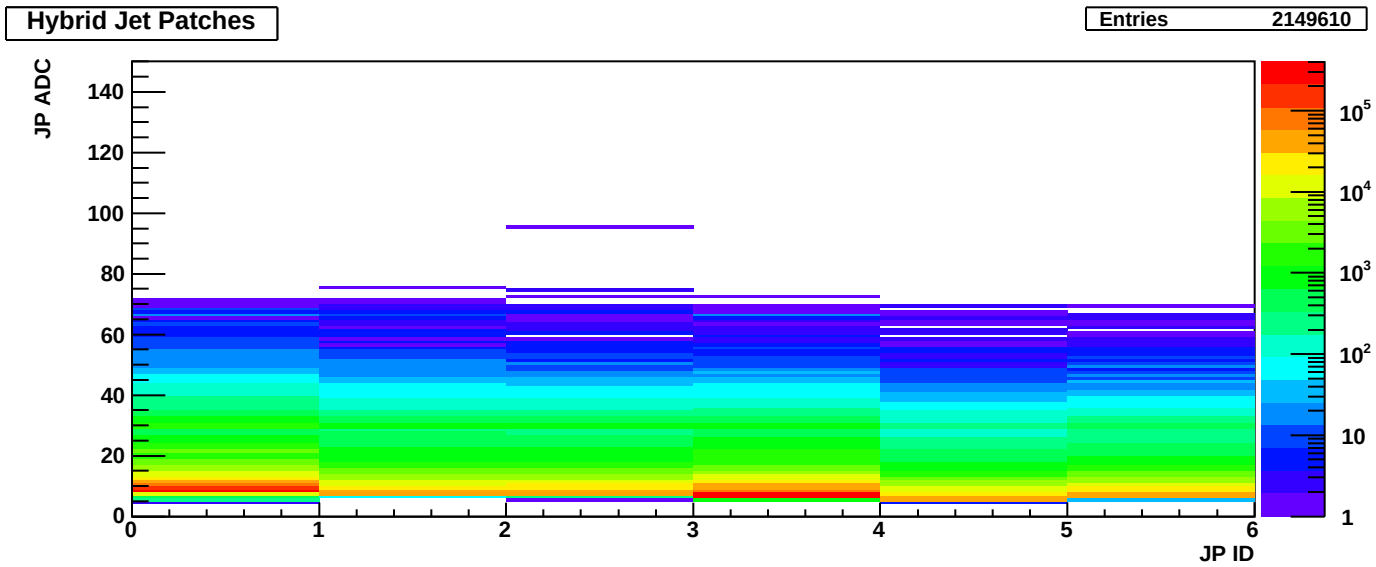

Barrel EMC L0 Input - Patch Sum
Entries 3.224415e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

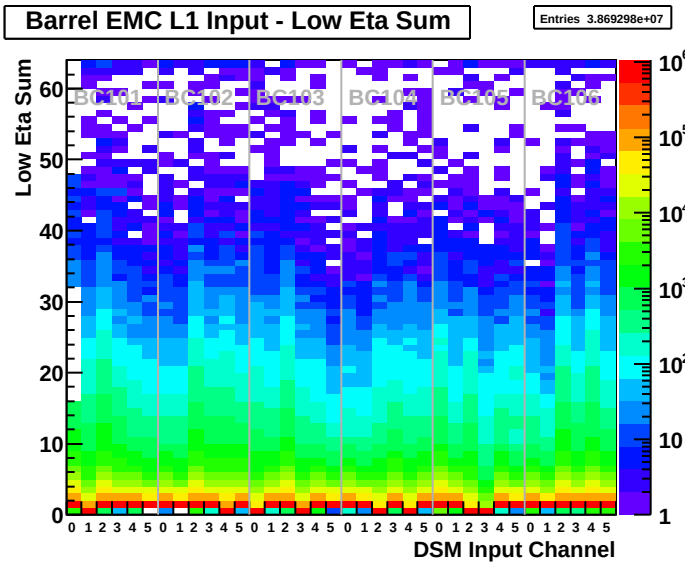

Endcap EMC L0 Input - High Tower
Entries 9.673245e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

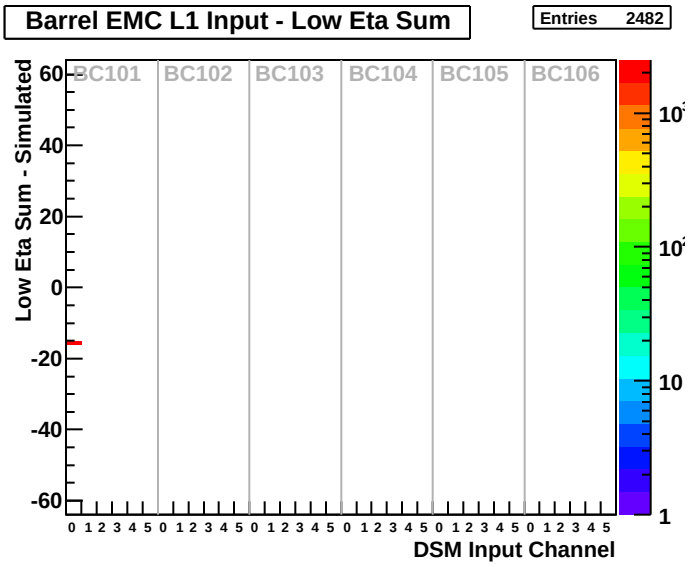

Endcap EMC L0 Input - Patch Sum
Entries 9.673245e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

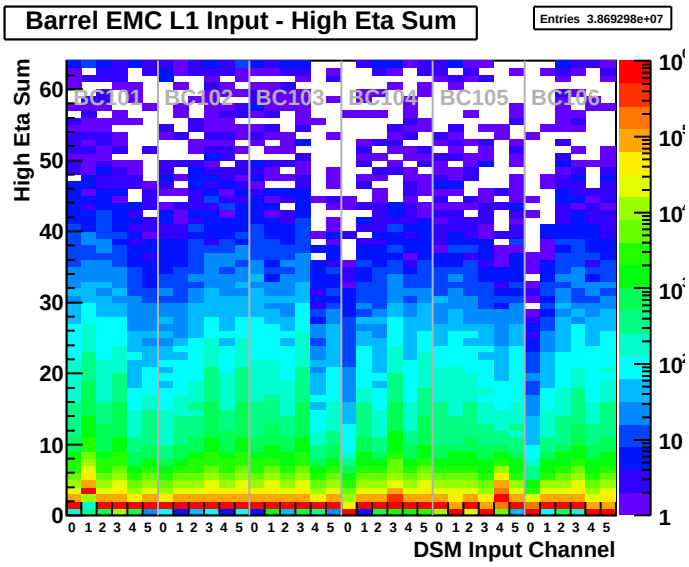

Barrel Jet Patches
Entries 1.934649e+07
JP ADC
JP ID

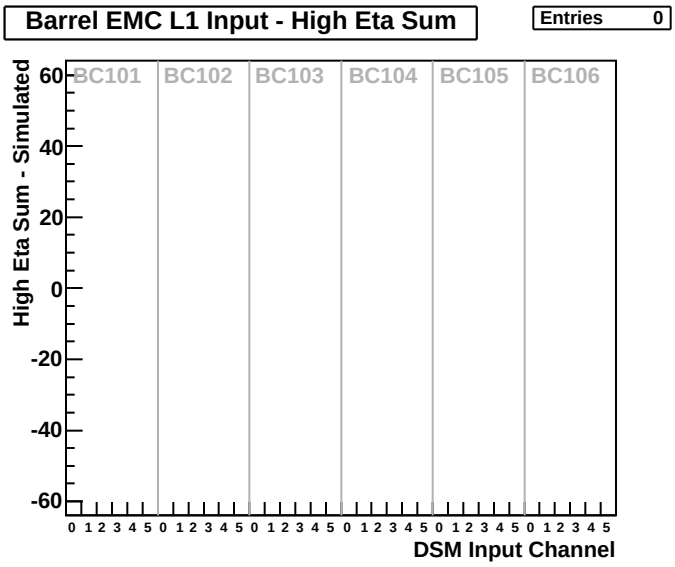

Endcap Jet Patches
Entries 6448830
JP ADC
JP ID

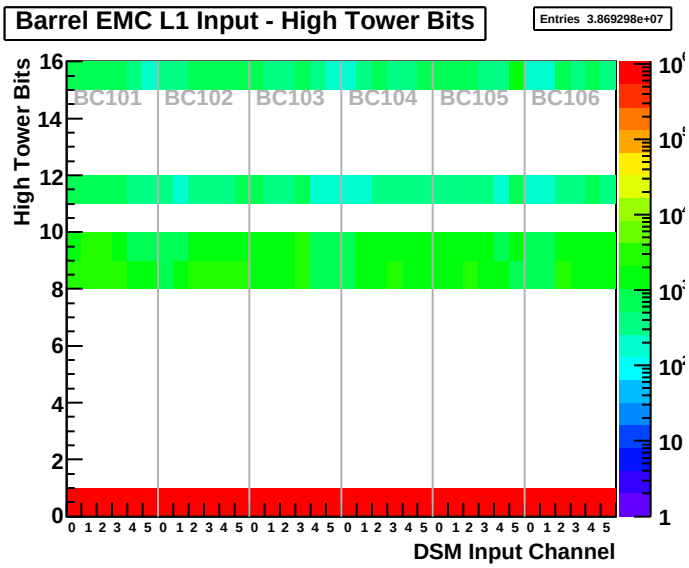

Hybrid Jet Patches
Entries 2149610
JP ADC
JP ID

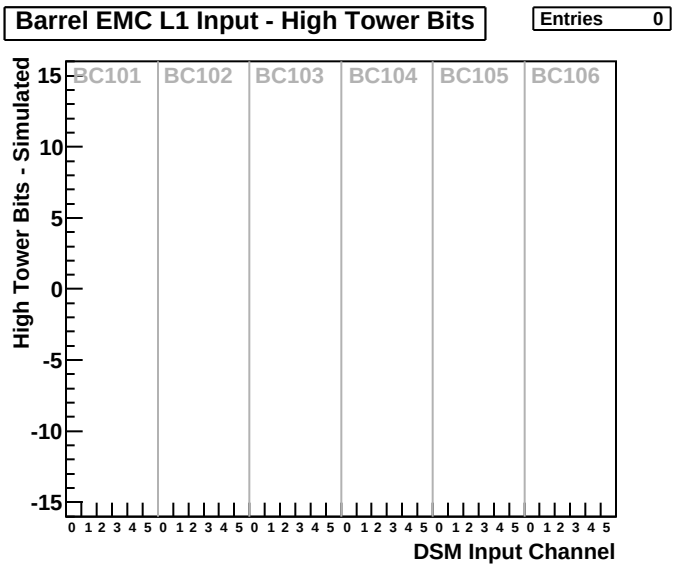

Barrel EMC L1 Input - Low Eta Sum
Entries 3.869298e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

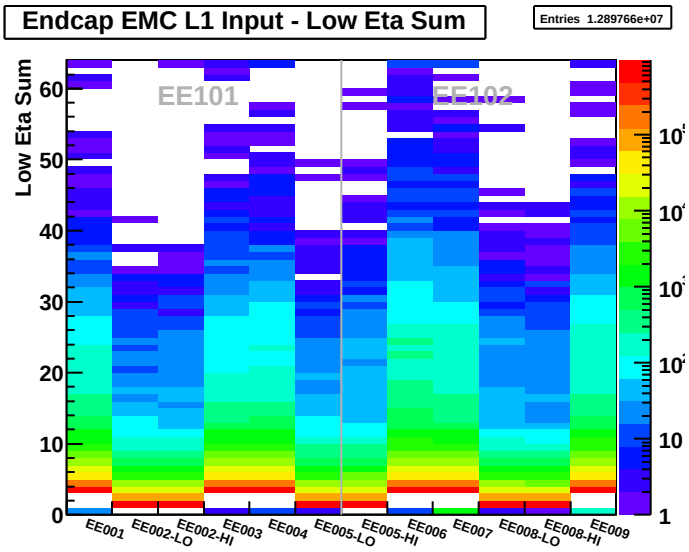

Barrel EMC L1 Input - Low Eta Sum
Entries 2482
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

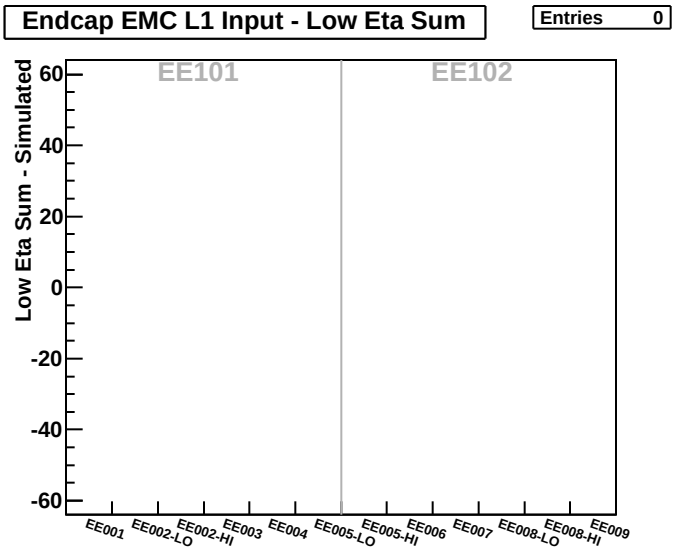

Barrel EMC L1 Input - High Eta Sum
Entries 3.869298e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

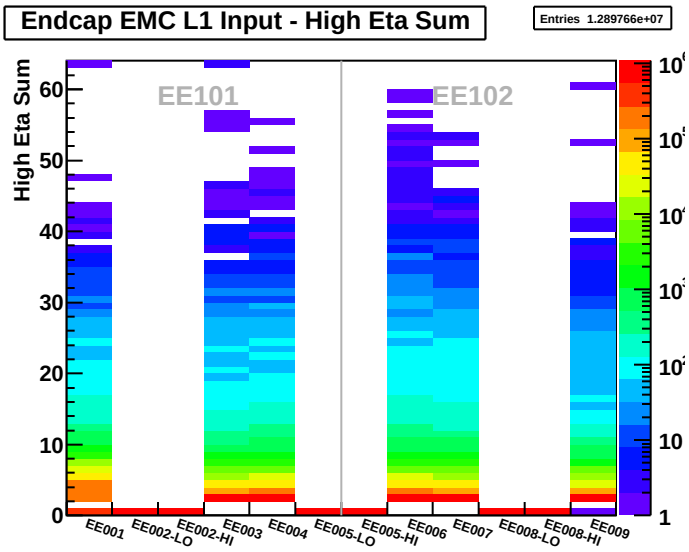

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

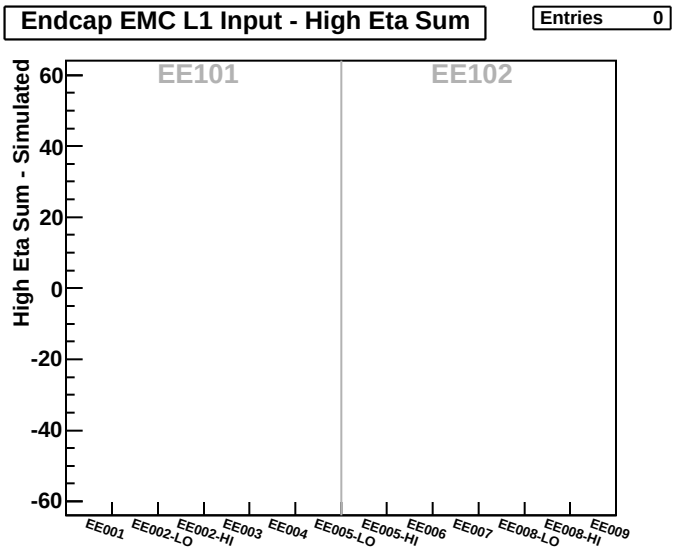

Barrel EMC L1 Input - High Tower Bits
Entries 3.869298e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

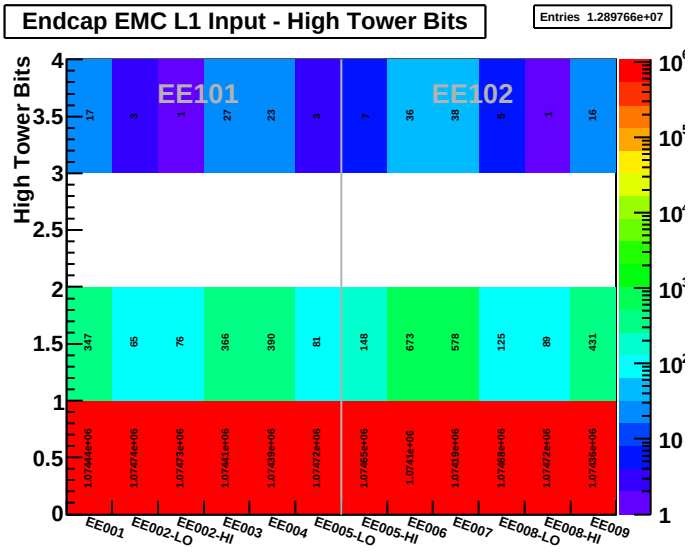

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

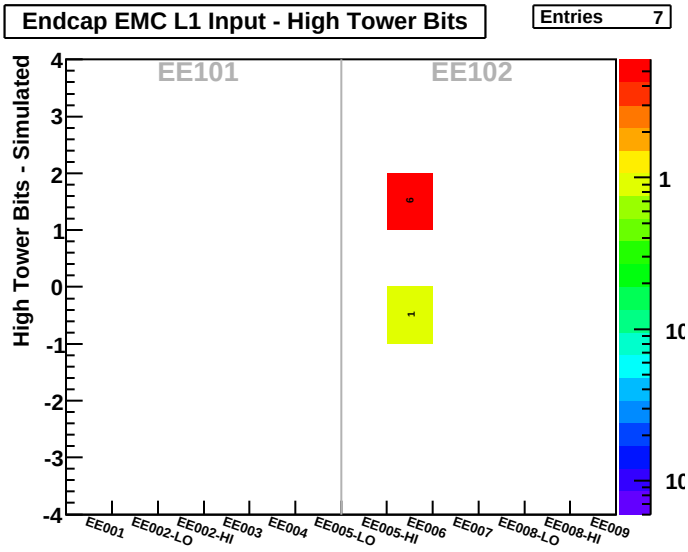

Endcap EMC L1 Input - Low Eta Sum
Entries 1.289766e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

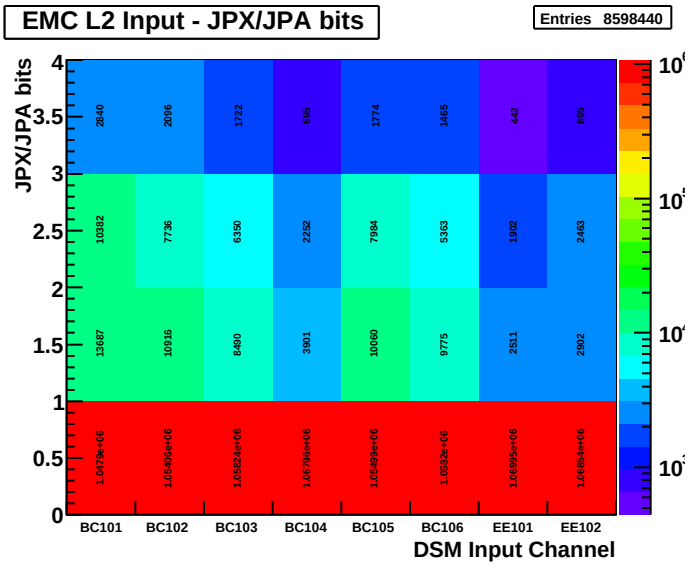

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

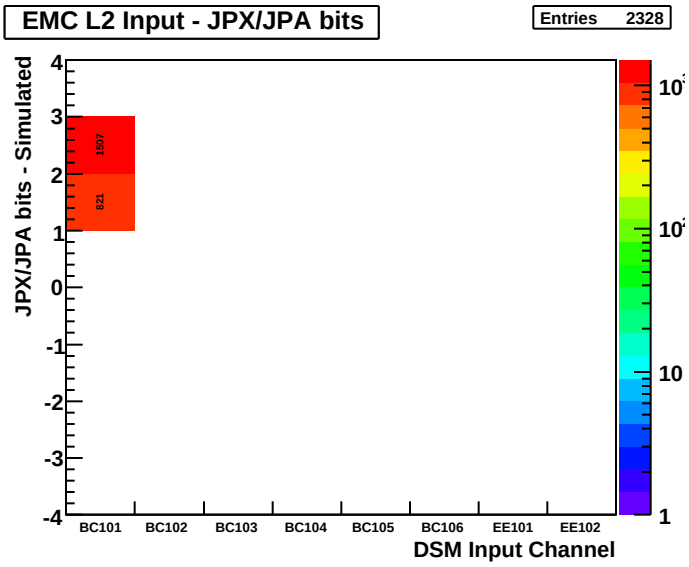

Endcap EMC L1 Input - High Eta Sum
Entries 1.289766e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

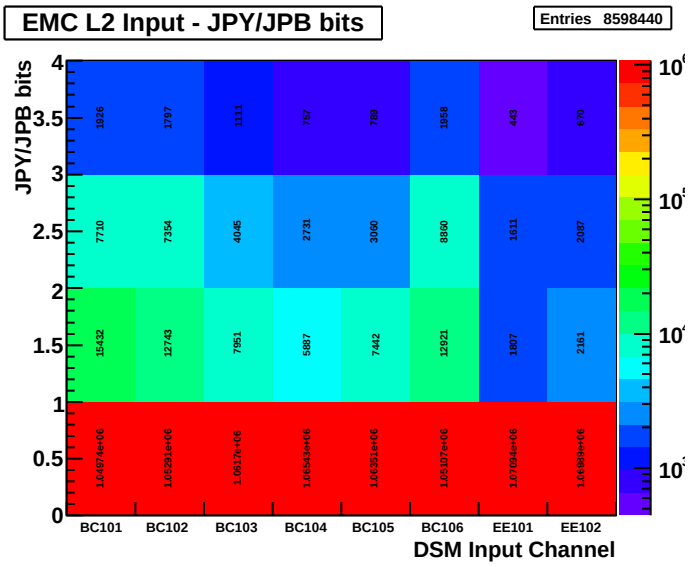

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

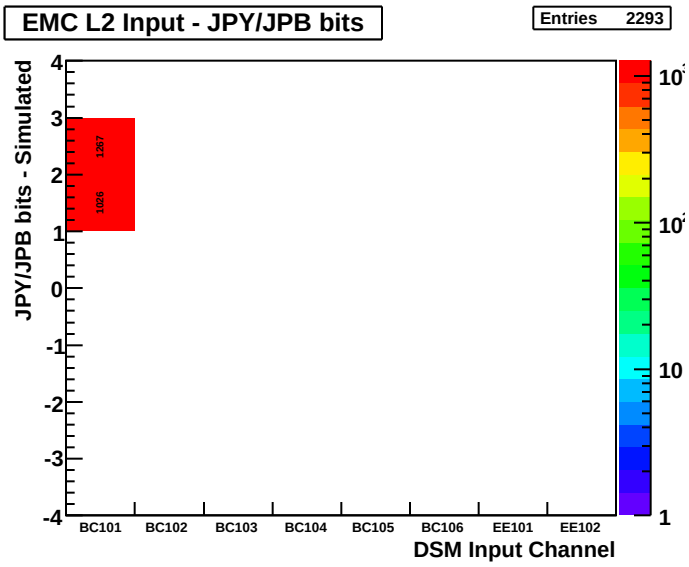

Endcap EMC L1 Input - High Tower Bits
Entries 1.289766e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

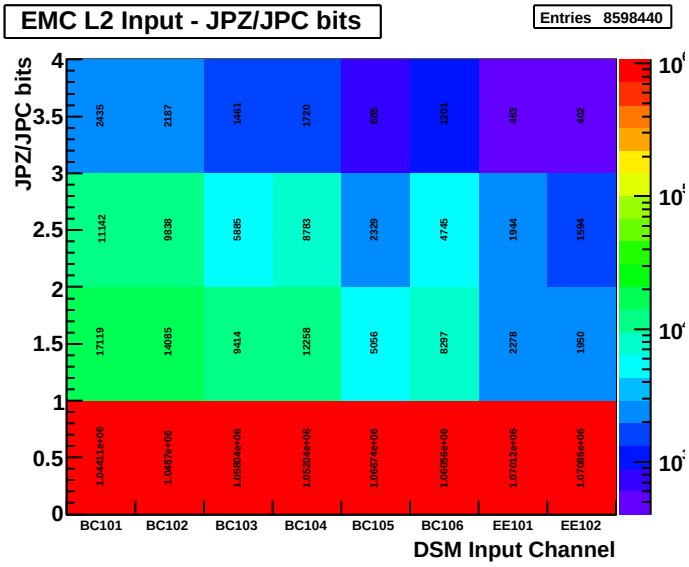

Endcap EMC L1 Input - High Tower Bits
Entries 7
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

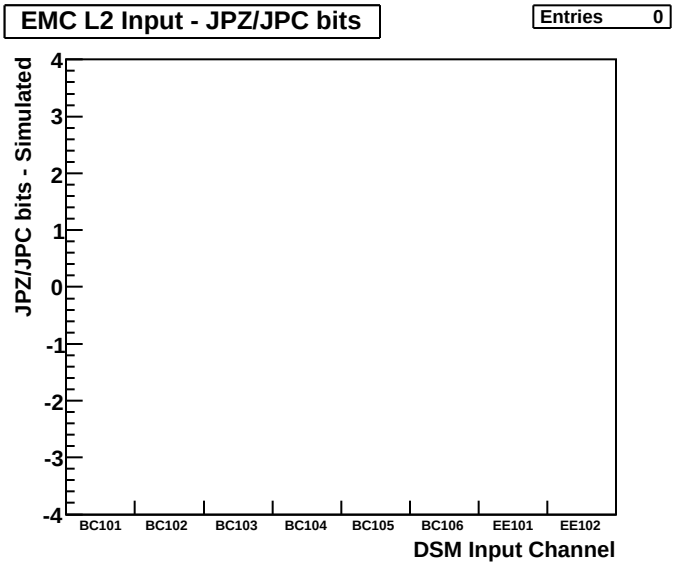

EMC L2 Input - JPX/JPA bits
Entries 8598440
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2840 2096 1722 695 1774 1465 442 895
10382 7736 6350 2252 7984 5363 1902 2463
13687 10916 8490 3901 10060 9775 2511 2902
1.0479e+06 1.05406e+06 1.05824e+06 1.06706e+06 1.05499e+06 1.0592e+06 1.06995e+06 1.06854e+06

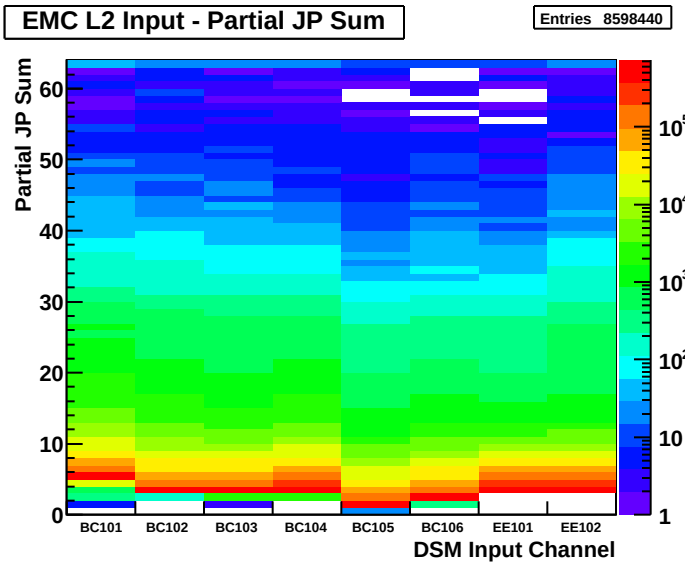

EMC L2 Input - JPX/JPA bits
Entries 2328
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1507
821

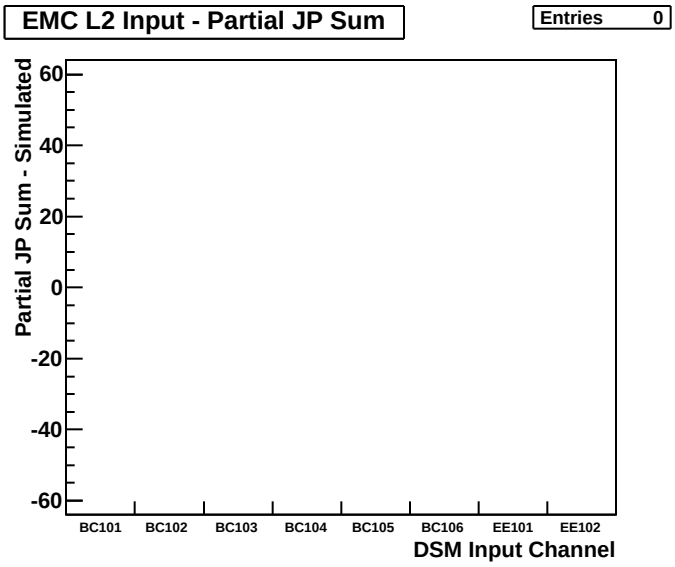

EMC L2 Input - JPY/JPB bits
Entries 8598440
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1926 1797 1111 757 789 1958 443 670
7710 7354 4045 2731 3060 8860 1611 2087
15432 12743 7951 5987 7442 12921 1807 2161
1.04974e+06 1.05291e+06 1.0617e+06 1.06543e+06 1.06351e+06 1.05107e+06 1.07094e+06 1.06989e+06

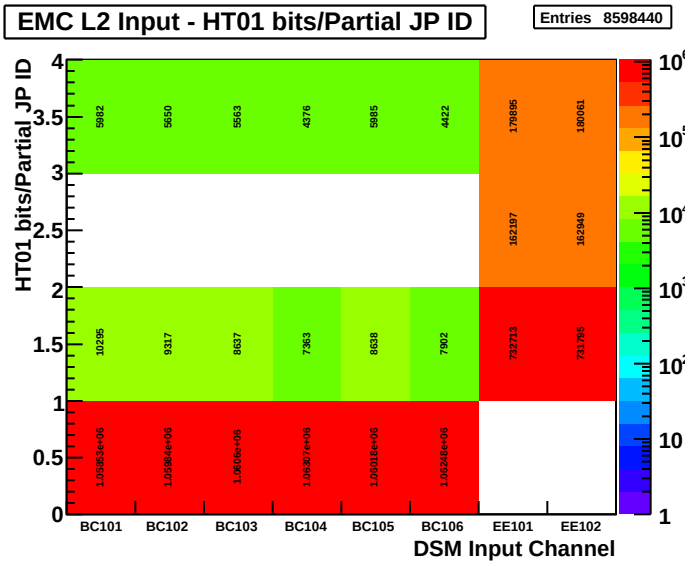

EMC L2 Input - JPY/JPB bits
Entries 2293
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1267
1026

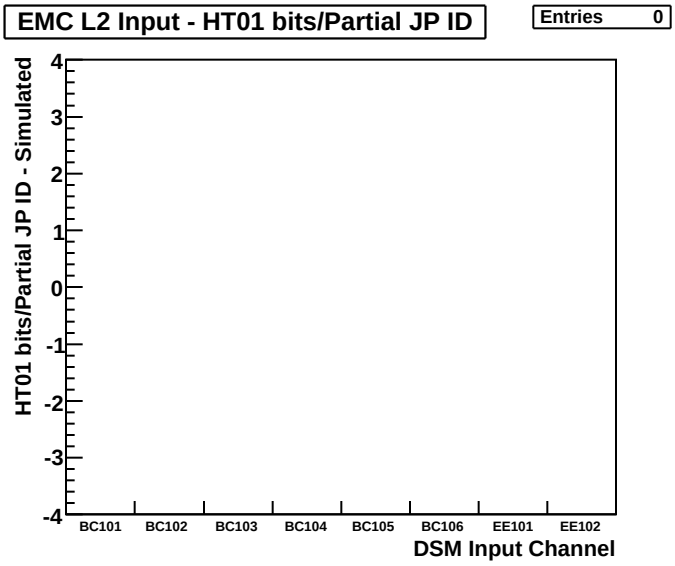

EMC L2 Input - JPZ/JPC bits
Entries 8598440
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2435 2187 1461 1720 685 1201 463 402
11142 9838 5885 8783 2329 4745 1944 1594
17119 14085 9414 12258 5056 8297 2278 1950
1.04411e+06 1.0487e+06 1.05804e+06 1.05204e+06 1.06674e+06 1.06056e+06 1.07012e+06 1.07086e+06

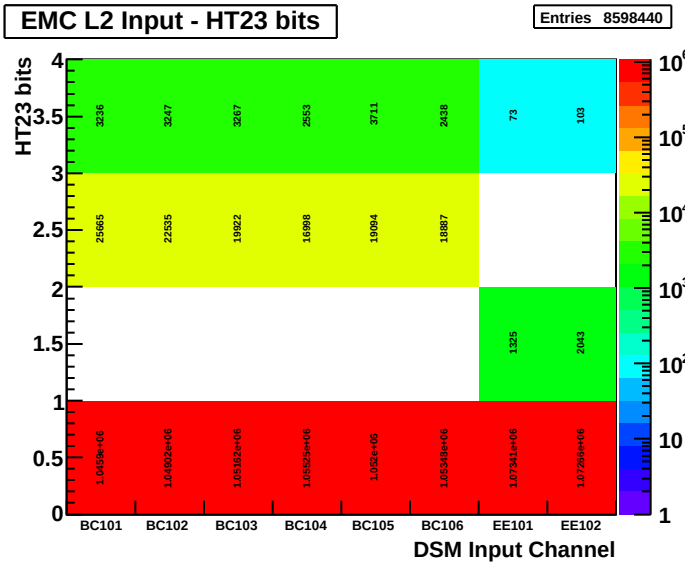

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

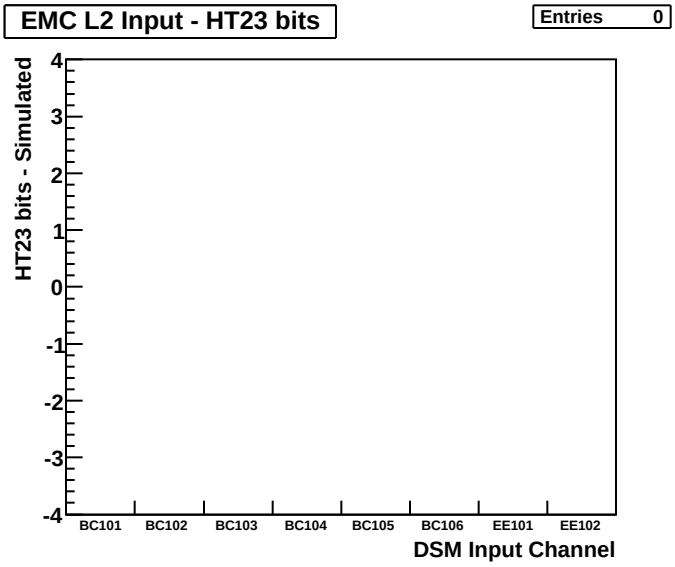

EMC L2 Input - Partial JP Sum
Entries 8598440
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

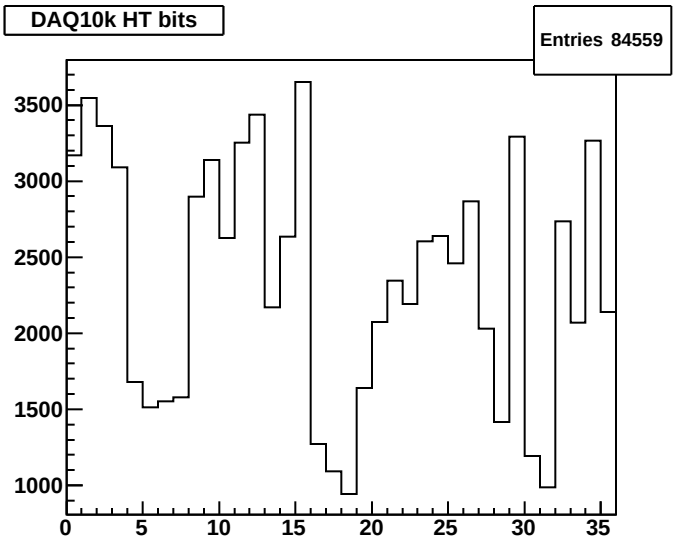

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

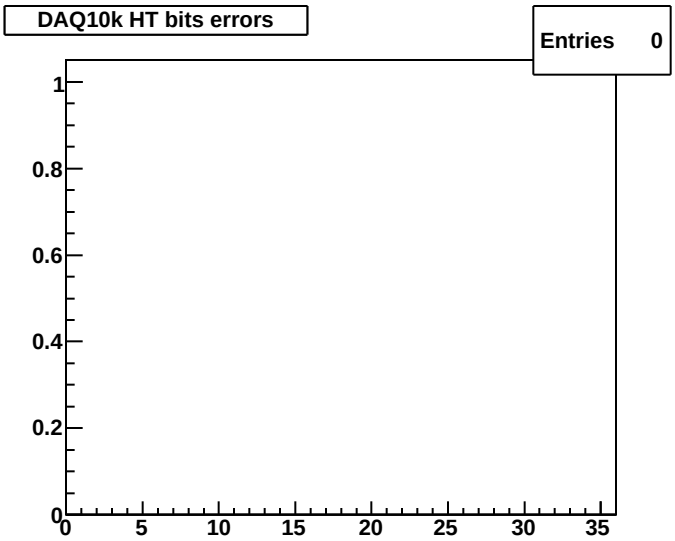

EMC L2 Input - HT01 bits/Partial JP ID
Entries 8598440
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

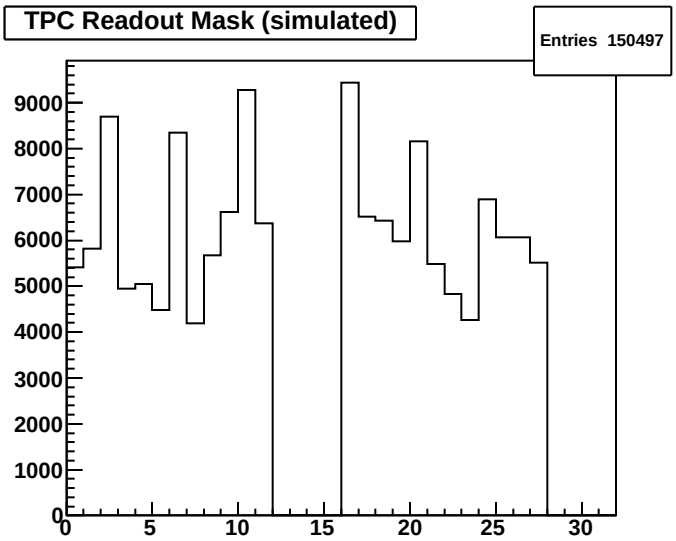

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

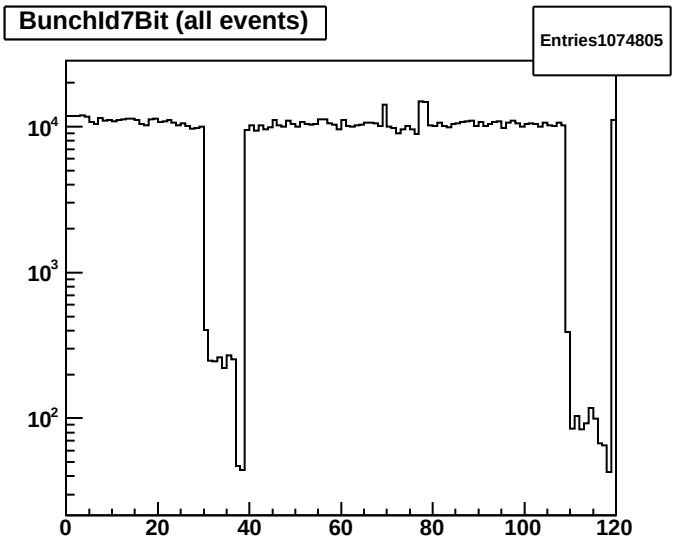

EMC L2 Input - HT23 bits
Entries 8598440
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

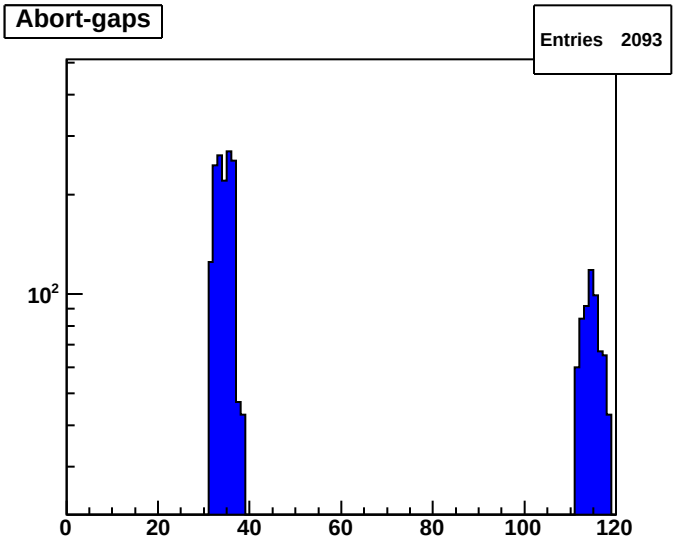

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

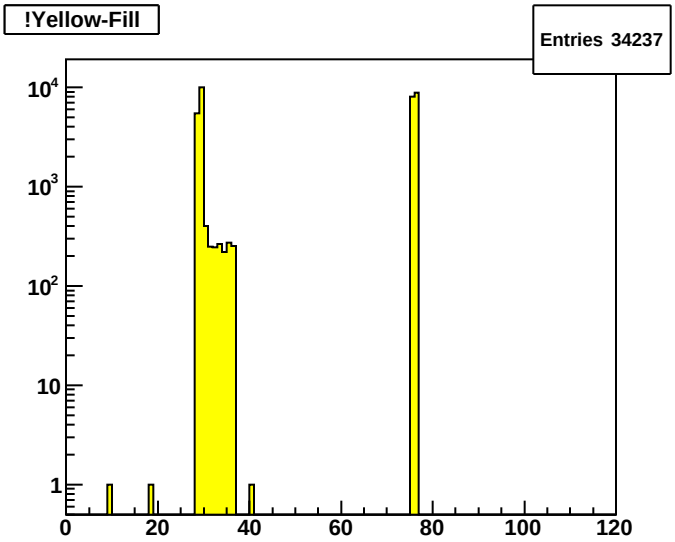

DAQ10k HT bits
Entries 84559

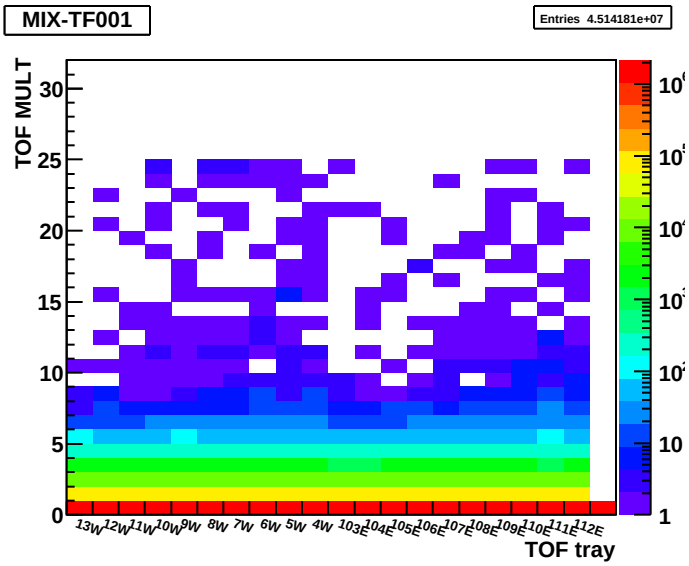

DAQ10k HT bits errors
Entries 0

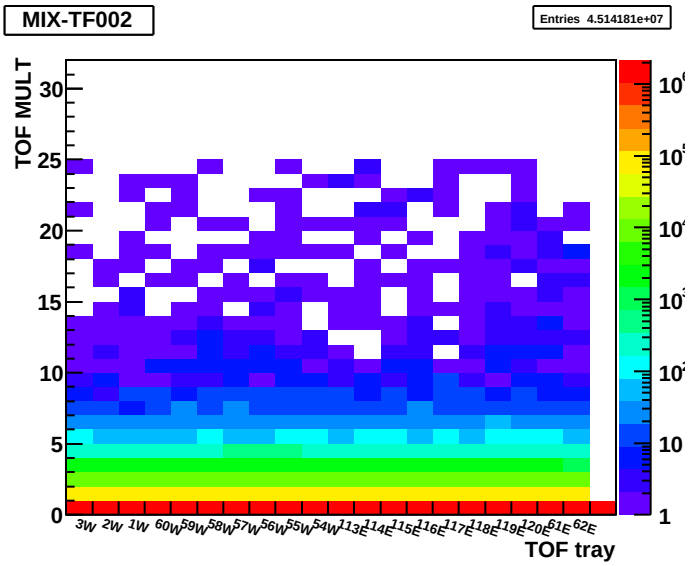

TPC Readout Mask (simulated)
Entries 150497

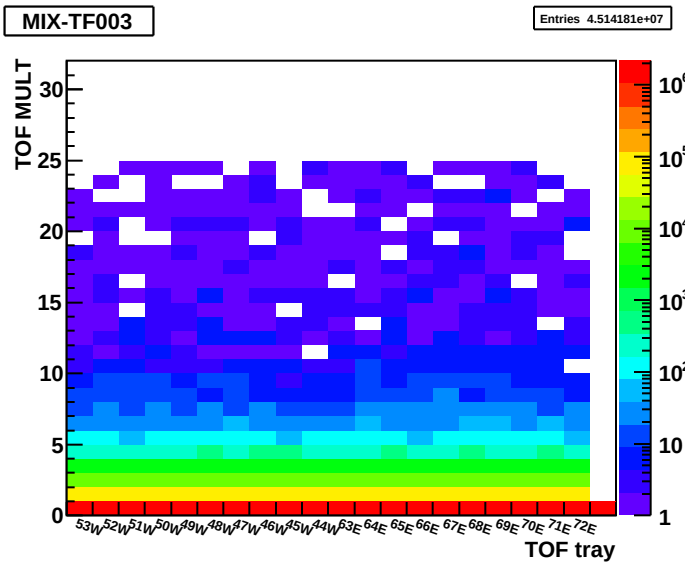

BunchId7Bit (all events)
Entries1074805

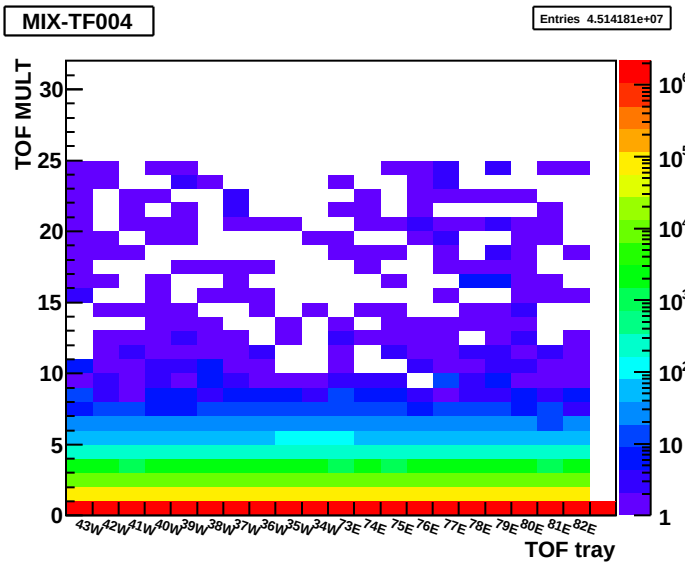

Abort-gaps
Entries 2093

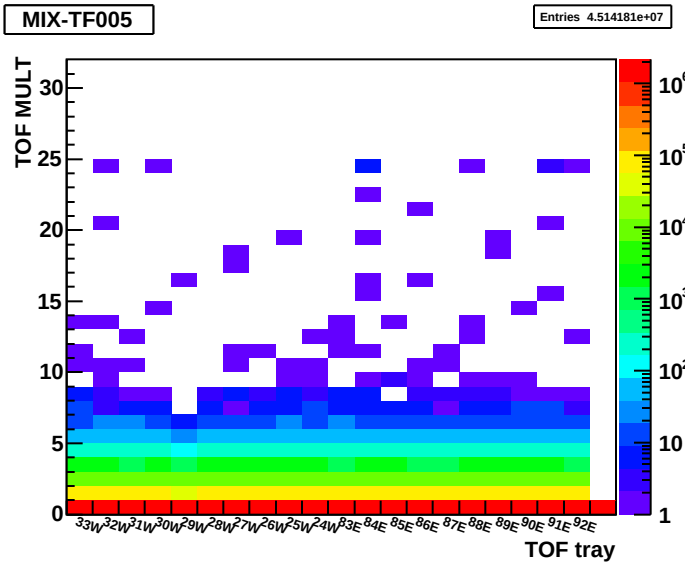

!Yellow-Fill
Entries 34237

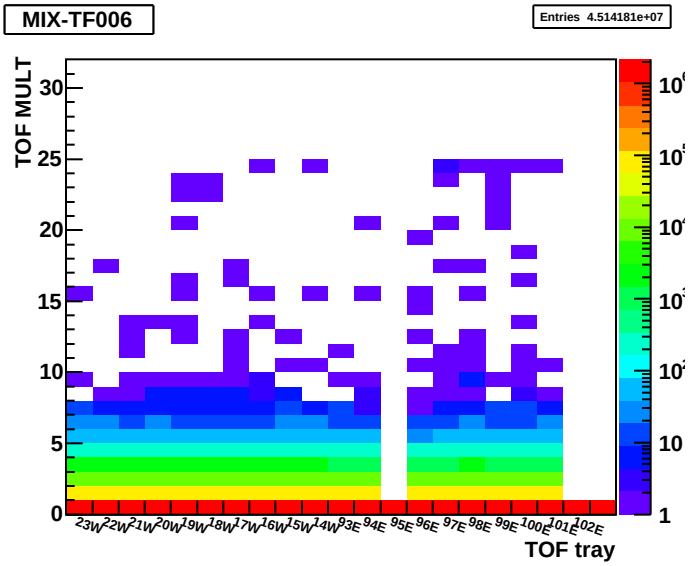

MIX-TF001
Entries 4.514181e+07
TOF MULT
TOF tray

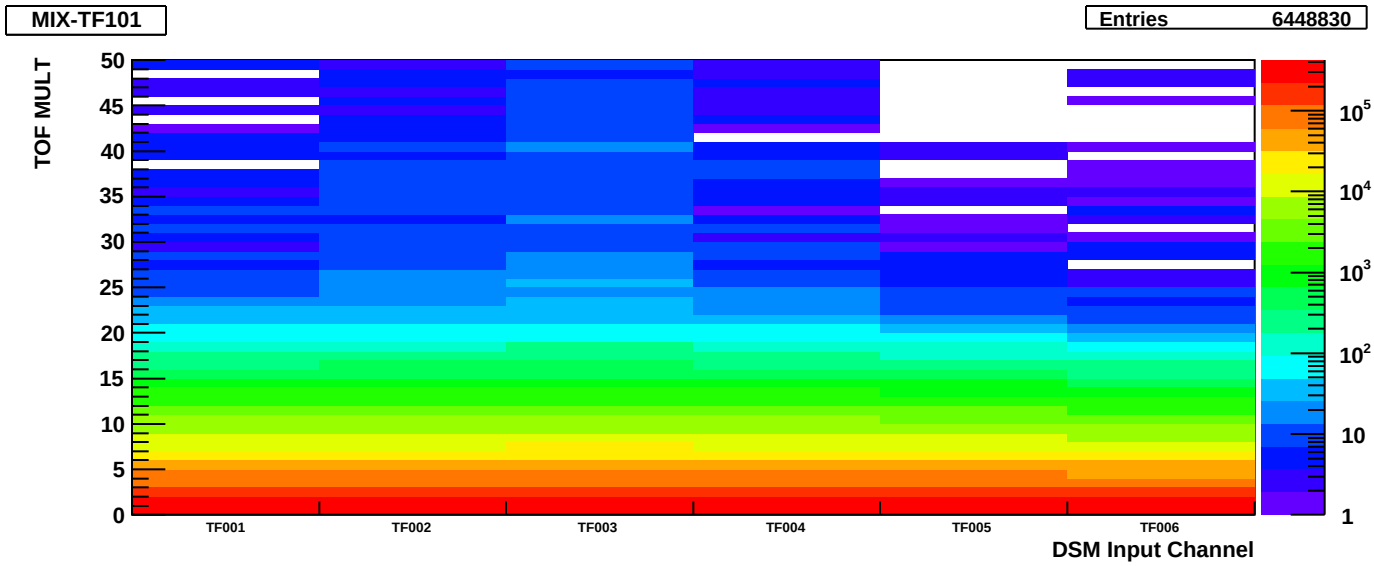

MIX-TF002
Entries 4.514181e+07
TOF MULT
TOF tray

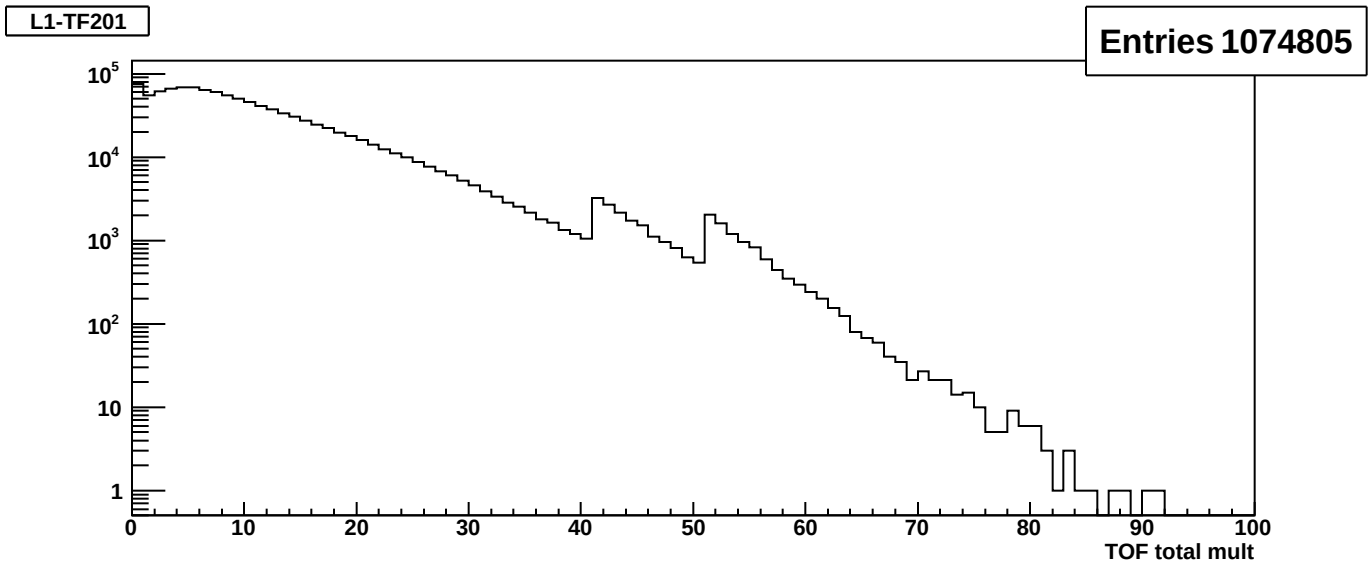

MIX-TF003
Entries 4.514181e+07
TOF MULT
TOF tray

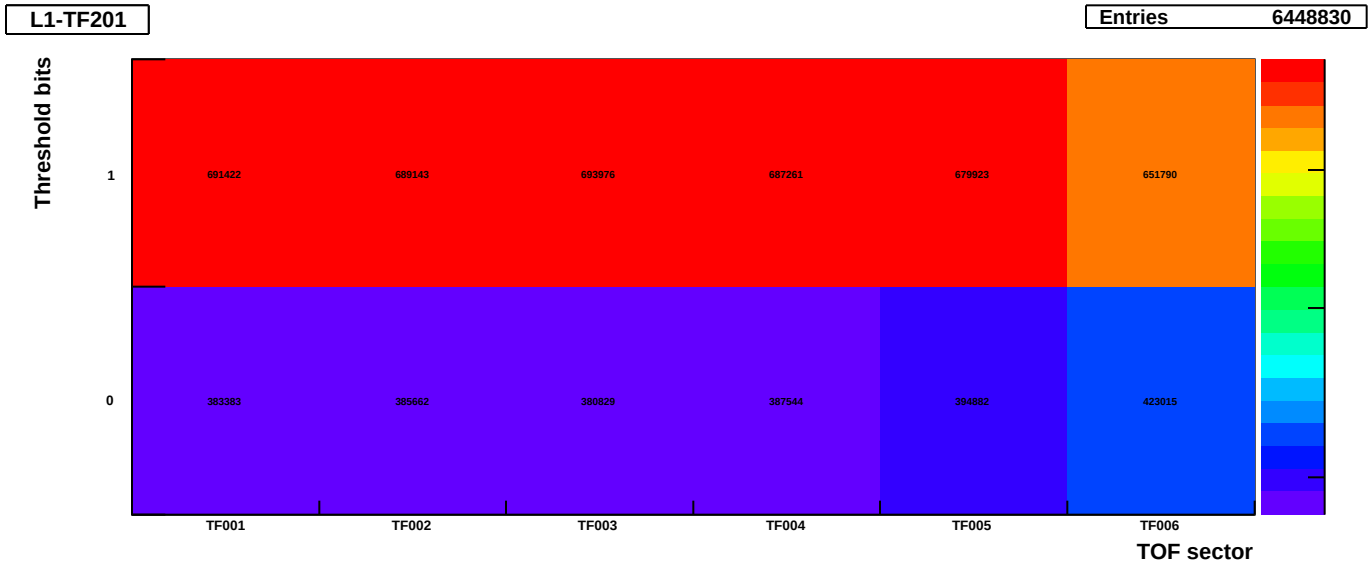

MIX-TF004
Entries 4.514181e+07
TOF MULT
TOF tray

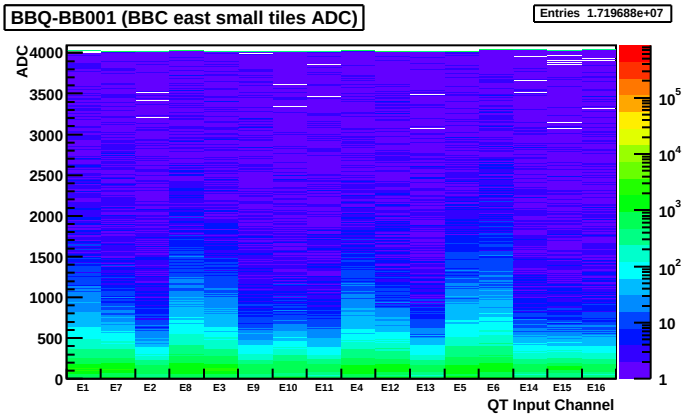

MIX-TF005
Entries 4.514181e+07
TOF MULT
TOF tray

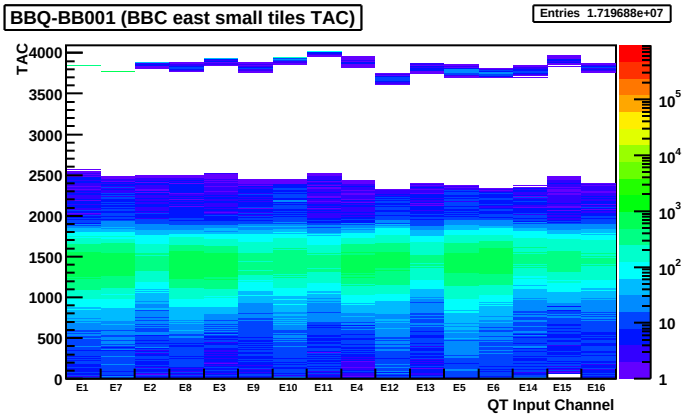

MIX-TF006
Entries 4.514181e+07
TOF MULT
TOF tray

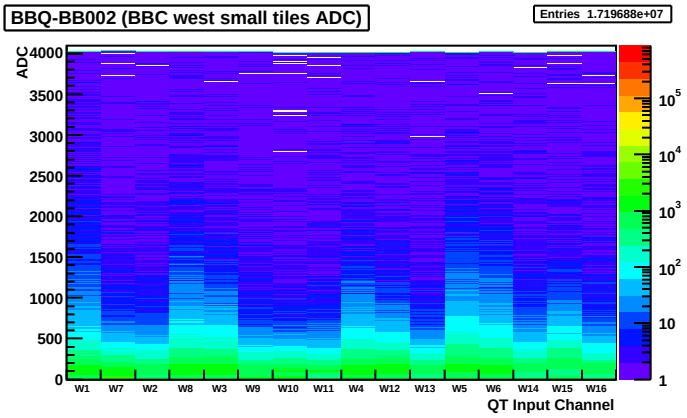

MIX-TF101
Entries 6448830
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

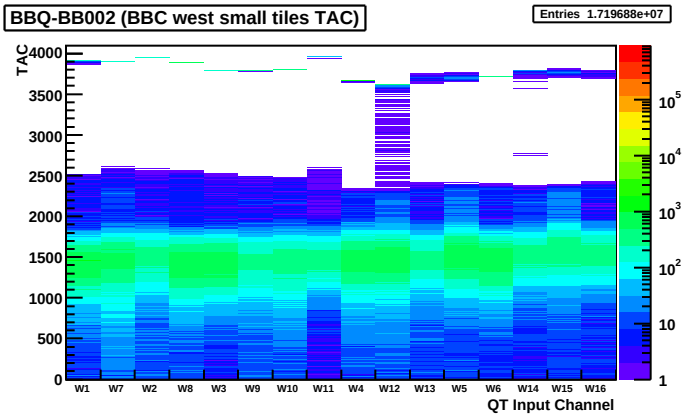

L1-TF201
Entries 1074805
TOF total mult

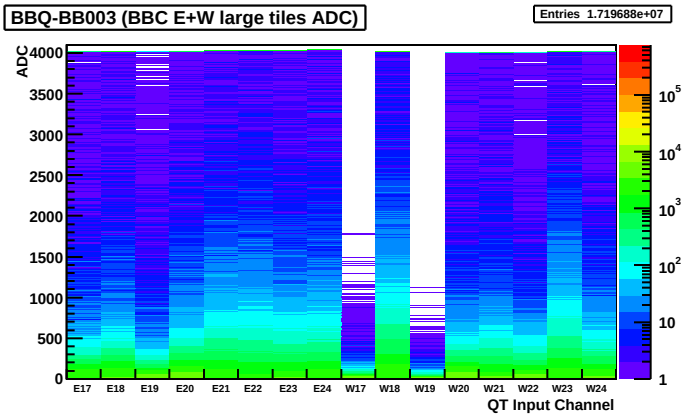

L1-TF201
Entries 6448830
Threshold bits
691422
689143
693976
687261
679923
651790
383383
385662
380829
387544
394882
423015
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

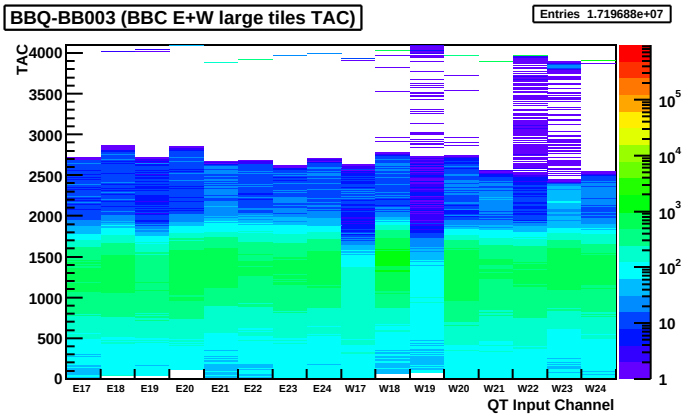

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.719688e+07
ADC
QT Input Channel

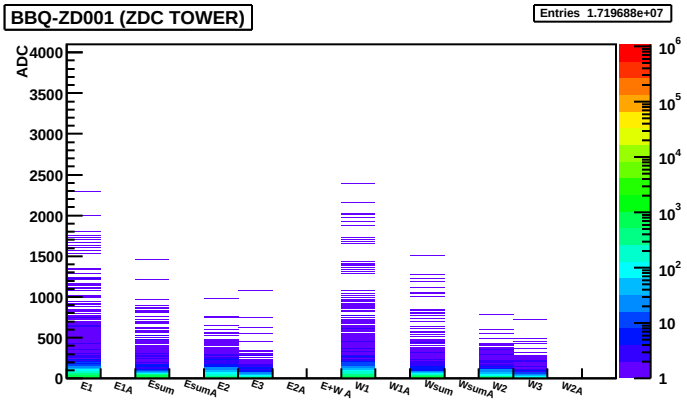

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.719688e+07
TAC
QT Input Channel

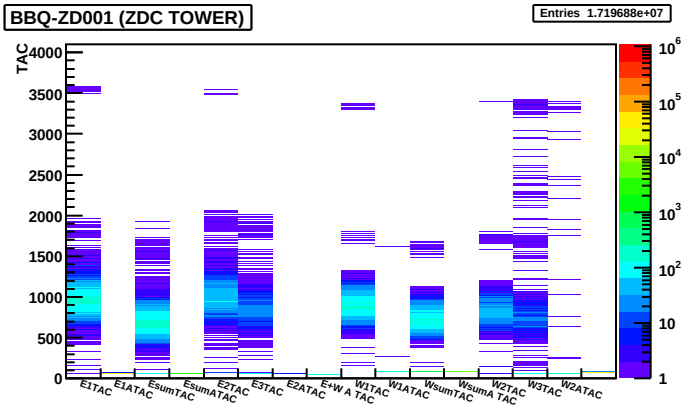

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.719688e+07
ADC
QT Input Channel

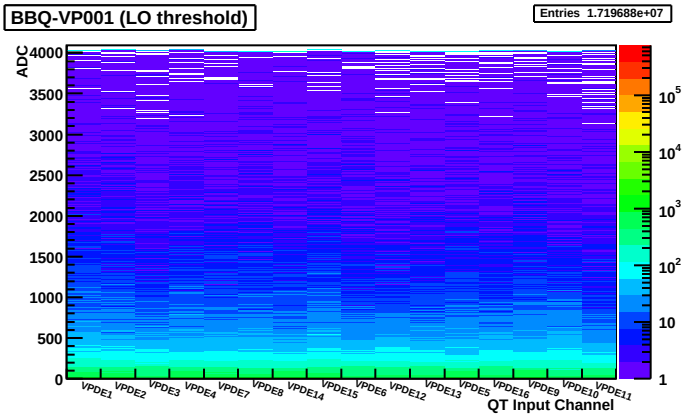

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.719688e+07
TAC
QT Input Channel

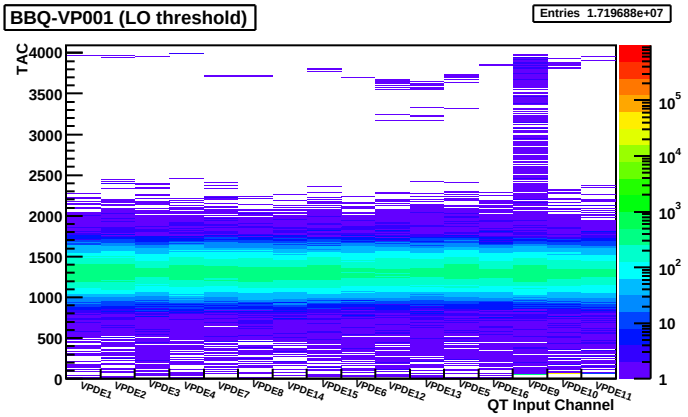

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.719688e+07
ADC
QT Input Channel

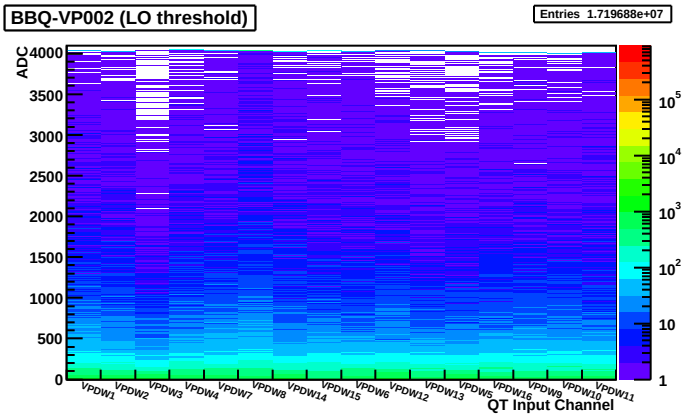

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.719688e+07
TAC
QT Input Channel

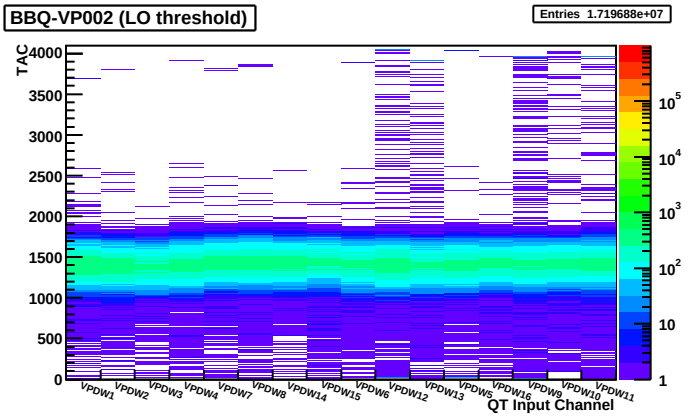

BBQ-ZD001 (ZDC TOWER)
Entries 1.719688e+07
ADC

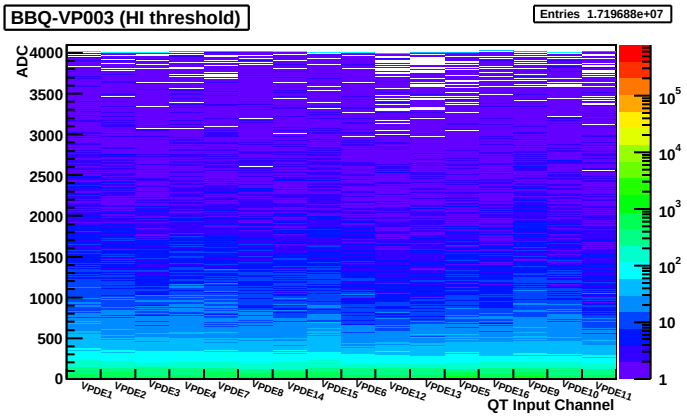

BBQ-ZD001 (ZDC TOWER)
Entries 1.719688e+07
TAC

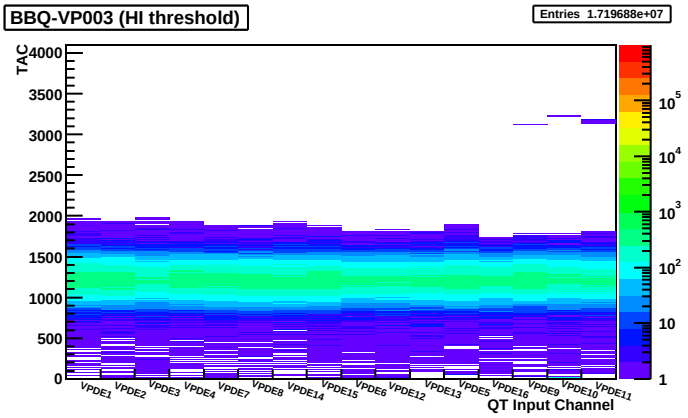

BBQ-VP001 (LO threshold)
Entries 1.719688e+07
ADC
QT Input Channel

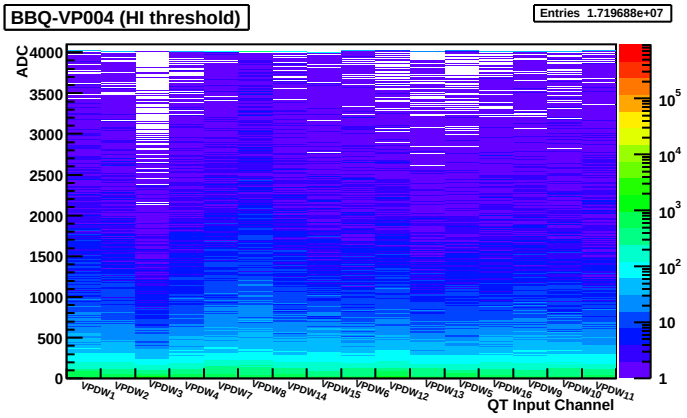

BBQ-VP001 (LO threshold)
Entries 1.719688e+07
TAC
QT Input Channel

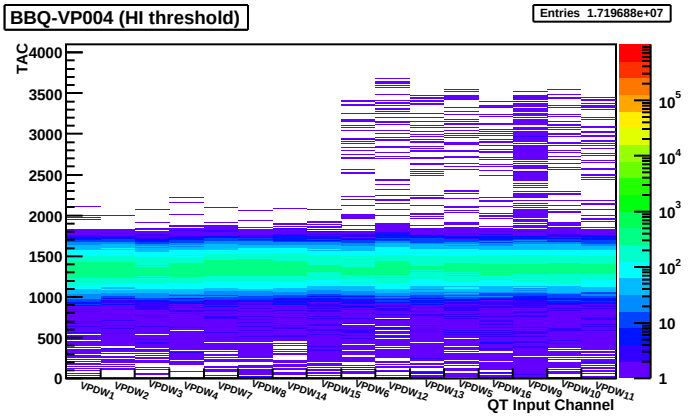

BBQ-VP002 (LO threshold)
Entries 1.719688e+07
ADC
QT Input Channel

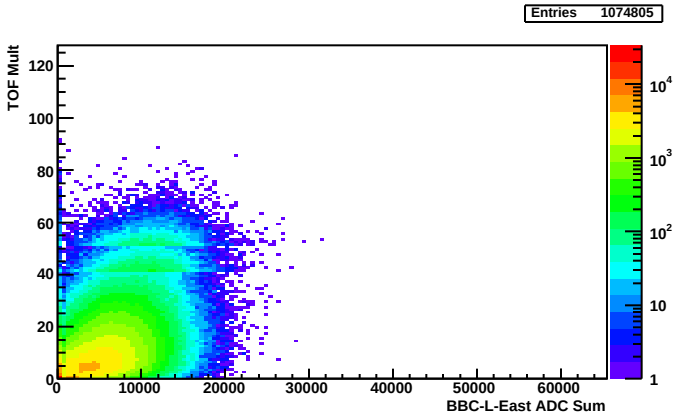

BBQ-VP002 (LO threshold)
Entries 1.719688e+07
TAC
QT Input Channel

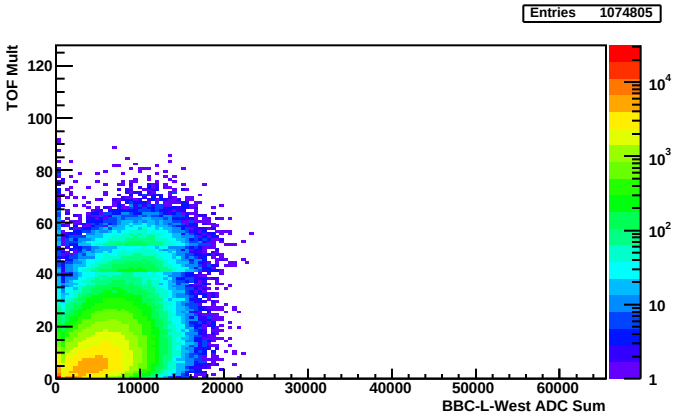

BBQ-VP003 (HI threshold)
Entries 1.719688e+07
ADC
QT Input Channel

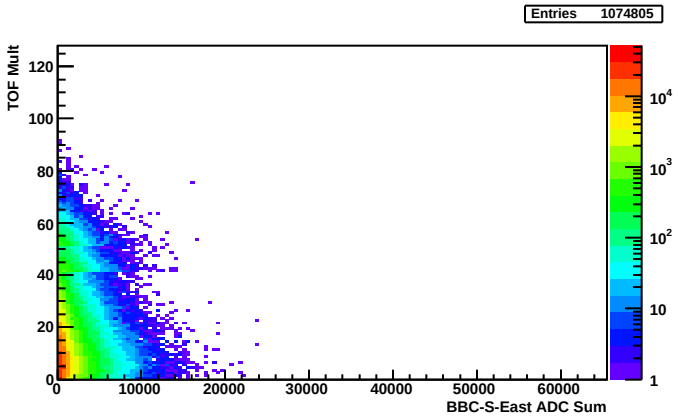

BBQ-VP003 (HI threshold)
Entries 1.719688e+07
TAC
QT Input Channel

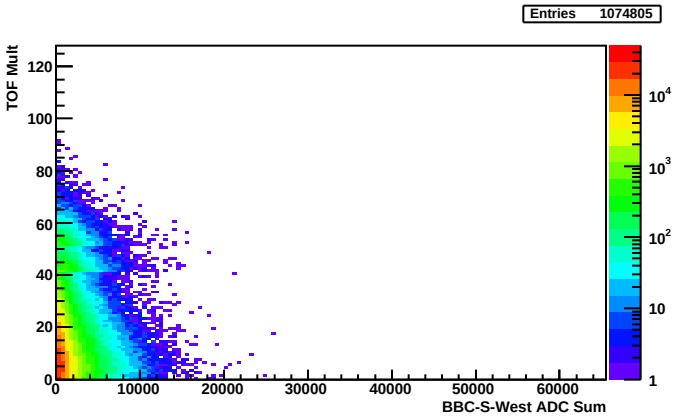

BBQ-VP004 (HI threshold)
Entries 1.719688e+07
ADC
QT Input Channel

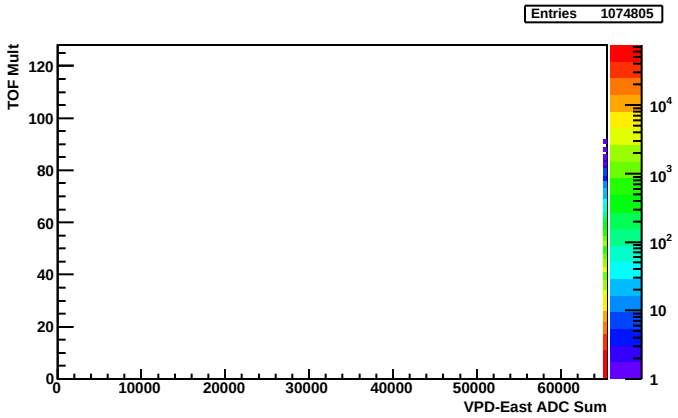

BBQ-VP004 (HI threshold)
Entries 1.719688e+07
TAC
QT Input Channel

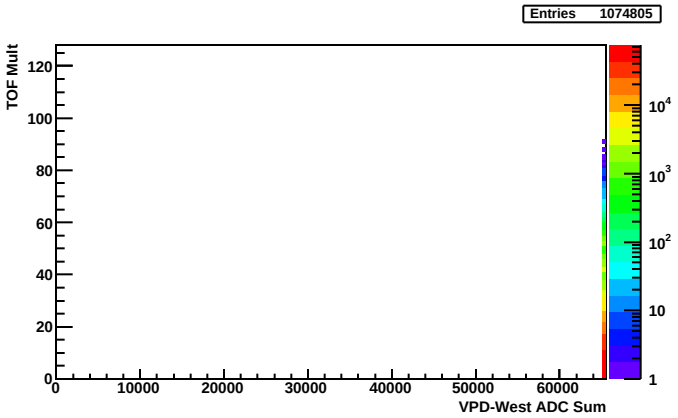

Entries 1074805
TOF Mult
BBC-L-East ADC Sum

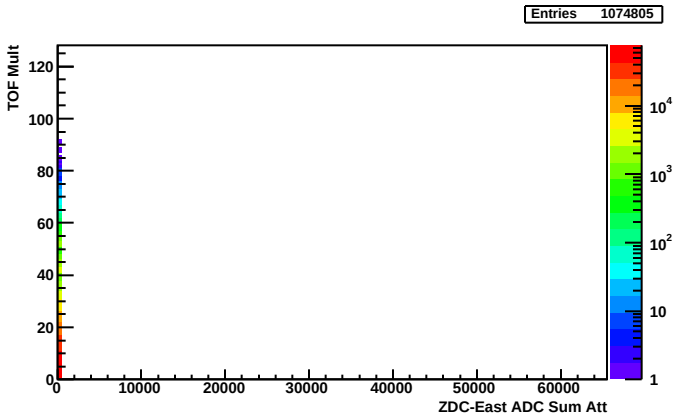

Entries 1074805
TOF Mult
BBC-L-West ADC Sum

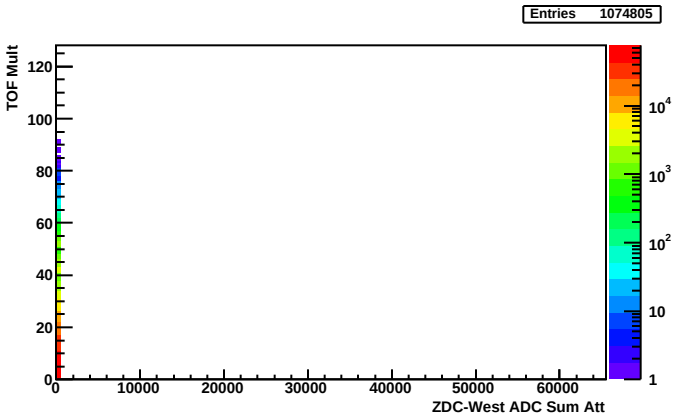

Entries 1074805
TOF Mult
BBC-S-East ADC Sum

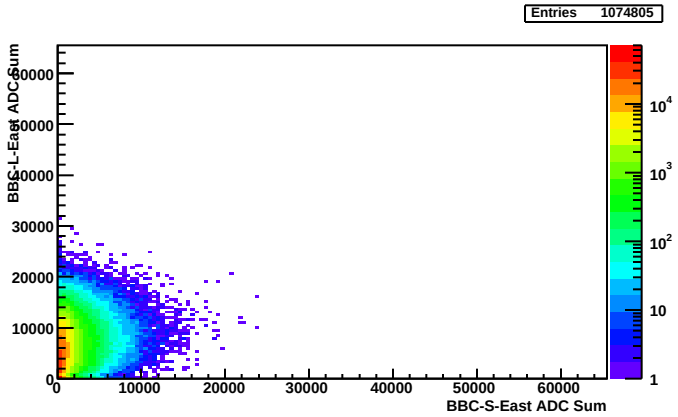

Entries 1074805
TOF Mult
BBC-S-West ADC Sum

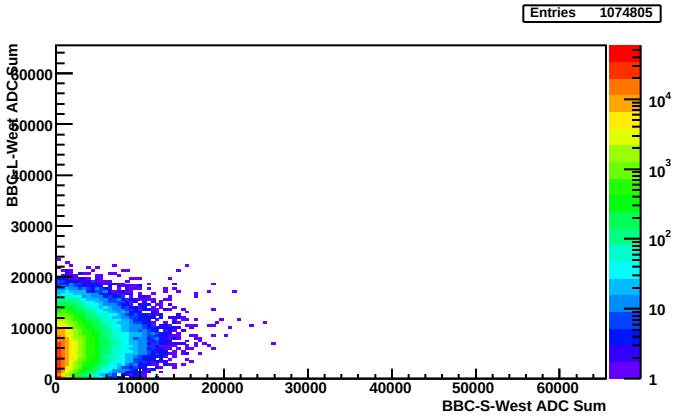

Entries 1074805
TOF Mult
VPD-East ADC Sum

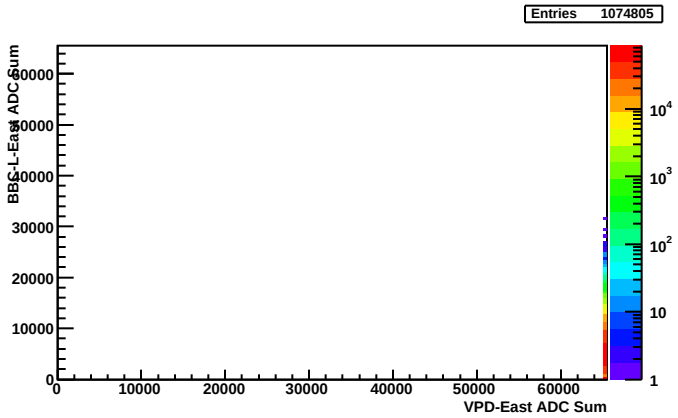

Entries 1074805
TOF Mult
VPD-West ADC Sum

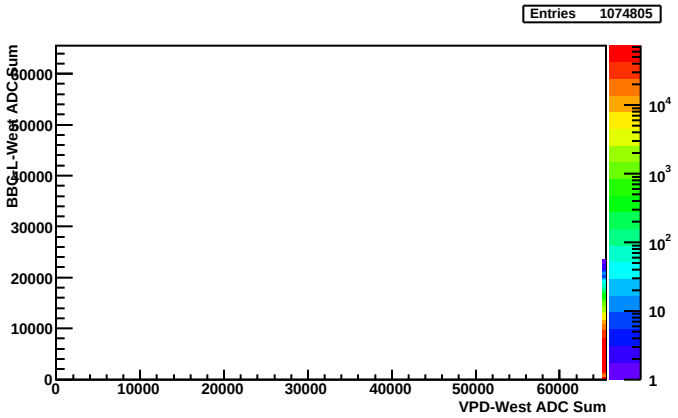

Entries 1074805
TOF Mult
ZDC-East ADC Sum Att

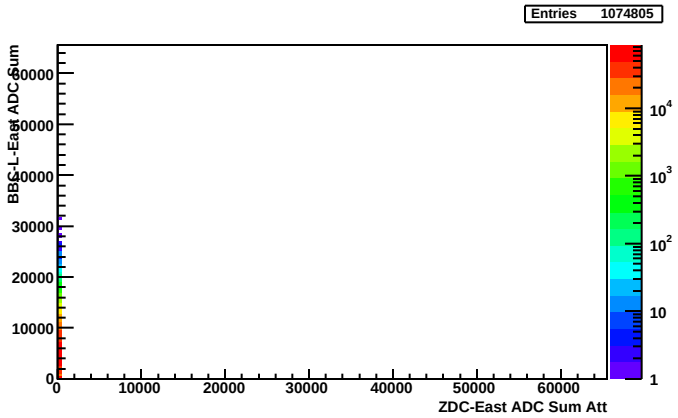

Entries 1074805
TOF Mult
ZDC-West ADC Sum Att

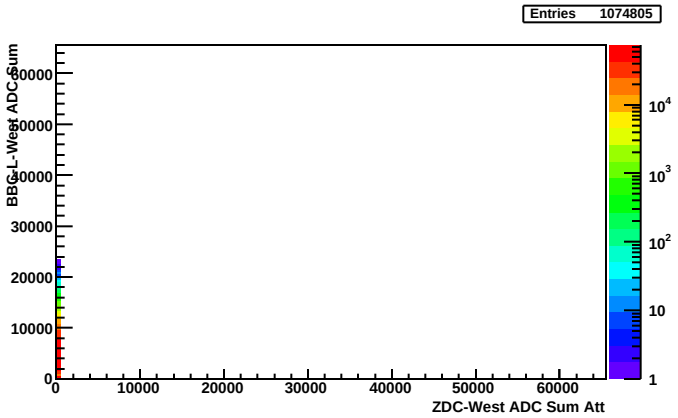

Entries 1074805
BBC-L-East ADC Sum
BBC-S-East ADC Sum

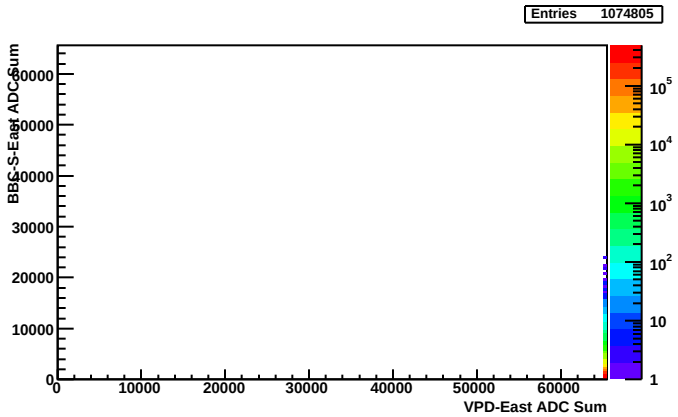

Entries 1074805
BBC-L-West ADC Sum
BBC-S-West ADC Sum

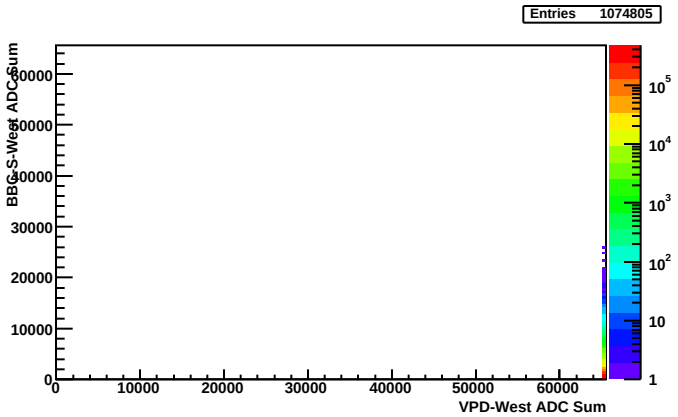

Entries 1074805
BBC-L-East ADC Sum
VPD-East ADC Sum

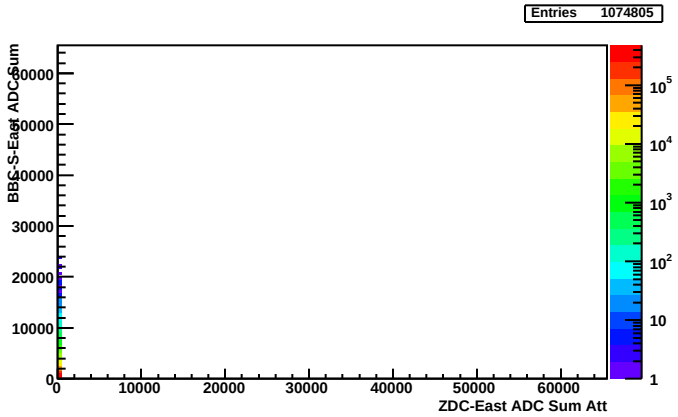

Entries 1074805
BBC-L-West ADC Sum
VPD-West ADC Sum

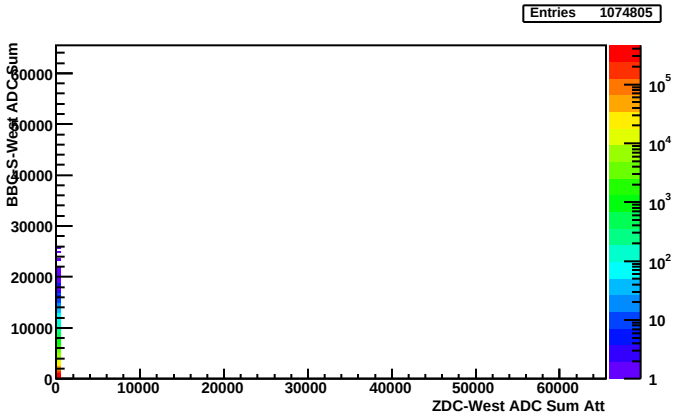

Entries 1074805
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

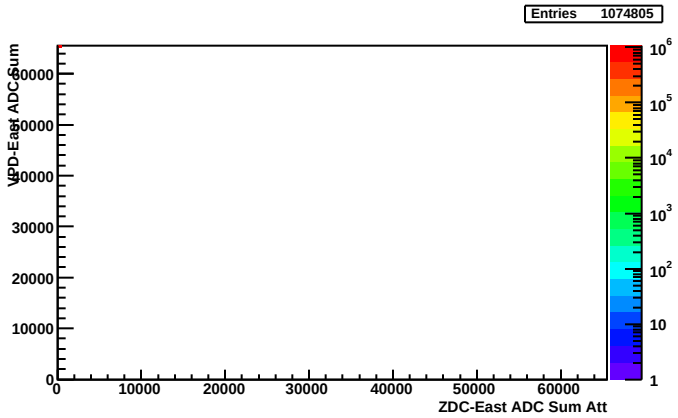

Entries 1074805
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

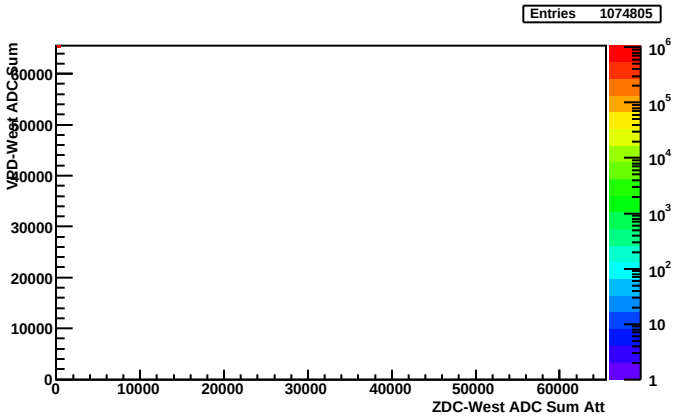

Entries 1074805
BBC-S-East ADC Sum
VPD-East ADC Sum

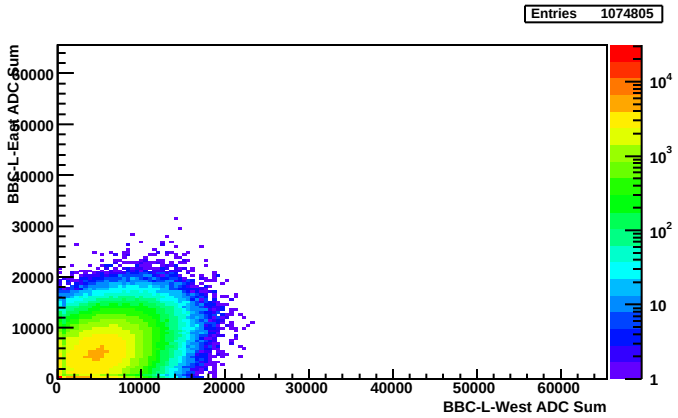

Entries 1074805
BBC-S-West ADC Sum
VPD-West ADC Sum

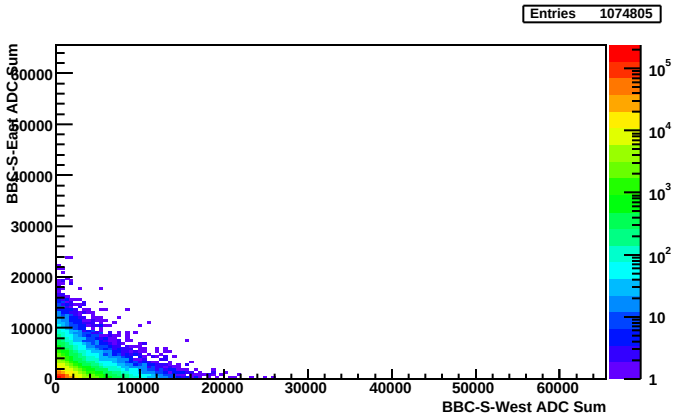

Entries 1074805
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

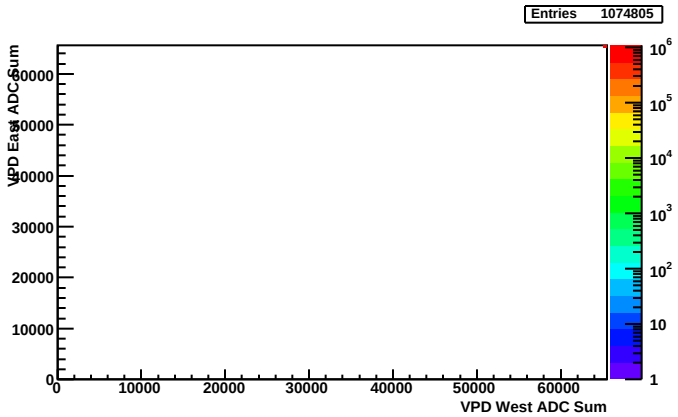

Entries 1074805
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

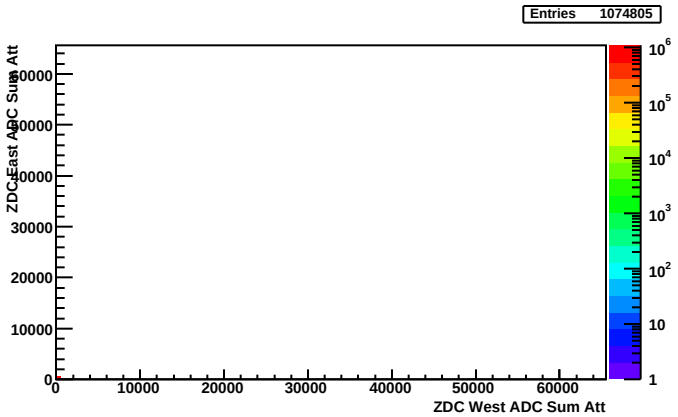

Entries 1074805
VPD-East ADC Sum
ZDC-East ADC Sum Att

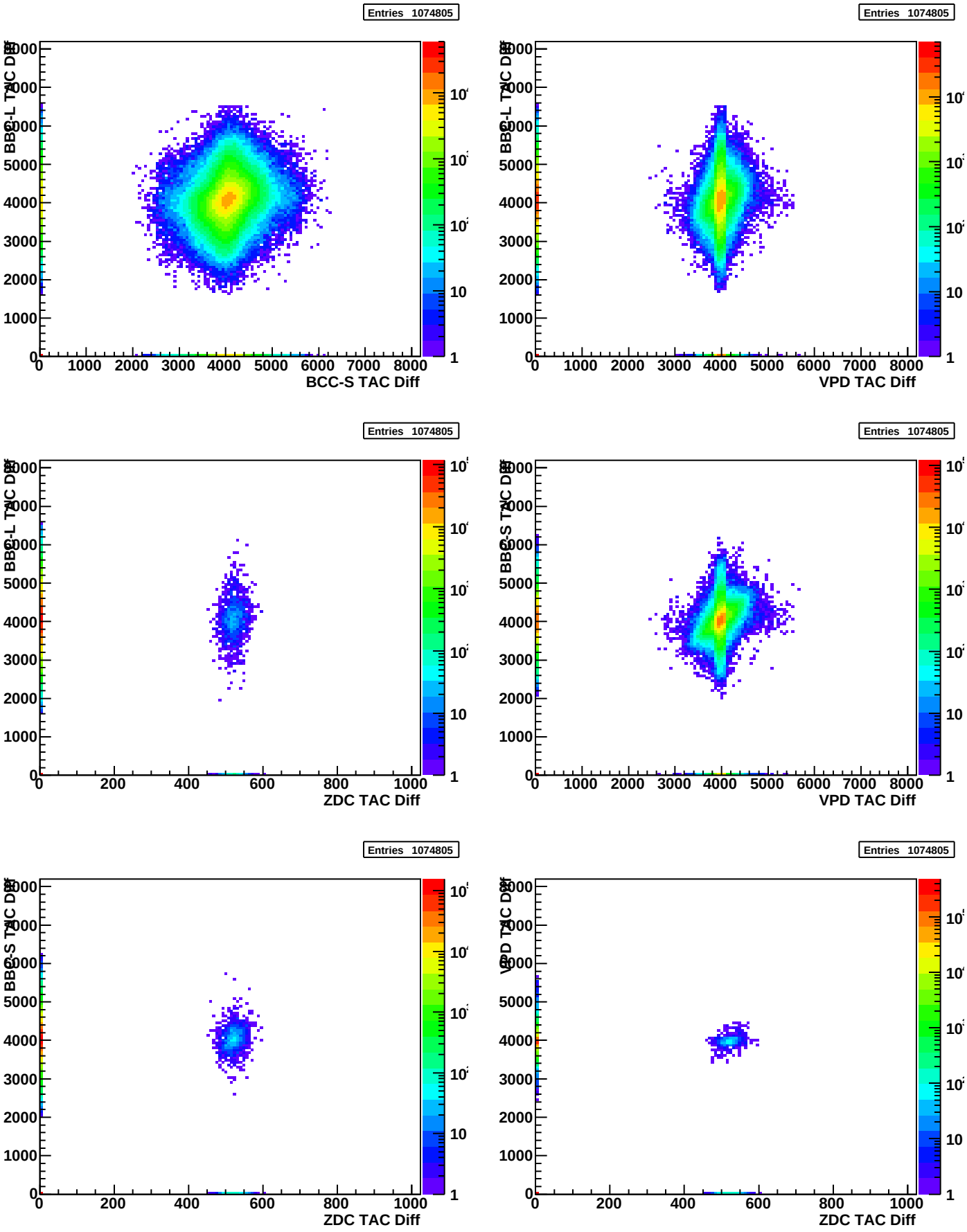

Entries 1074805
VPD-West ADC Sum
ZDC-West ADC Sum Att

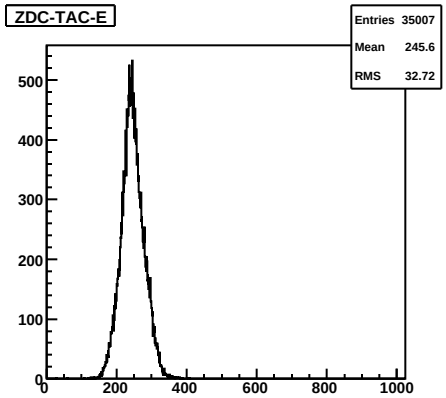

Entries 1074805
BBC-L-East ADC Sum
BBC-L-West ADC Sum

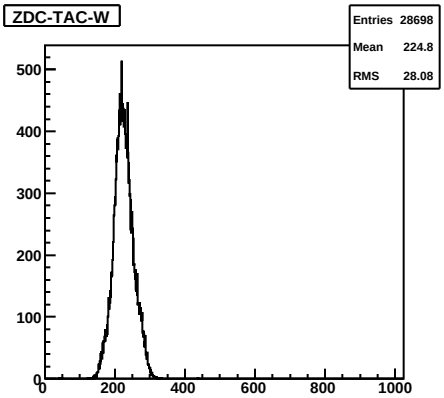

Entries 1074805
BBC-S-East ADC Sum
BBC-S-West ADC Sum

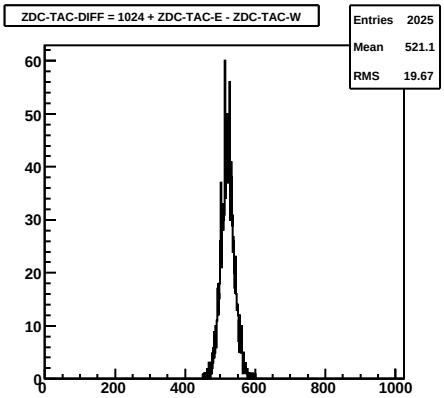

Entries 1074805
VPD East ADC Sum
VPD West ADC Sum

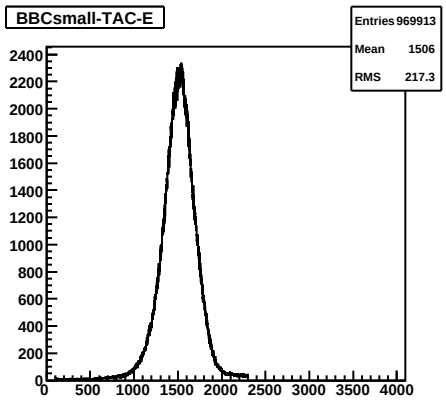

Entries 1074805
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

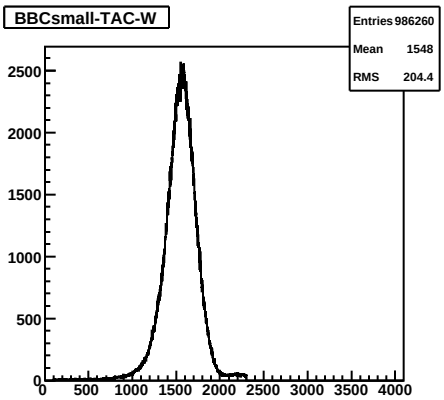

Entries 1074805
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1074805
BBC-L TAC Diff
VPD TAC Diff
Entries 1074805
BBC-L TAC Diff
ZDC TAC Diff
Entries 1074805
BBC-S TAC Diff
VPD TAC Diff
Entries 1074805
BBC-S TAC Diff
ZDC TAC Diff
Entries 1074805
VPD TAC Diff
ZDC TAC Diff

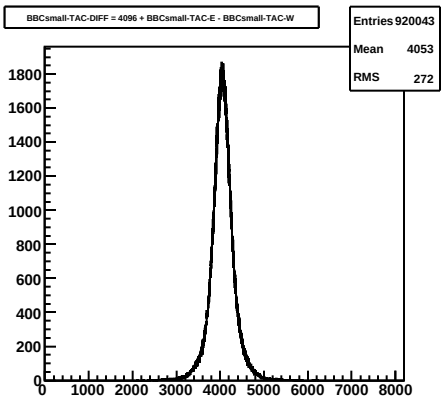

ZDC-TAC-E
Entries 35007
Mean 245.6
RMS 32.72

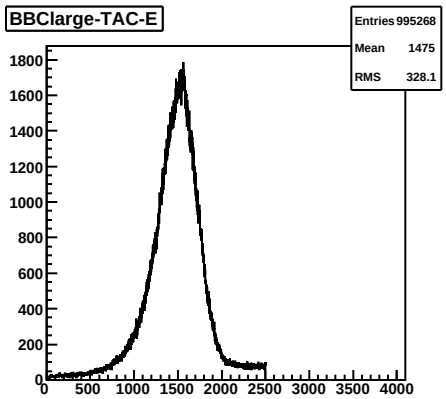

ZDC-TAC-W
Entries 28698
Mean 224.8
RMS 28.08

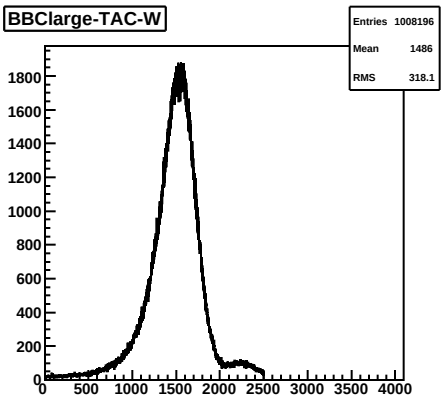

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2025
Mean 521.1
RMS 19.67

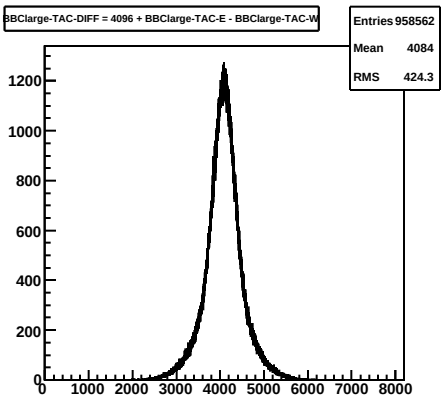

BBCsmall-TAC-E
Entries 969913
Mean 1506
RMS 217.3

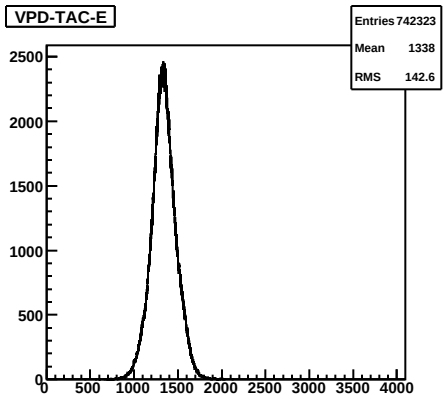

BBCsmall-TAC-W
Entries 986260
Mean 1548
RMS 204.4

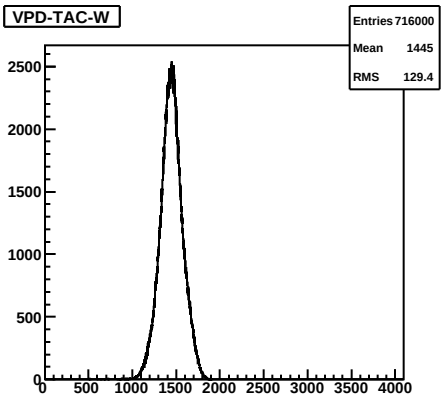

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 920043
Mean 4053
RMS 272

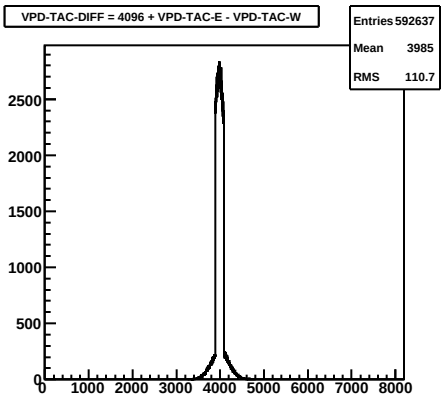

BBClarge-TAC-E
Entries 995268
Mean 1475
RMS 328.1

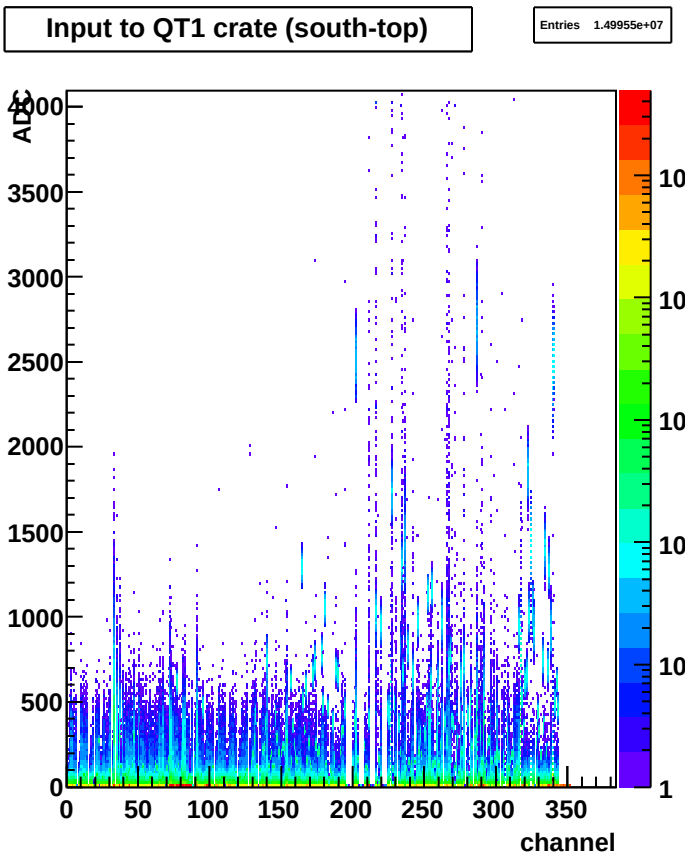

BBClarge-TAC-W
Entries 1008196
Mean 1486
RMS 318.1

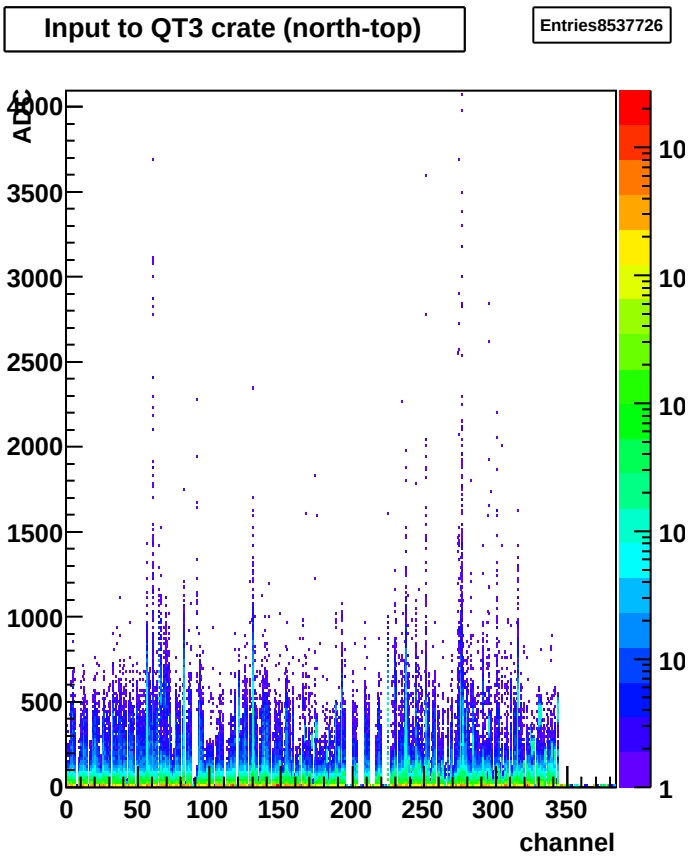

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 958562
Mean 4084
RMS 424.3

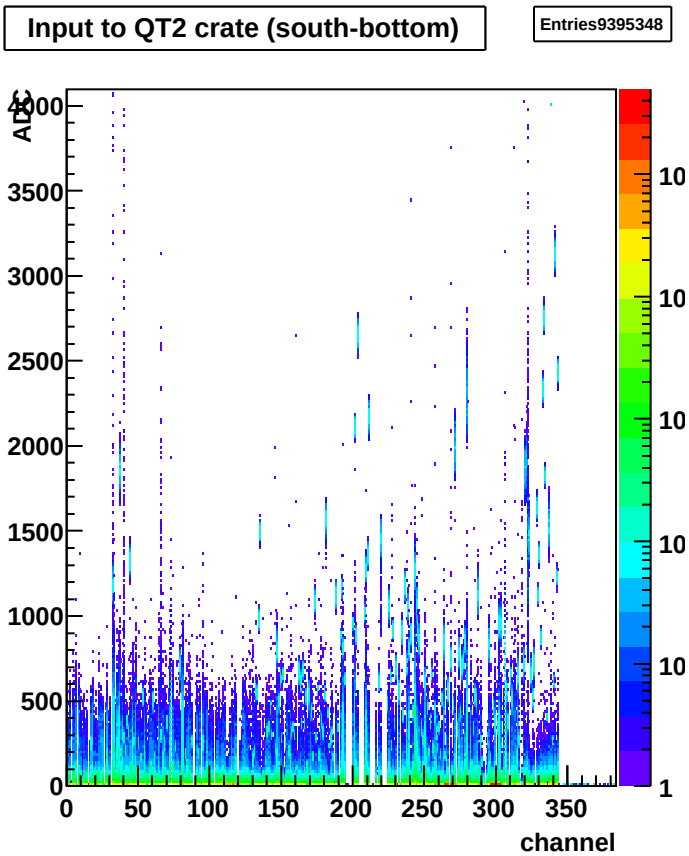

VPD-TAC-E
Entries 742323
Mean 1338
RMS 142.6

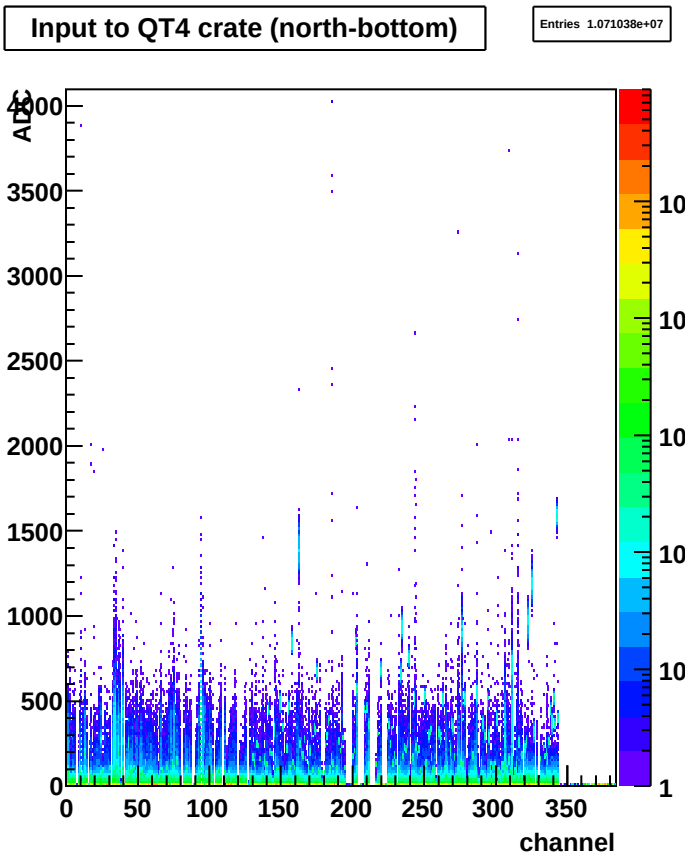

VPD-TAC-W
Entries 716000
Mean 1445
RMS 129.4

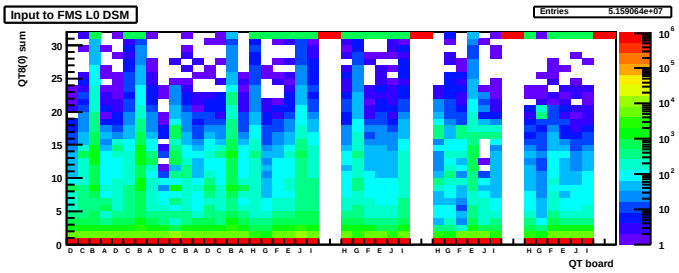

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 592637
Mean 3985
RMS 110.7

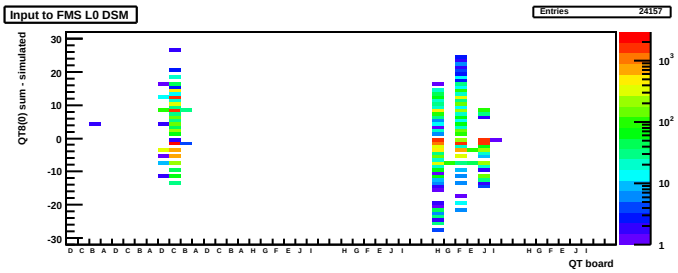

Input to QT1 crate (south-top)
Entries 1.49955e+07
ADC
channel

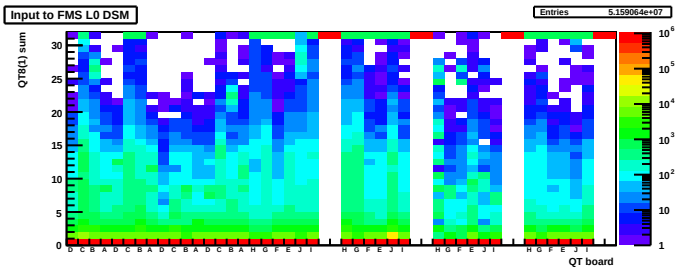

Input to QT3 crate (north-top)
Entries8537726
ADC
channel

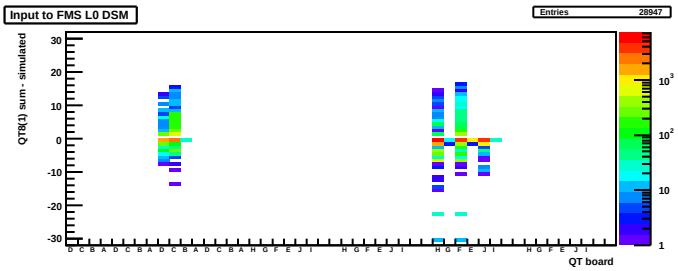

Input to QT2 crate (south-bottom)
Entries9395348
ADC
channel

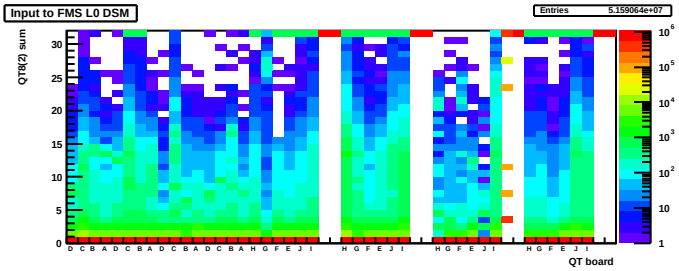

Input to QT4 crate (north-bottom)
Entries 1.071038e+07
ADC
channel

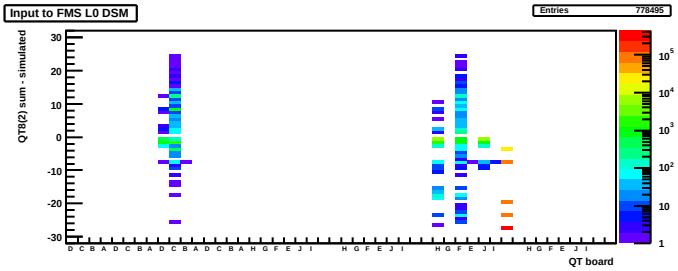

Input to FMS L0 DSM
Entries 5.159064e+07
QT8(0) sum
QT board

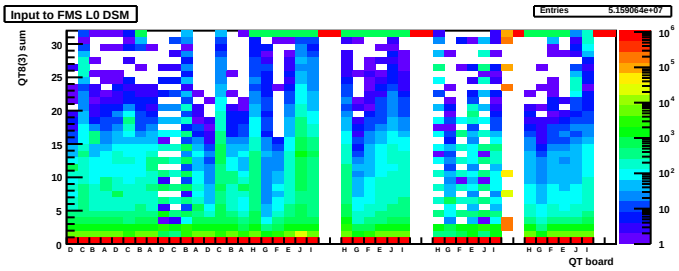

Input to FMS L0 DSM
Entries 24157
QT8(0) sum - simulated
QT board

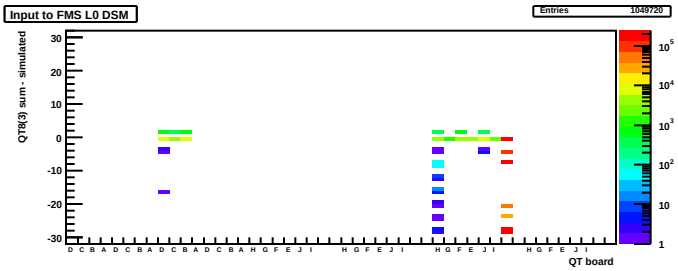

Input to FMS L0 DSM
Entries 5.159064e+07
QT8(1) sum
QT board

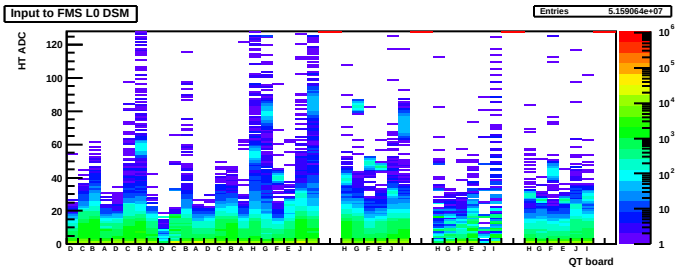

Input to FMS L0 DSM
Entries 28947
QT8(1) sum - simulated
QT board

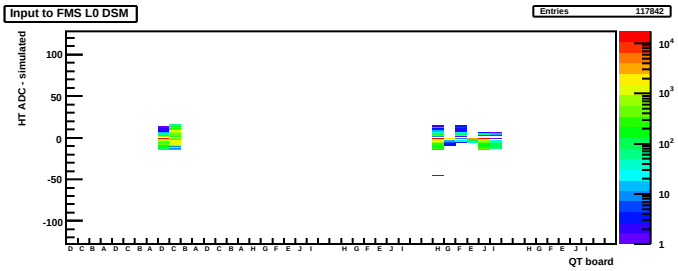

Input to FMS L0 DSM
Entries 5.159064e+07
QT8(2) sum
QT board

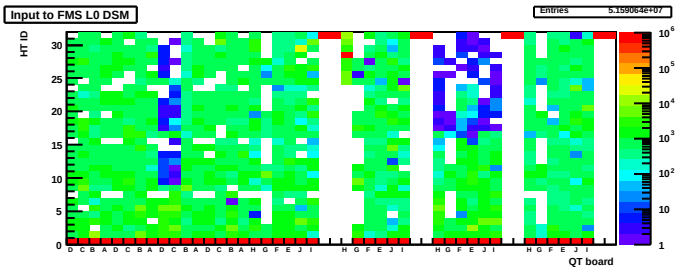

Input to FMS L0 DSM
Entries 778495
QT8(2) sum - simulated
QT board

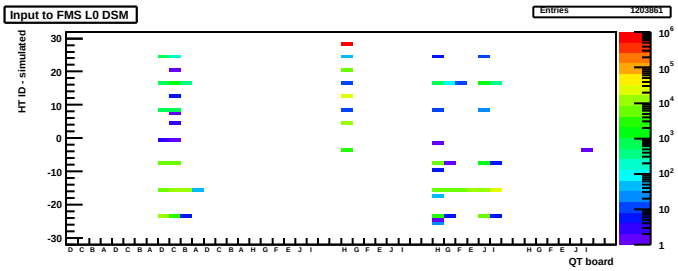

Input to FMS L0 DSM
Entries 5.159064e+07
QT8(3) sum
QT board

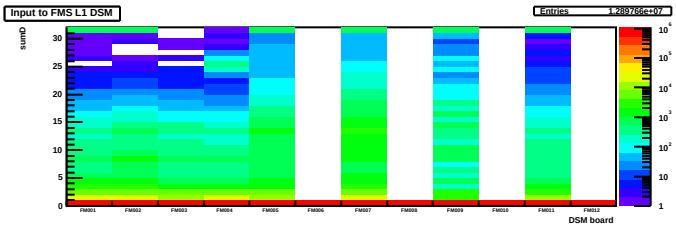

Input to FMS L0 DSM
Entries 1049720
QT8(3) sum - simulated
QT board

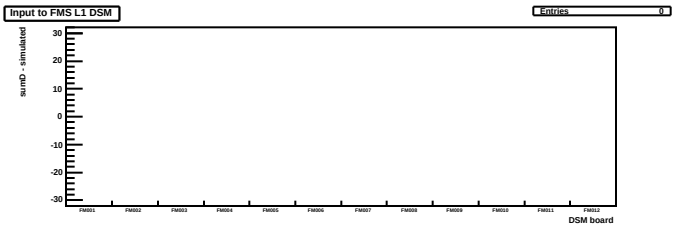

Input to FMS L0 DSM
Entries 5.159064e+07
HT ADC
QT board

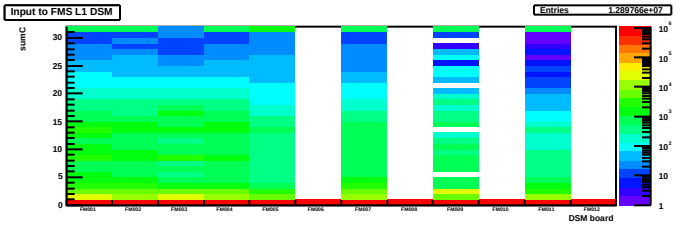

Input to FMS L0 DSM
Entries 117842
HT ADC - simulated
QT board

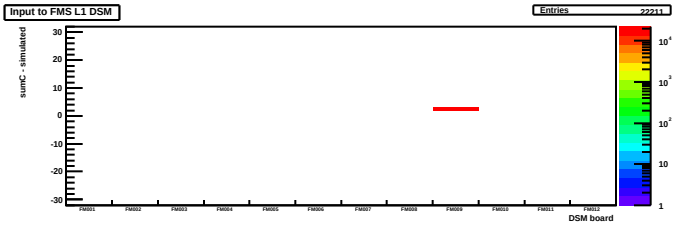

Input to FMS L0 DSM
Entries 5.159064e+07
HT ID
QT board

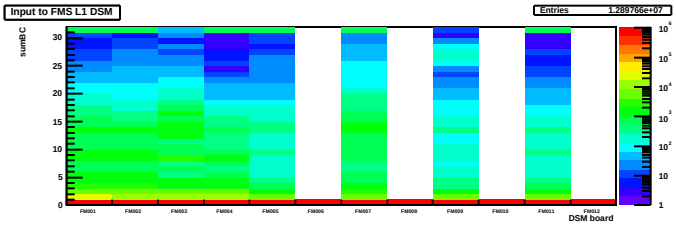

Input to FMS L0 DSM
Entries 1203861
HT ID - simulated
QT board

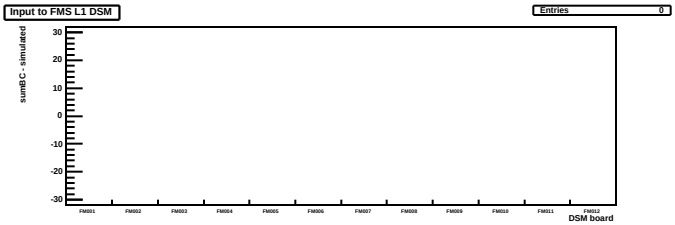

Input to FMS L1 DSM
Entries 1.289766e+07
sumD
DSM board

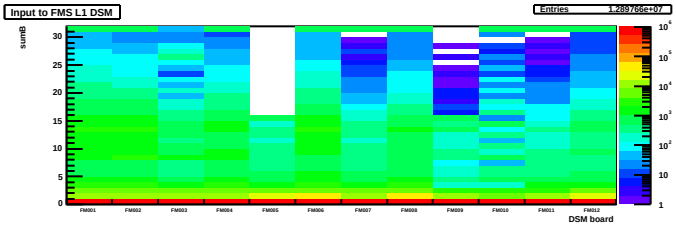

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

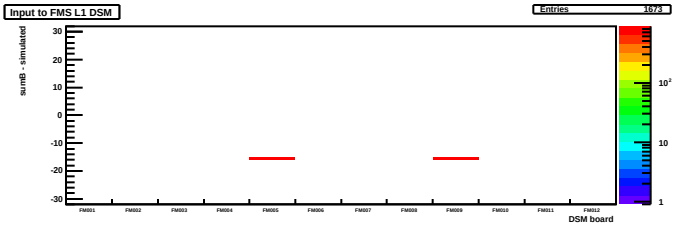

Input to FMS L1 DSM
Entries 1.289766e+07
sumC
DSM board

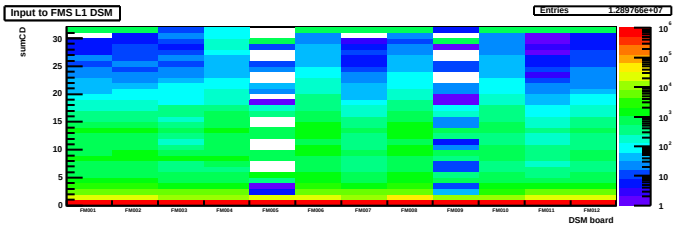

Input to FMS L1 DSM
Entries 22211
sumC - simulated
DSM board

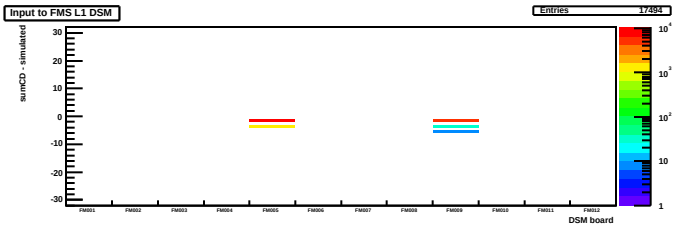

Input to FMS L1 DSM
Entries 1.289766e+07
sumBC
DSM board

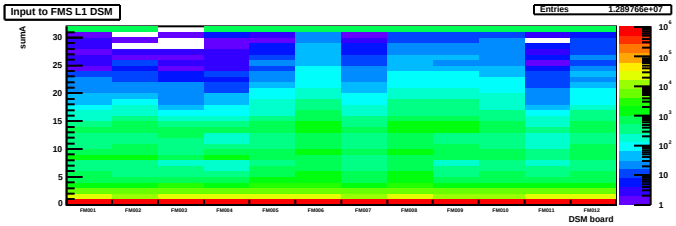

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

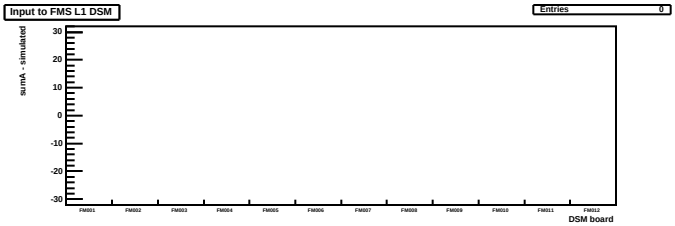

Input to FMS L1 DSM
Entries 1.289766e+07
sumB
DSM board

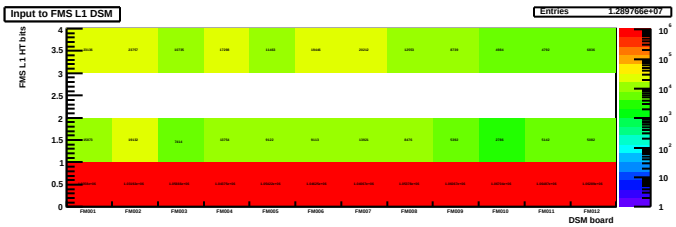

Input to FMS L1 DSM
Entries 1673
sumB - simulated
DSM board

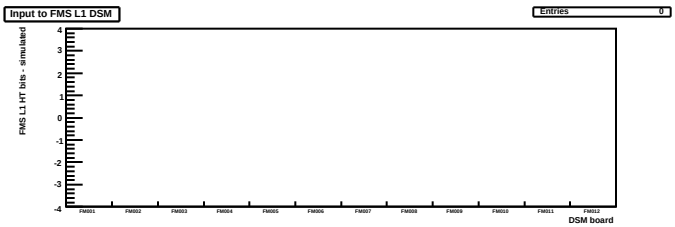

Input to FMS L1 DSM
Entries 1.289766e+07
sumCD
DSM board

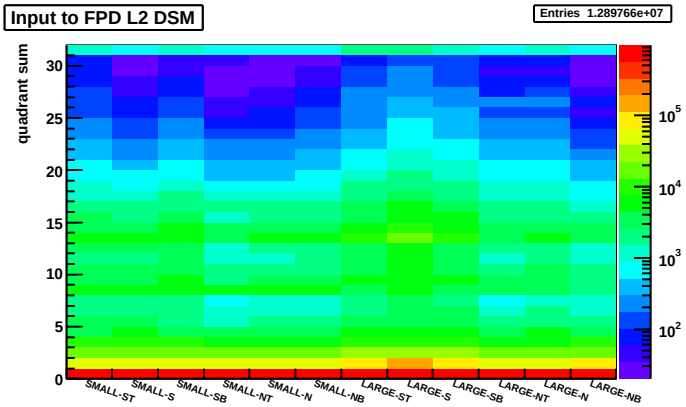

Input to FMS L1 DSM
Entries 17494
sumCD - simulated
DSM board

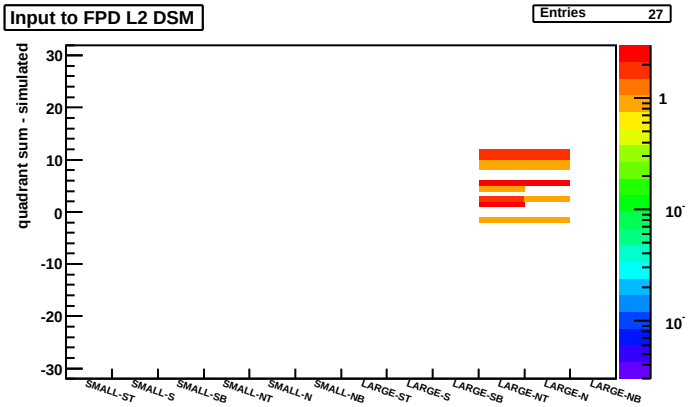

Input to FMS L1 DSM
Entries 1.289766e+07
sumA
DSM board

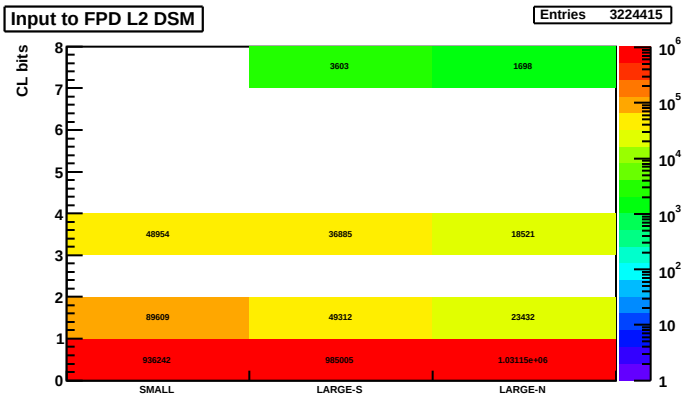

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

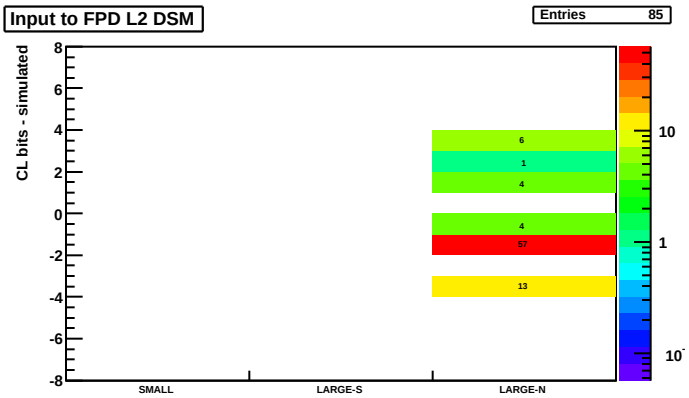

Input to FMS L1 DSM
Entries 1.289766e+07
FMS L1 HT bits
DSM board

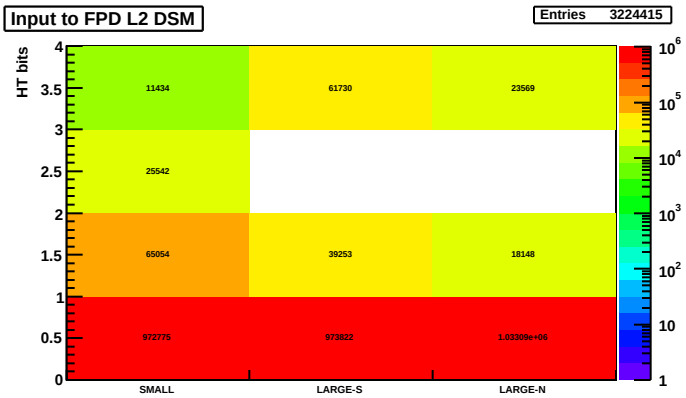

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

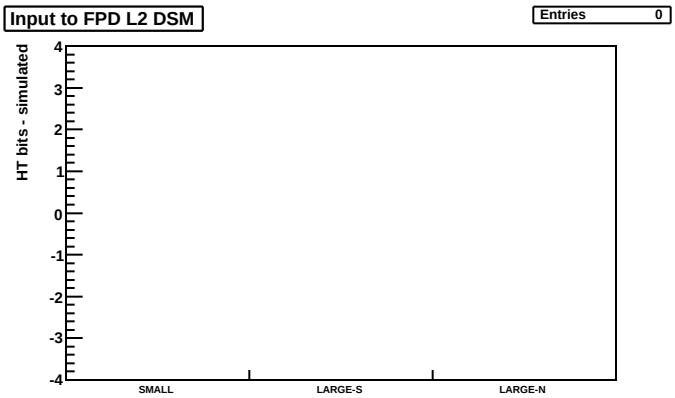

Input to FPD L2 DSM
Entries 1.289766e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

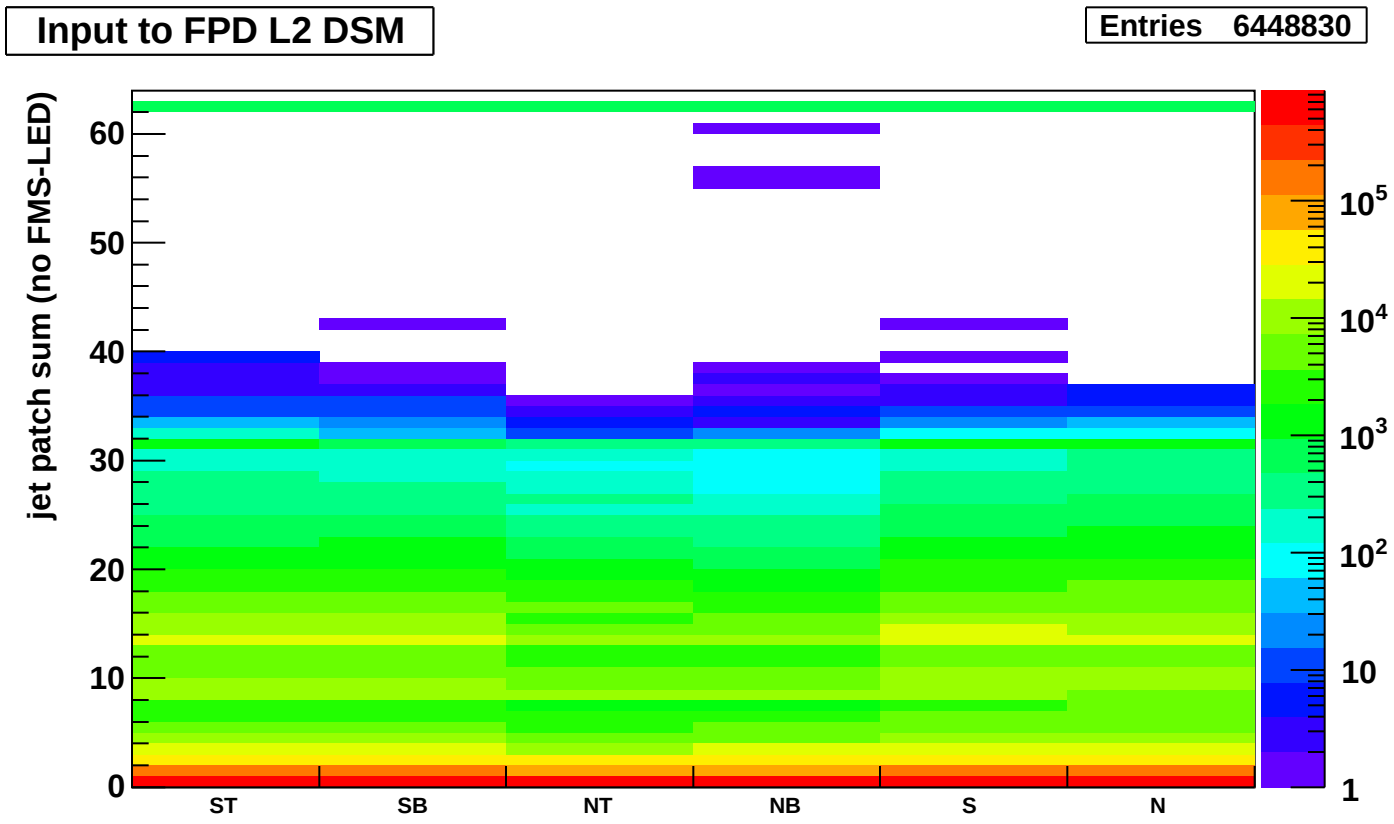

Input to FPD L2 DSM
Entries 27
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

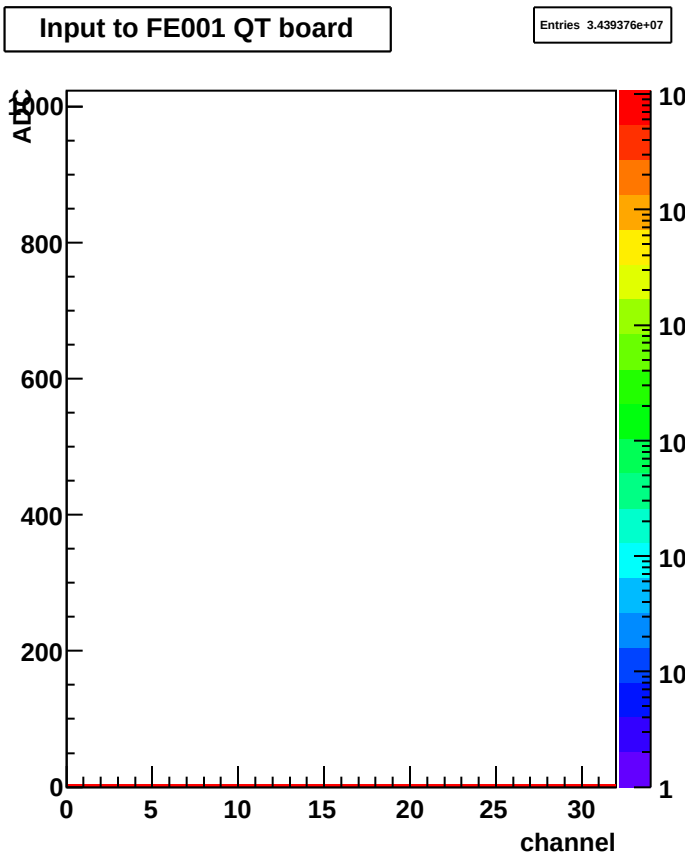

Input to FPD L2 DSM
Entries 3224415
CL bits
3603
1698
48954
36885
18521
89609
49312
23432
936242
985005
1.03115e+06
SMALL
LARGE-S
LARGE-N

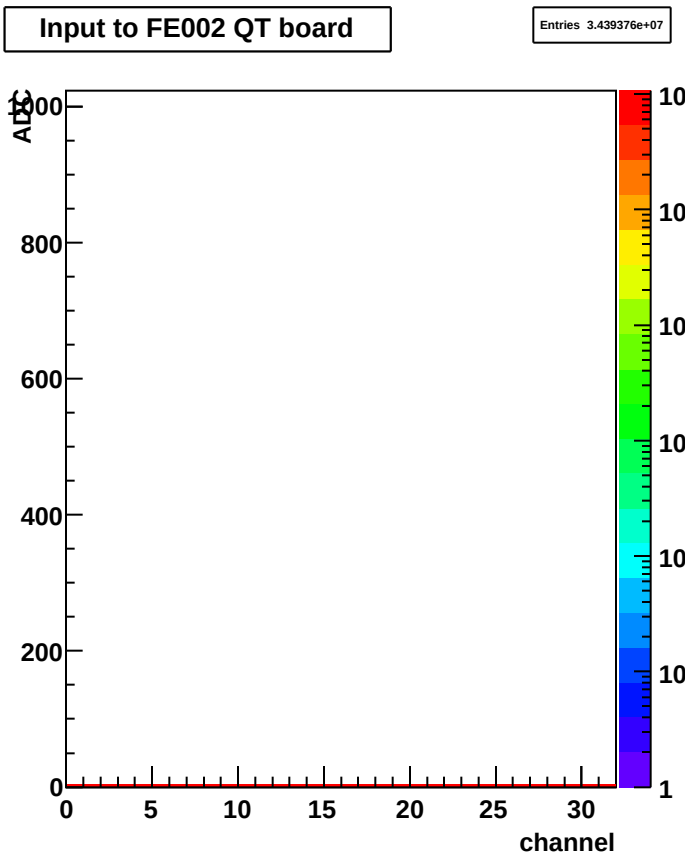

Input to FPD L2 DSM
Entries 85
CL bits - simulated
6
1
4
4
57
13
SMALL
LARGE-S
LARGE-N

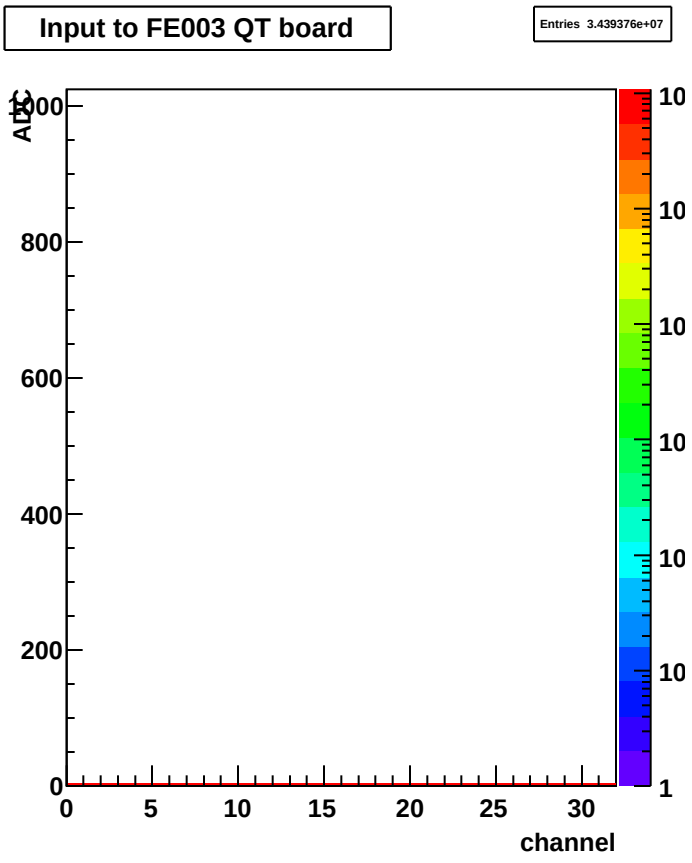

Input to FPD L2 DSM
Entries 3224415
HT bits
11434
61730
23569
25542
65054
39253
18148
972775
973822
1.03309e+06
SMALL
LARGE-S
LARGE-N

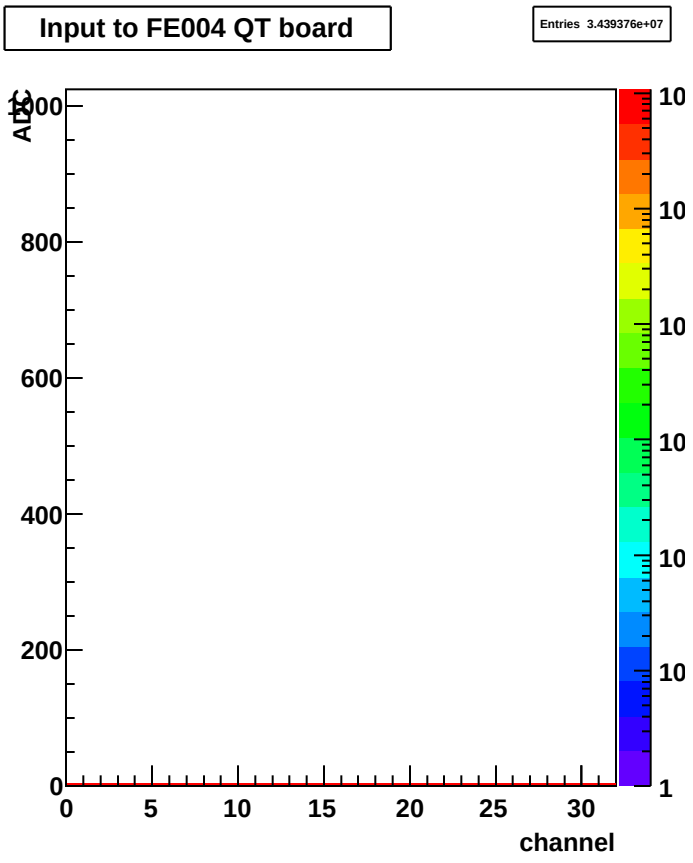

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

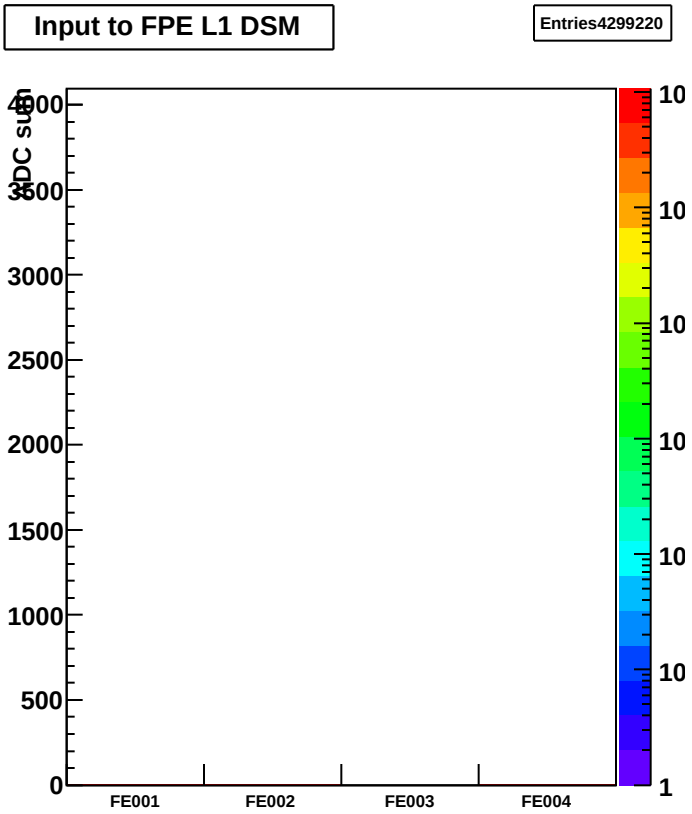

Input to FPD L2 DSM
Entries 6448830
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

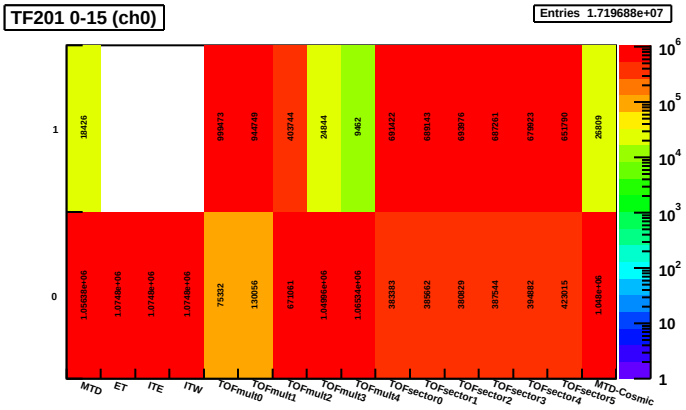

Input to FE001 QT board
Entries 3.439376e+07
ADC
channel

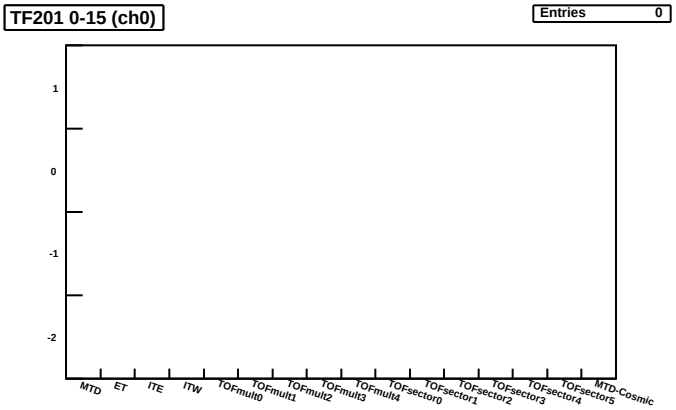

Input to FE002 QT board
Entries 3.439376e+07
ADC
channel

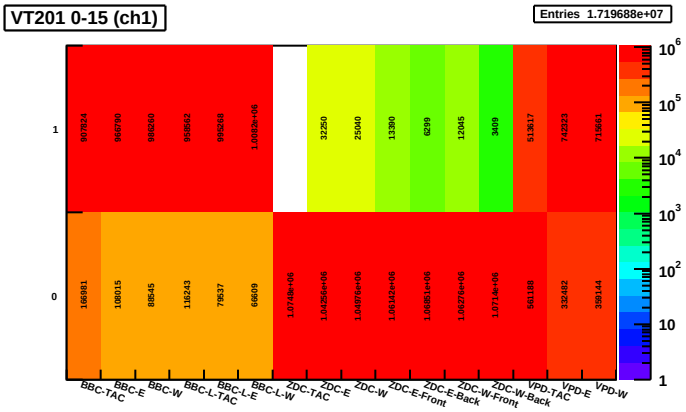

Input to FE003 QT board
Entries 3.439376e+07
ADC
channel

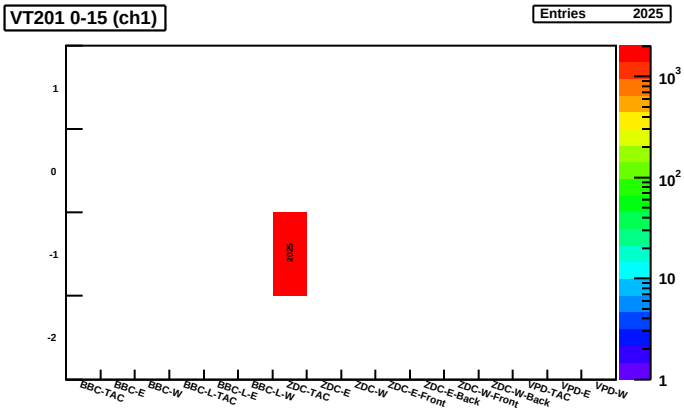

Input to FE004 QT board
Entries 3.439376e+07
ADC
channel


Input to FPE L1 DSM
Entries4299220
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 1.719688e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 1.719688e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 2025
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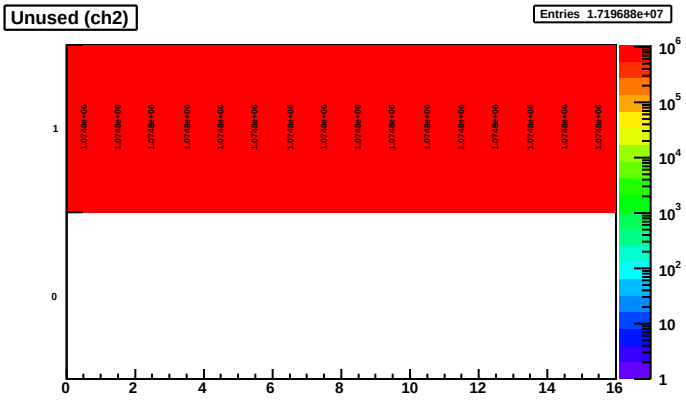
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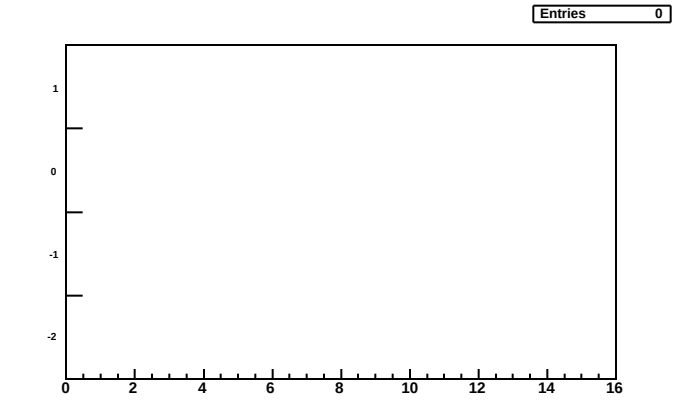
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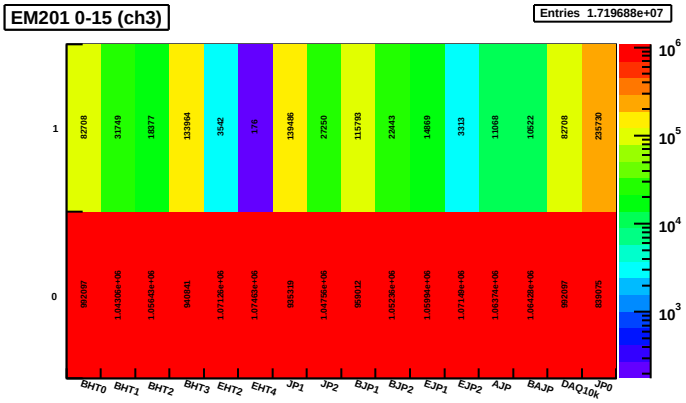
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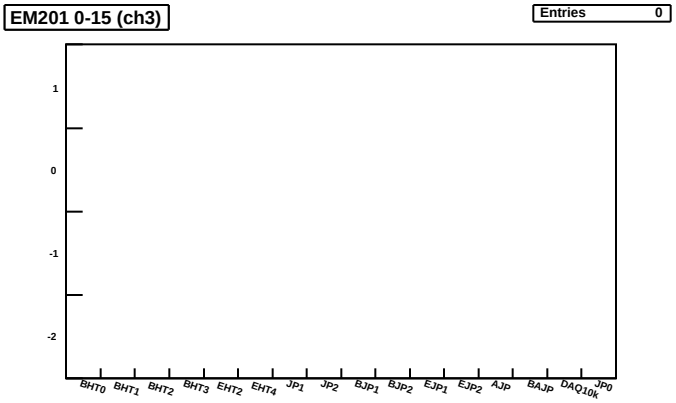
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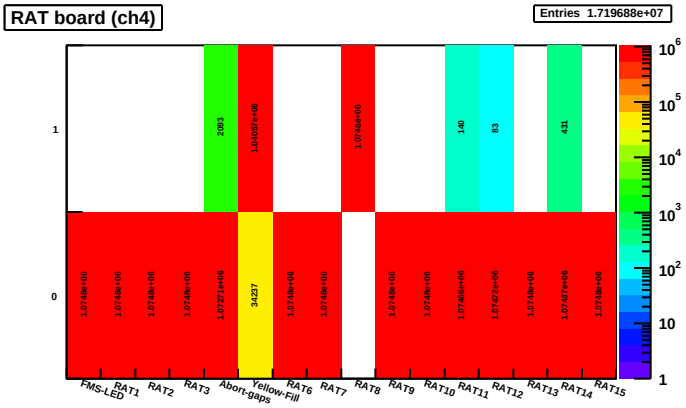
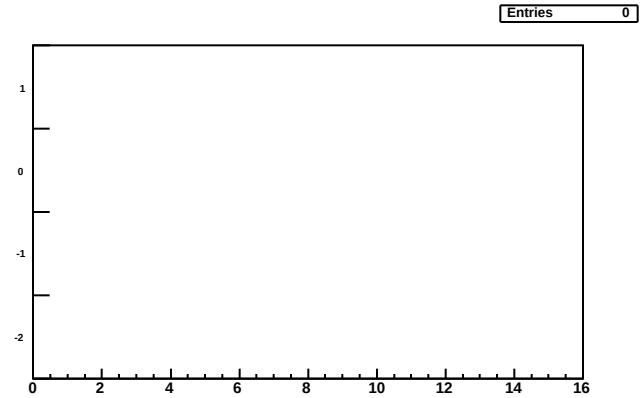
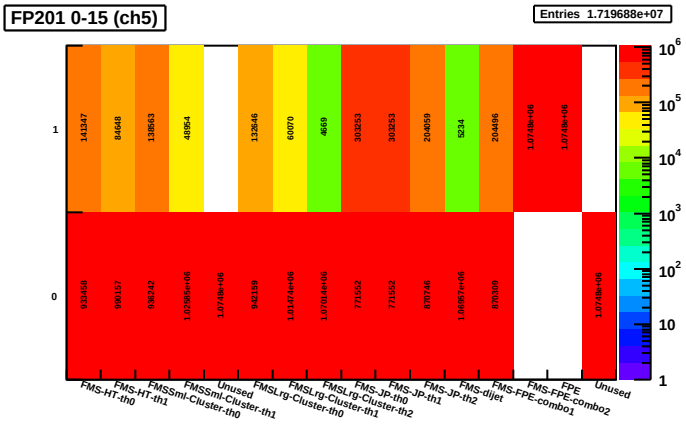
RAT board (ch4)





FP201 0-15 (ch5)



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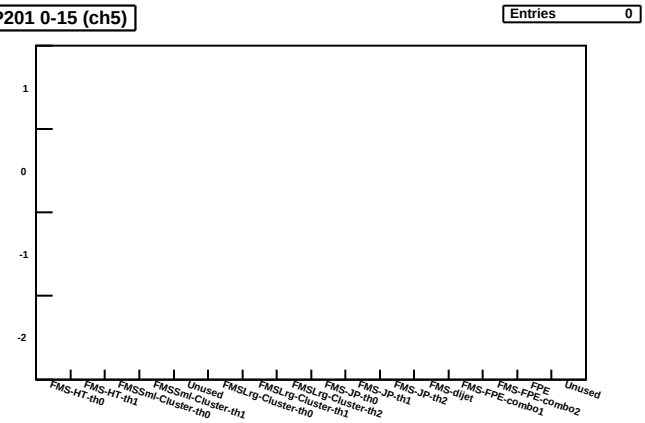
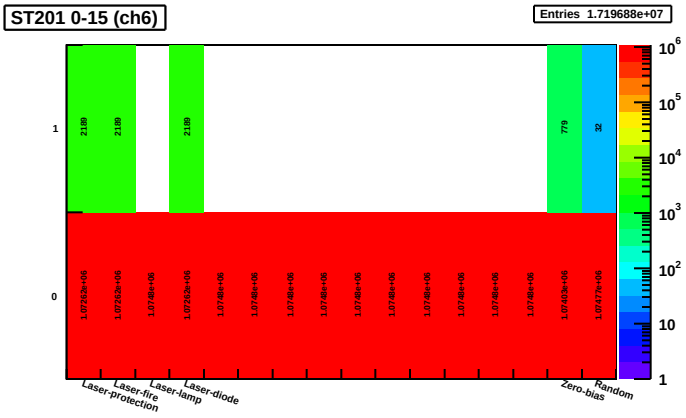
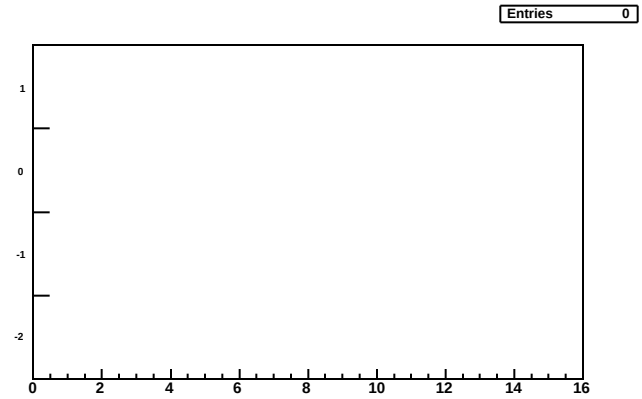
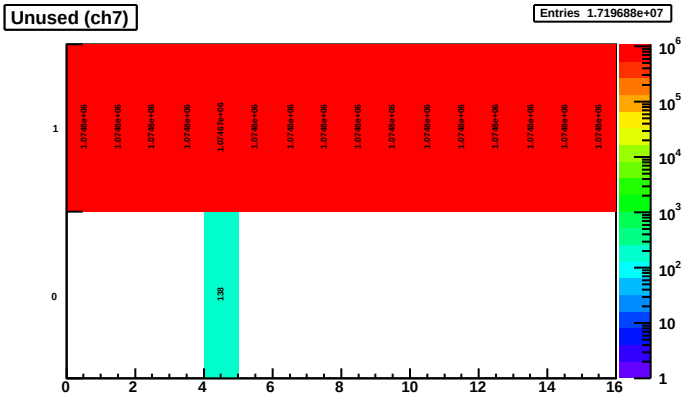
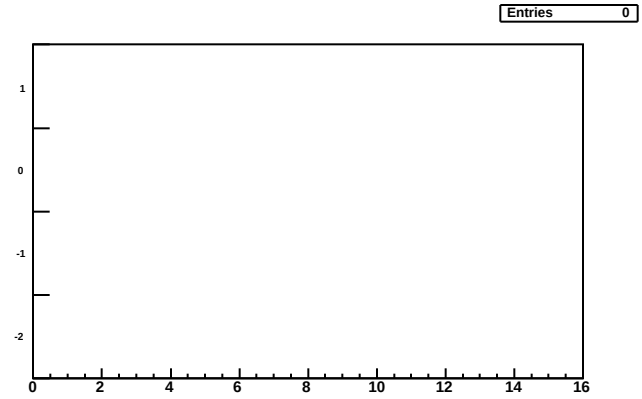


ST201 0-15 (ch6)





Unused (ch7)