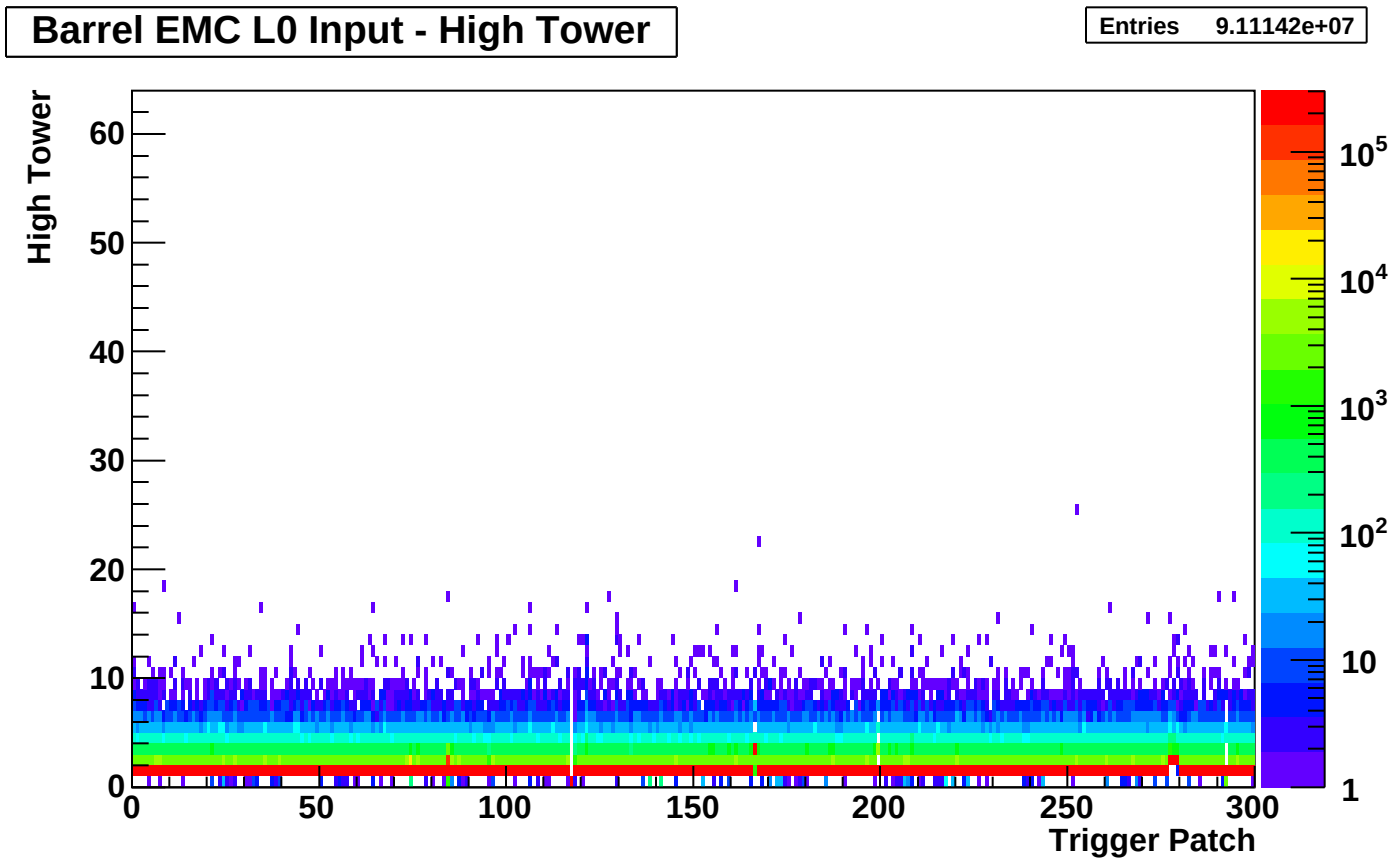
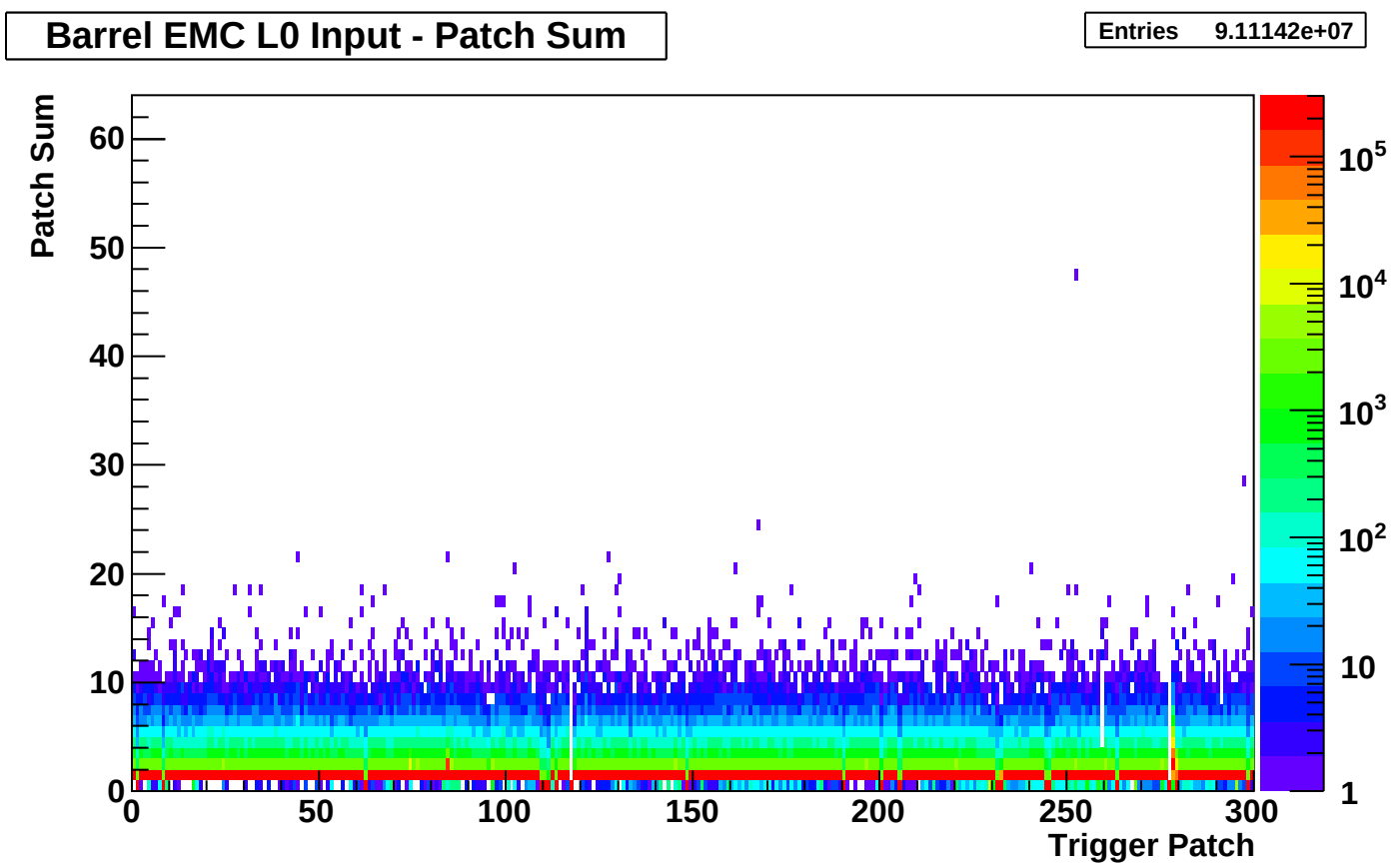
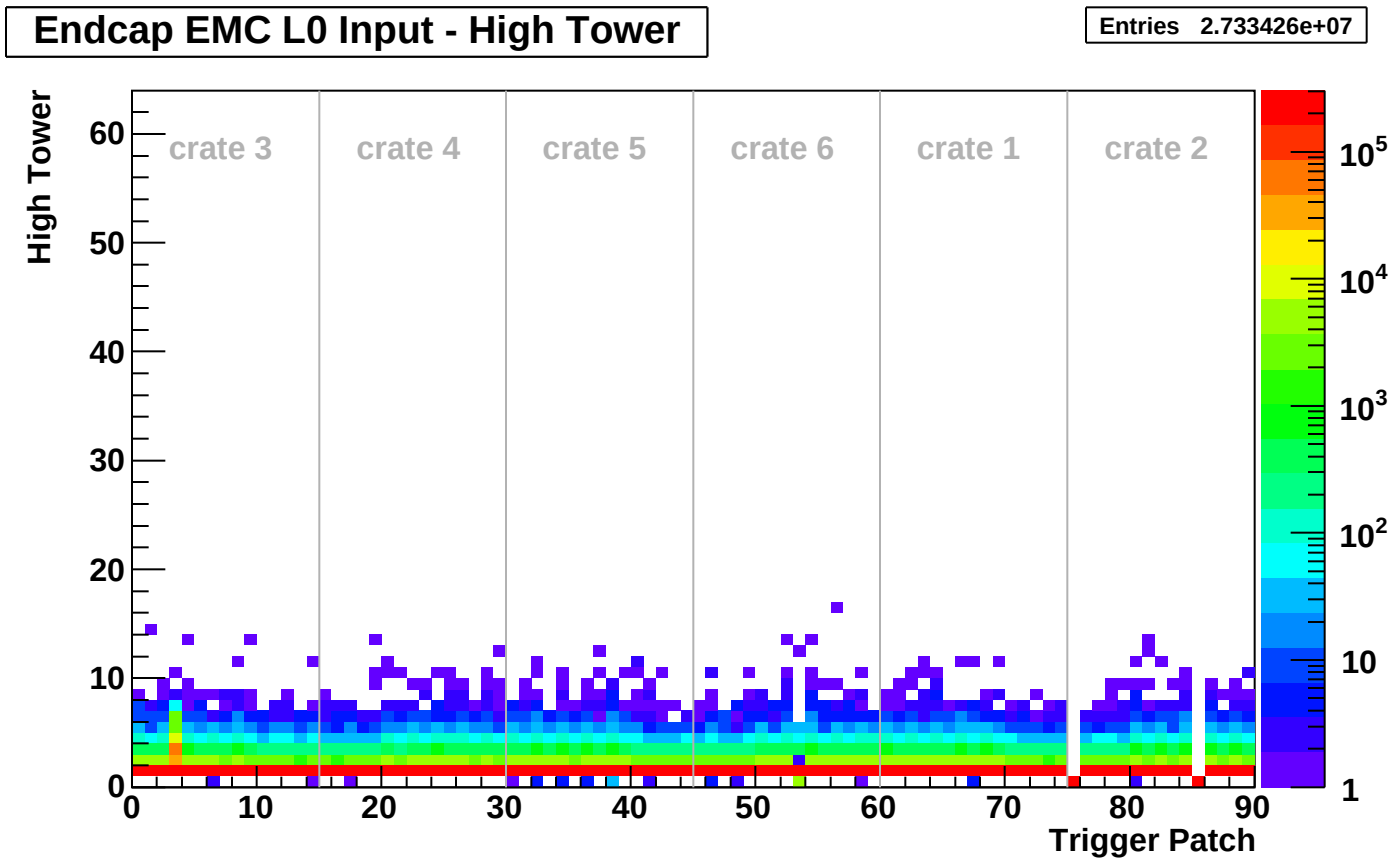
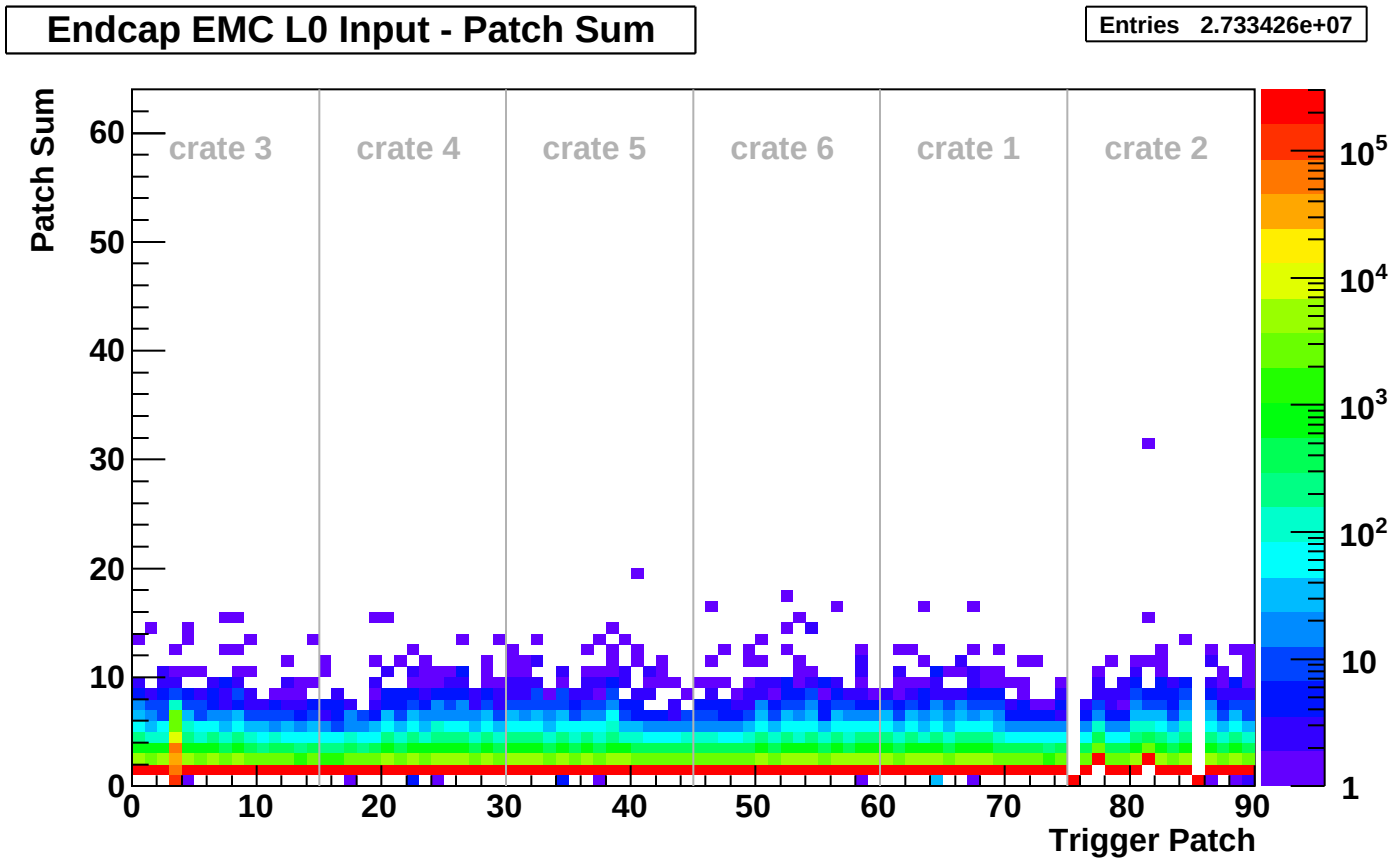
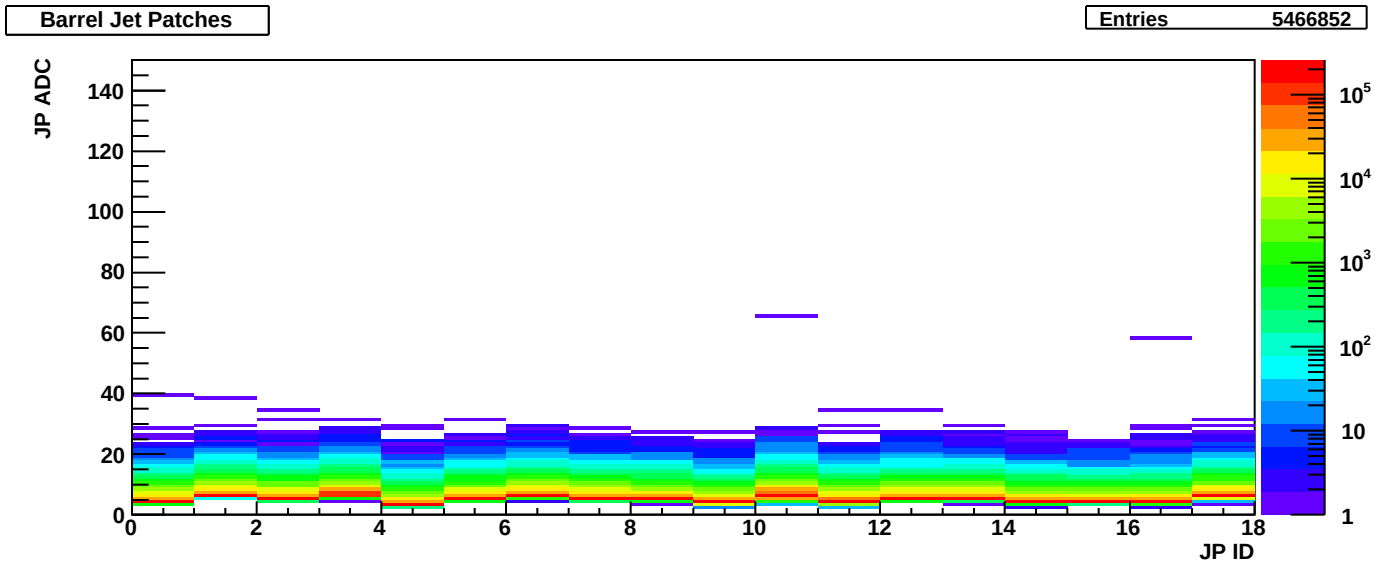
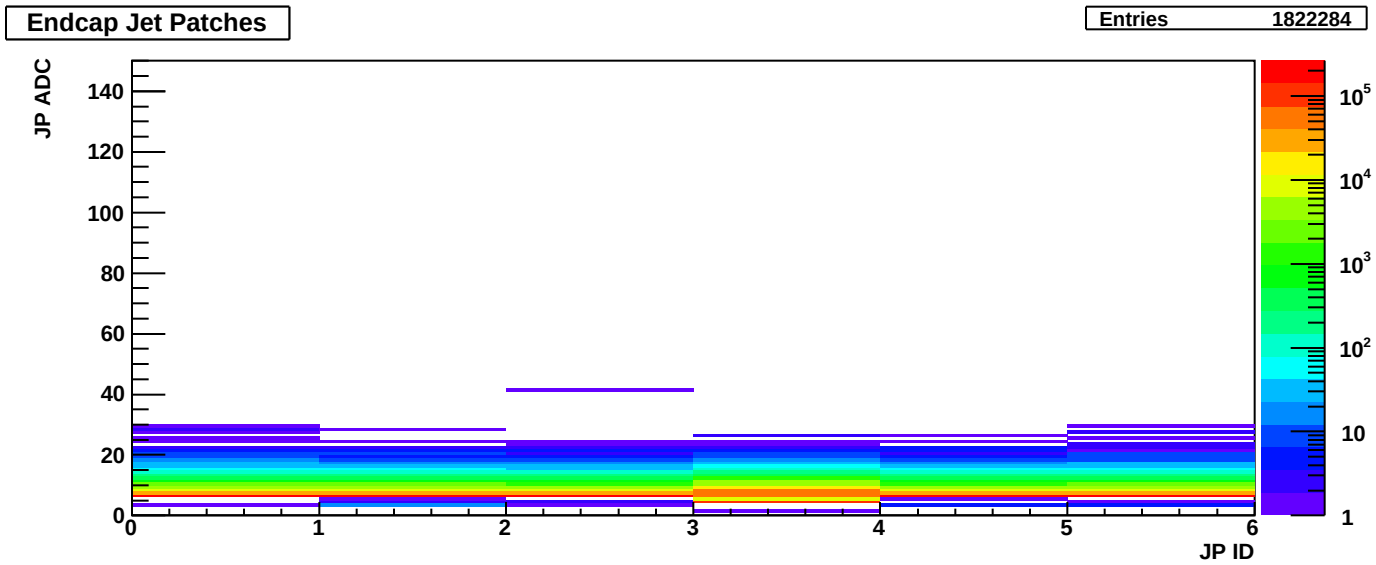

Barrel EMC L0 Input - High Tower
Entries 9.11142e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

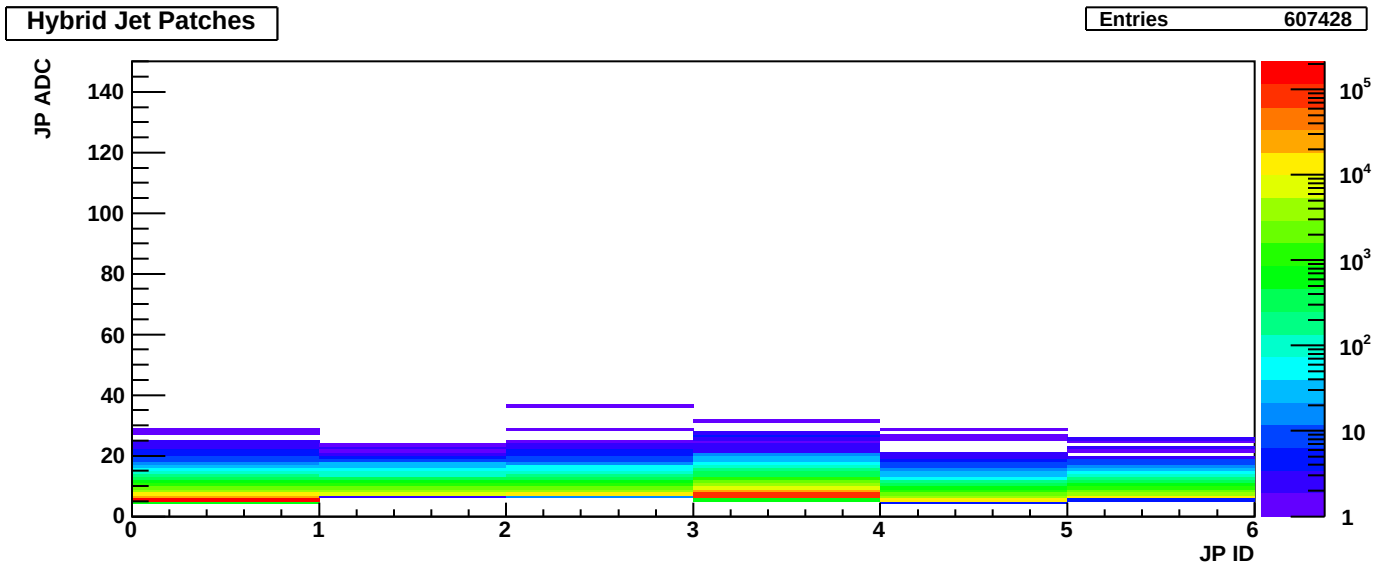

Barrel EMC L0 Input - Patch Sum
Entries 9.11142e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

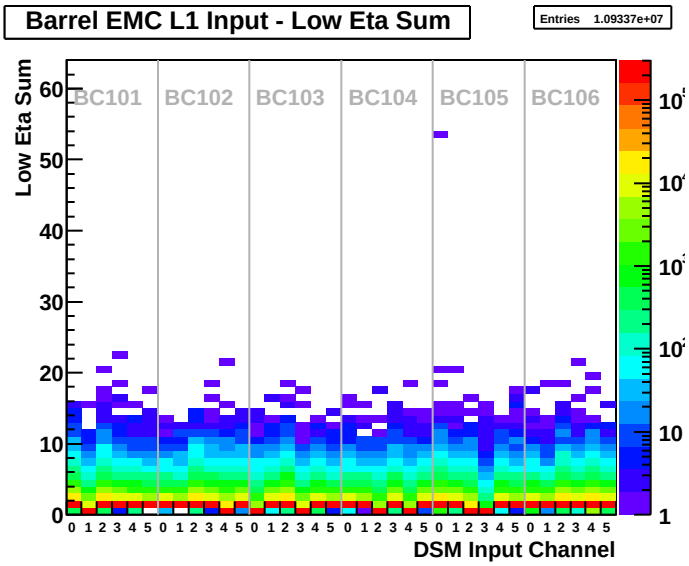

Endcap EMC L0 Input - High Tower
Entries 2.733426e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

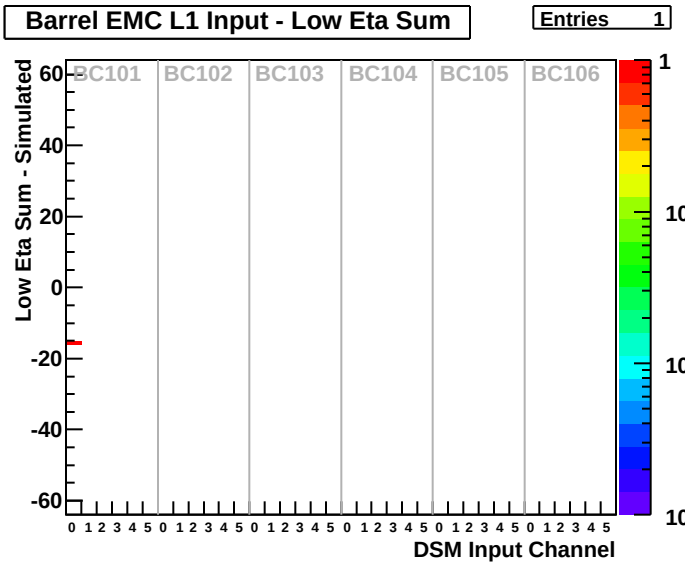

Endcap EMC L0 Input - Patch Sum
Entries 2.733426e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

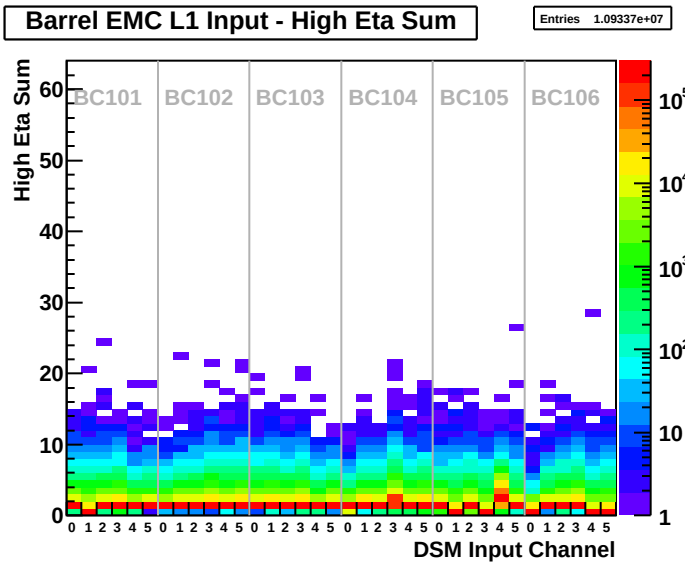

Barrel Jet Patches
Entries 5466852
JP ADC
JP ID

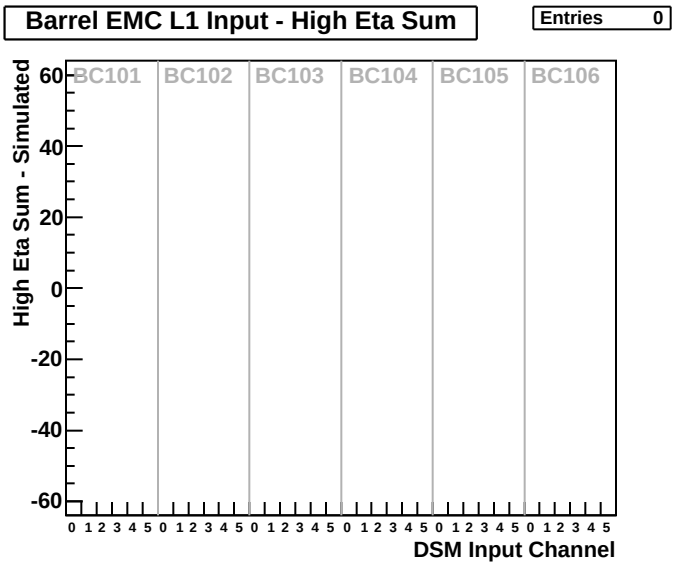

Endcap Jet Patches
Entries 1822284
JP ADC
JP ID

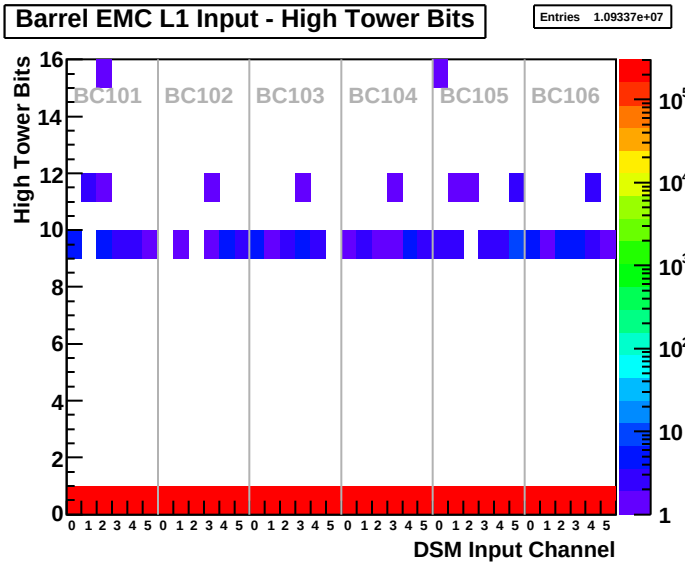

Hybrid Jet Patches
Entries 607428
JP ADC
JP ID

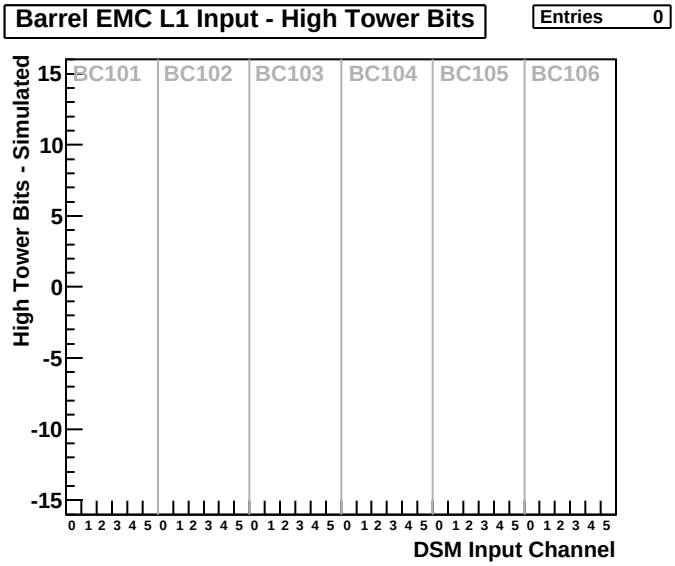

Barrel EMC L1 Input - Low Eta Sum
Entries 1.09337e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

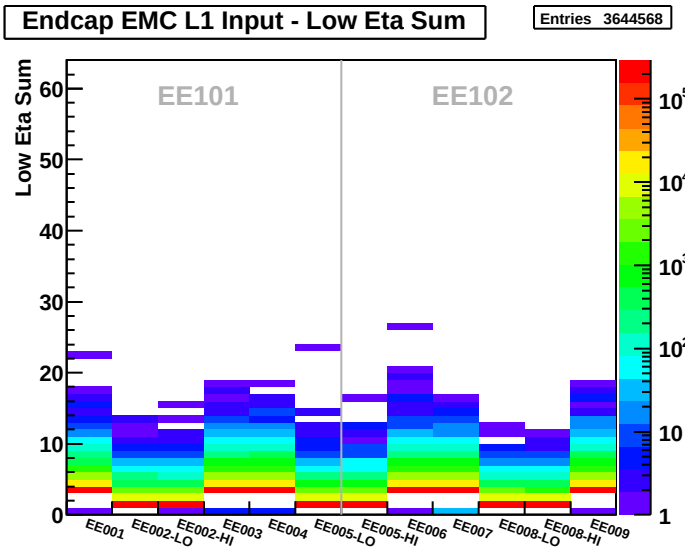

Barrel EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

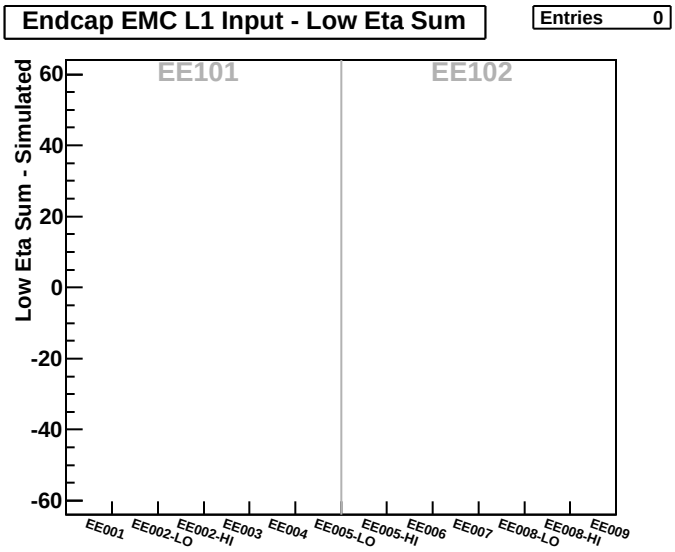

Barrel EMC L1 Input - High Eta Sum
Entries 1.09337e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

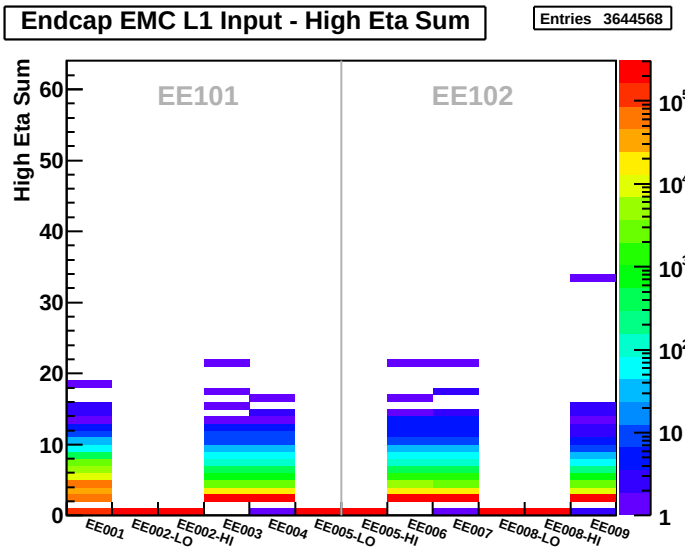

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

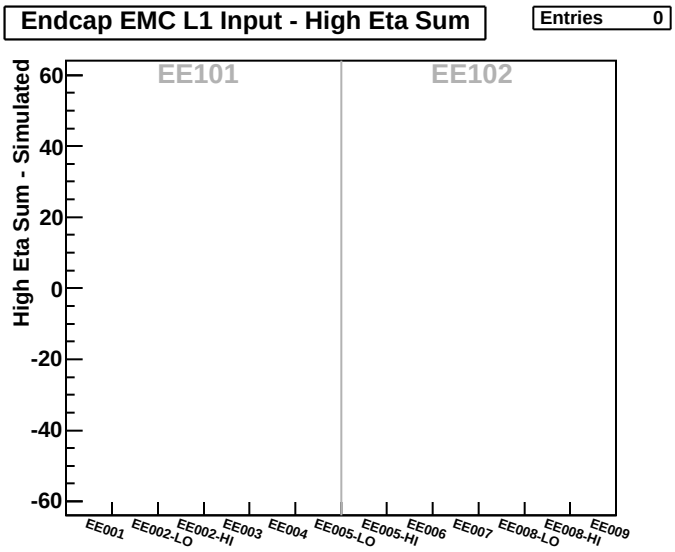

Barrel EMC L1 Input - High Tower Bits
Entries 1.09337e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

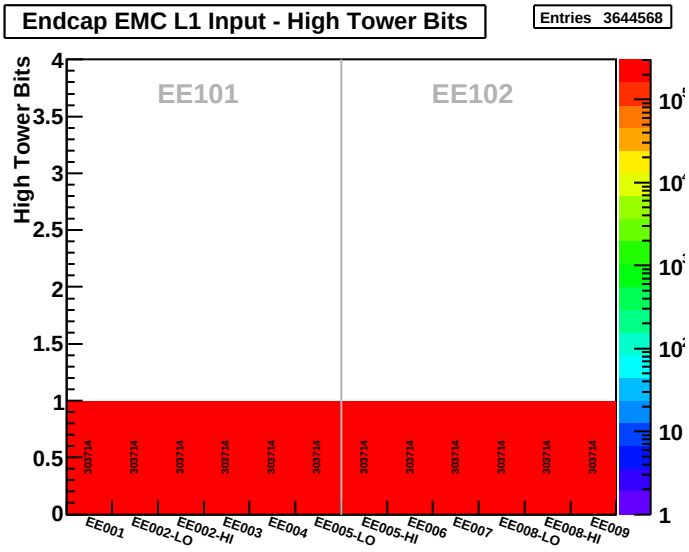

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

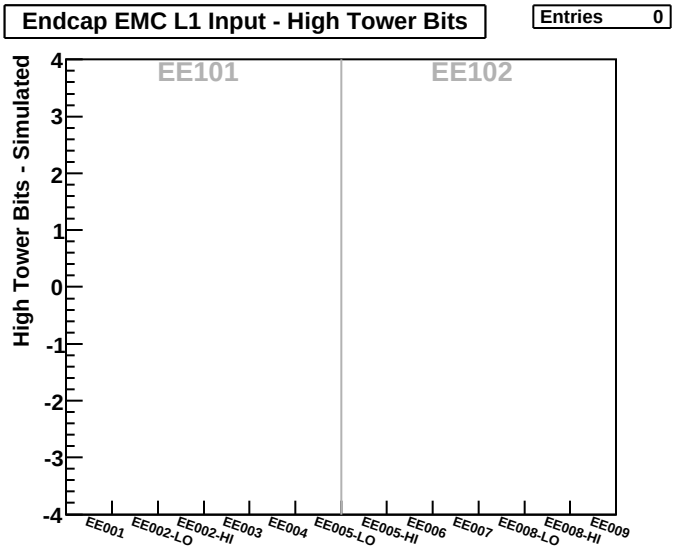

Endcap EMC L1 Input - Low Eta Sum
Entries 3644568
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

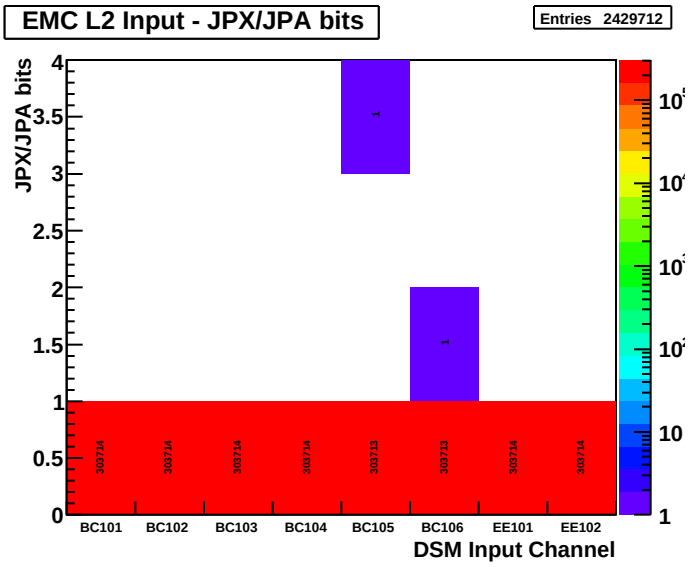

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

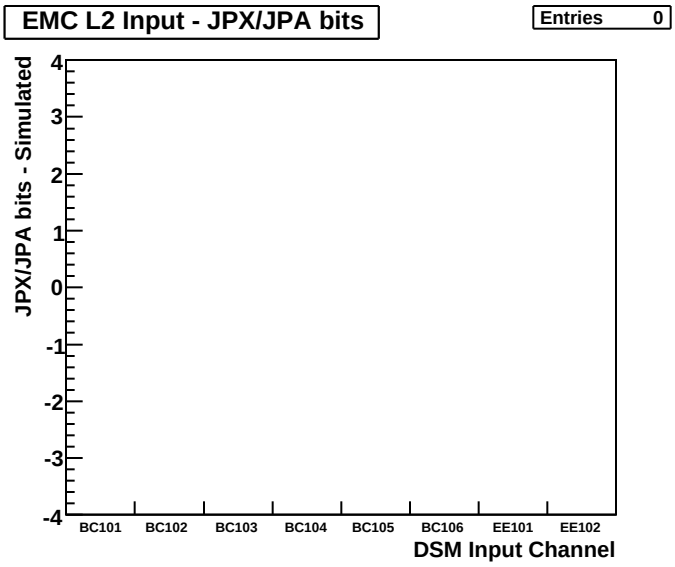

Endcap EMC L1 Input - High Eta Sum
Entries 3644568
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

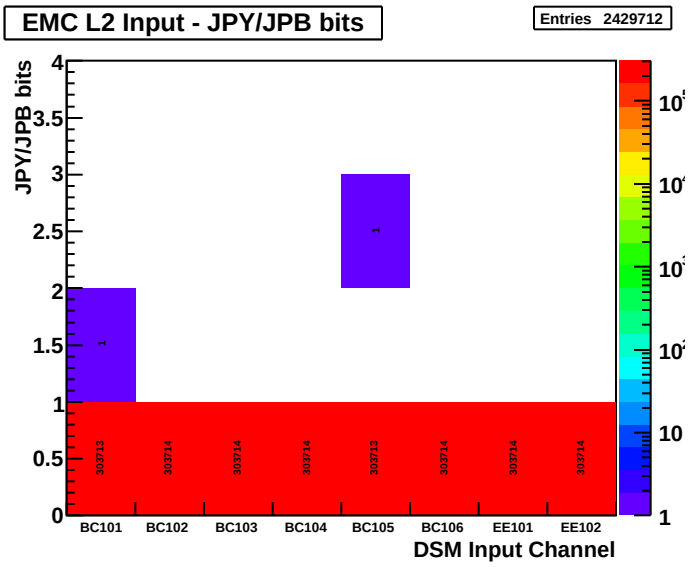

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

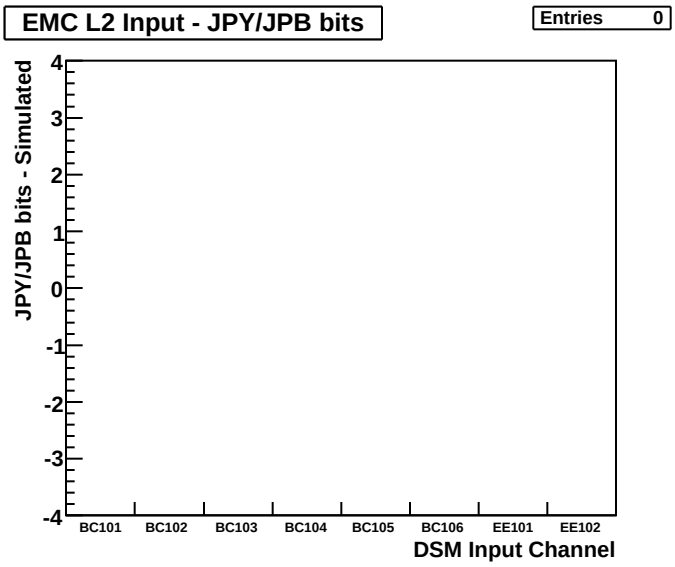

Endcap EMC L1 Input - High Tower Bits
Entries 3644568
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

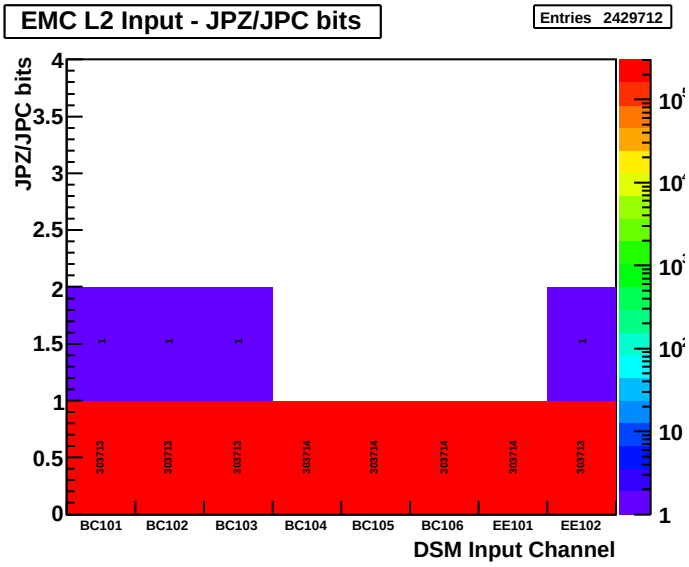

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

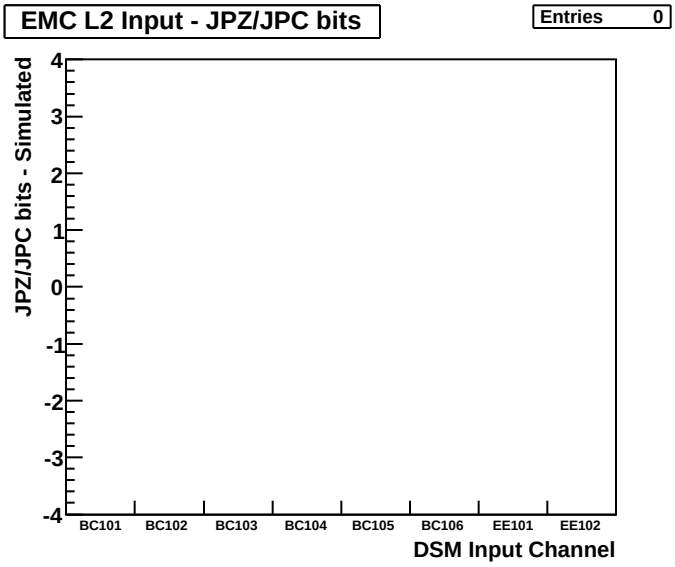

EMC L2 Input - JPX/JPA bits
Entries 2429712
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

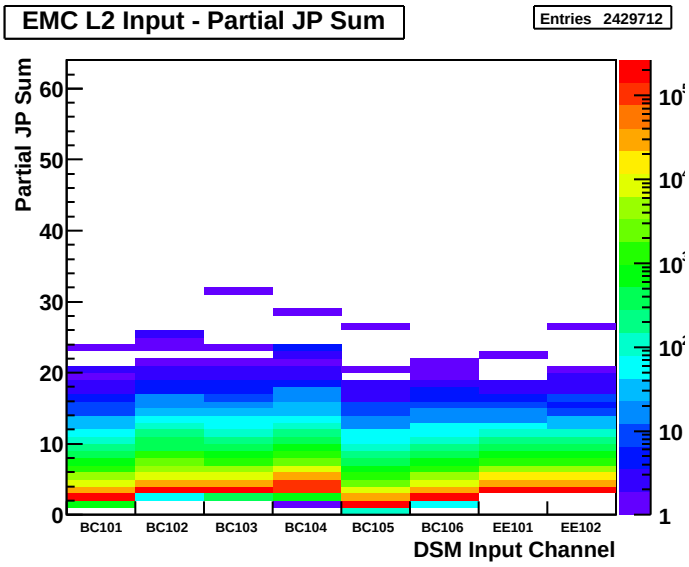

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

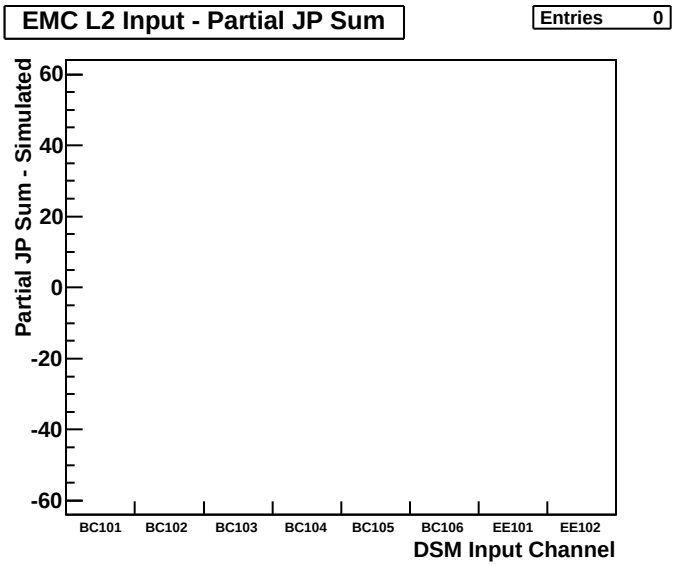

EMC L2 Input - JPY/JPB bits
Entries 2429712
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

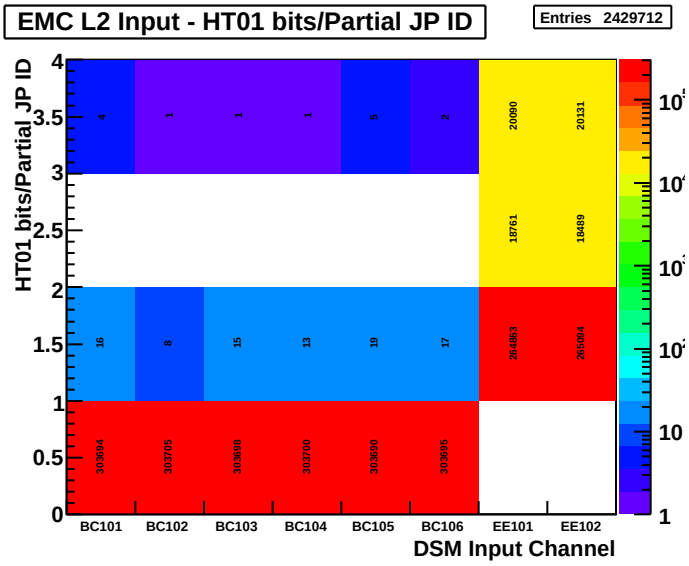

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

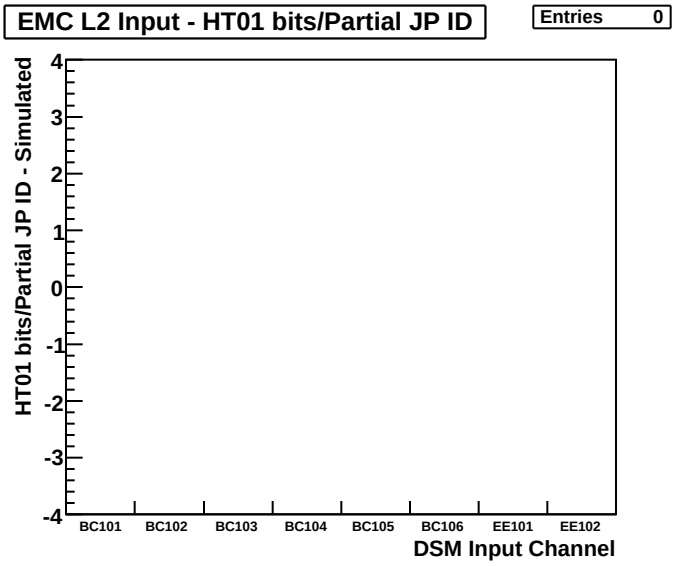

EMC L2 Input - JPZ/JPC bits
Entries 2429712
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

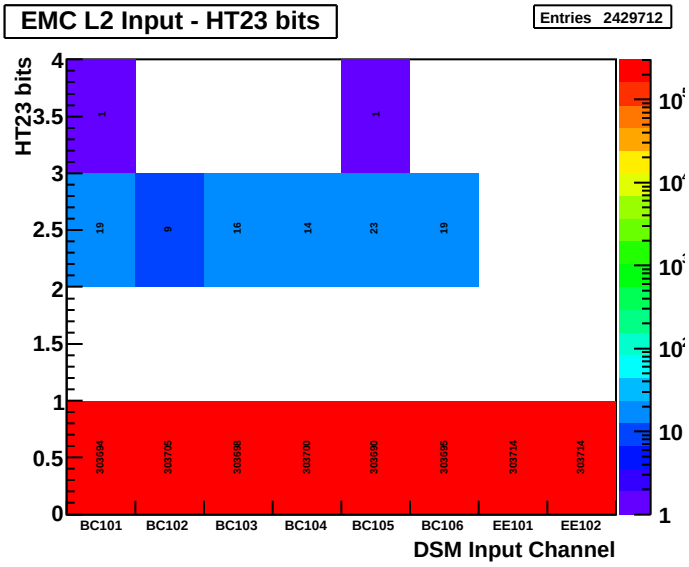

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

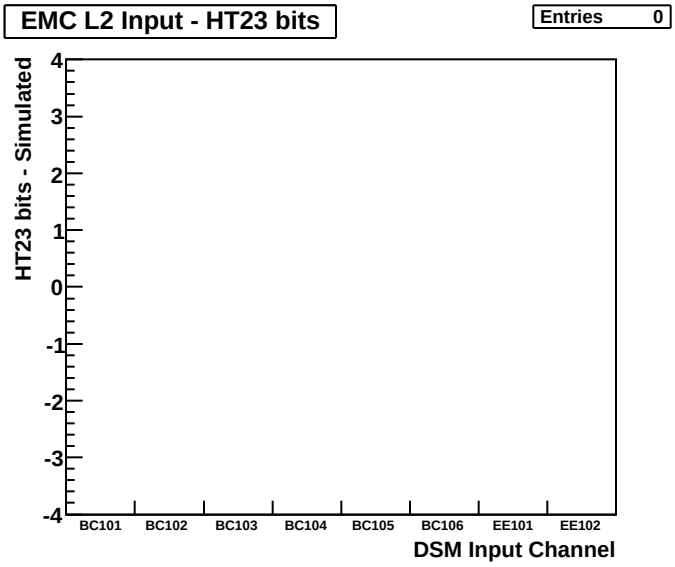

EMC L2 Input - Partial JP Sum
Entries 2429712
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

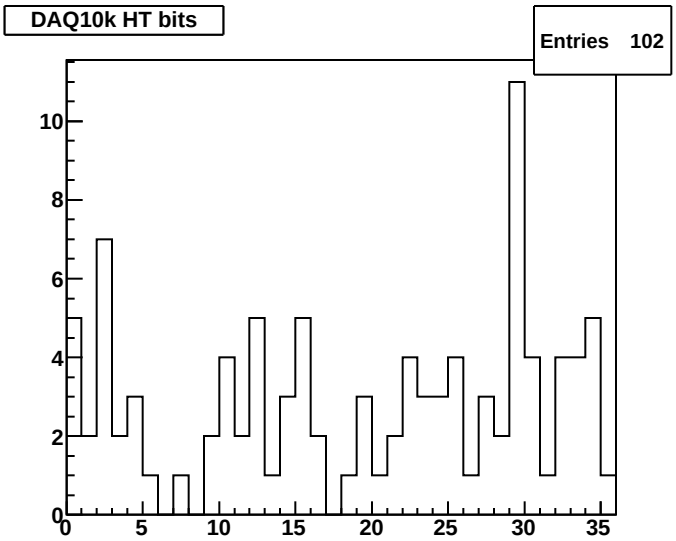

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

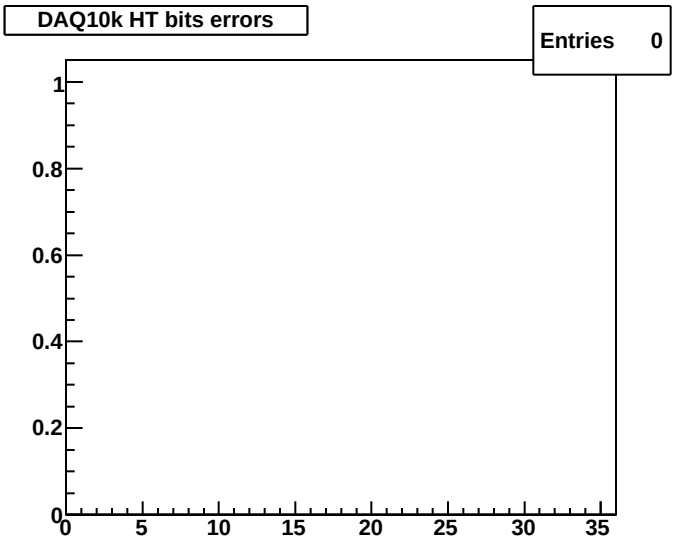

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2429712
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

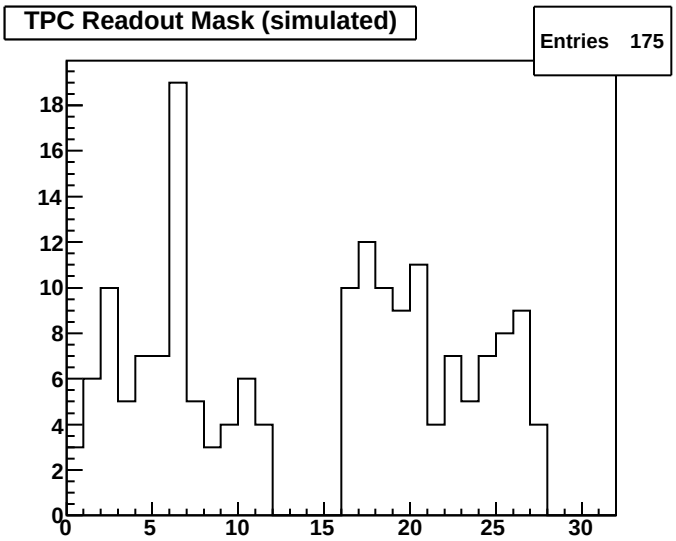

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

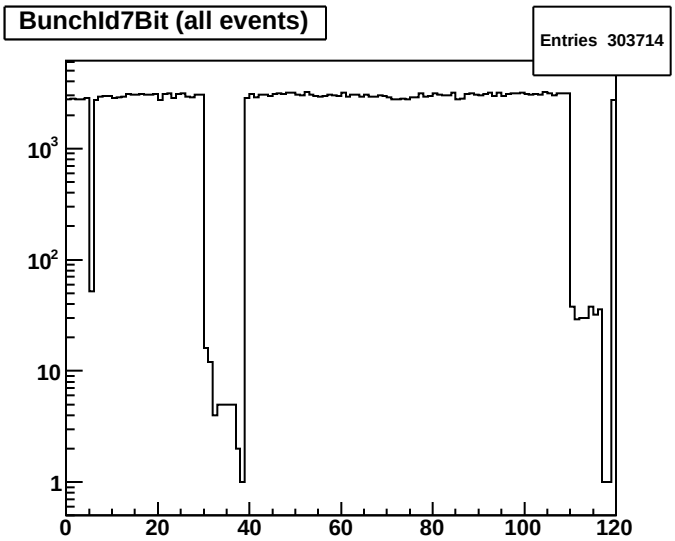

EMC L2 Input - HT23 bits
Entries 2429712
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

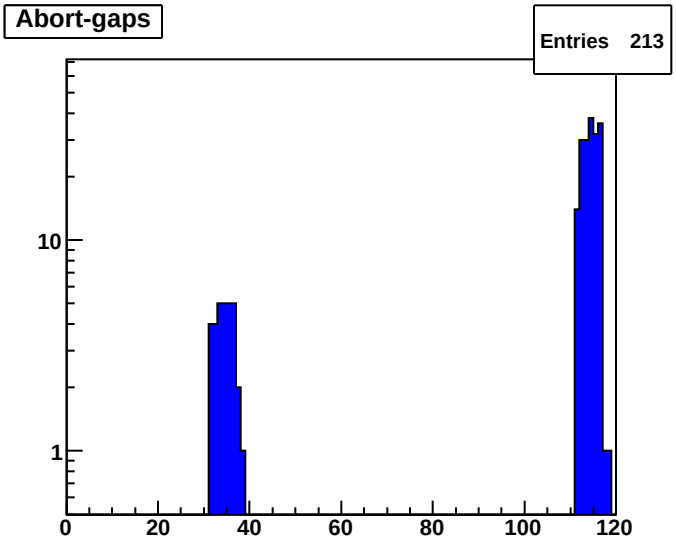

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

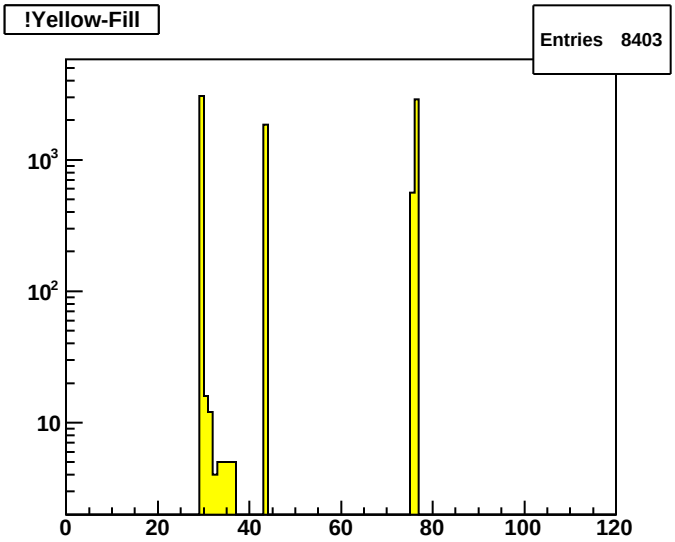

DAQ10k HT bits
Entries 102

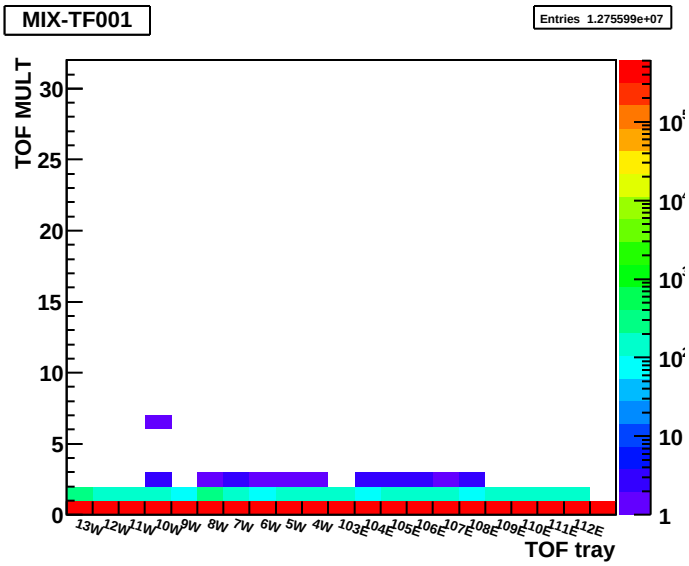

DAQ10k HT bits errors
Entries 0

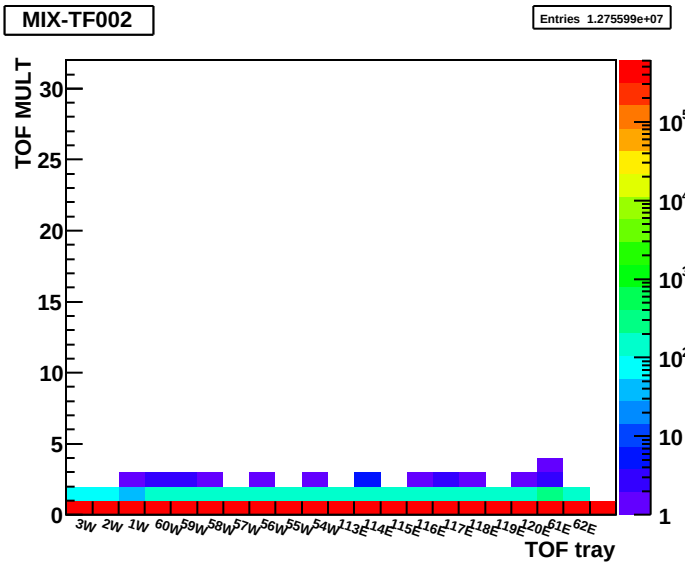

TPC Readout Mask (simulated)
Entries 175

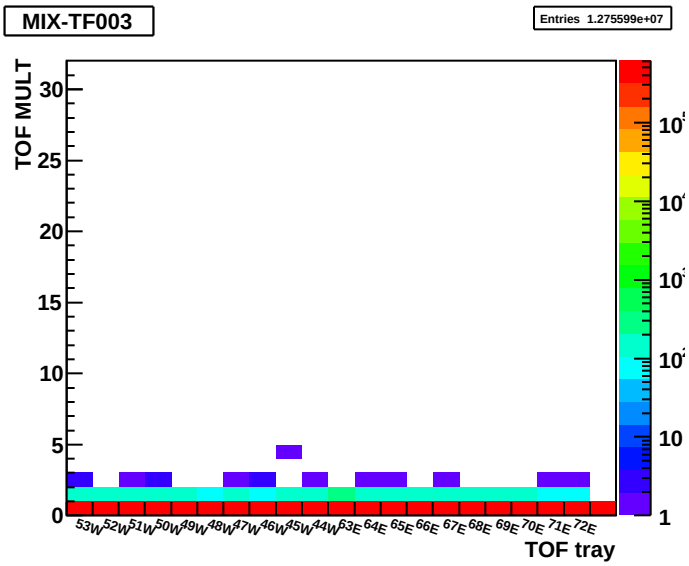

BunchId7Bit (all events)
Entries 303714

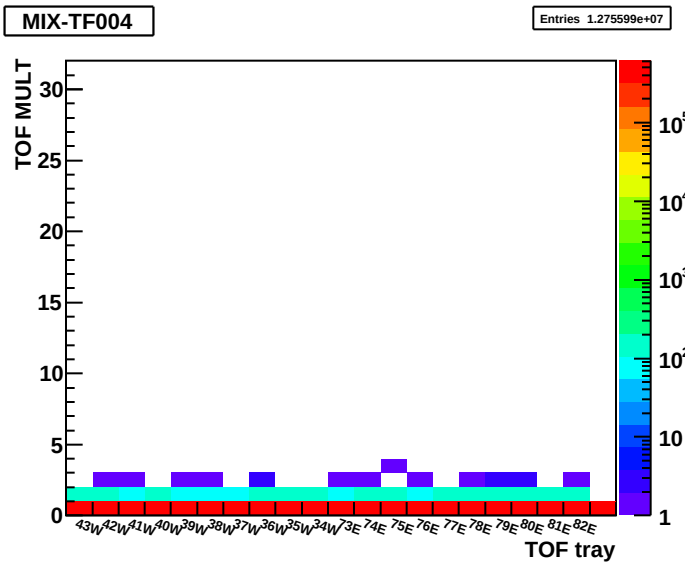

Abort-gaps
Entries 213

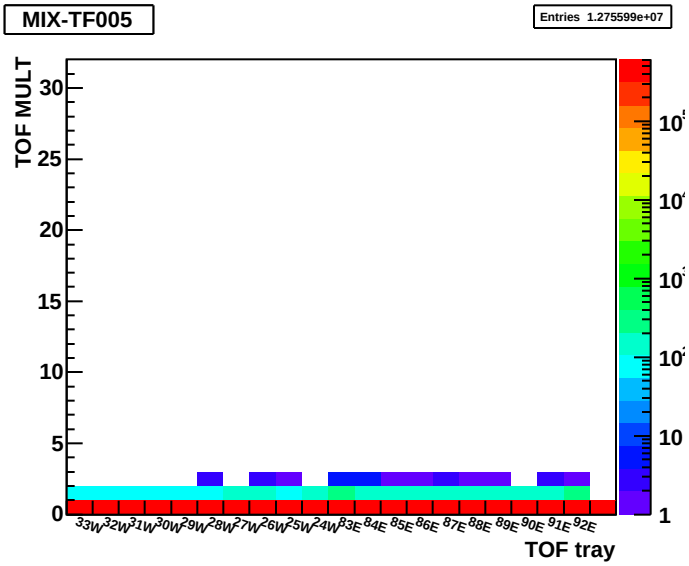

!Yellow-Fill
Entries 8403

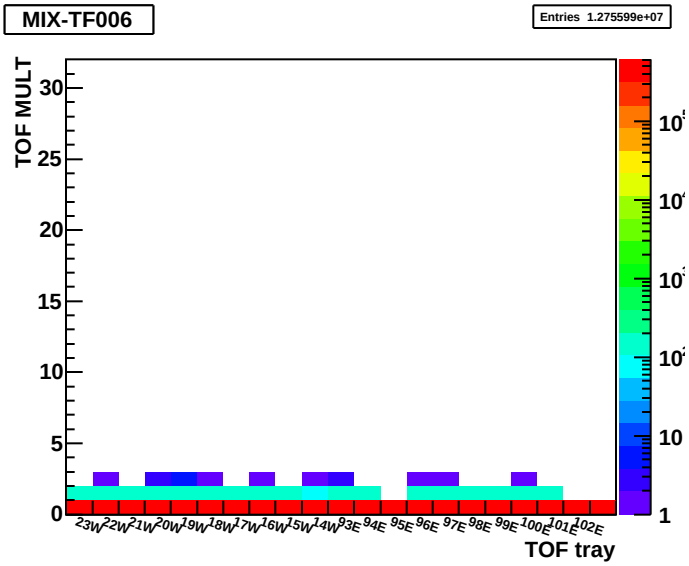

MIX-TF001
Entries 1.275599e+07
TOF MULT
TOF tray

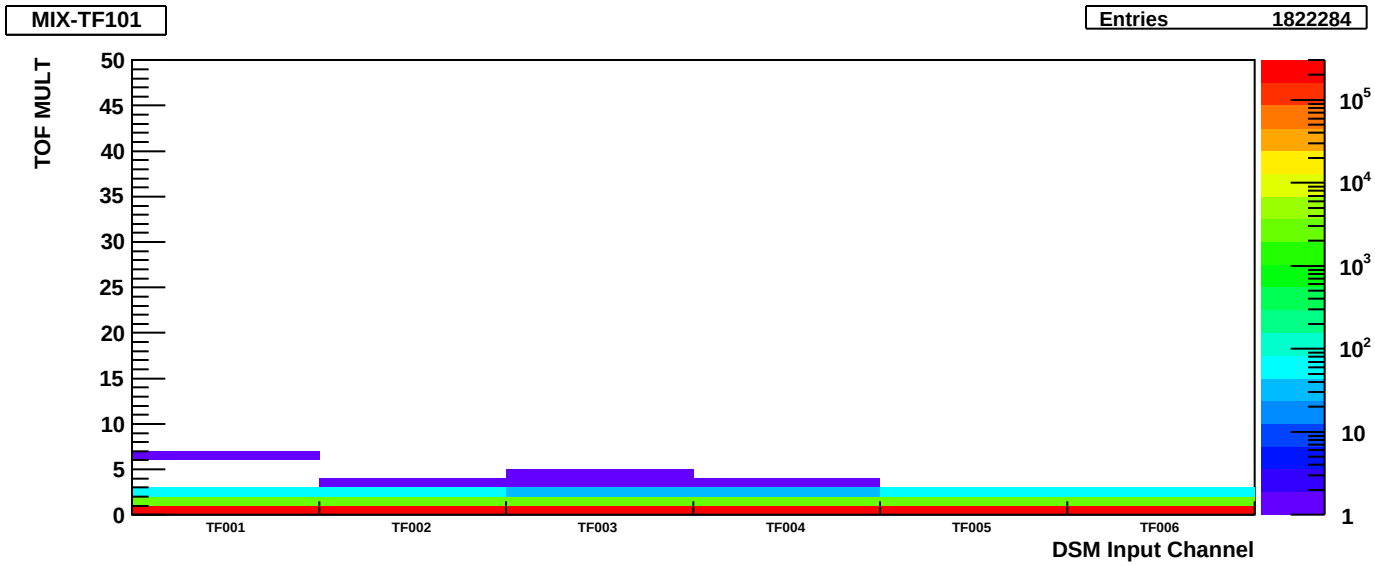

MIX-TF002
Entries 1.275599e+07
TOF MULT
TOF tray

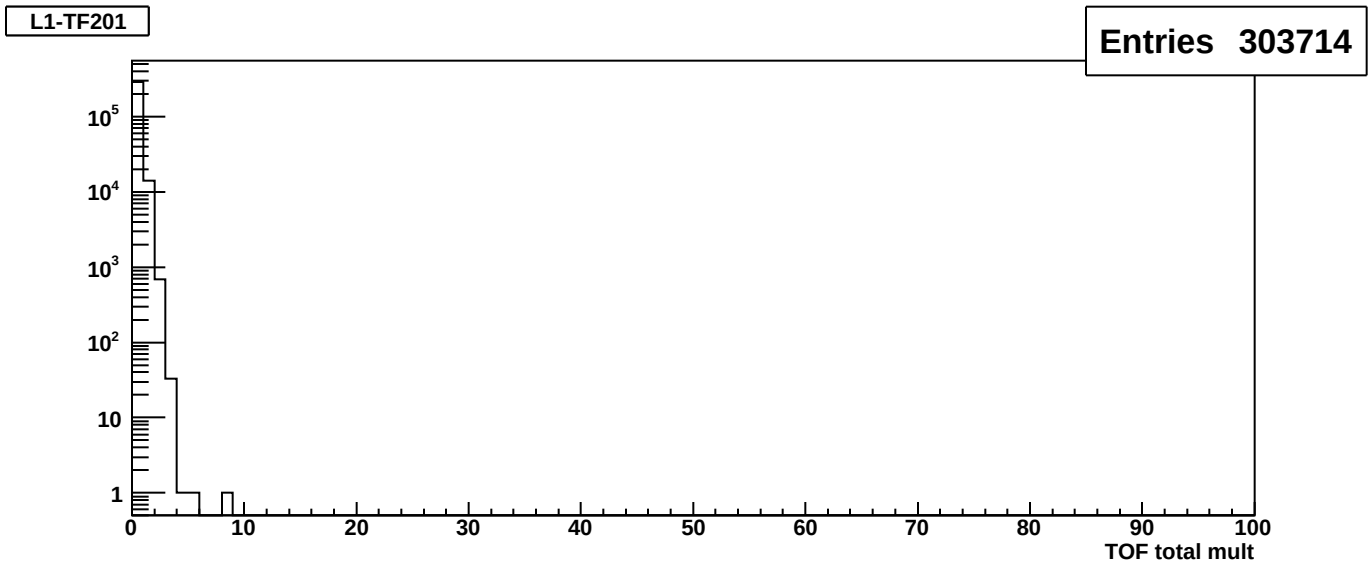

MIX-TF003
Entries 1.275599e+07
TOF MULT
TOF tray

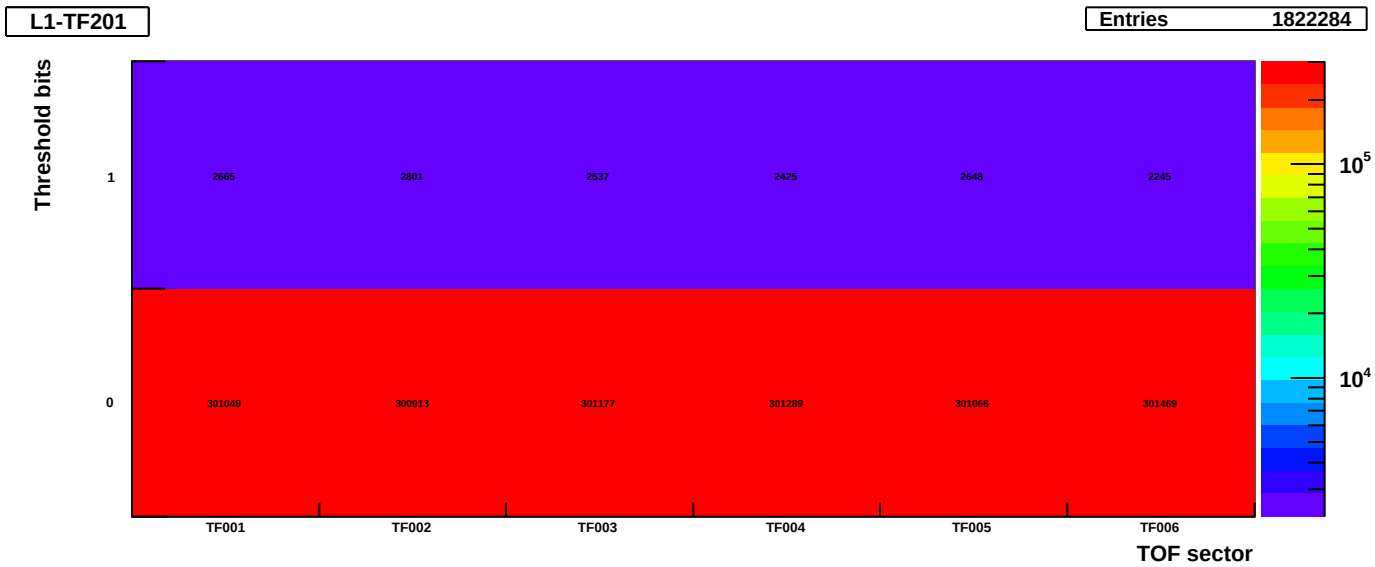

MIX-TF004
Entries 1.275599e+07
TOF MULT
TOF tray

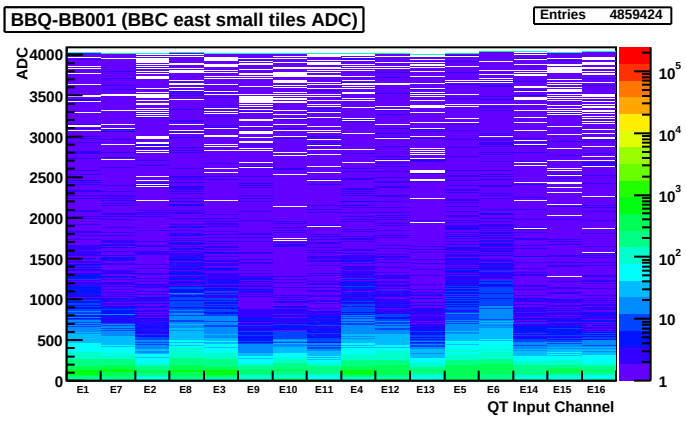

MIX-TF005
Entries 1.275599e+07
TOF MULT
TOF tray

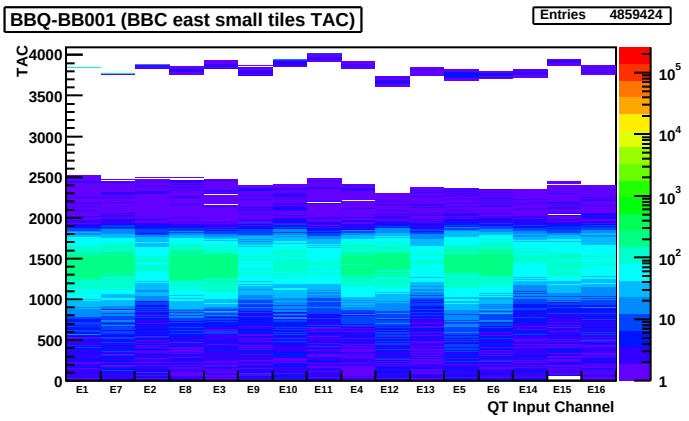

MIX-TF006
Entries 1.275599e+07
TOF MULT
TOF tray

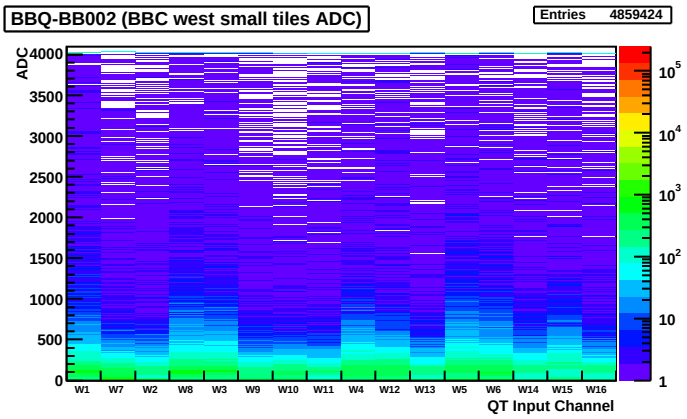

MIX-TF101
Entries 1822284
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

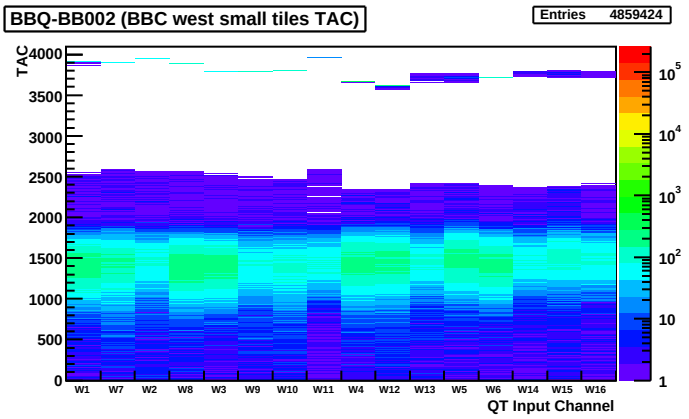

L1-TF201
Entries 303714
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

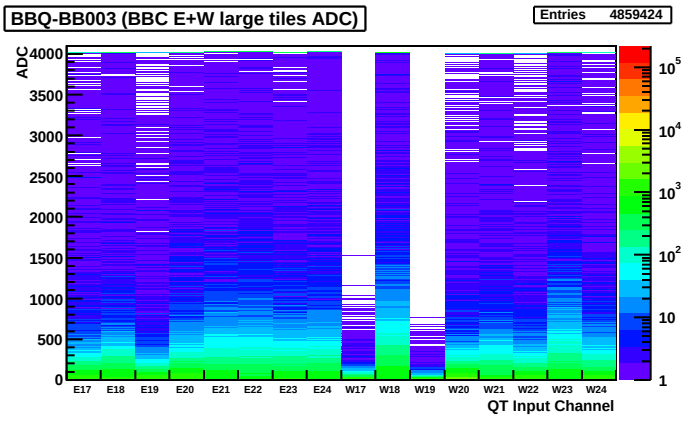

L1-TF201
Entries 1822284
Threshold bits
1
0
2665
2801
2537
2425
2648
2245
301049
300913
301177
301289
301066
301469
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4

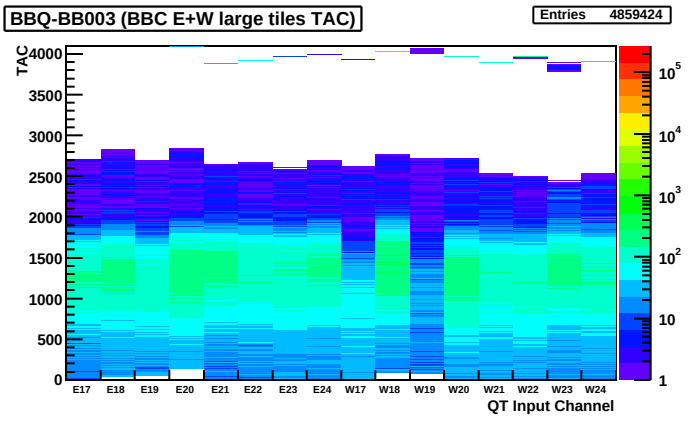

BBQ-BB001 (BBC east small tiles ADC)
Entries 4859424
ADC
QT Input Channel

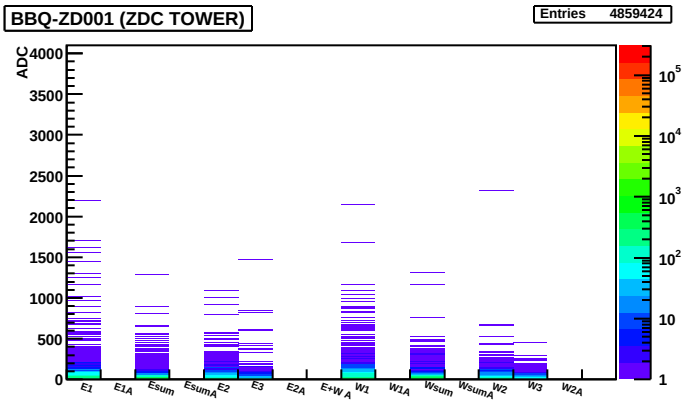

BBQ-BB001 (BBC east small tiles TAC)
Entries 4859424
TAC
QT Input Channel

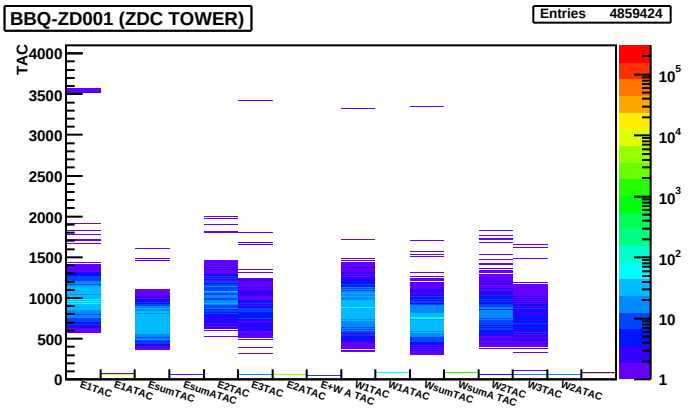

BBQ-BB002 (BBC west small tiles ADC)
Entries 4859424
ADC
QT Input Channel

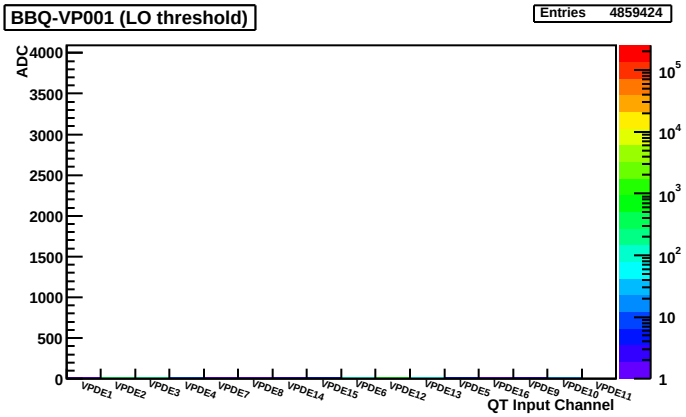

BBQ-BB002 (BBC west small tiles TAC)
Entries 4859424
TAC
QT Input Channel

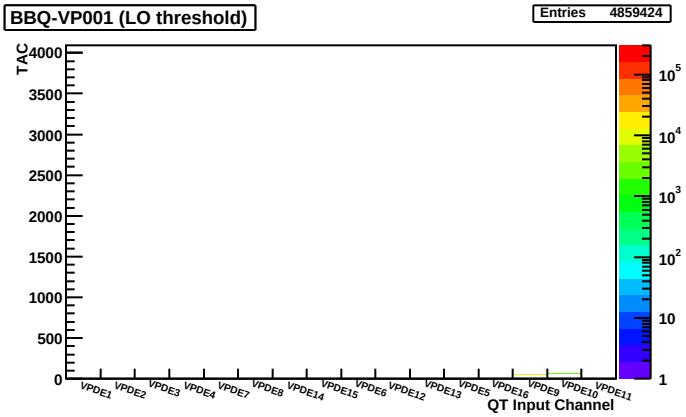

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4859424
ADC
QT Input Channel

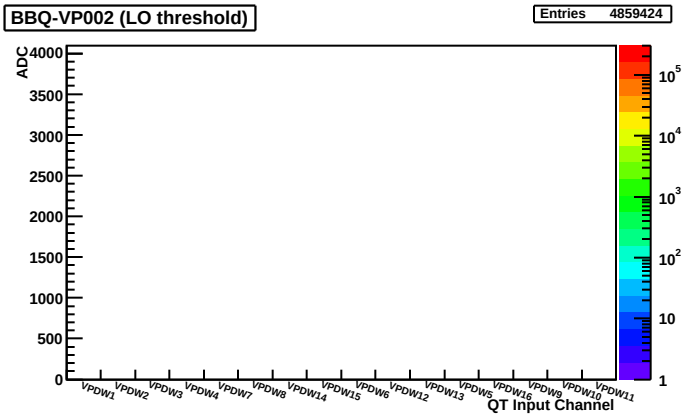

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4859424
TAC
QT Input Channel

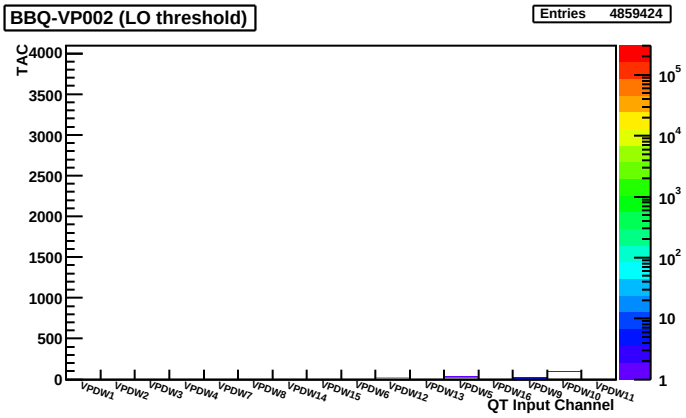

BBQ-ZD001 (ZDC TOWER)
Entries 4859424
ADC

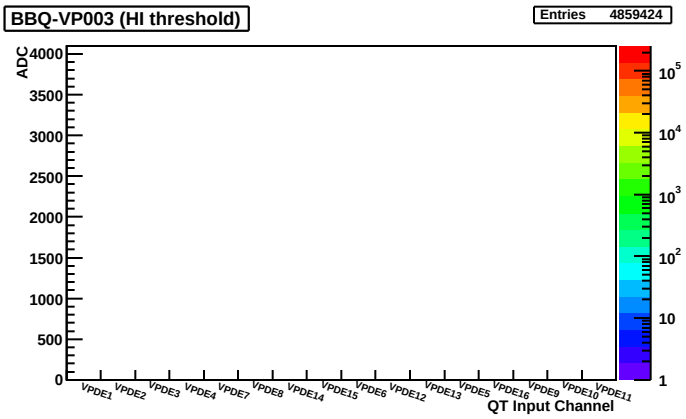

BBQ-ZD001 (ZDC TOWER)
Entries 4859424
TAC

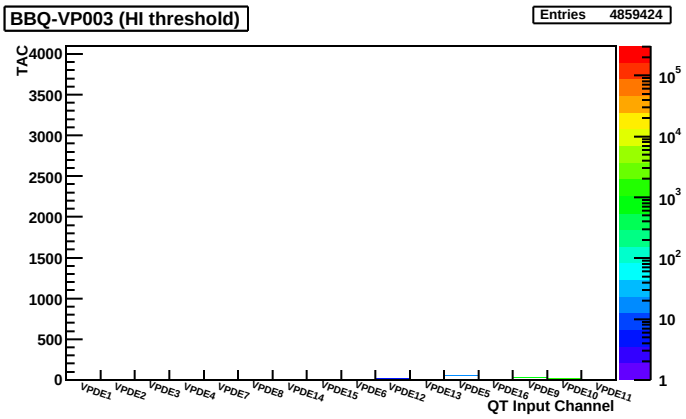

BBQ-VP001 (LO threshold)
Entries 4859424
ADC
QT Input Channel

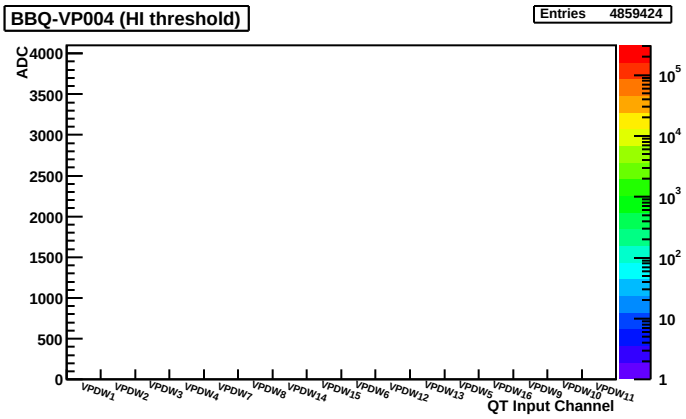

BBQ-VP001 (LO threshold)
Entries 4859424
TAC
QT Input Channel

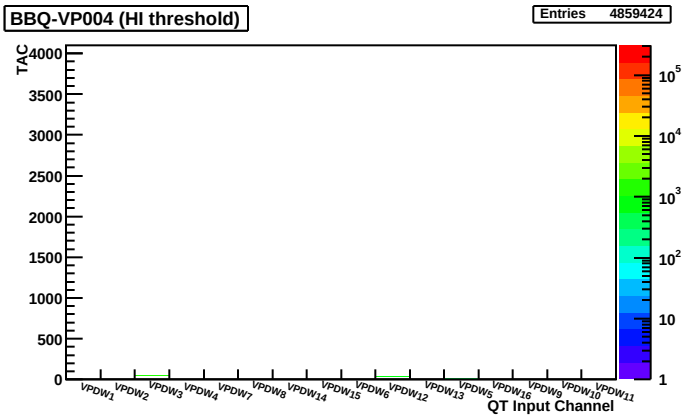

BBQ-VP002 (LO threshold)
Entries 4859424
ADC
QT Input Channel

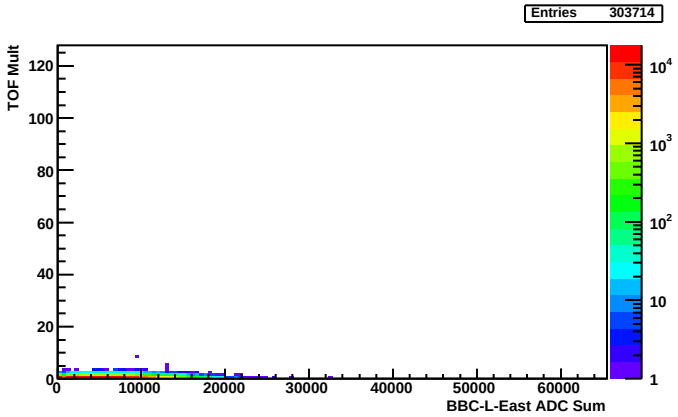

BBQ-VP002 (LO threshold)
Entries 4859424
TAC
QT Input Channel

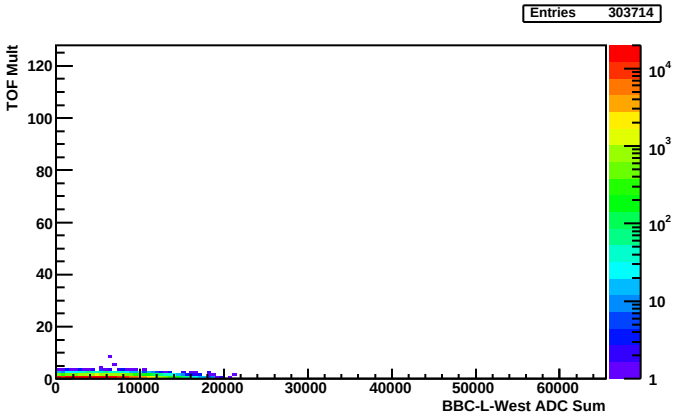

BBQ-VP003 (HI threshold)
Entries 4859424
ADC
QT Input Channel

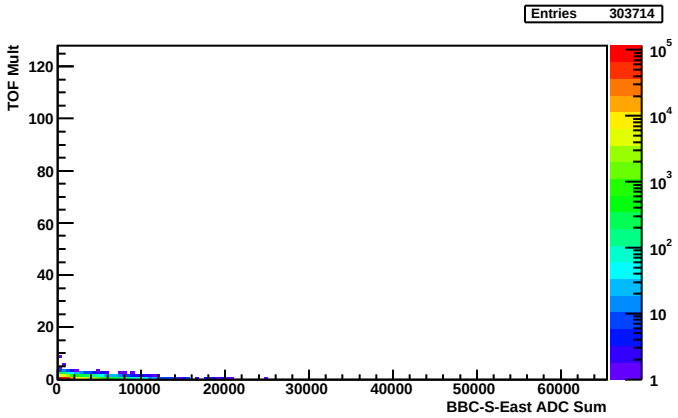

BBQ-VP003 (HI threshold)
Entries 4859424
TAC
QT Input Channel

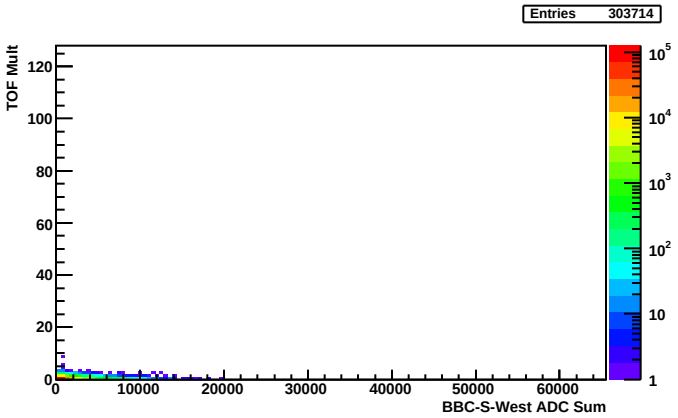

BBQ-VP004 (HI threshold)
Entries 4859424
ADC
QT Input Channel

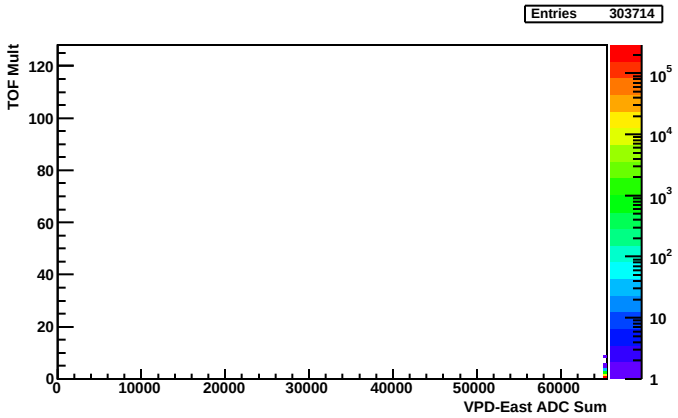

BBQ-VP004 (HI threshold)
Entries 4859424
TAC
QT Input Channel

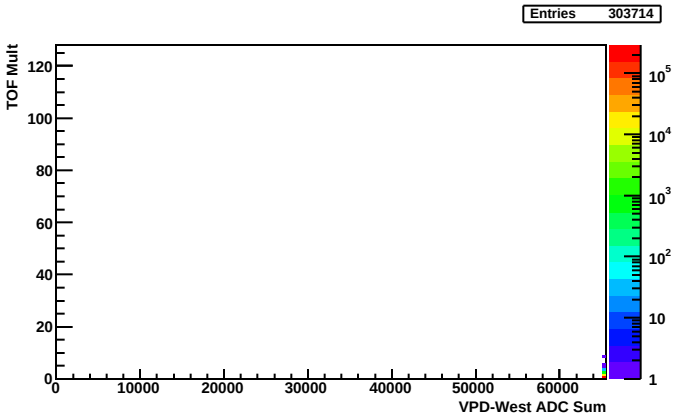

Entries 303714
TOF Mult
BBC-L-East ADC Sum

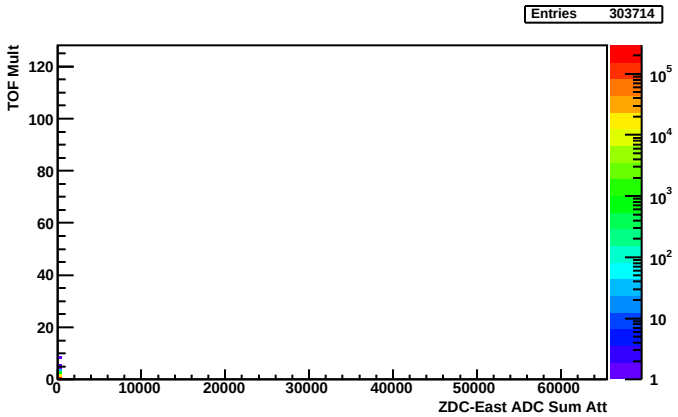

Entries 303714
TOF Mult
BBC-L-West ADC Sum

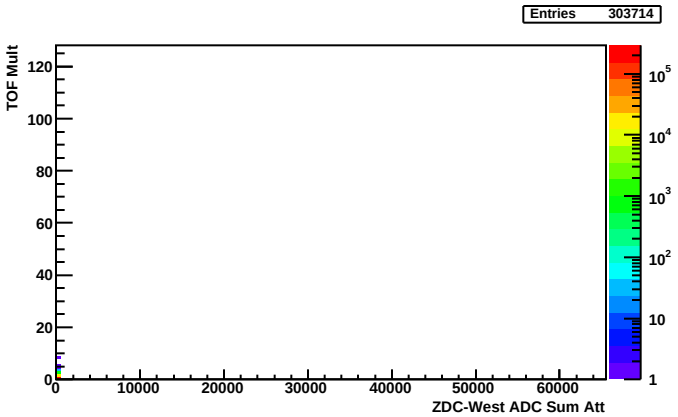

Entries 303714
TOF Mult
BBC-S-East ADC Sum

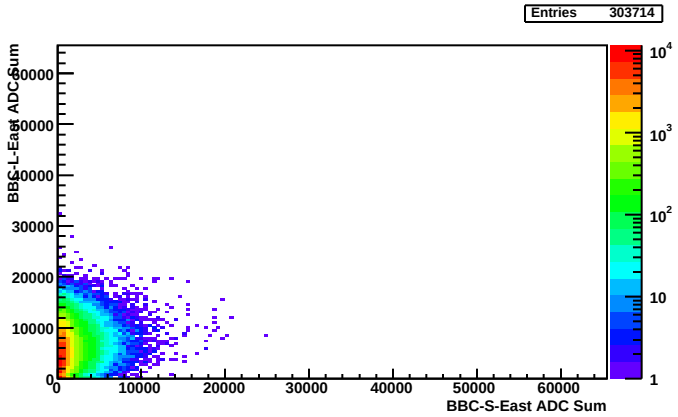

Entries 303714
TOF Mult
BBC-S-West ADC Sum

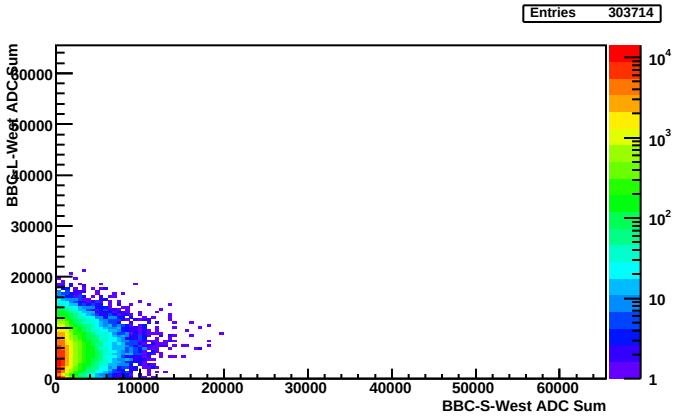

Entries 303714
TOF Mult
VPD-East ADC Sum

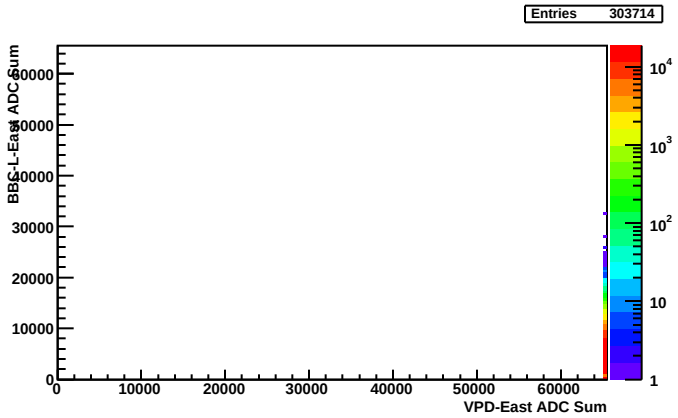

Entries 303714
TOF Mult
VPD-West ADC Sum

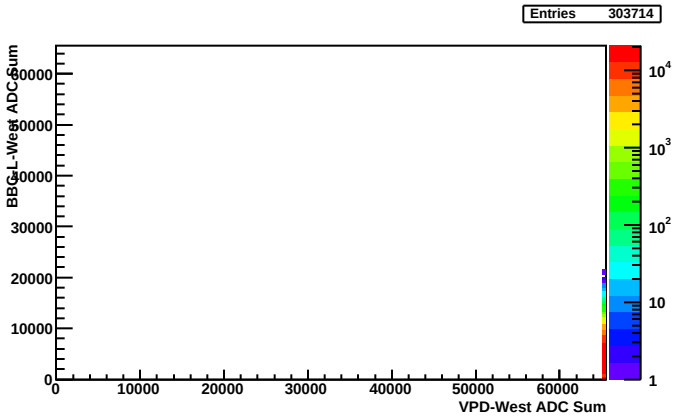

Entries 303714
TOF Mult
ZDC-East ADC Sum Att

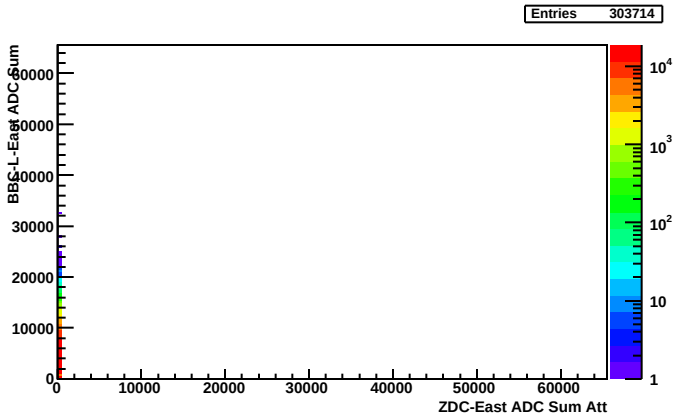

Entries 303714
TOF Mult
ZDC-West ADC Sum Att


Entries 303714
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 303714
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 303714
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 303714
BBC-L-West ADC Sum
VPD-West ADC Sum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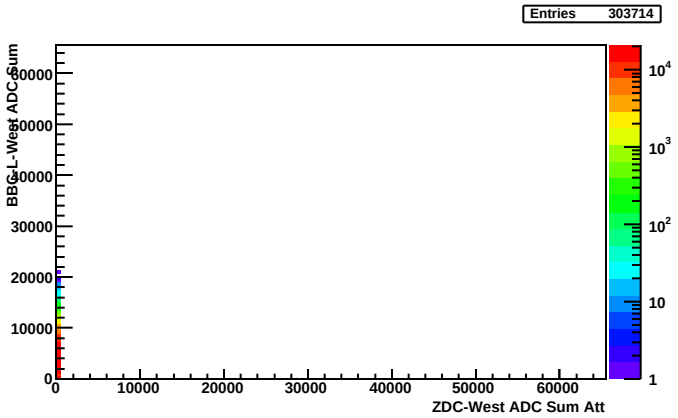
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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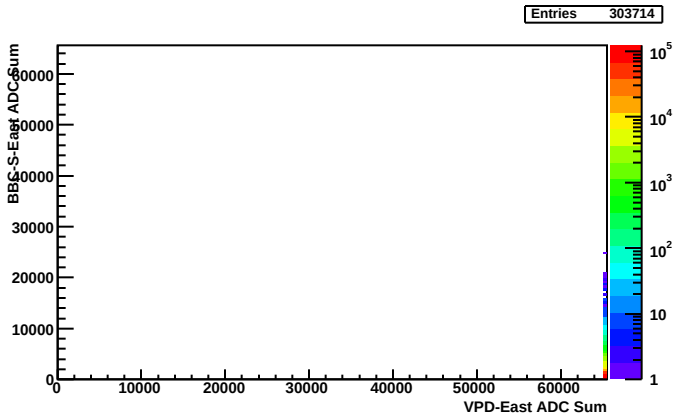
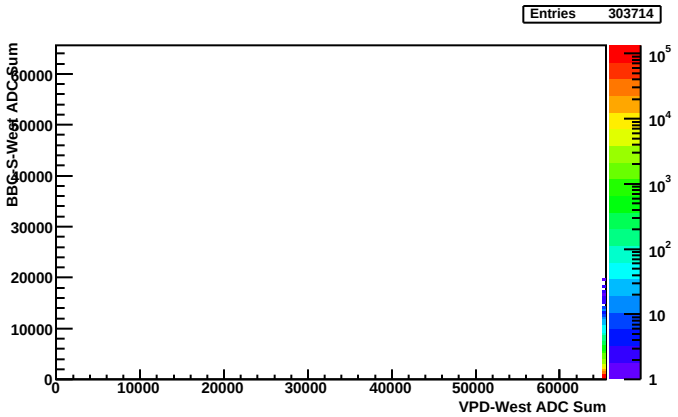
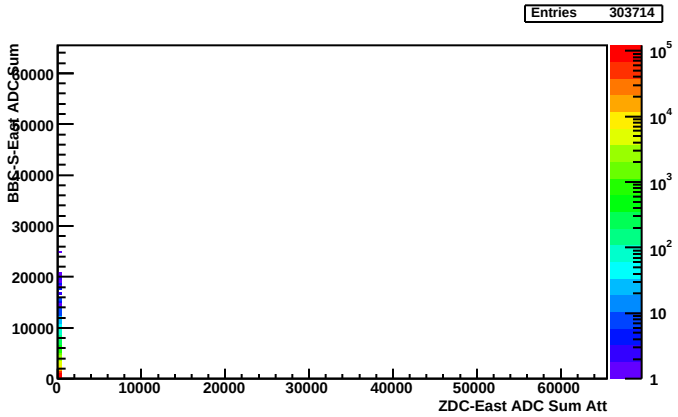
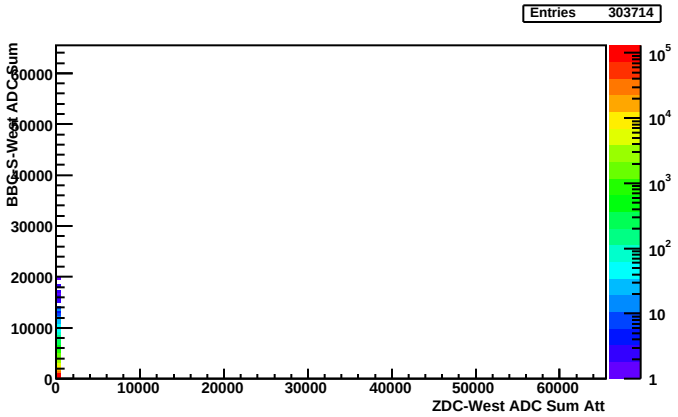
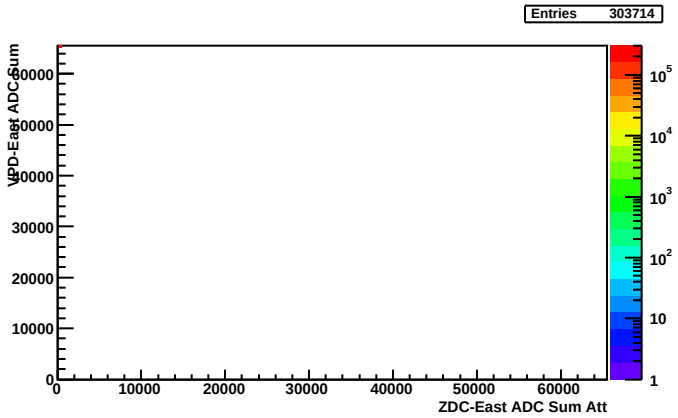
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

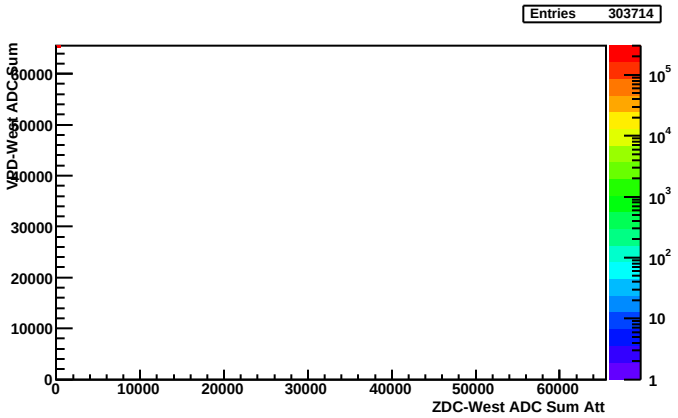

Entries 303714
BBC-S-East ADC Sum
VPD-East ADC Sum

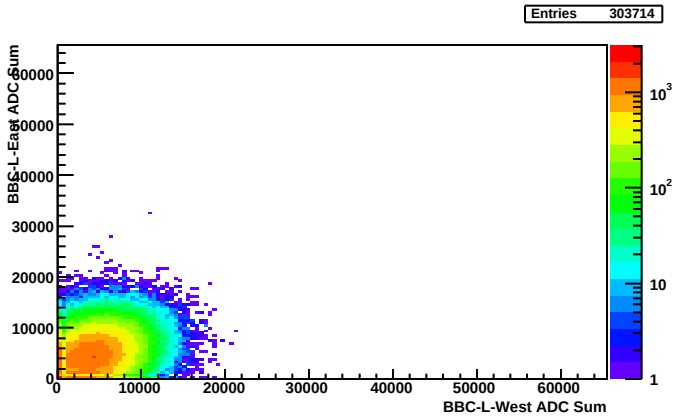

Entries 303714
BBC-S-West ADC Sum
VPD-West ADC Sum

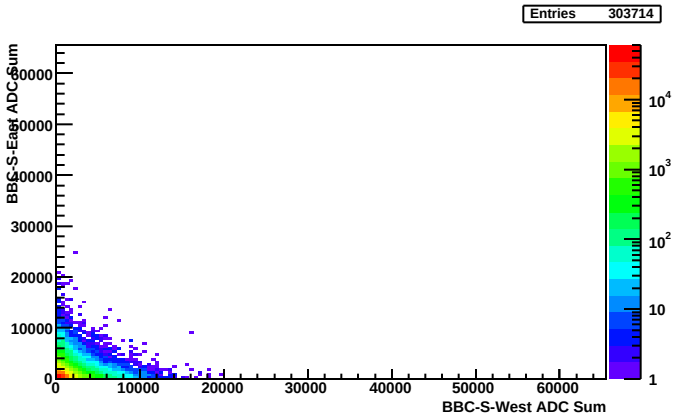

Entries 303714
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

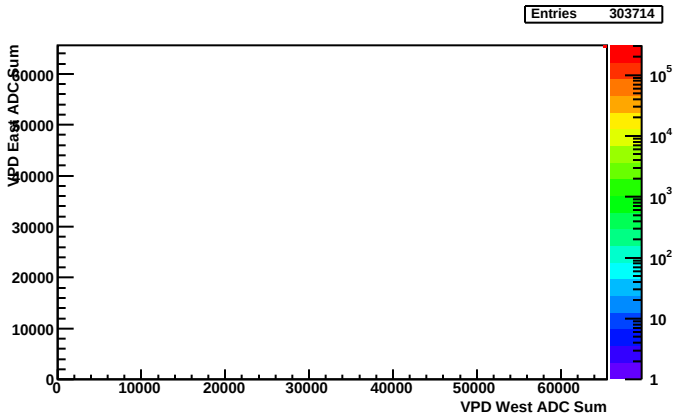

Entries 303714
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

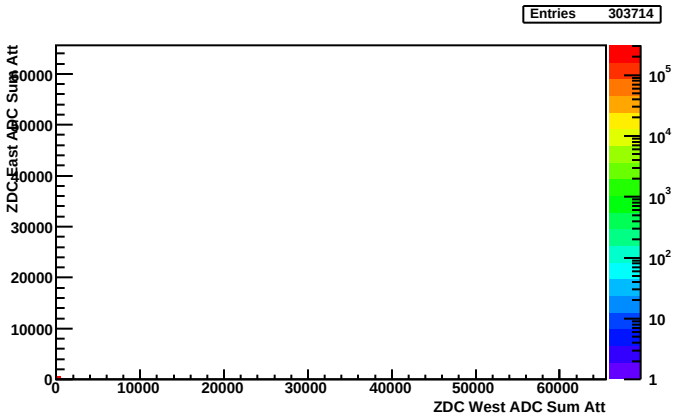

Entries 303714
VPD-East ADC Sum
ZDC-East ADC Sum Att

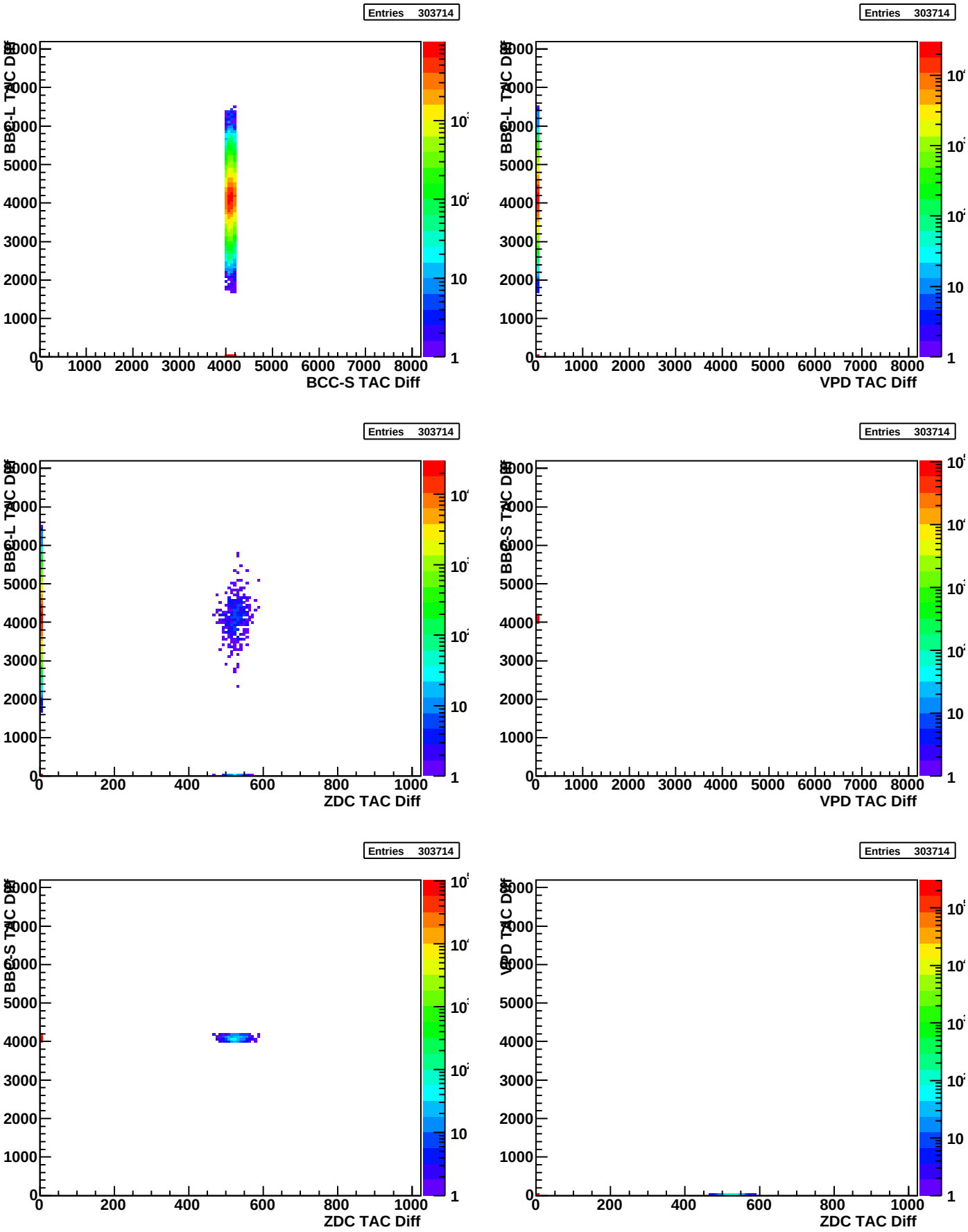

Entries 303714
VPD-West ADC Sum
ZDC-West ADC Sum Att

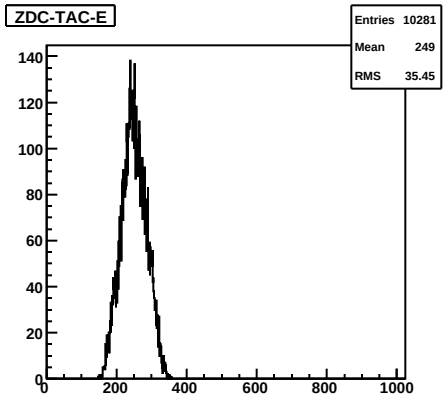

Entries 303714
BBC-L-East ADC Sum
BBC-L-West ADC Sum

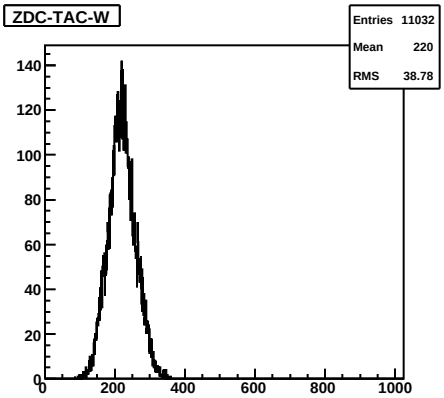

Entries 303714
BBC-S-East ADC Sum
BBC-S-West ADC Sum

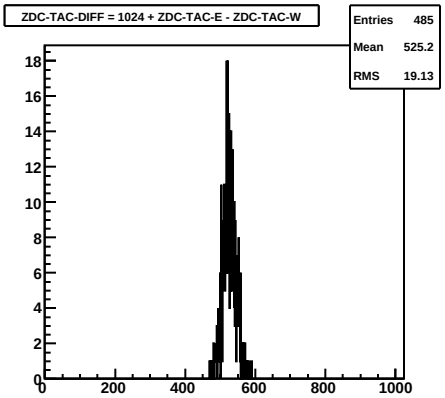

Entries 303714
VPD East ADC Sum
VPD West ADC Sum

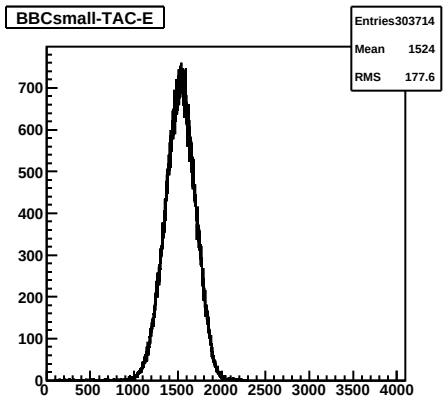

Entries 303714
ZDC East ADC Sum Att
ZDC West ADC Sum Att

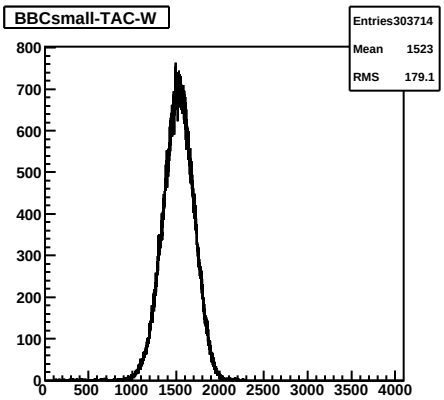

Entries 303714
BBC-L TAC Diff
BCC-S TAC Diff
Entries 303714
BBC-L TAC Diff
VPD TAC Diff
Entries 303714
BBC-L TAC Diff
ZDC TAC Diff
Entries 303714
BBC-S TAC Diff
VPD TAC Diff
Entries 303714
BBC-S TAC Diff
ZDC TAC Diff
Entries 303714
VPD TAC Diff
ZDC TAC Diff

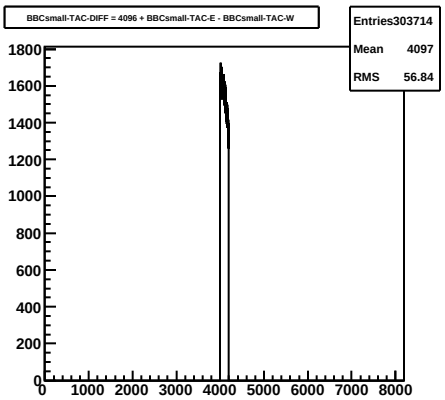

ZDC-TAC-E
Entries 10281
Mean 249
RMS 35.45

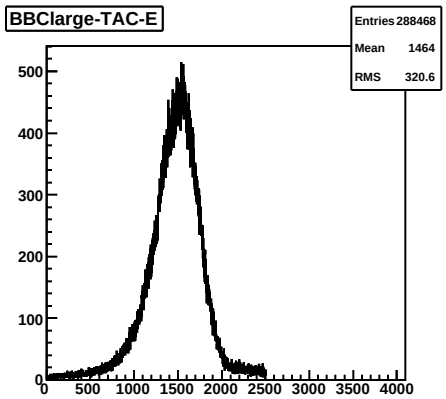

ZDC-TAC-W
Entries 11032
Mean 220
RMS 38.78

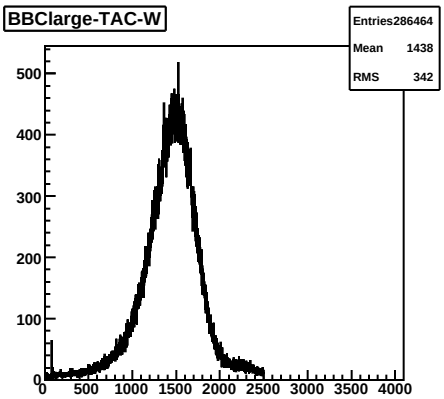

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 485
Mean 525.2
RMS 19.13

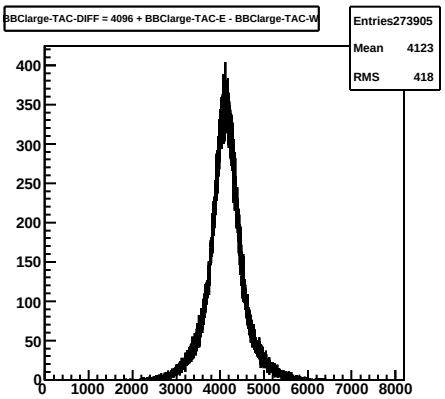

BBCsmall-TAC-E
Entries303714
Mean 1524
RMS 177.6

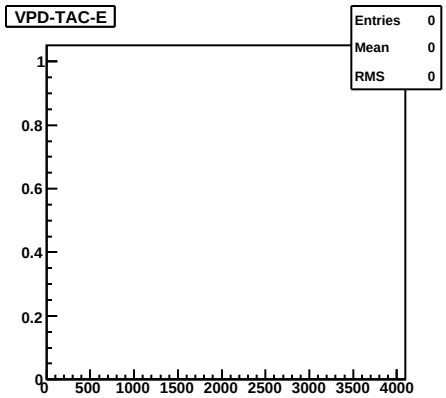

BBCsmall-TAC-W
Entries303714
Mean 1523
RMS 179.1

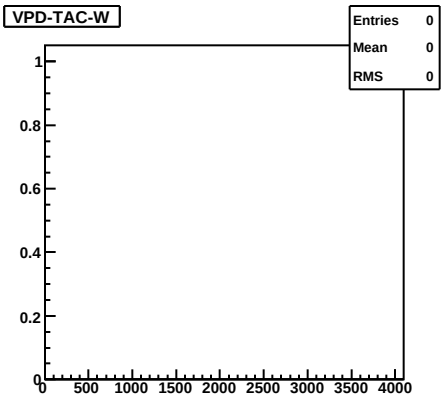

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries303714
Mean 4097
RMS 56.84

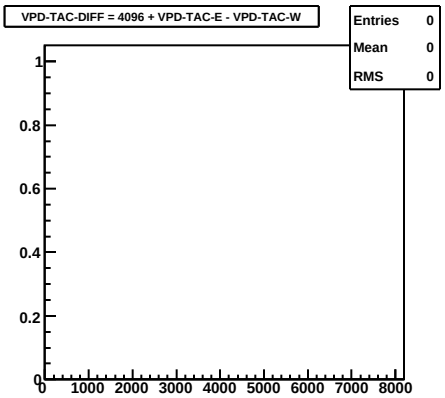

BBClarge-TAC-E
Entries288468
Mean 1464
RMS 320.6

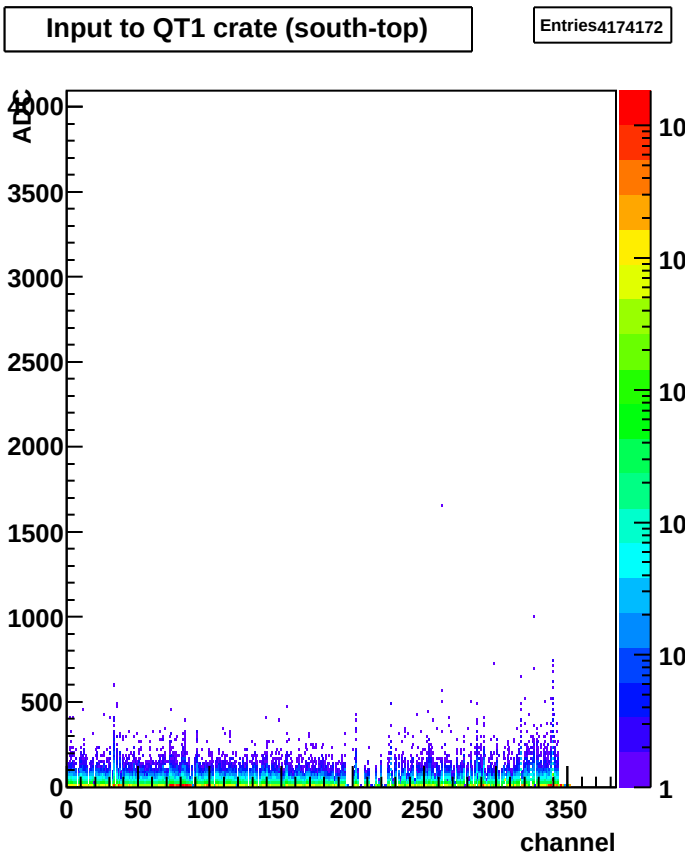

BBClarge-TAC-W
Entries286464
Mean 1438
RMS 342

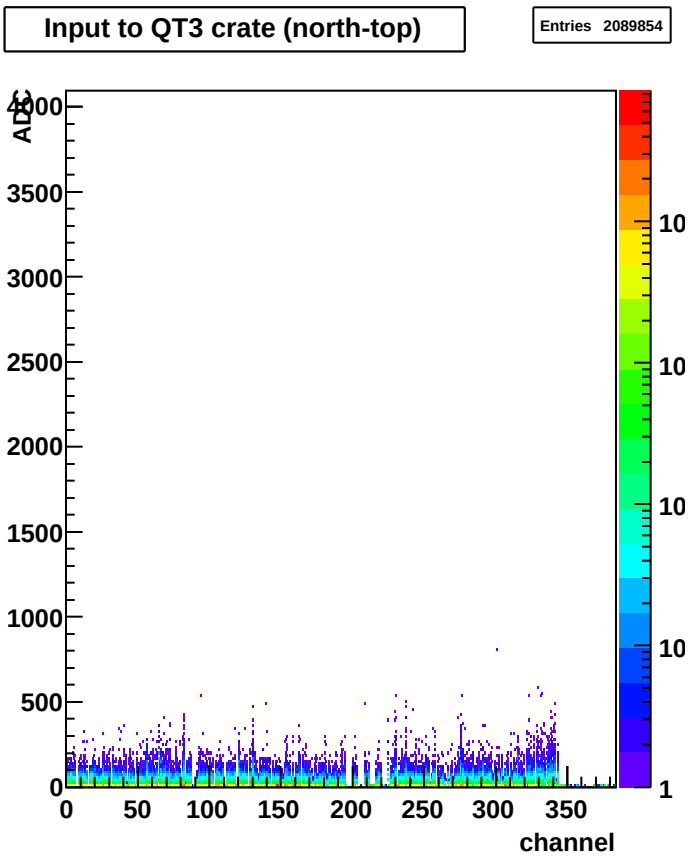

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries273905
Mean 4123
RMS 418

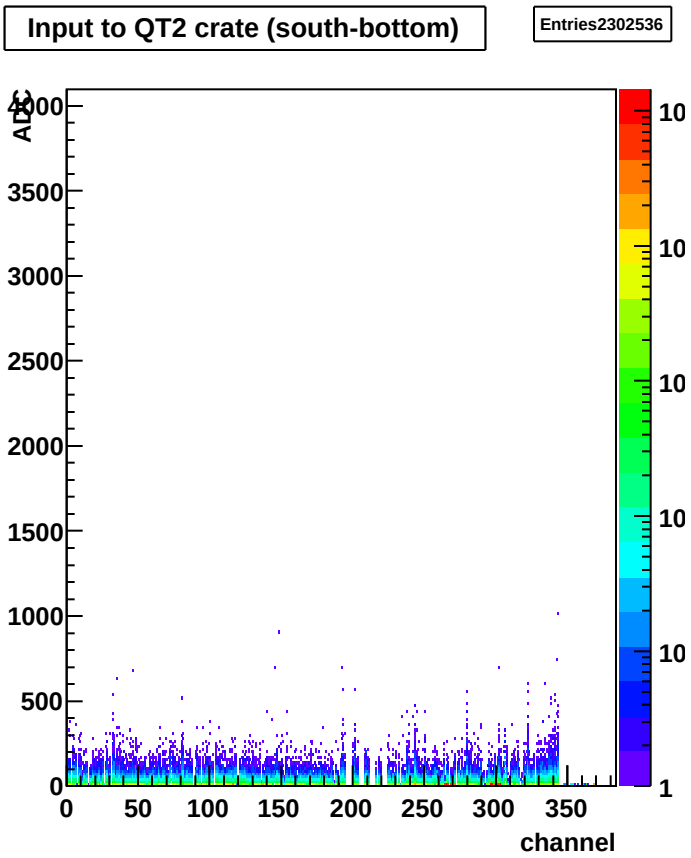

VPD-TAC-E
Entries 0
Mean 0
RMS 0

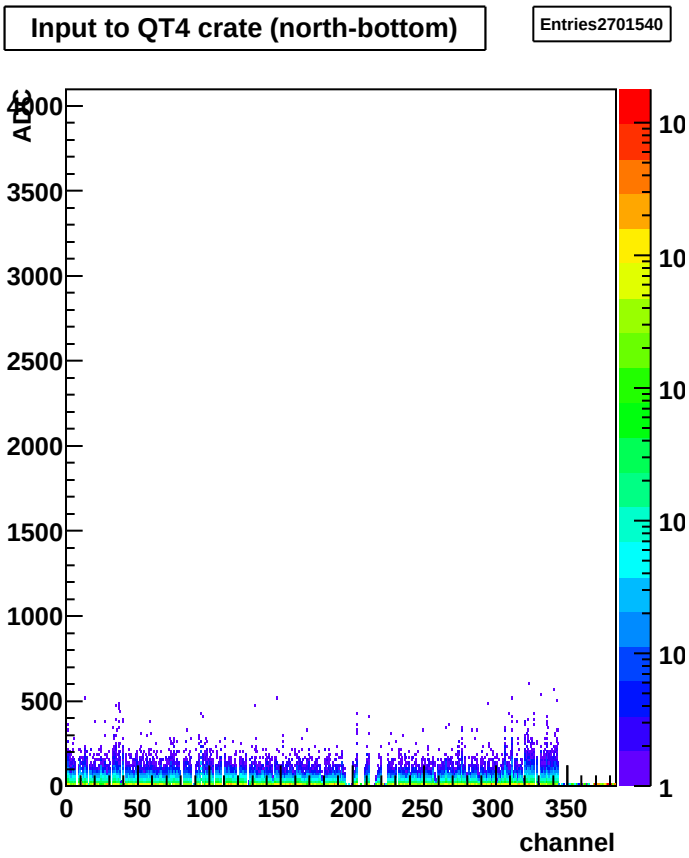

VPD-TAC-W
Entries 0
Mean 0
RMS 0

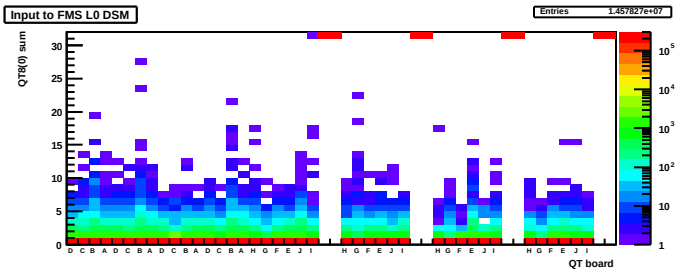

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 0
Mean 0
RMS 0

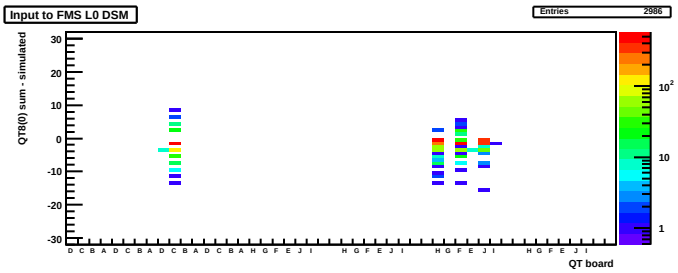

Input to QT1 crate (south-top)
Entries4174172
ADC
channel

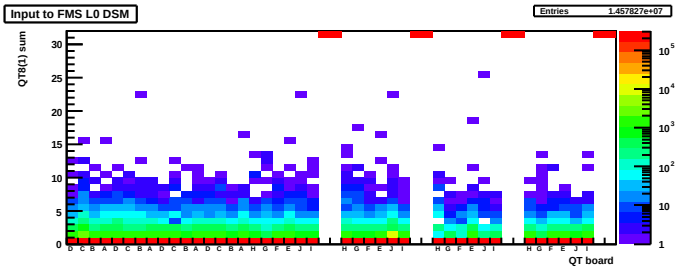

Input to QT3 crate (north-top)
Entries 2089854
ADC
channel

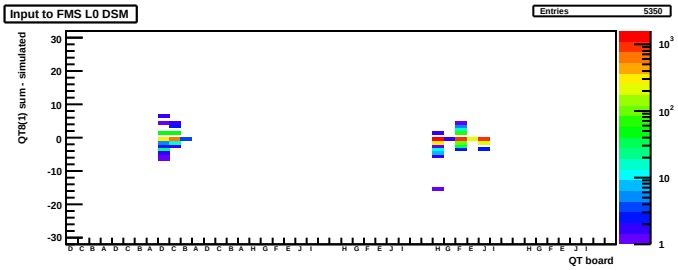

Input to QT2 crate (south-bottom)
Entries2302536
ADC
channel

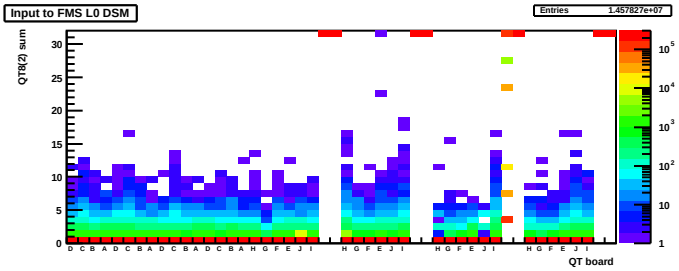

Input to QT4 crate (north-bottom)
Entries2701540
ADC
channel

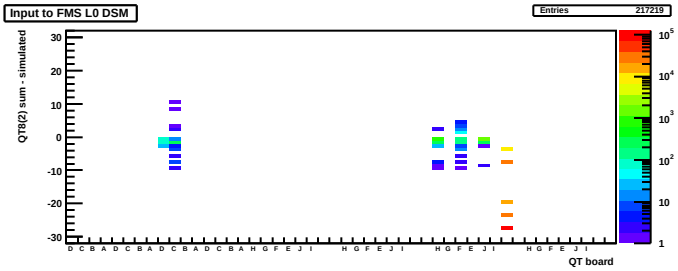

Input to FMS L0 DSM
Entries 1.457827e+07
QT8(0) sum
QT board

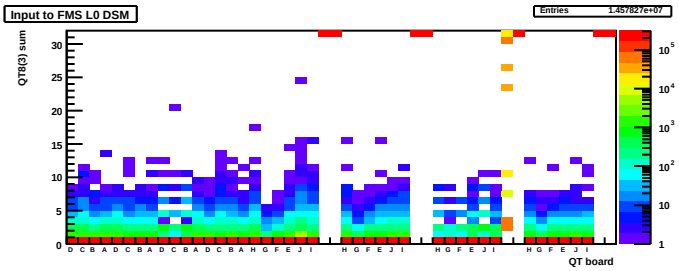

Input to FMS L0 DSM
Entries 2986
QT8(0) sum - simulated
QT board

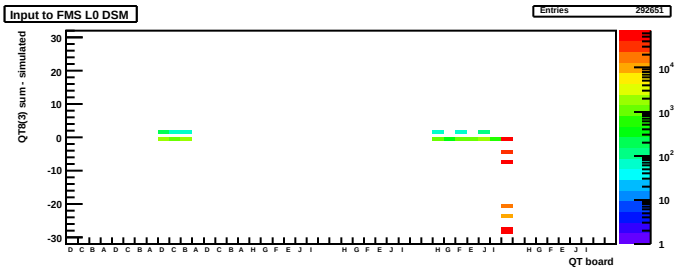

Input to FMS L0 DSM
Entries 1.457827e+07
QT8(1) sum
QT board

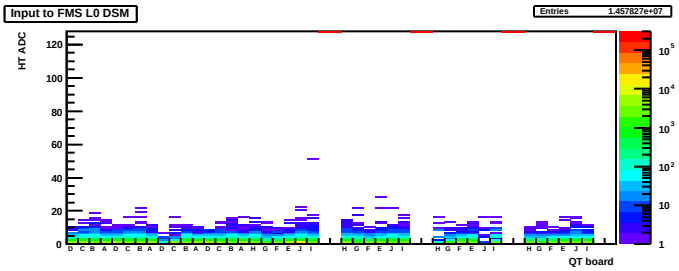

Input to FMS L0 DSM
Entries 5350
QT8(1) sum - simulated
QT board

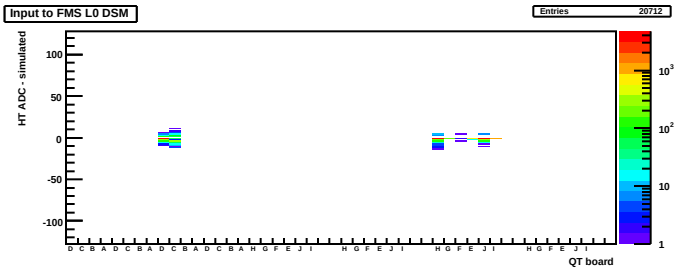

Input to FMS L0 DSM
Entries 1.457827e+07
QT8(2) sum
QT board

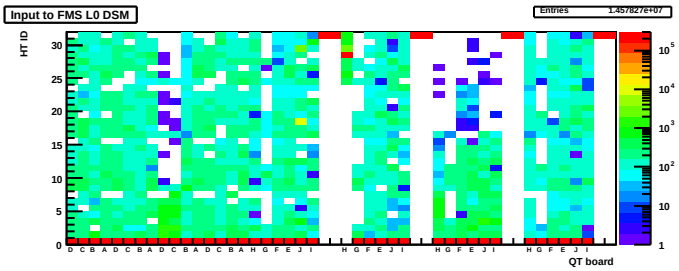

Input to FMS L0 DSM
Entries 217219
QT8(2) sum - simulated
QT board

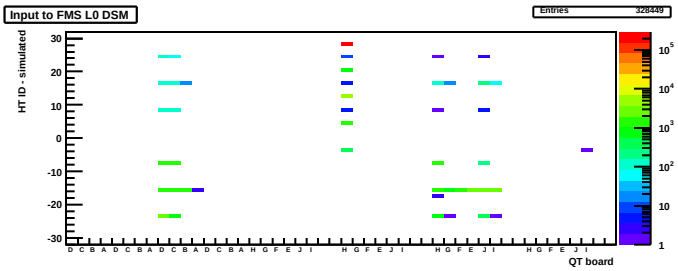

Input to FMS L0 DSM
Entries 1.457827e+07
QT8(3) sum
QT board

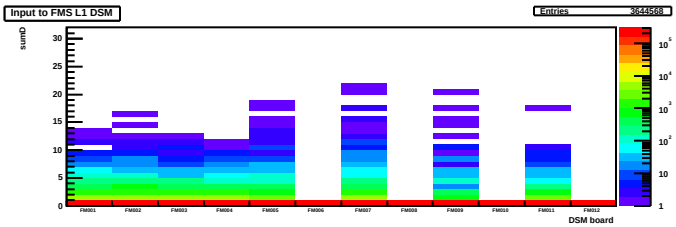

Input to FMS L0 DSM
Entries 292651
QT8(3) sum - simulated
QT board

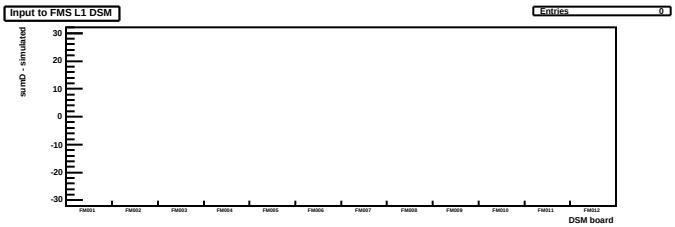

Input to FMS L0 DSM
Entries 1.457827e+07
HT ADC
QT board

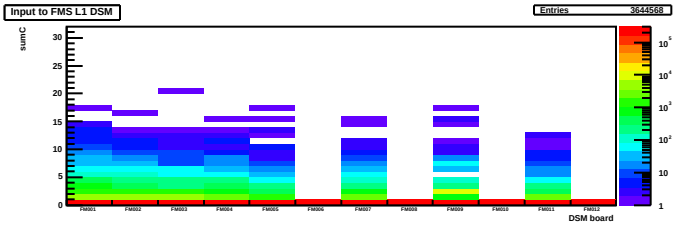

Input to FMS L0 DSM
Entries 20712
HT ADC - simulated
QT board

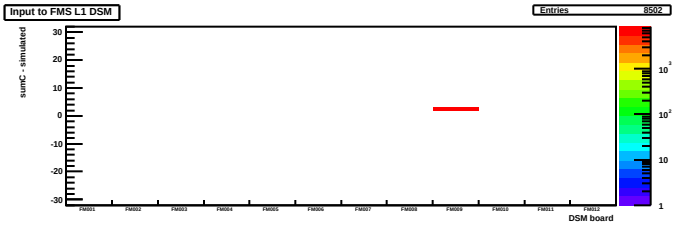

Input to FMS L0 DSM
Entries 1.457827e+07
HT ID
QT board

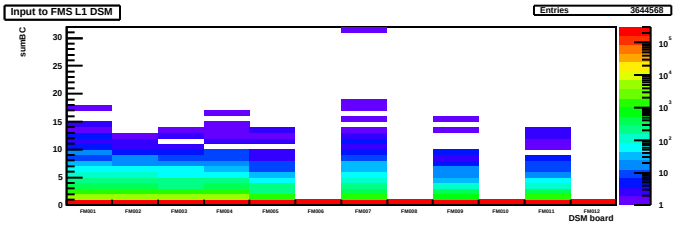

Input to FMS L0 DSM
Entries 328449
HT ID - simulated
QT board

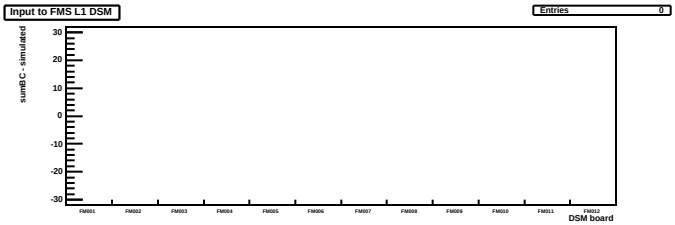

Input to FMS L1 DSM
Entries 3644568
sumD
DSM board

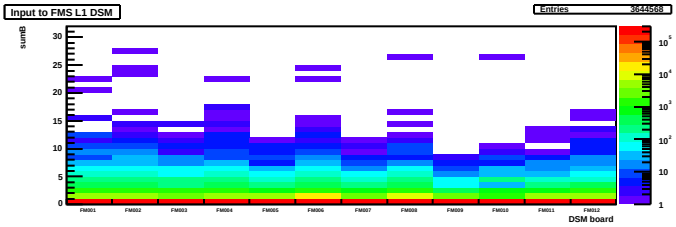

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

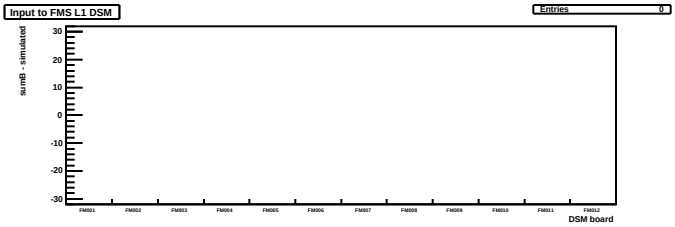

Input to FMS L1 DSM
Entries 3644568
sumC
DSM board

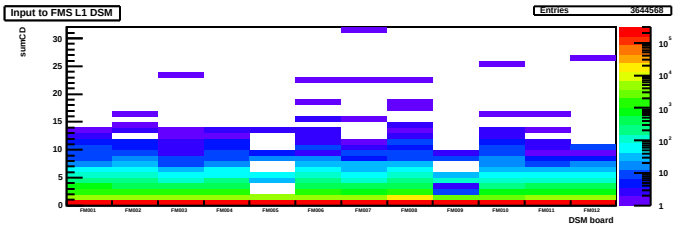

Input to FMS L1 DSM
Entries 8502
sumC - simulated
DSM board

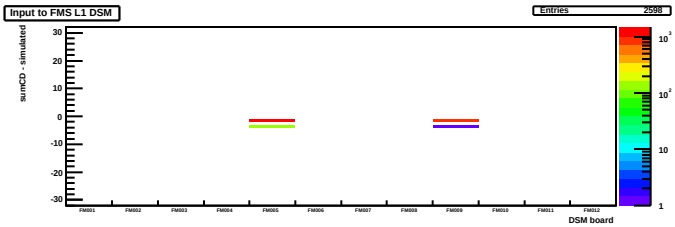

Input to FMS L1 DSM
Entries 3644568
sumBC
DSM board

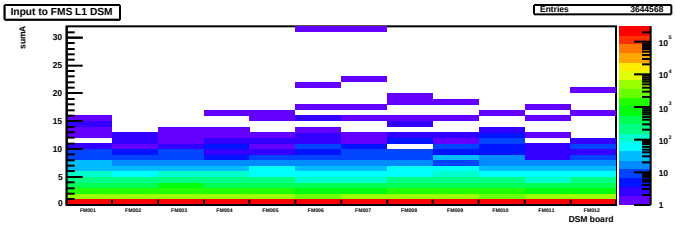

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

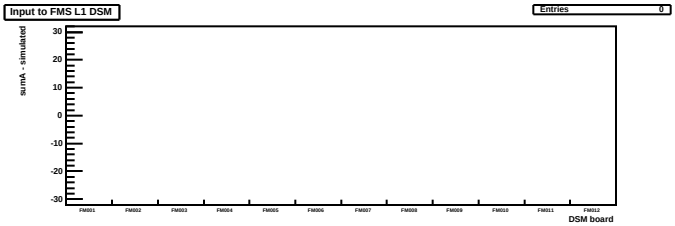

Input to FMS L1 DSM
Entries 3644568
sumB
DSM board

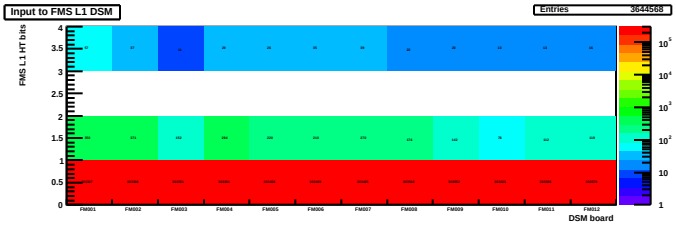

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

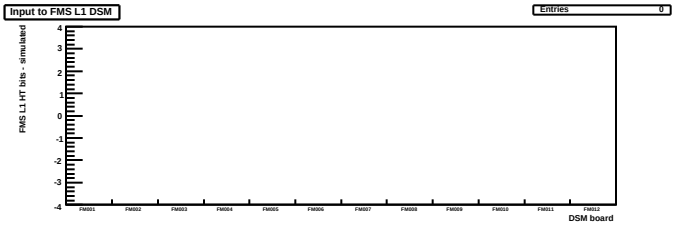

Input to FMS L1 DSM
Entries 3644568
sumCD
DSM board

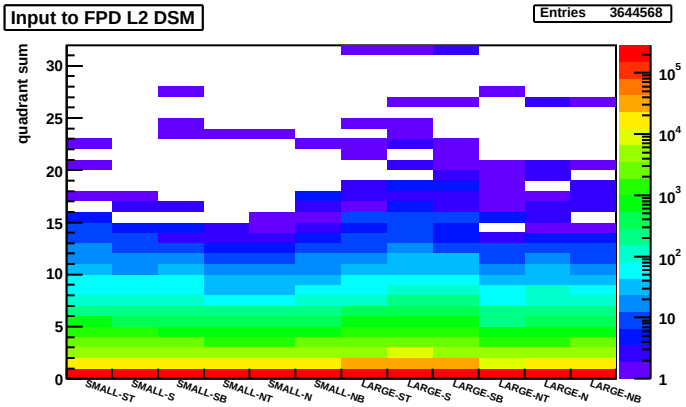

Input to FMS L1 DSM
Entries 2598
sumCD - simulated
DSM board

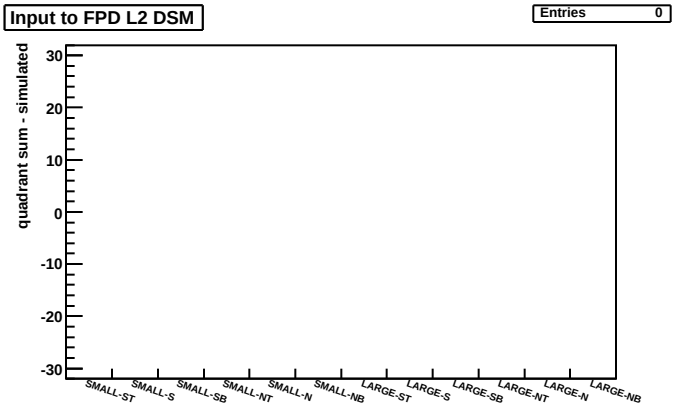

Input to FMS L1 DSM
Entries 3644568
sumA
DSM board

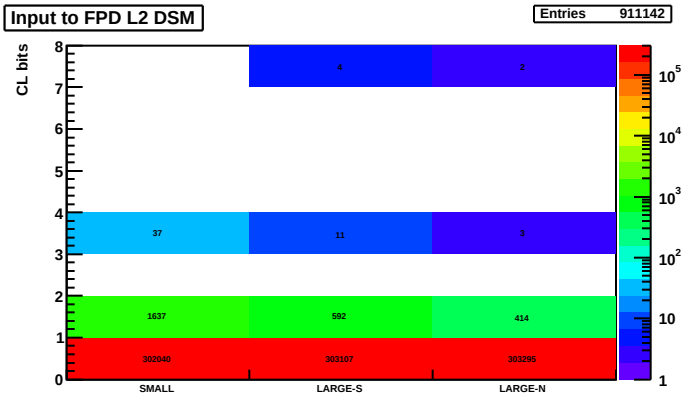

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

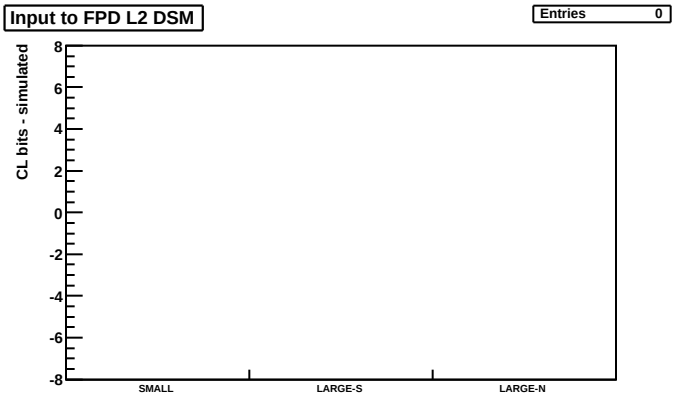

Input to FMS L1 DSM
Entries 3644568
FMS L1 HT bits
DSM board

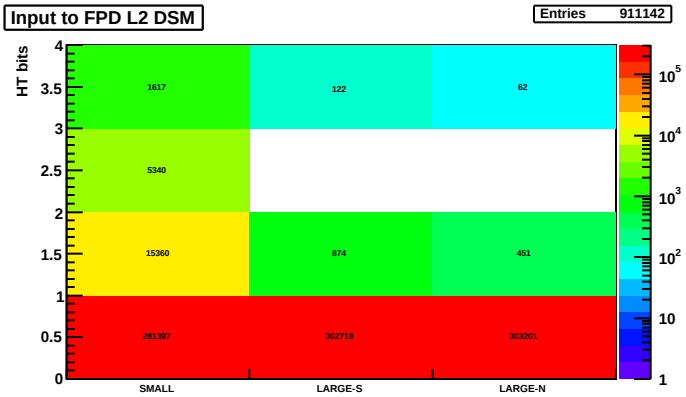

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

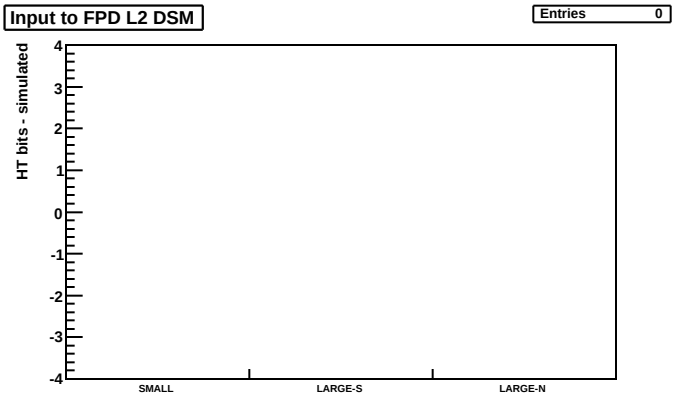

Input to FPD L2 DSM
Entries 3644568
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

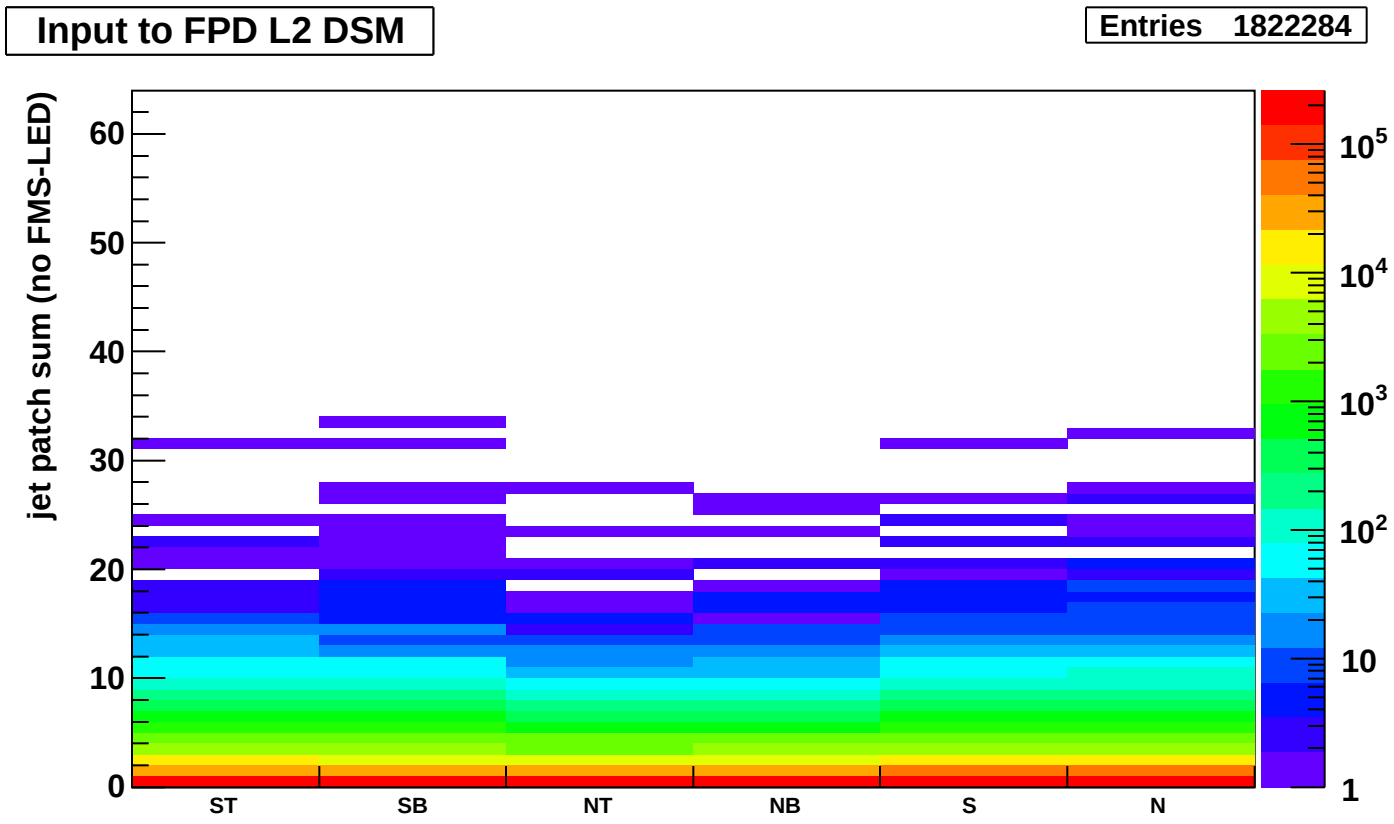

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

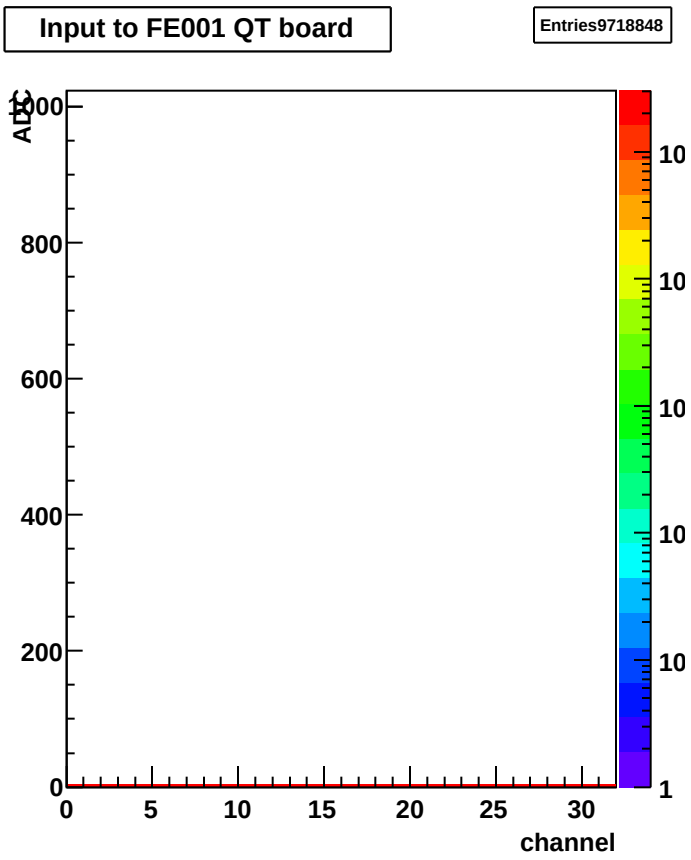

Input to FPD L2 DSM
Entries 911142
CL bits
4
2
37
11
3
1637
592
414
302040
303107
303295
SMALL
LARGE-S
LARGE-N

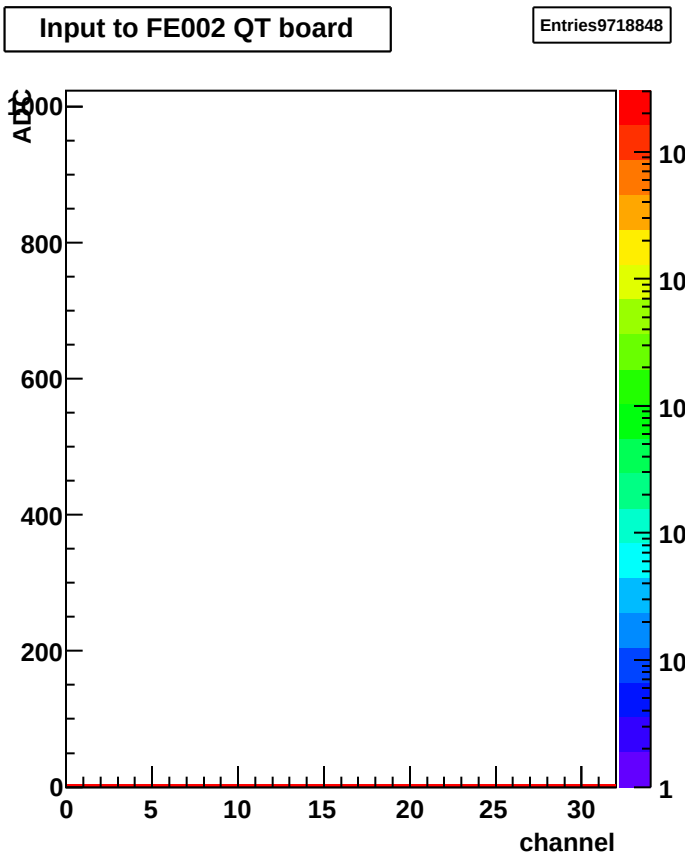

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

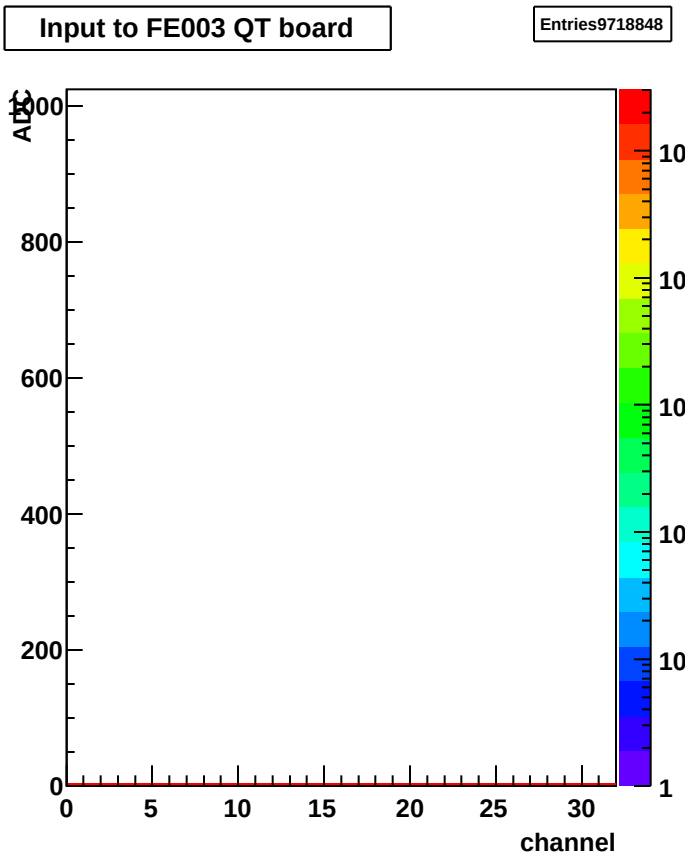

Input to FPD L2 DSM
Entries 911142
HT bits
1617
122
62
5340
15360
874
451
281397
302718
303201
SMALL
LARGE-S
LARGE-N

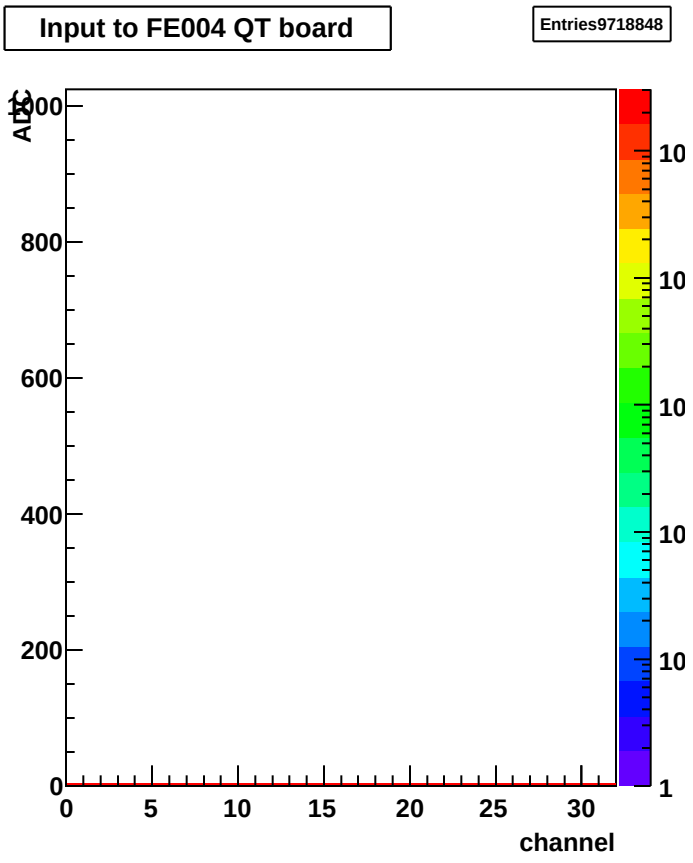

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

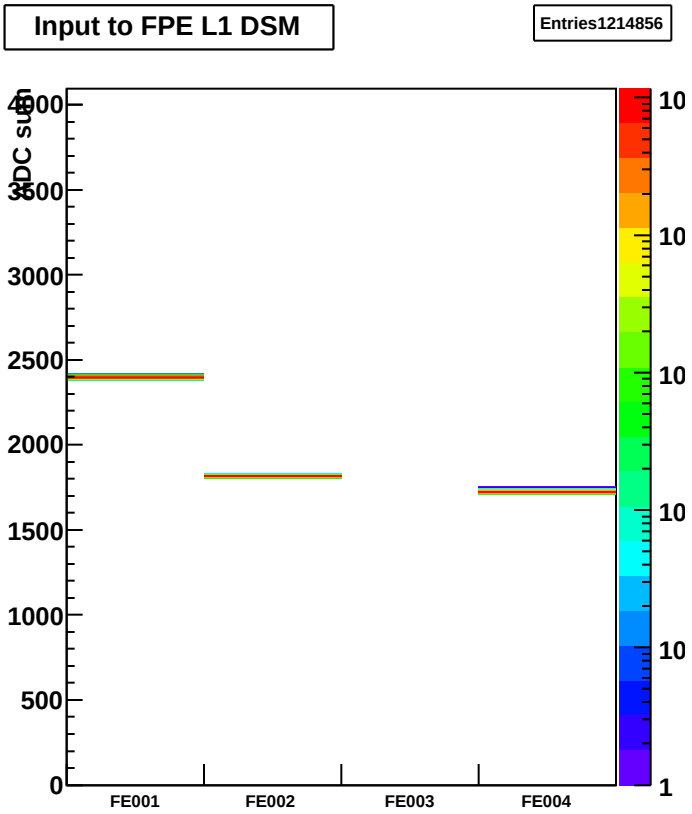

Input to FPD L2 DSM
Entries 1822284
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

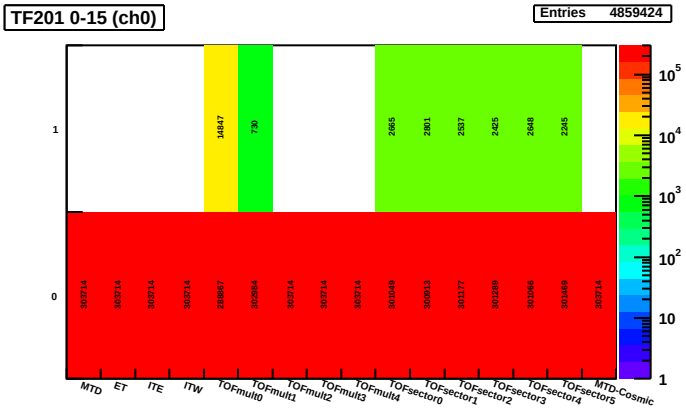

Input to FE001 QT board
Entries9718848
ADC
channel

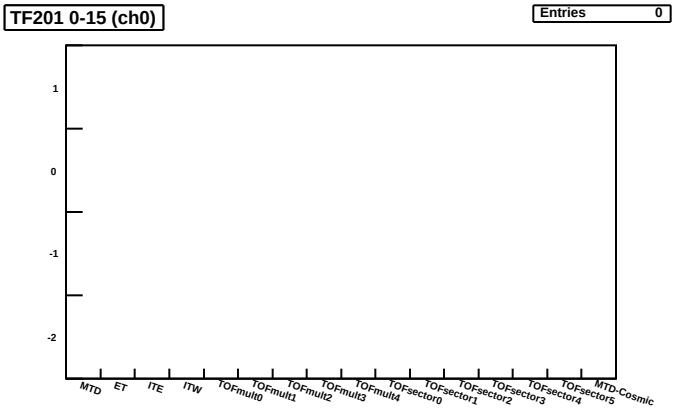

Input to FE002 QT board
Entries9718848
ADC
channel

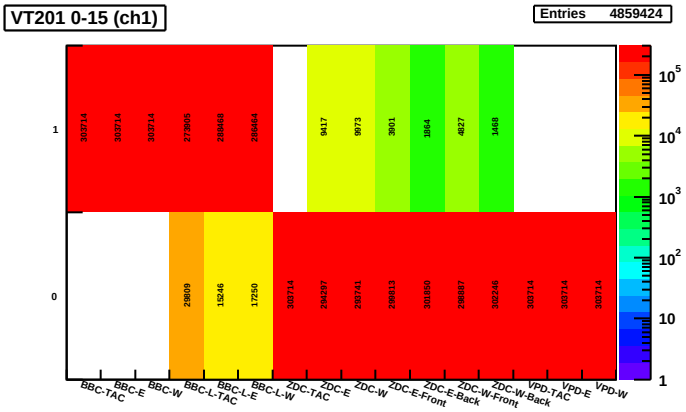

Input to FE003 QT board
Entries9718848
ADC
channel

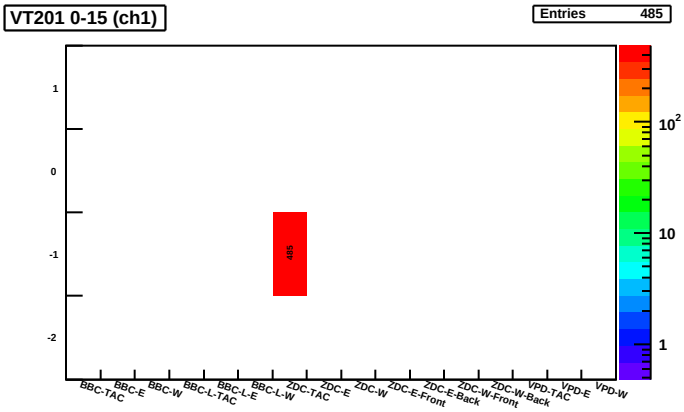

Input to FE004 QT board
Entries9718848
ADC
channel

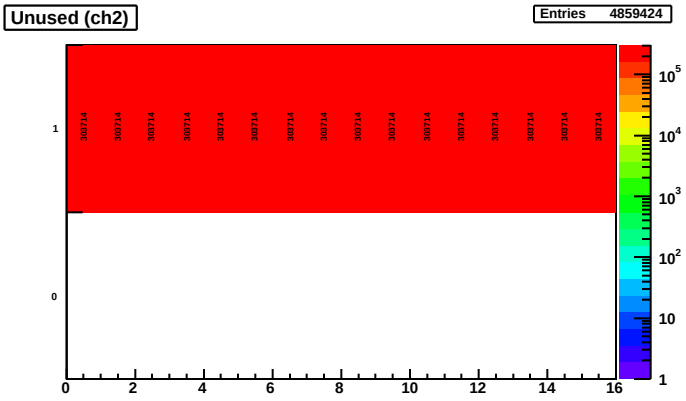

Input to FPE L1 DSM
Entries1214856
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

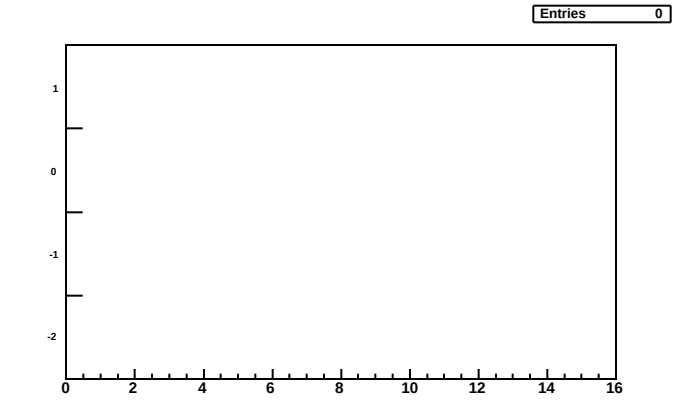

TF201 0-15 (ch0)
Entries 4859424
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

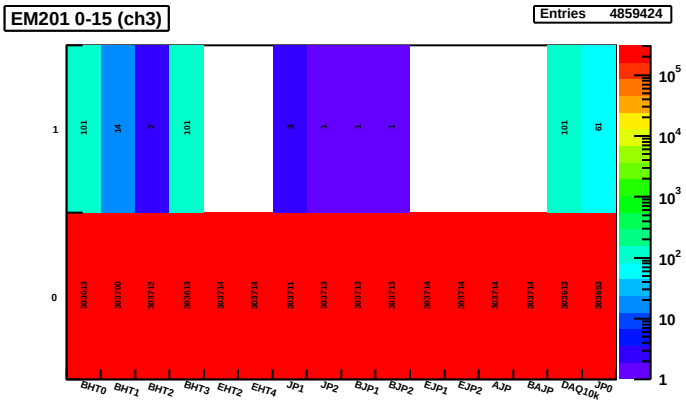

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 4859424
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 485
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 4859424


Entries 0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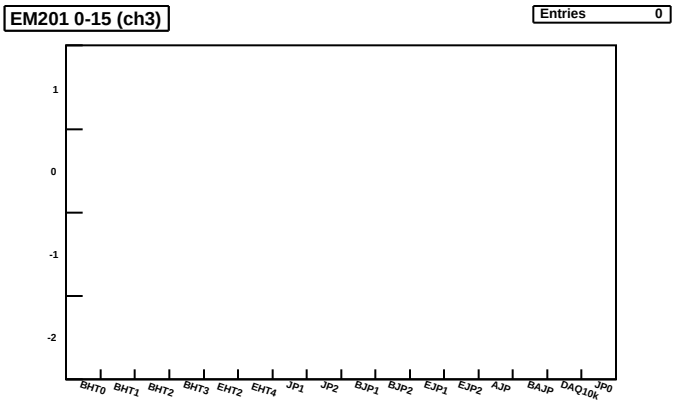
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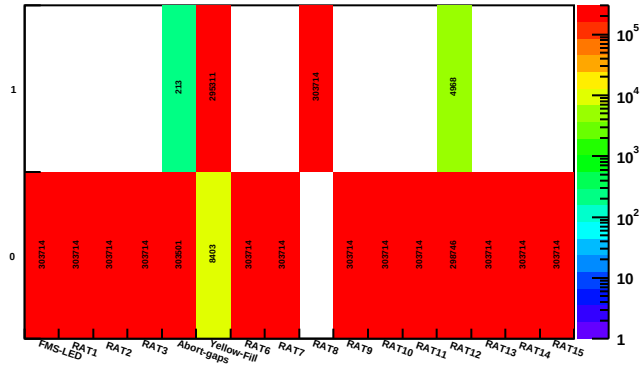
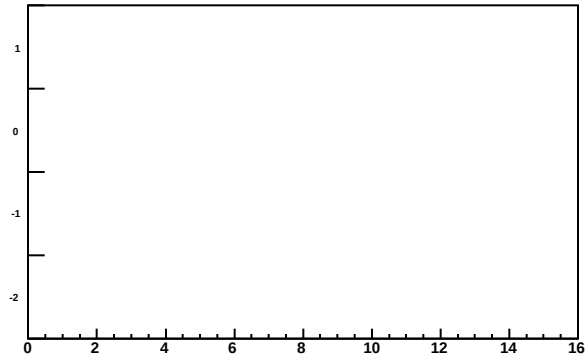
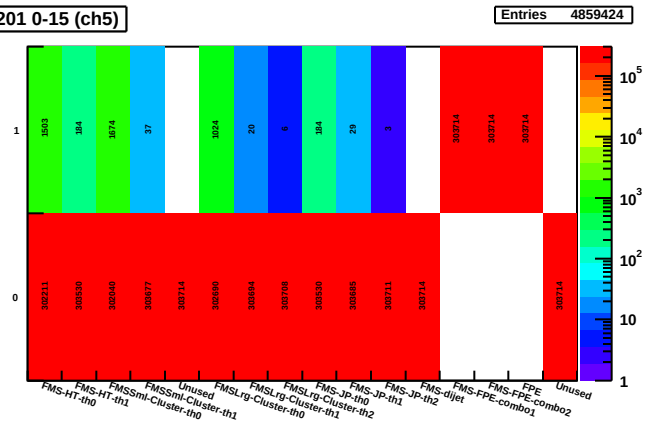
RAT board (ch4)





FP201 0-15 (ch5)



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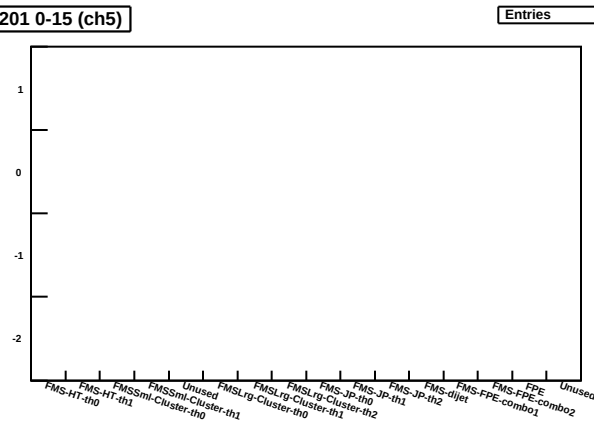
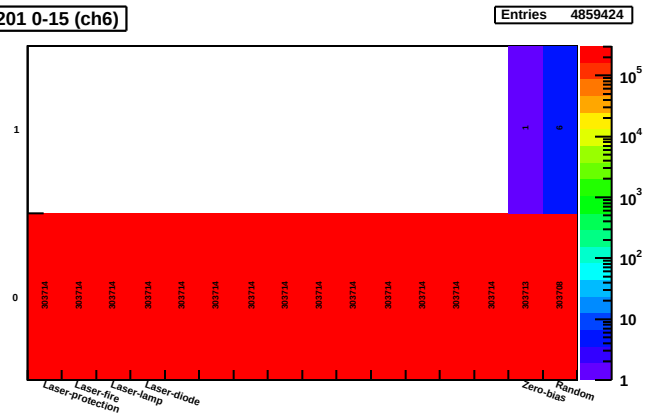
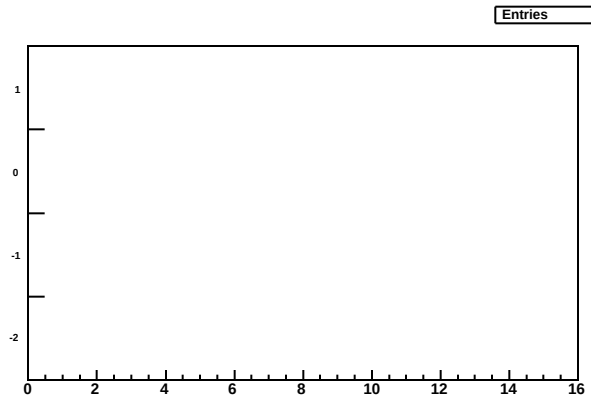
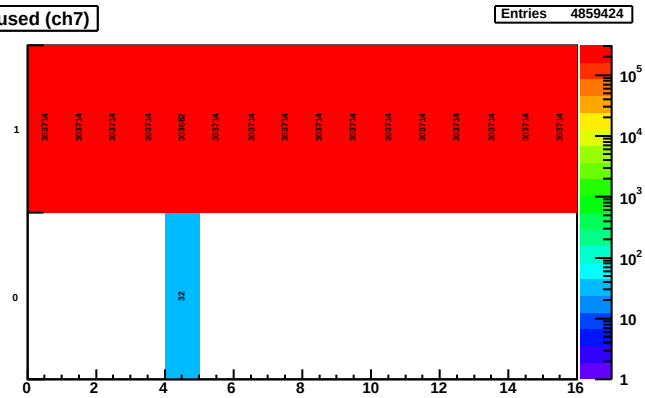
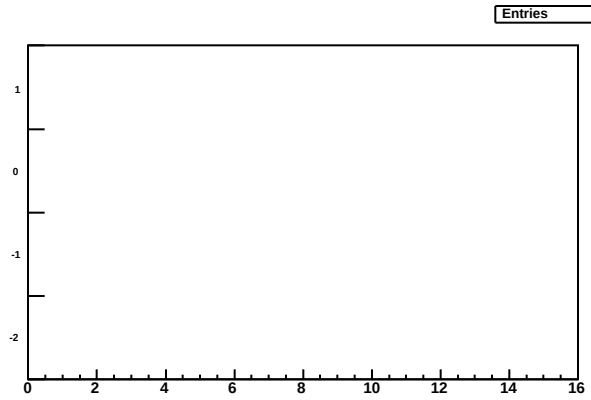


ST201 0-15 (ch6)





Unused (ch7)