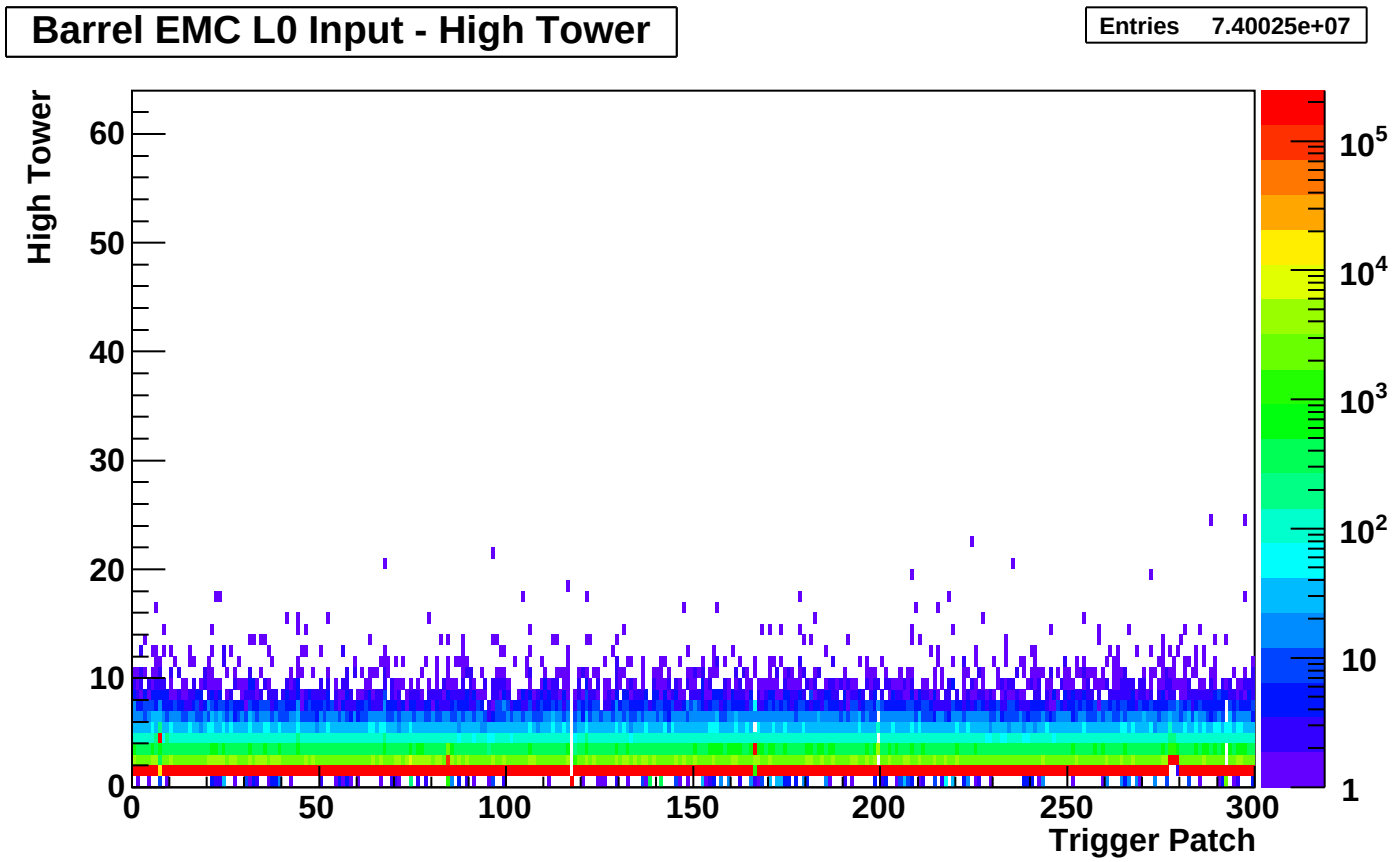
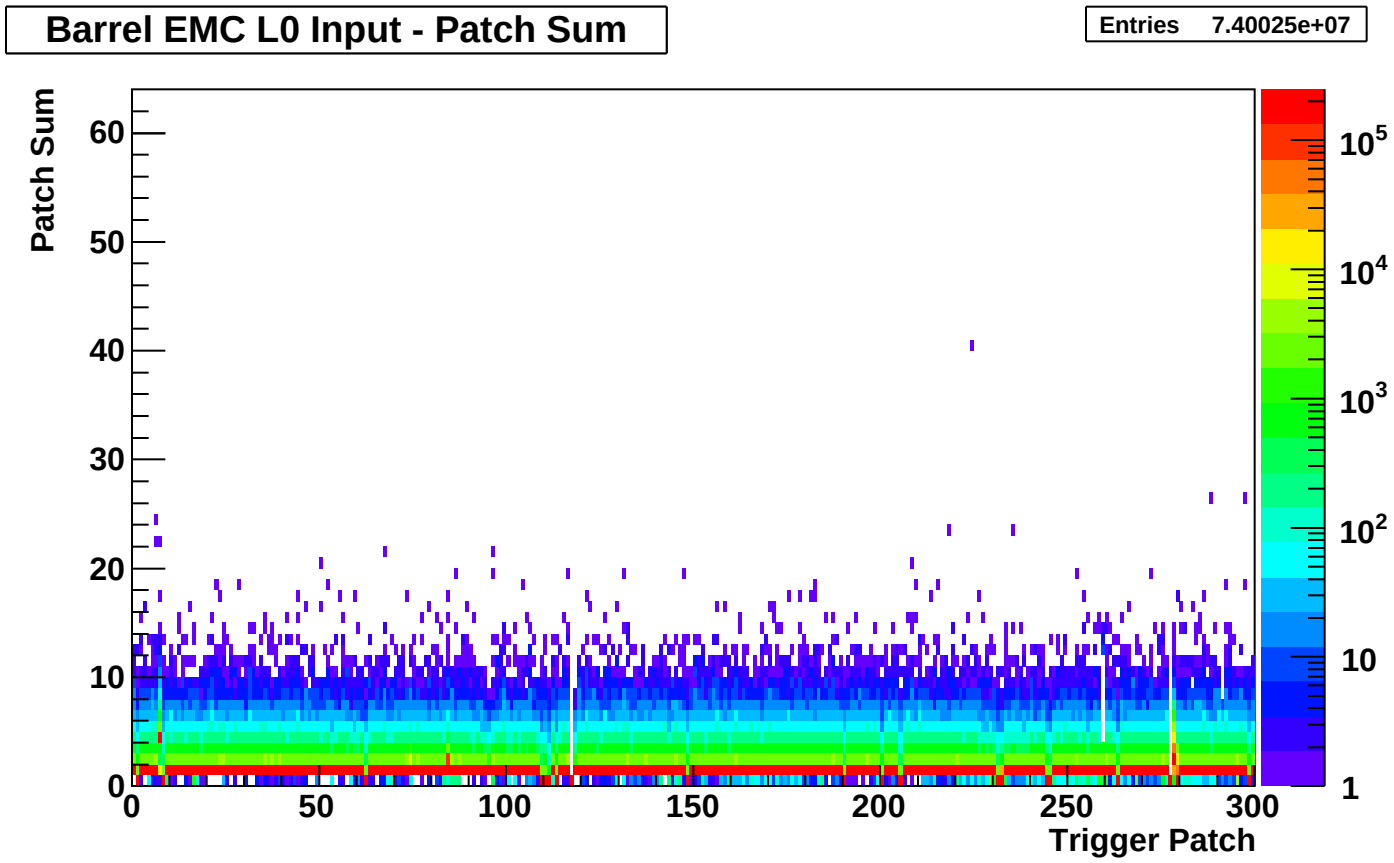
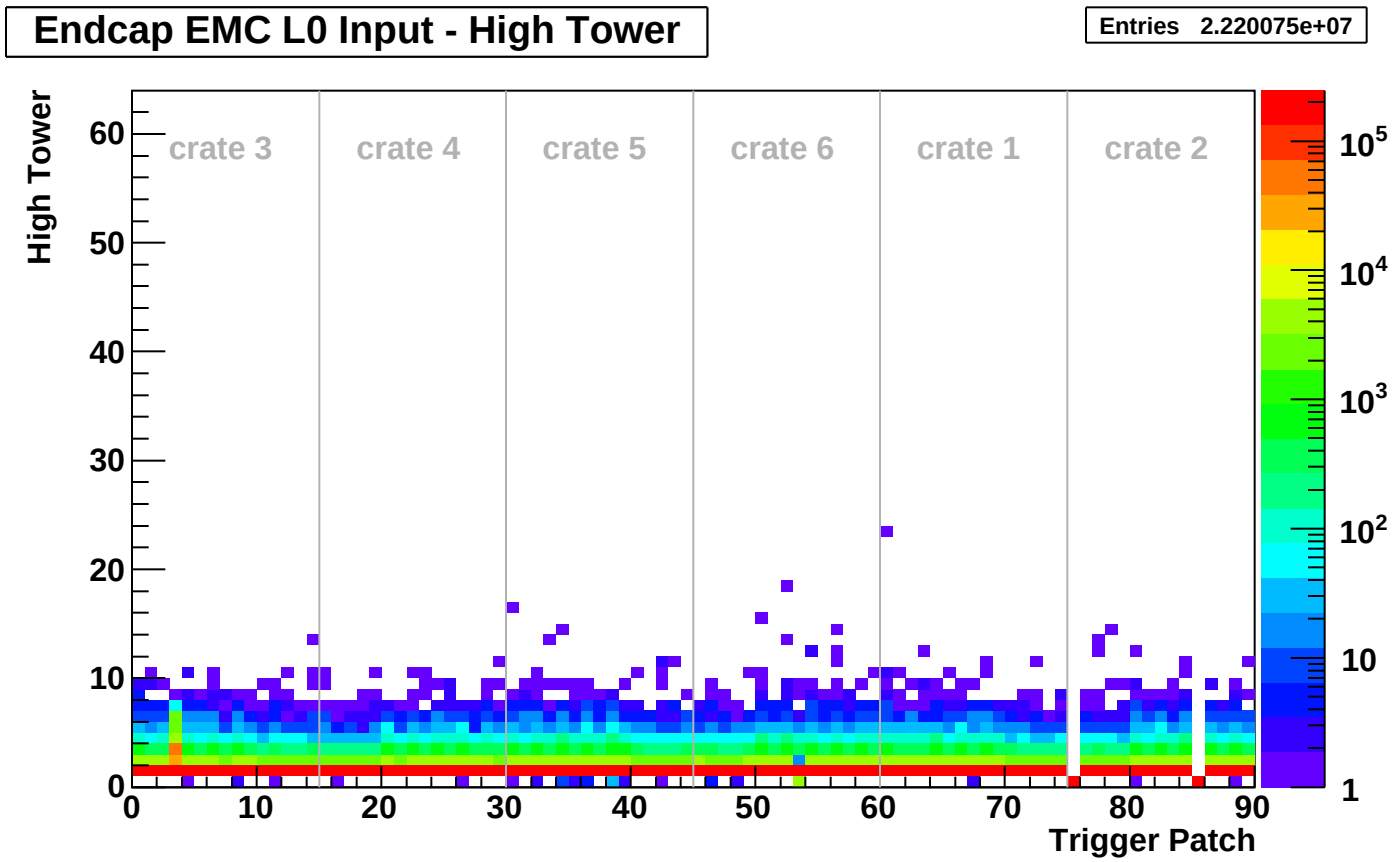
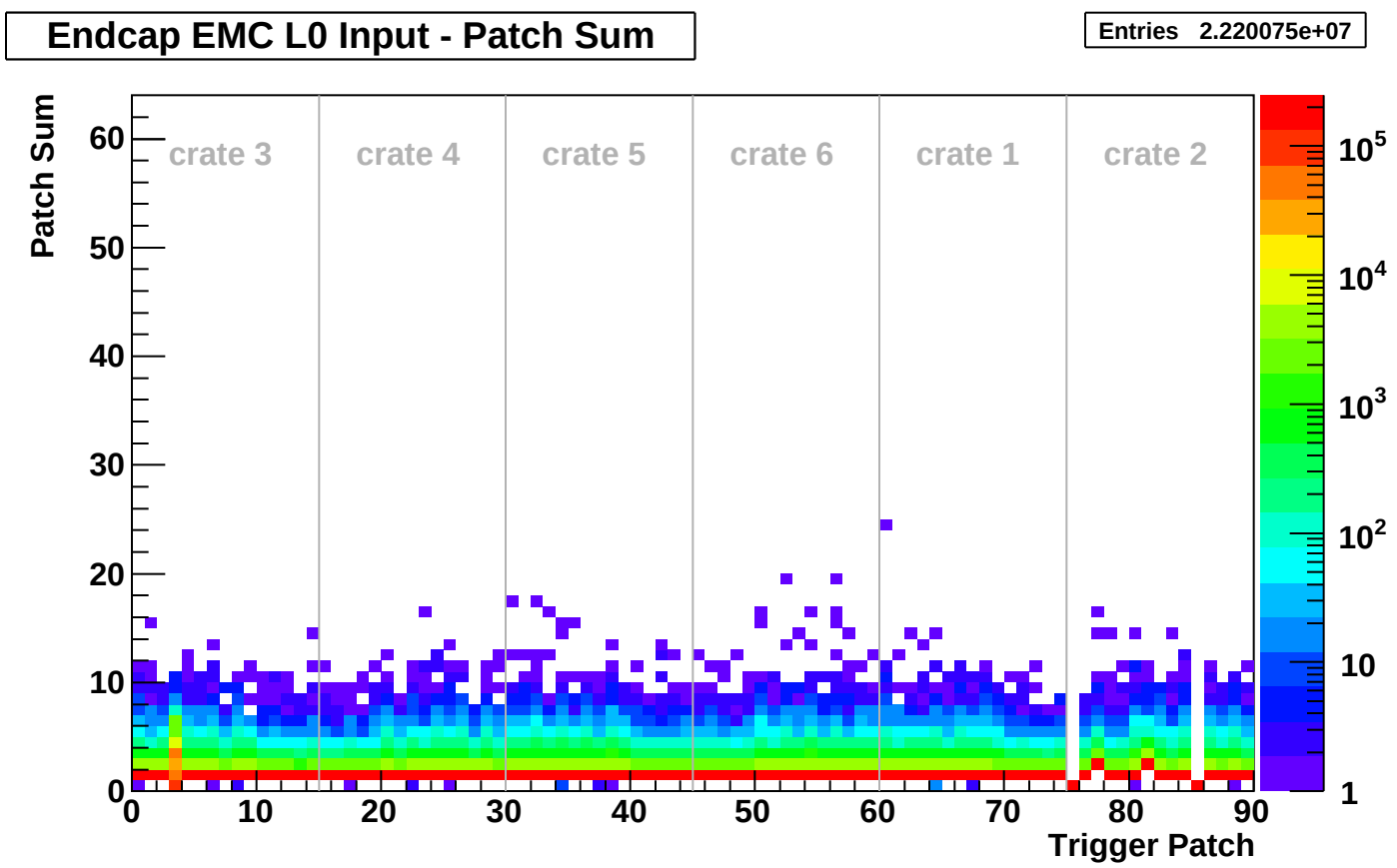
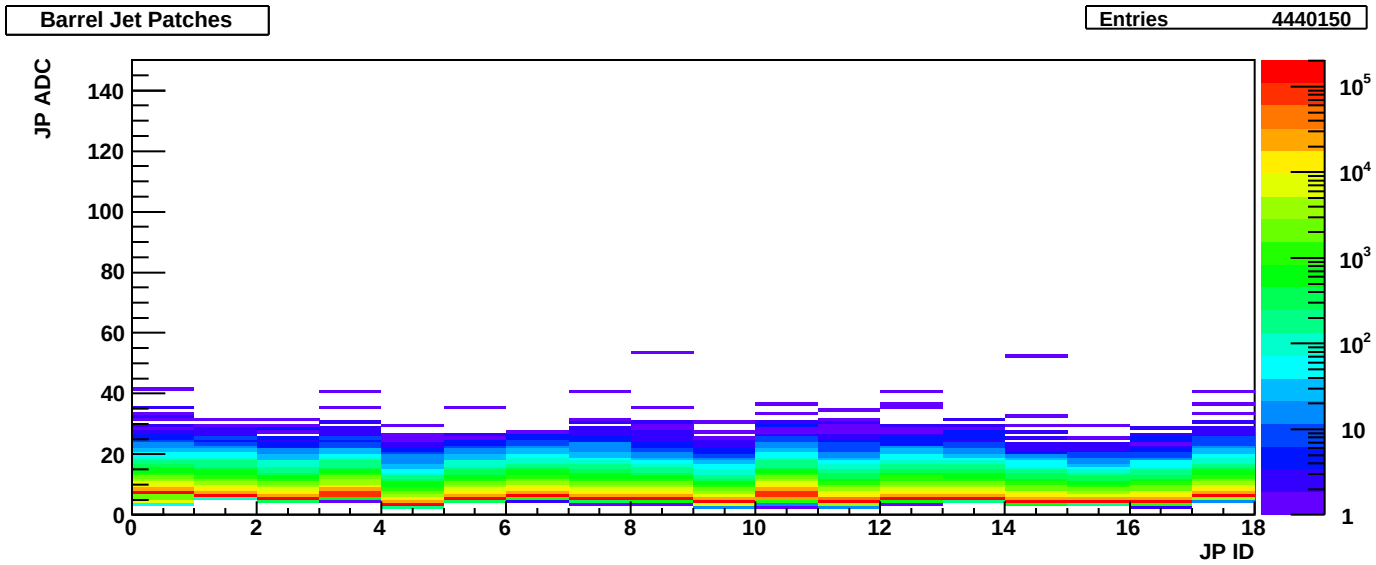
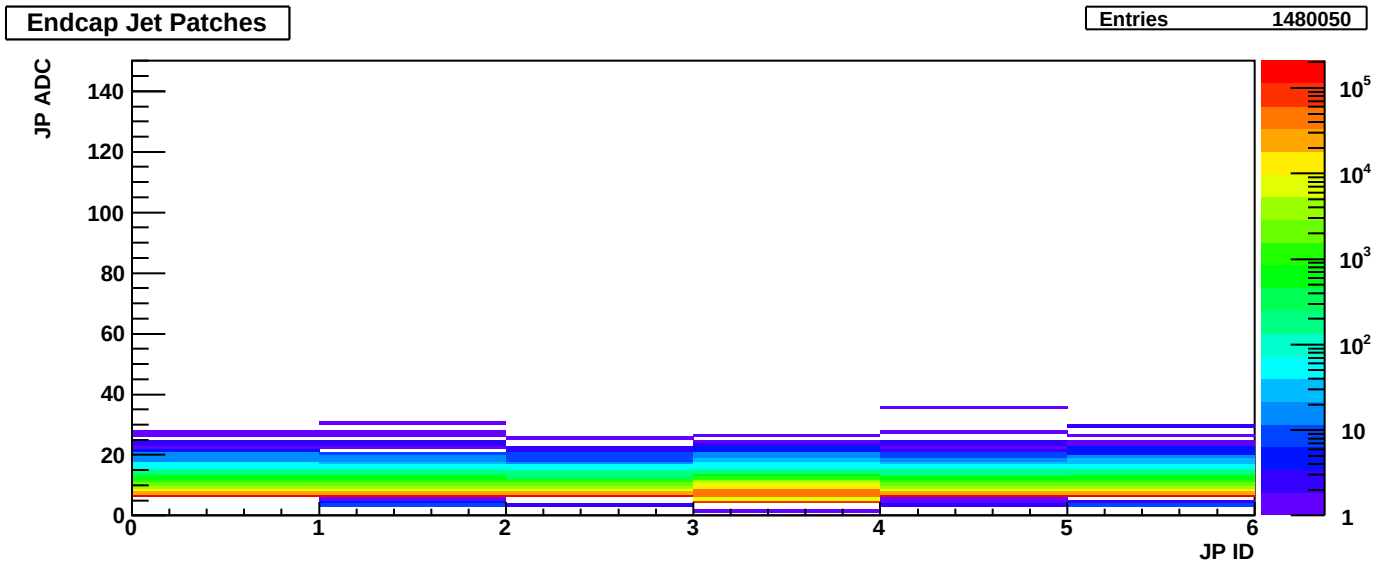

Barrel EMC L0 Input - High Tower
Entries 7.40025e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

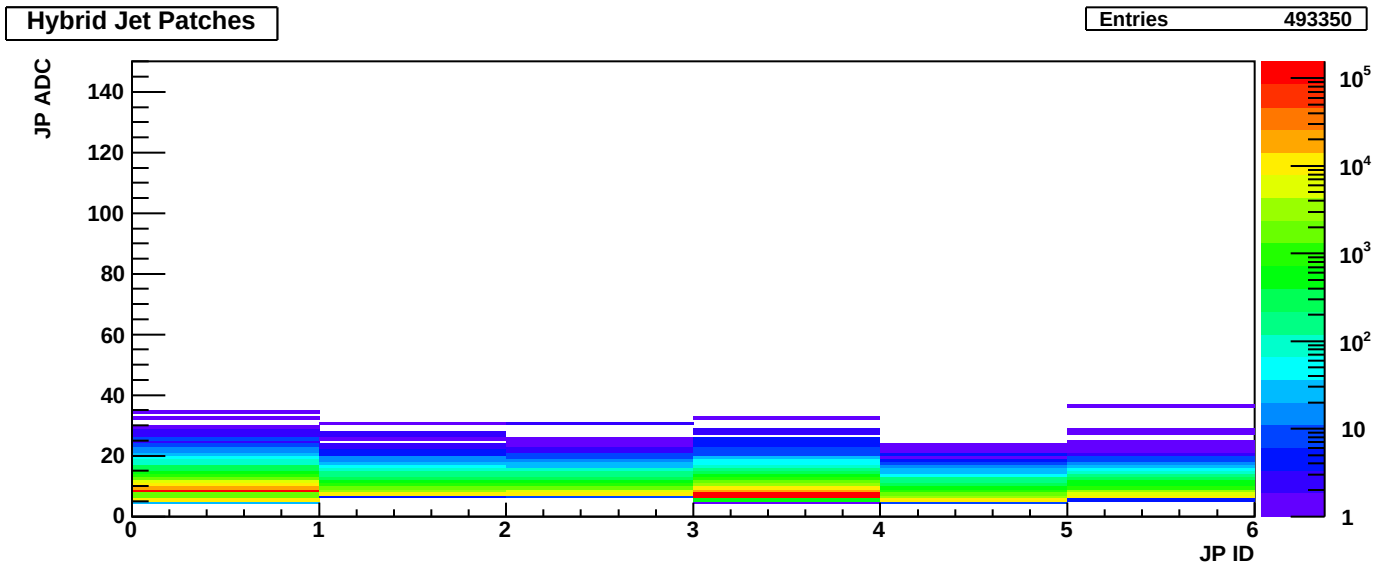

Barrel EMC L0 Input - Patch Sum
Entries 7.40025e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

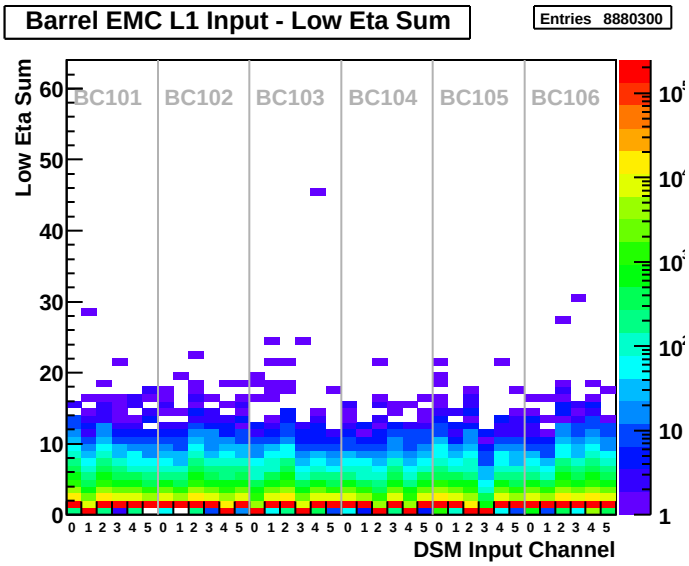

Endcap EMC L0 Input - High Tower
Entries 2.220075e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

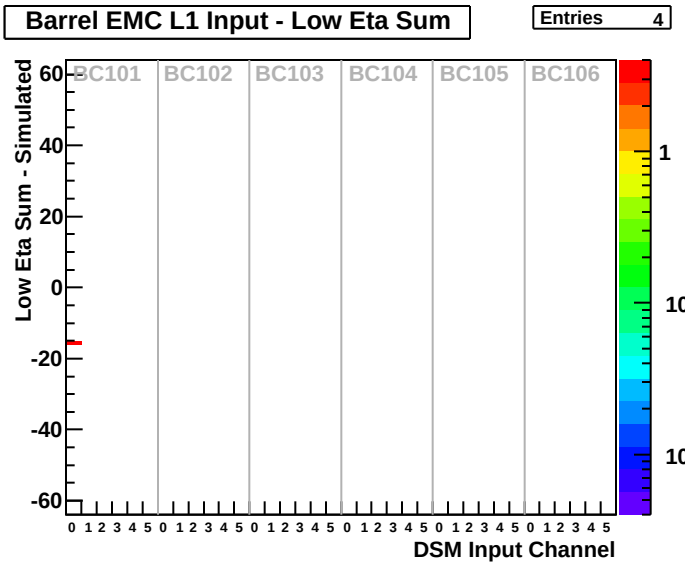

Endcap EMC L0 Input - Patch Sum
Entries 2.220075e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

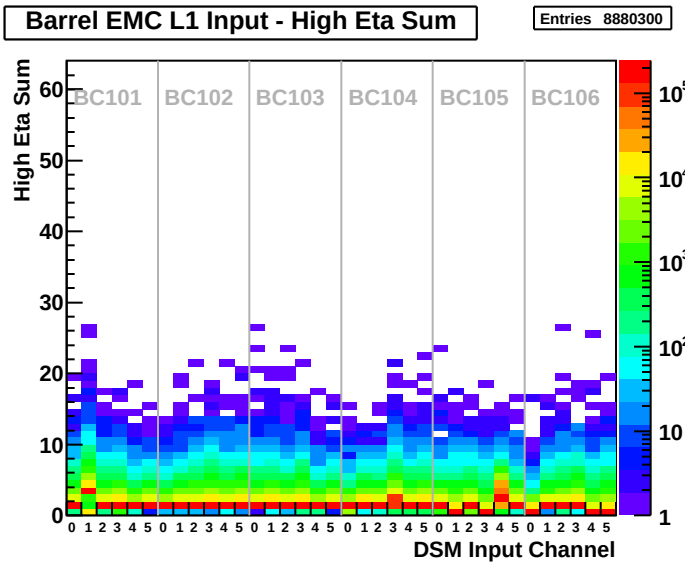

Barrel Jet Patches
Entries 4440150
JP ADC
JP ID

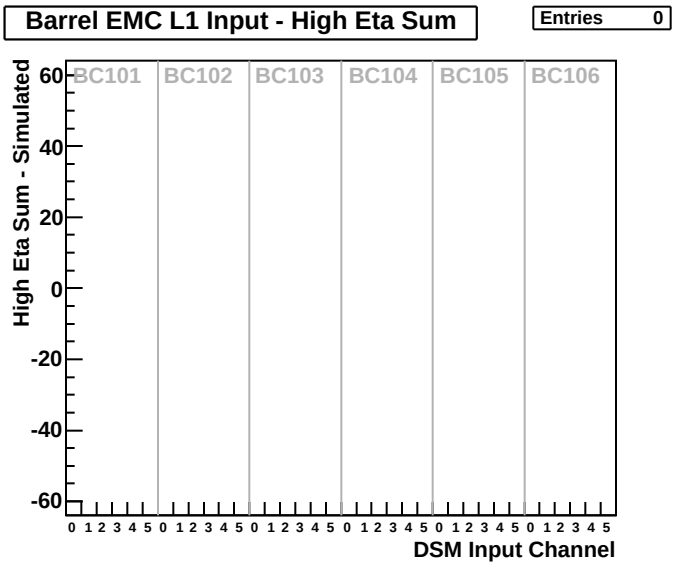

Endcap Jet Patches
Entries 1480050
JP ADC
JP ID

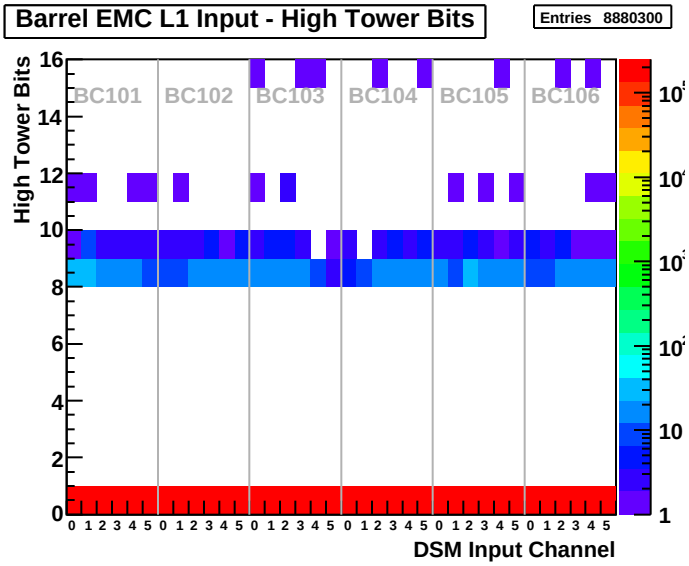

Hybrid Jet Patches
Entries 493350
JP ADC
JP ID

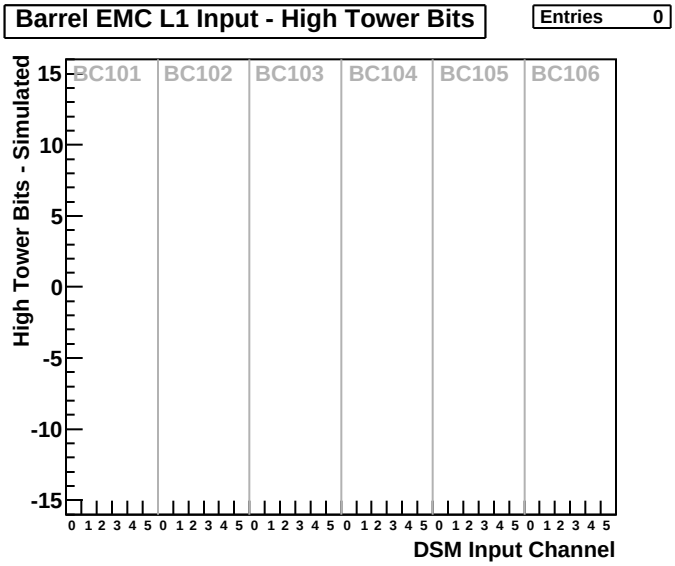

Barrel EMC L1 Input - Low Eta Sum
Entries 8880300
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

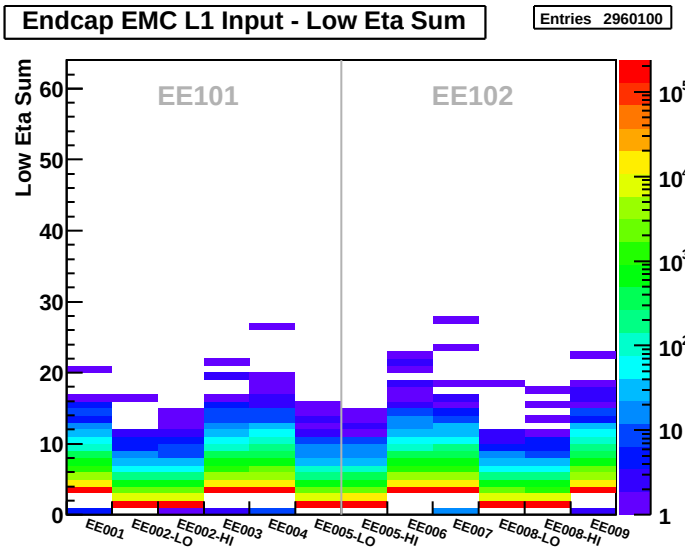

Barrel EMC L1 Input - Low Eta Sum
Entries 4
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

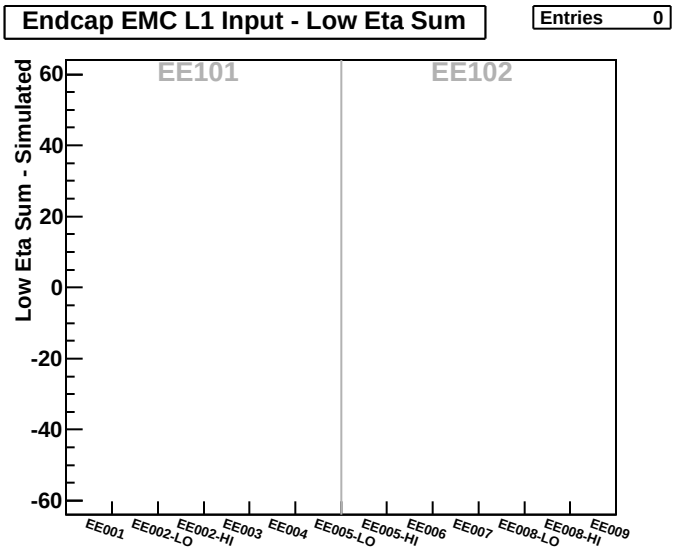

Barrel EMC L1 Input - High Eta Sum
Entries 8880300
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

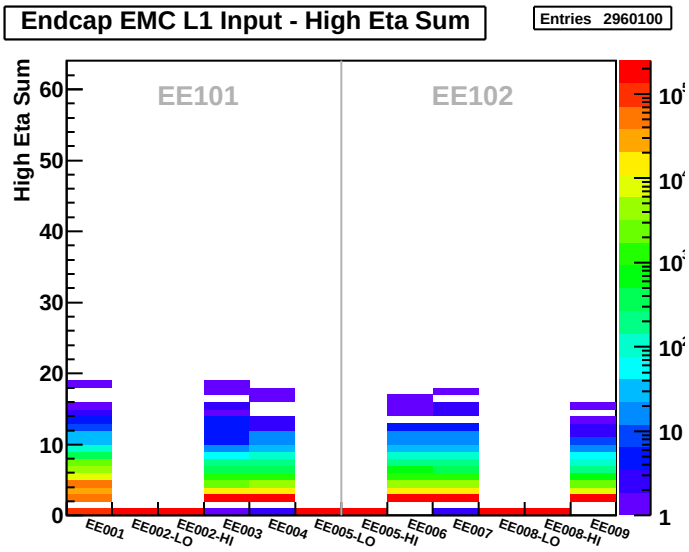

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

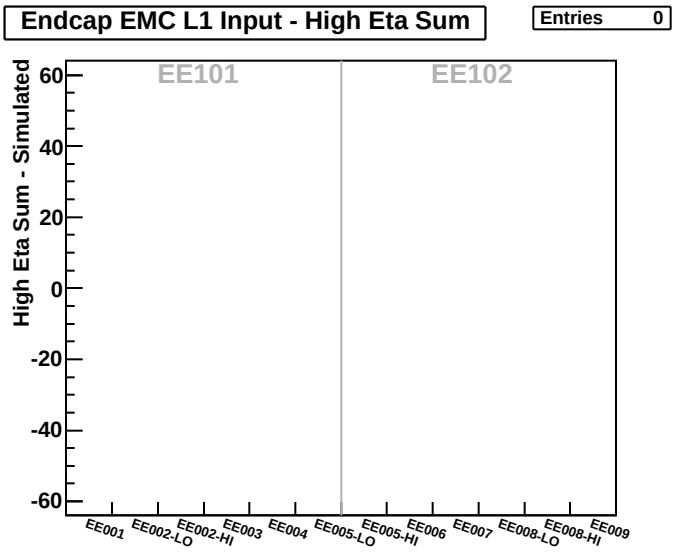

Barrel EMC L1 Input - High Tower Bits
Entries 8880300
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

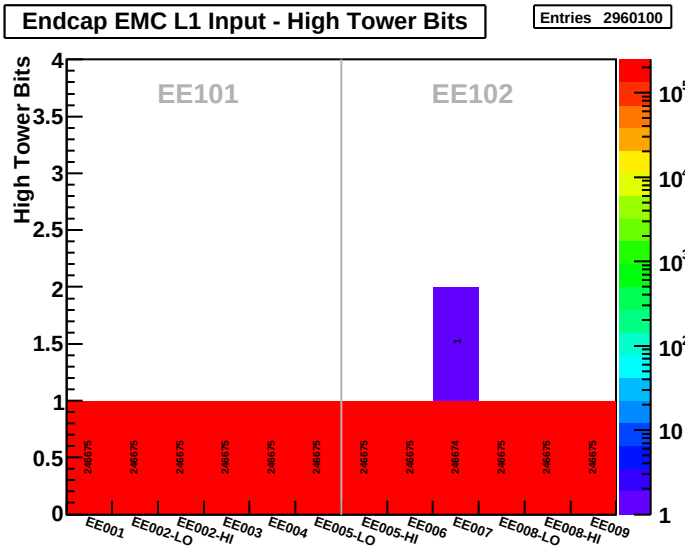

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

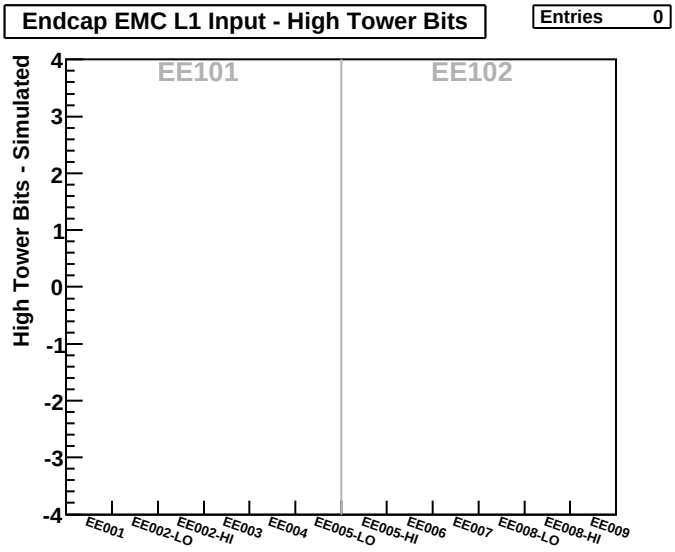

Endcap EMC L1 Input - Low Eta Sum
Entries 2960100
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

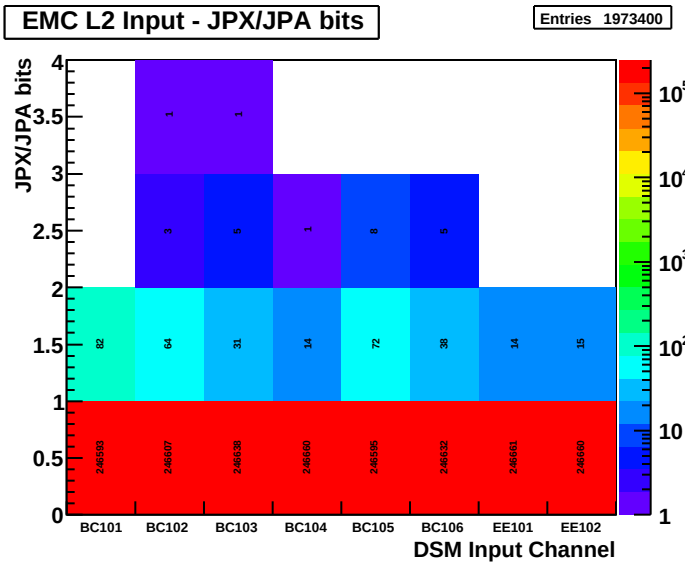

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

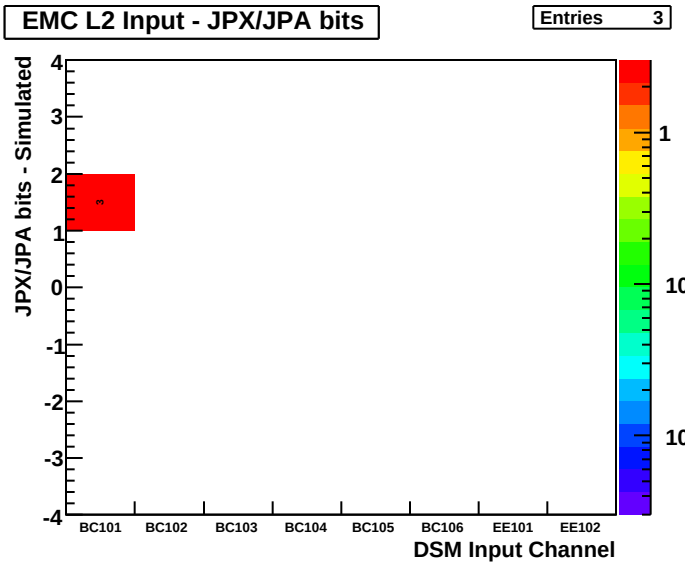

Endcap EMC L1 Input - High Eta Sum
Entries 2960100
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

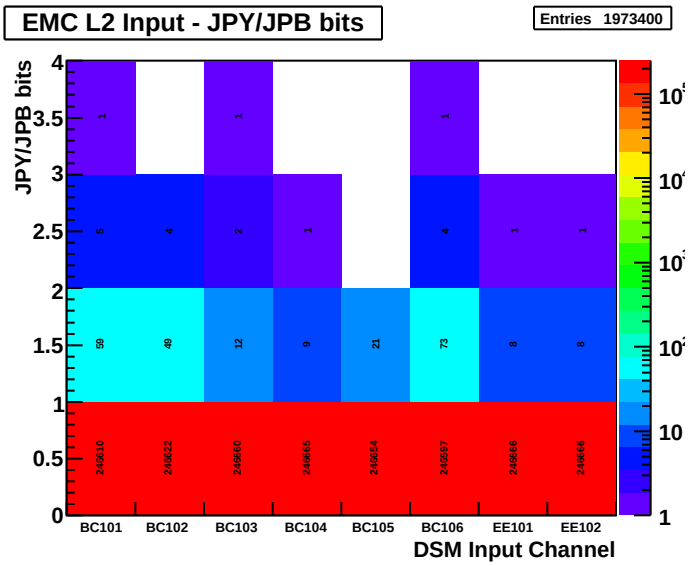

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

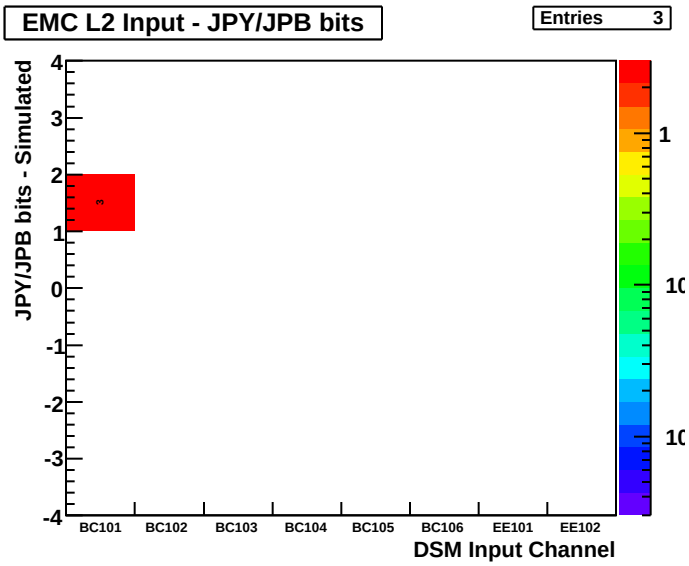

Endcap EMC L1 Input - High Tower Bits
Entries 2960100
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

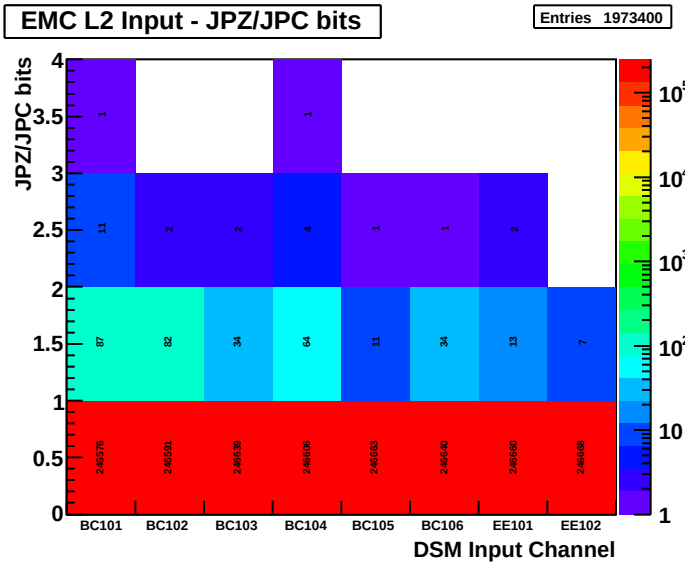

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

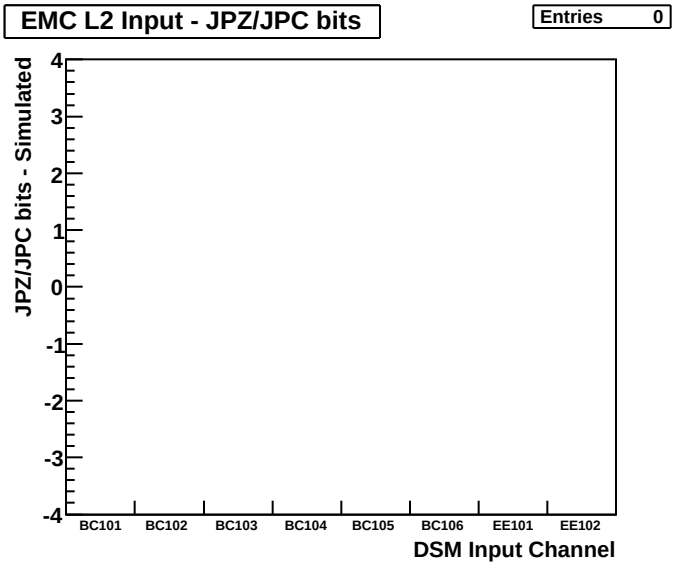

EMC L2 Input - JPX/JPA bits
Entries 1973400
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

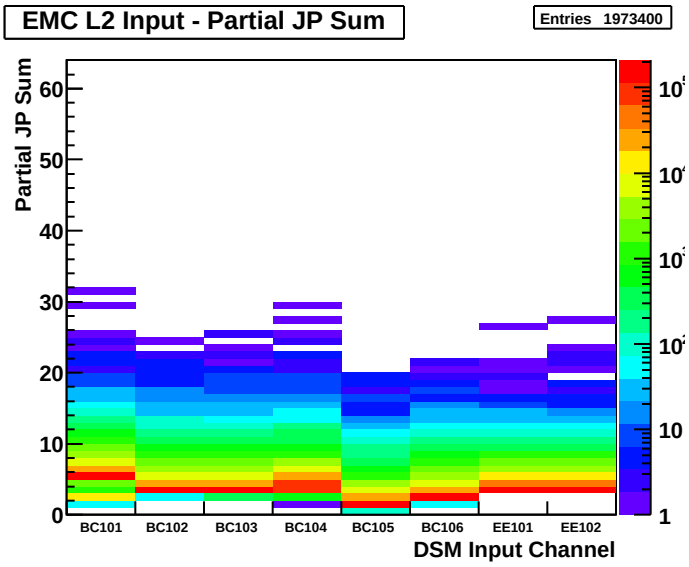

EMC L2 Input - JPX/JPA bits
Entries 3
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

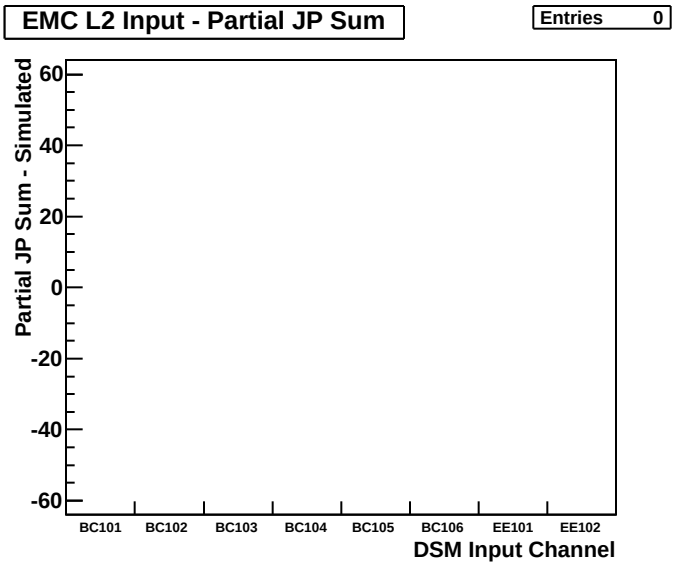

EMC L2 Input - JPY/JPB bits
Entries 1973400
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

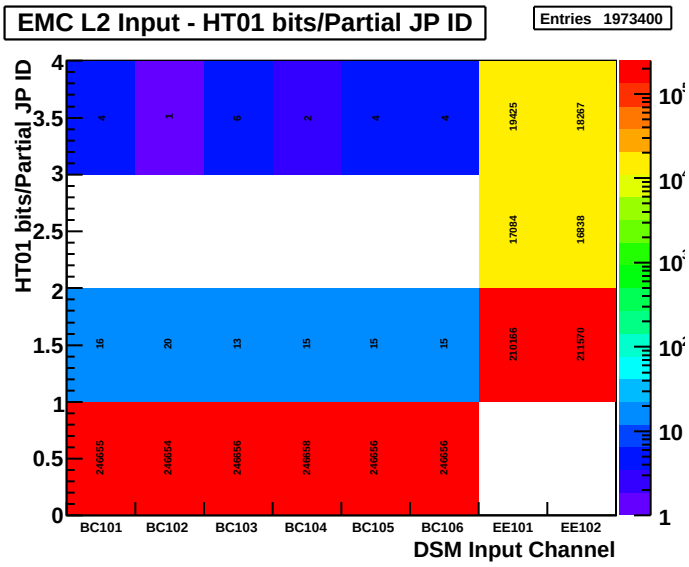

EMC L2 Input - JPY/JPB bits
Entries 3
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

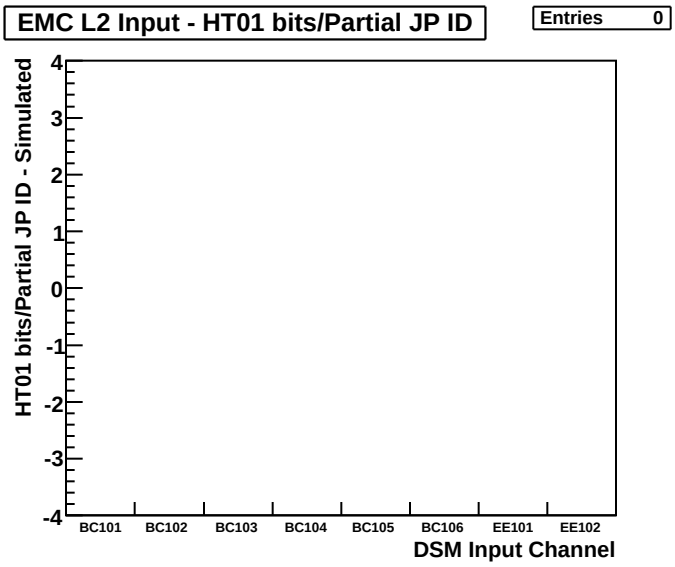

EMC L2 Input - JPZ/JPC bits
Entries 1973400
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

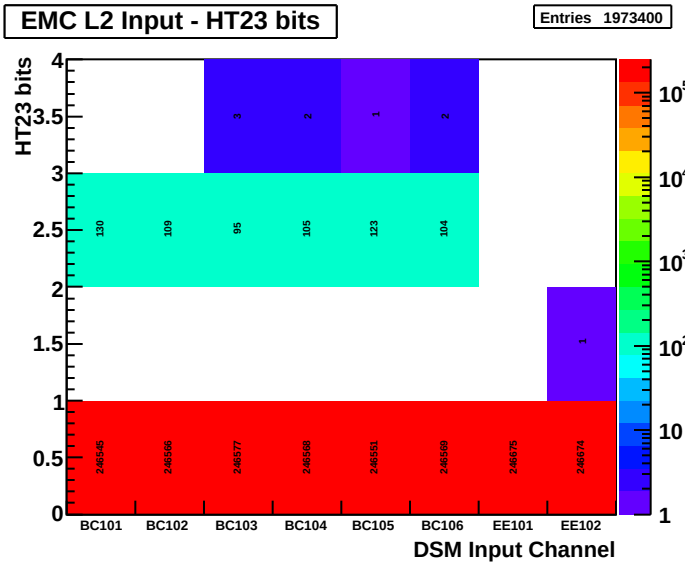

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

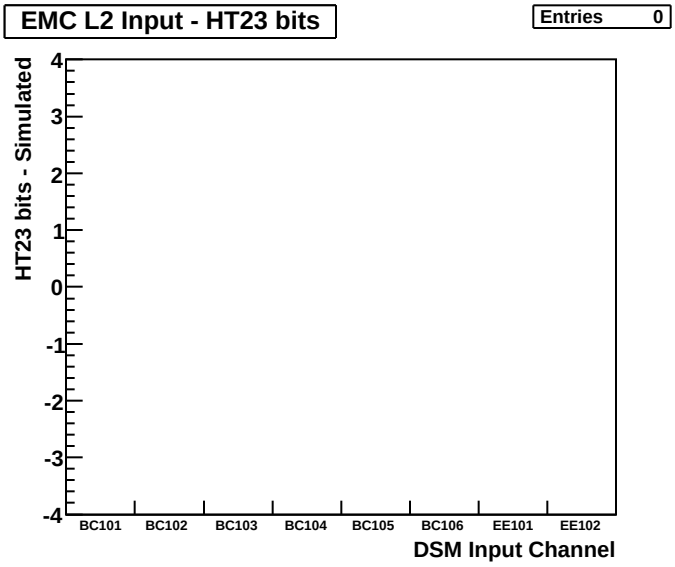

EMC L2 Input - Partial JP Sum
Entries 1973400
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

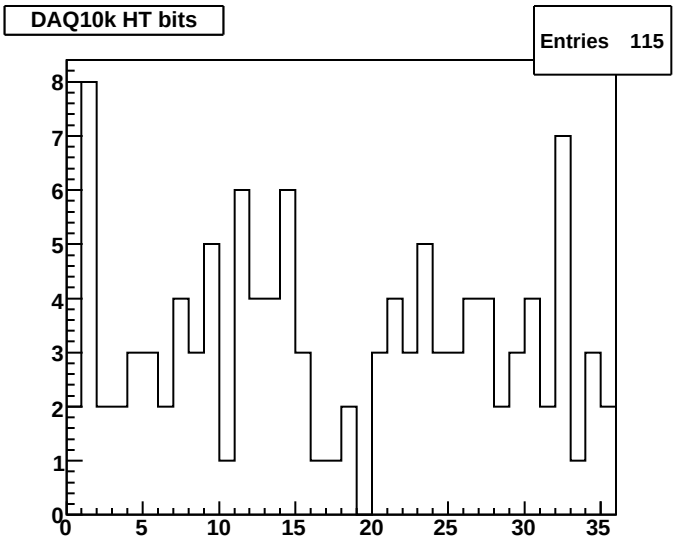

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

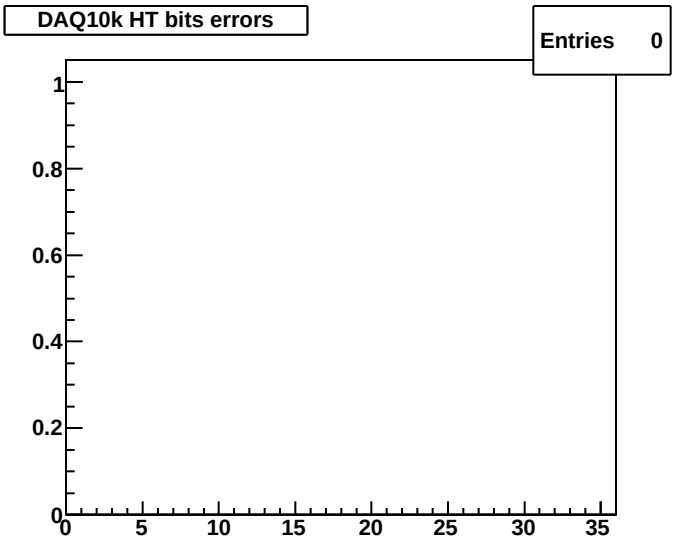

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1973400
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

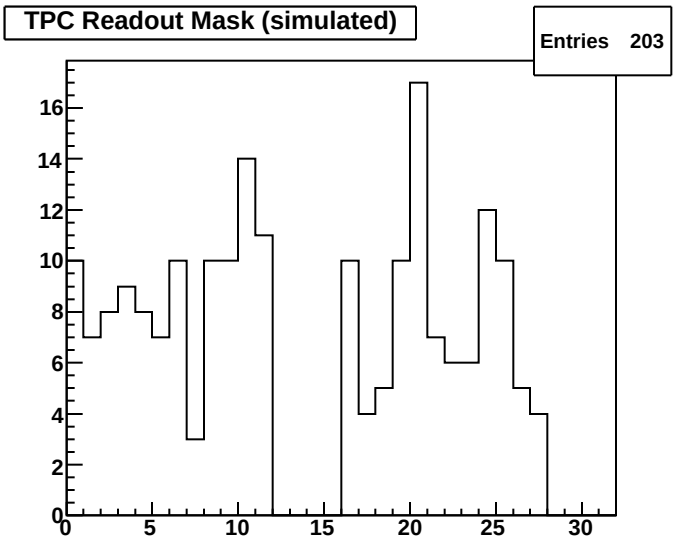

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

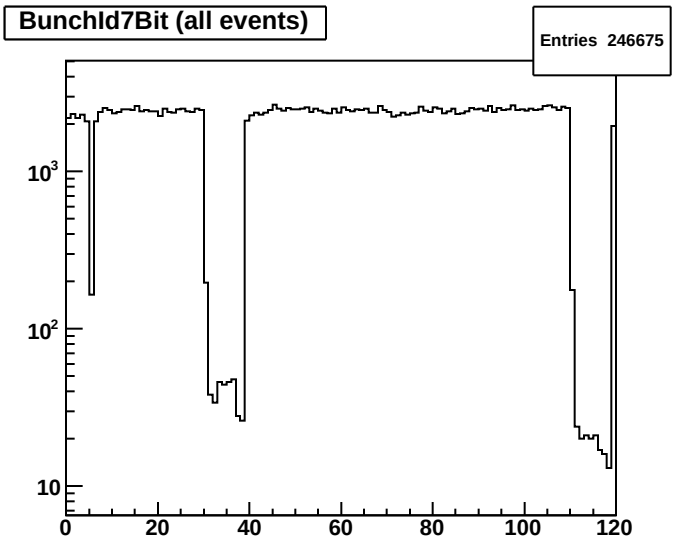

EMC L2 Input - HT23 bits
Entries 1973400
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

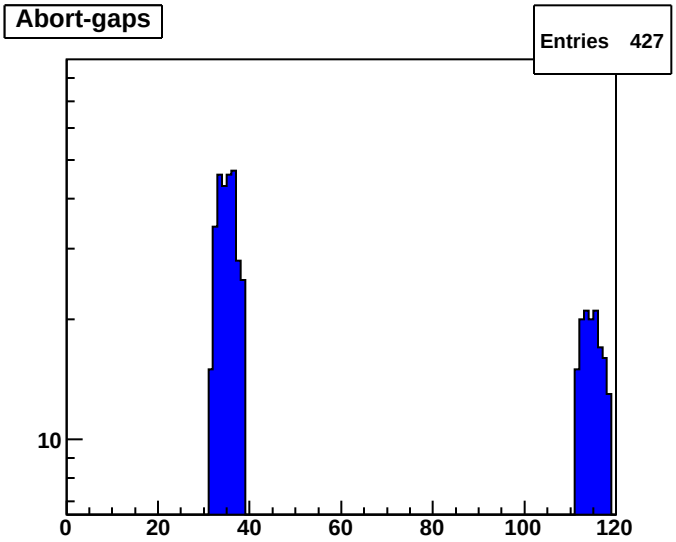

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

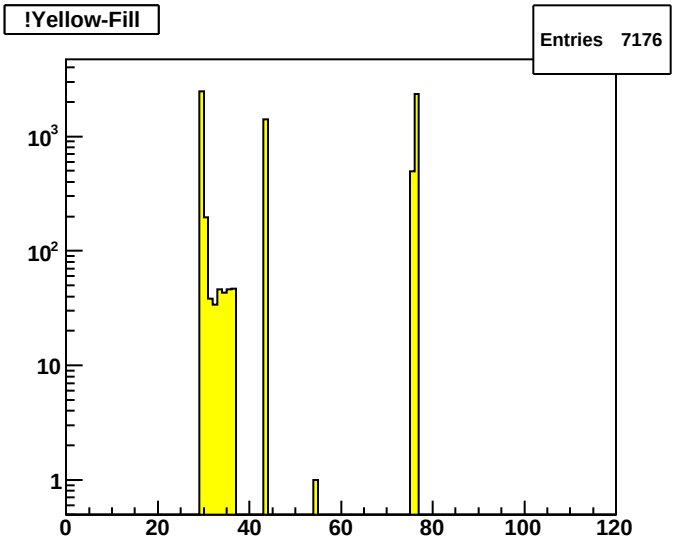

DAQ10k HT bits
Entries 115

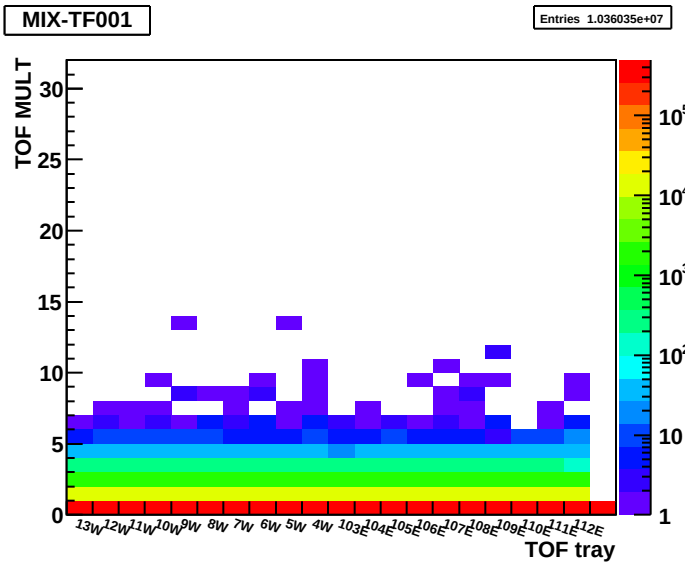

DAQ10k HT bits errors
Entries 0

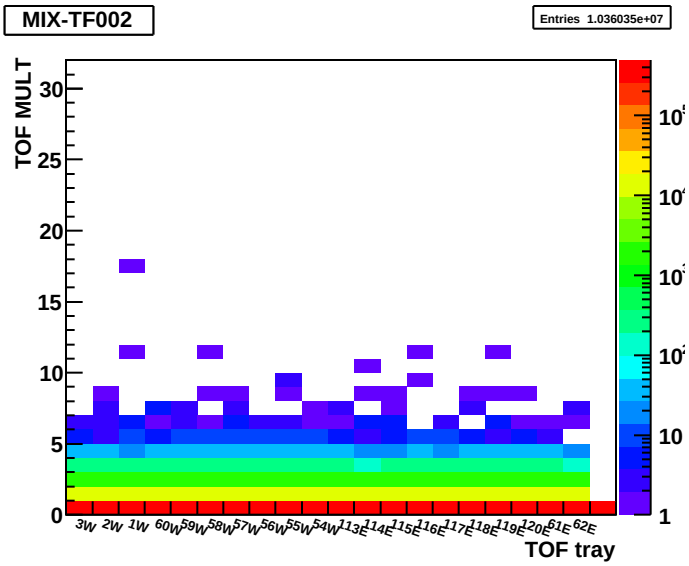

TPC Readout Mask (simulated)
Entries 203

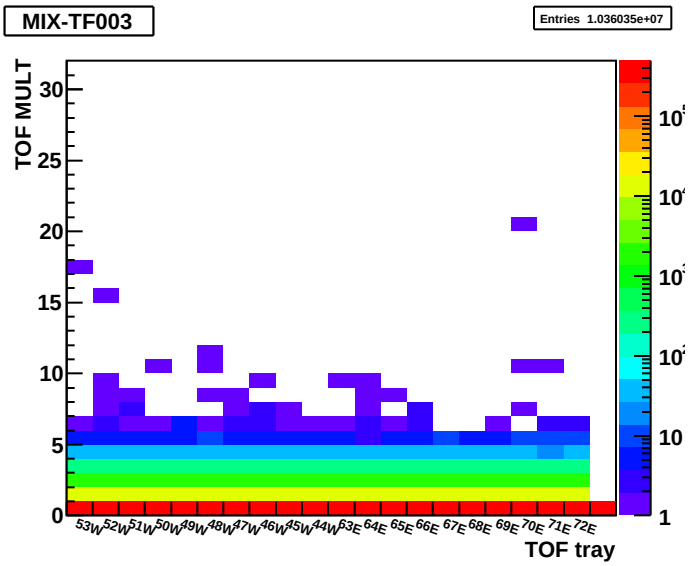

BunchId7Bit (all events)
Entries 246675

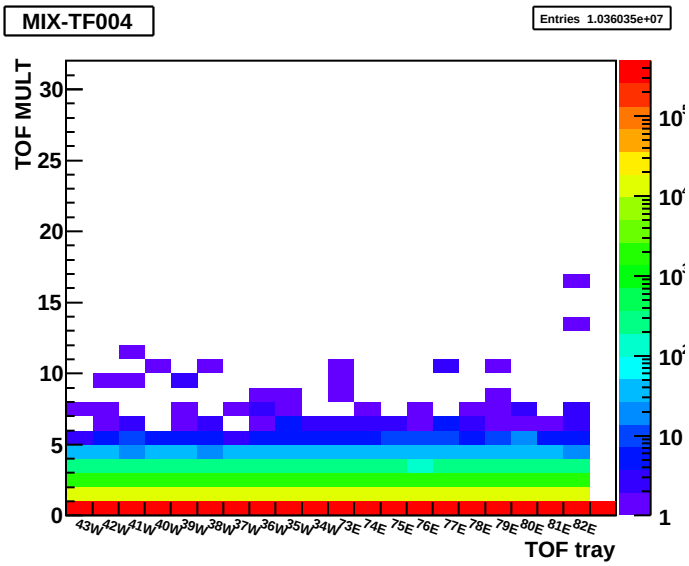

Abort-gaps
Entries 427

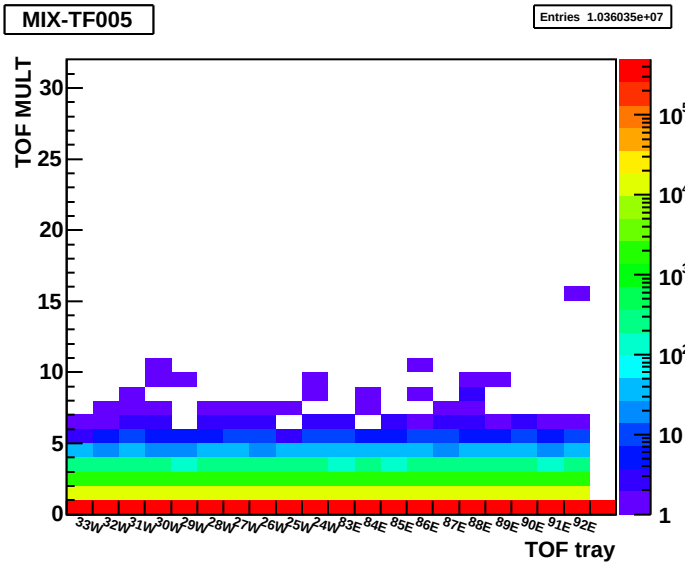

!Yellow-Fill
Entries 7176

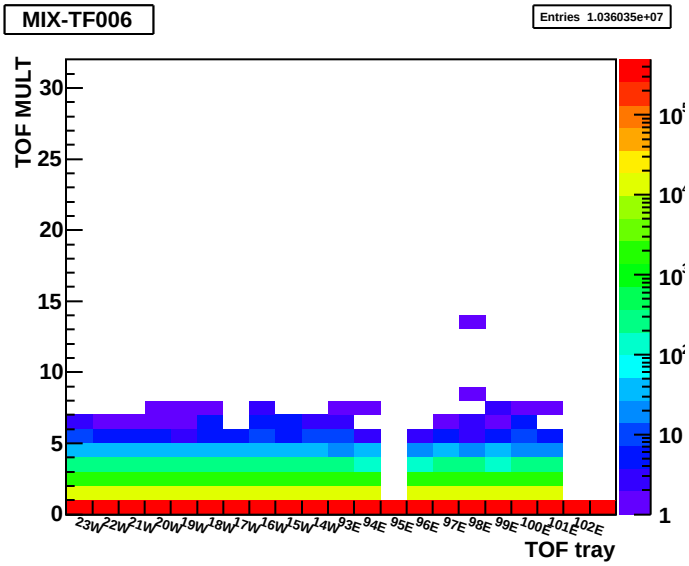

MIX-TF001
Entries 1.036035e+07
TOF MULT
TOF tray

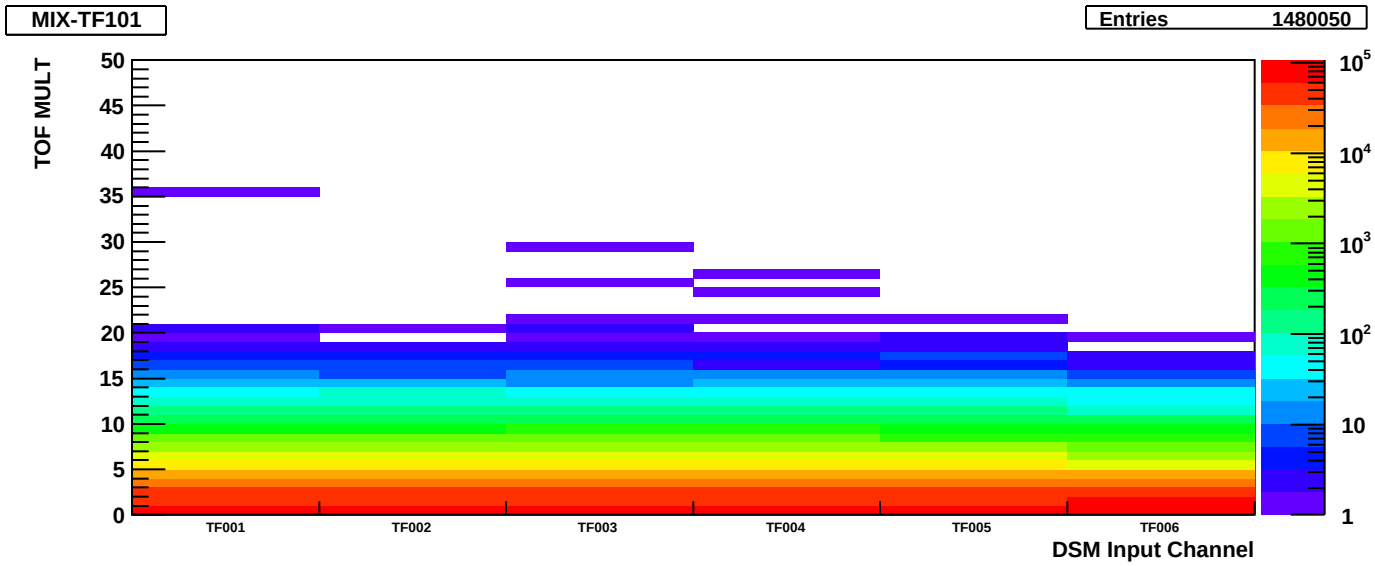

MIX-TF002
Entries 1.036035e+07
TOF MULT
TOF tray

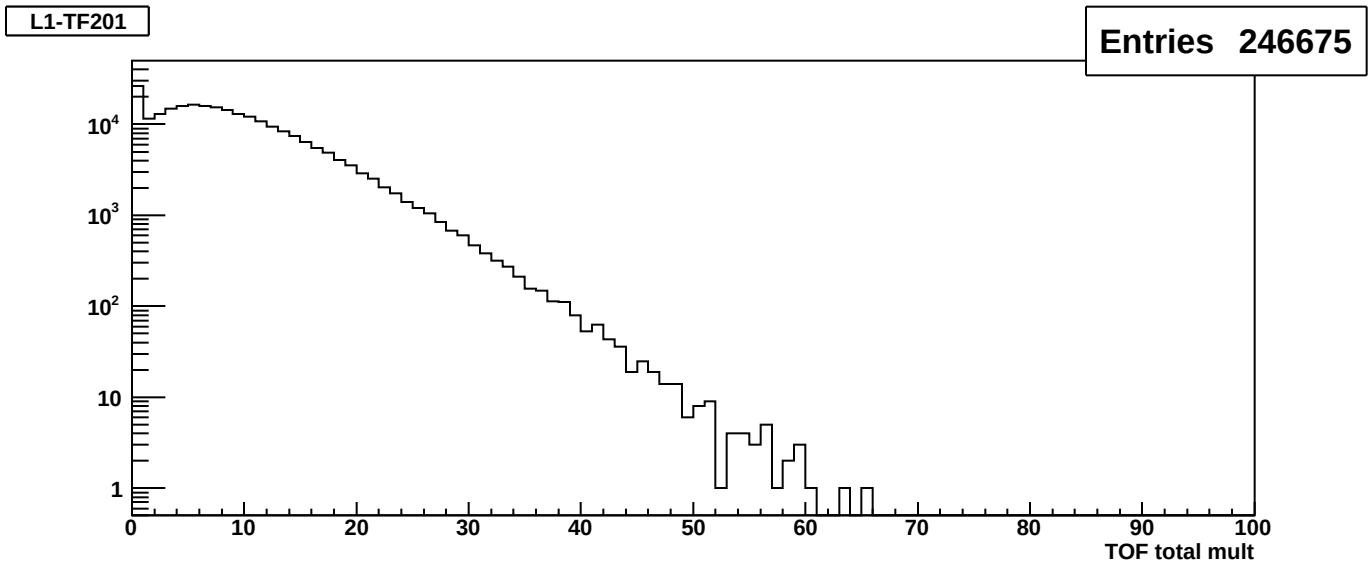

MIX-TF003
Entries 1.036035e+07
TOF MULT
TOF tray

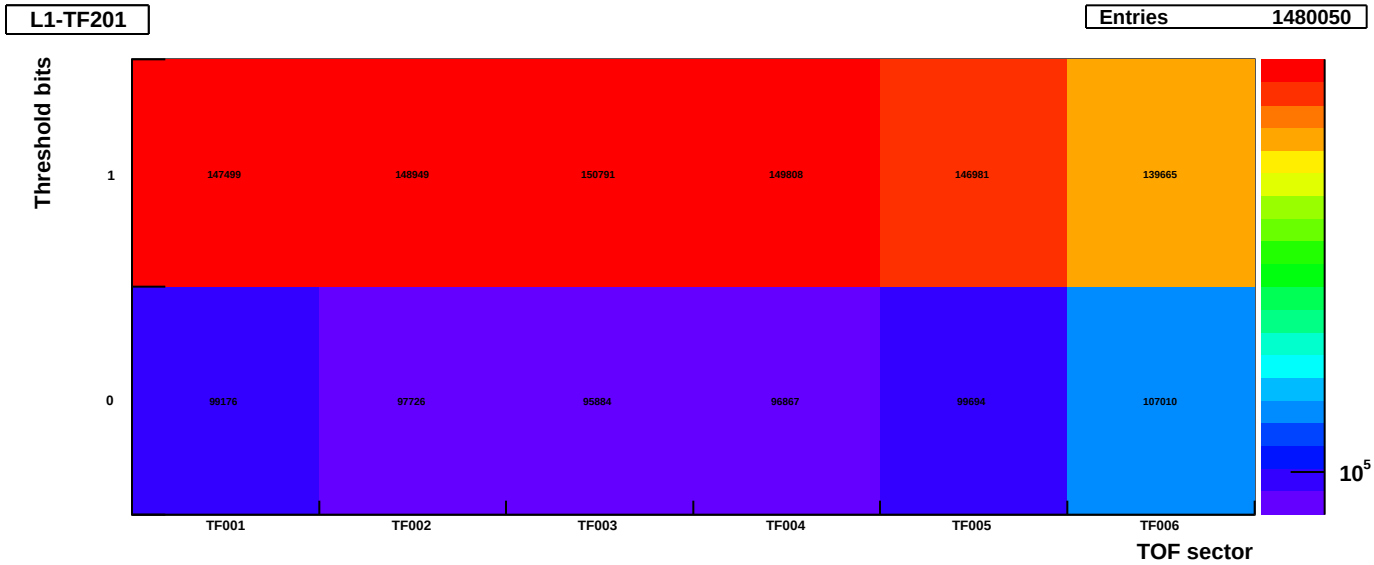

MIX-TF004
Entries 1.036035e+07
TOF MULT
TOF tray

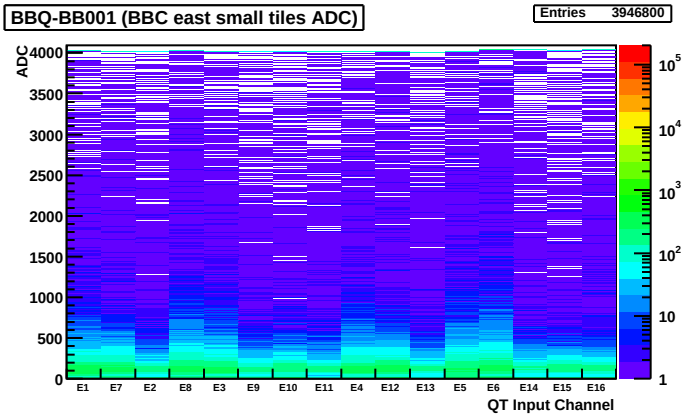

MIX-TF005
Entries 1.036035e+07
TOF MULT
TOF tray

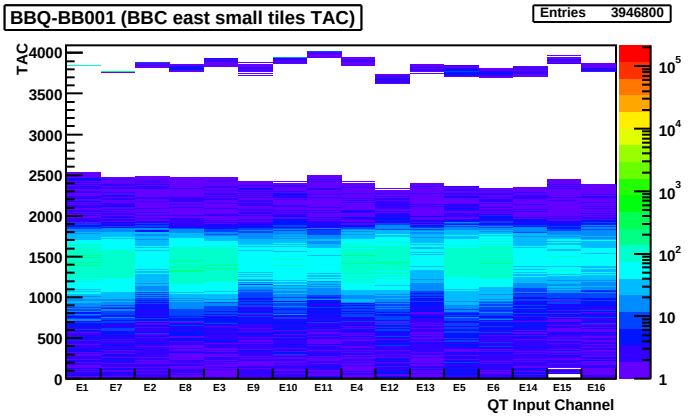

MIX-TF006
Entries 1.036035e+07
TOF MULT
TOF tray

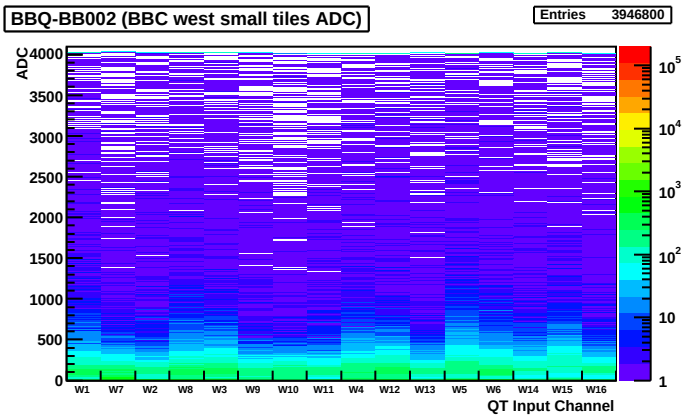

MIX-TF101
Entries 1480050
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

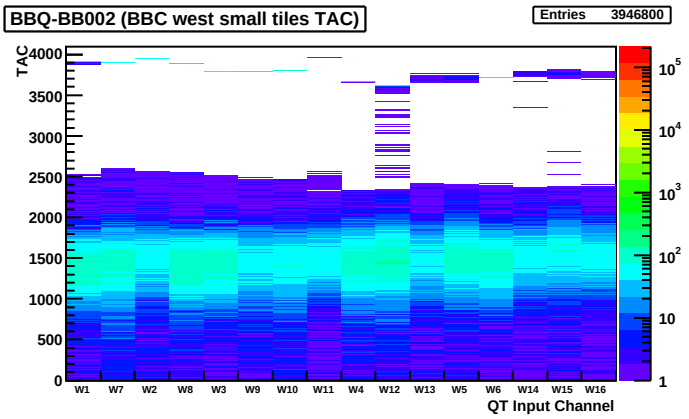

L1-TF201
Entries 246675
TOF total mult

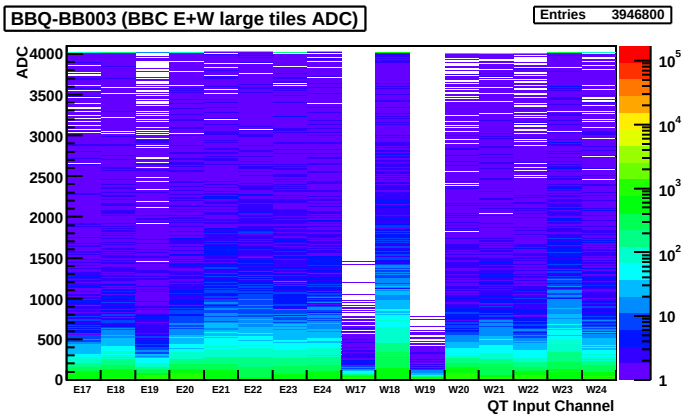

L1-TF201
Entries 1480050
Threshold bits
147499
148949
150791
149808
146981
139665
99176
97726
95884
96867
99694
107010
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

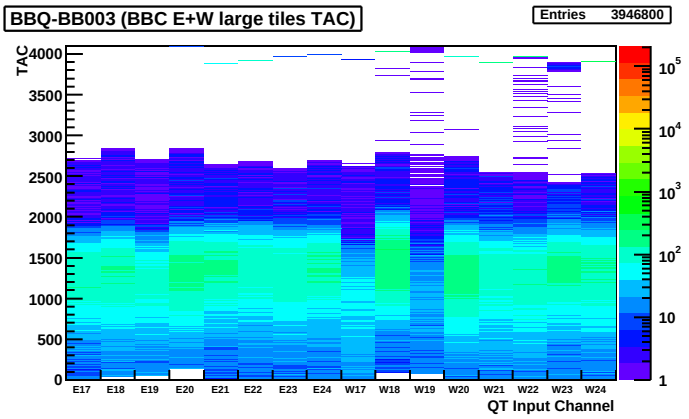

BBQ-BB001 (BBC east small tiles ADC)
Entries 3946800
ADC
QT Input Channel

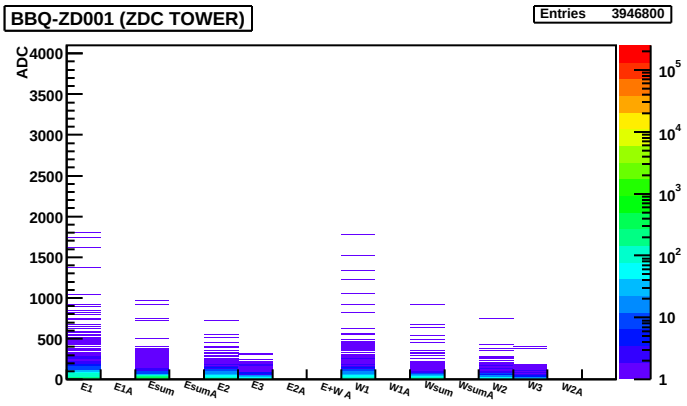

BBQ-BB001 (BBC east small tiles TAC)
Entries 3946800
TAC
QT Input Channel

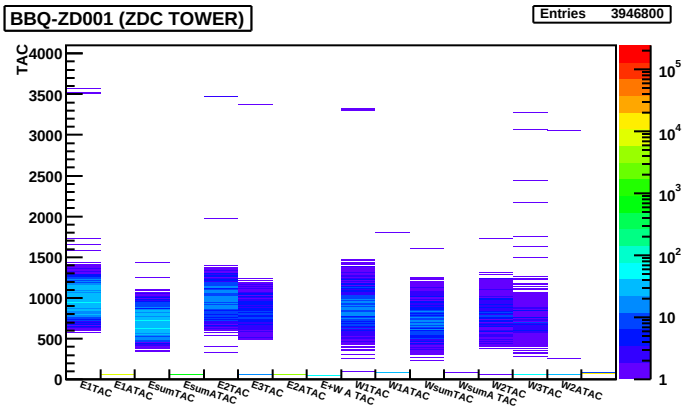

BBQ-BB002 (BBC west small tiles ADC)
Entries 3946800
ADC
QT Input Channel

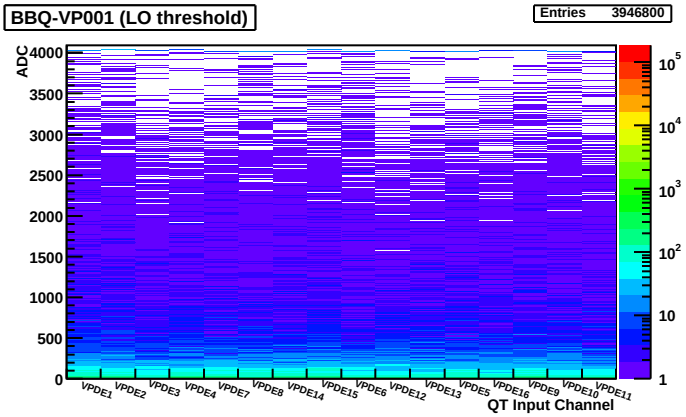

BBQ-BB002 (BBC west small tiles TAC)
Entries 3946800
TAC
QT Input Channel

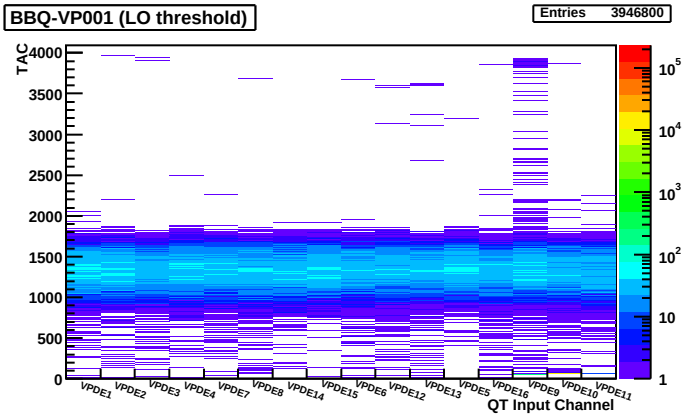

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3946800
ADC
QT Input Channel

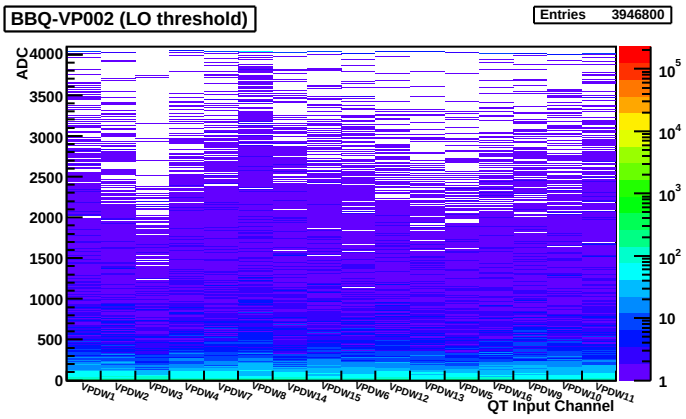

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3946800
TAC
QT Input Channel

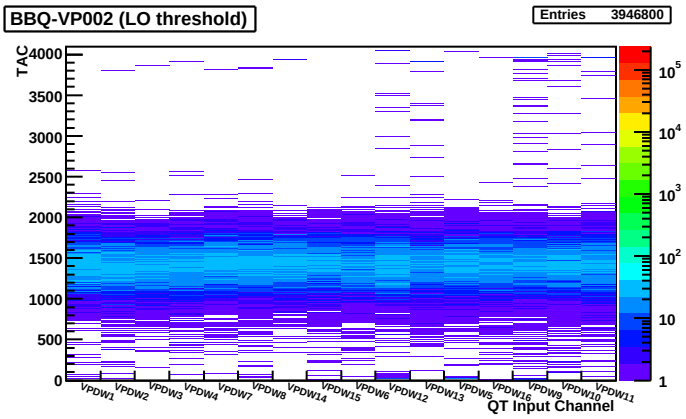

BBQ-ZD001 (ZDC TOWER)
Entries 3946800
ADC

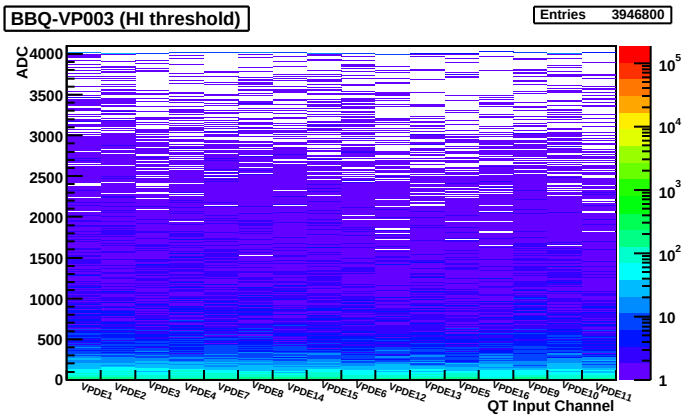

BBQ-ZD001 (ZDC TOWER)
Entries 3946800
TAC

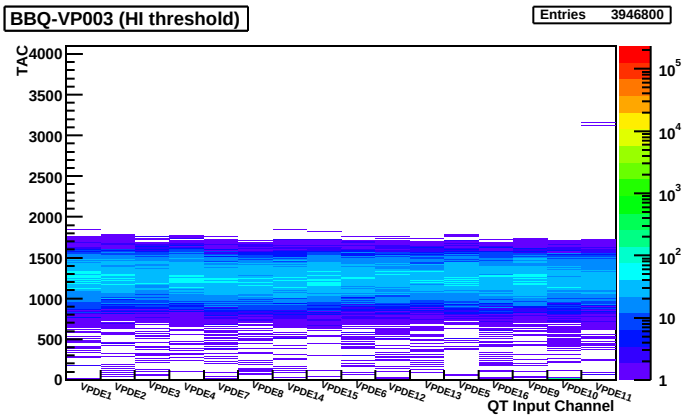

BBQ-VP001 (LO threshold)
Entries 3946800
ADC
QT Input Channel

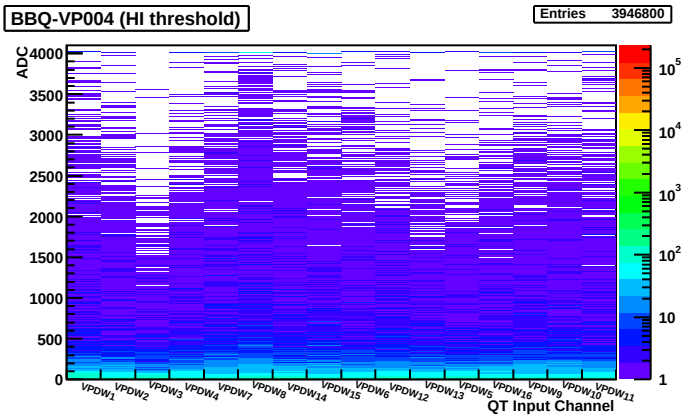

BBQ-VP001 (LO threshold)
Entries 3946800
TAC
QT Input Channel

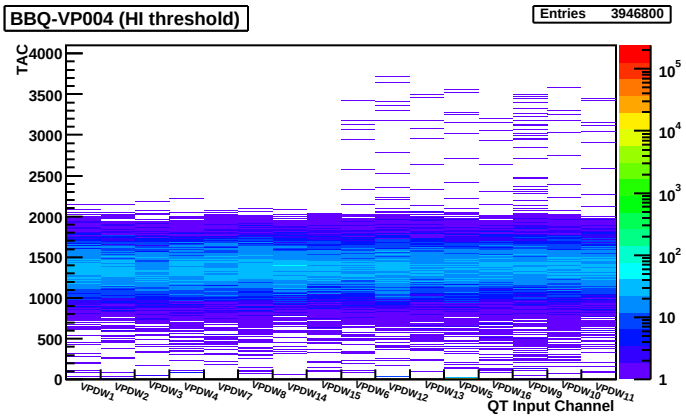

BBQ-VP002 (LO threshold)
Entries 3946800
ADC
QT Input Channel

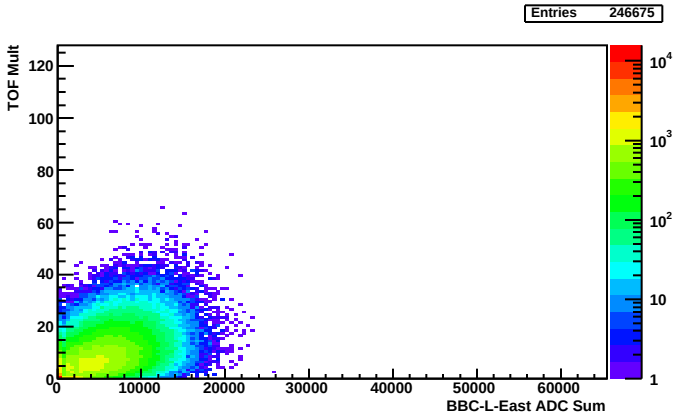

BBQ-VP002 (LO threshold)
Entries 3946800
TAC
QT Input Channel

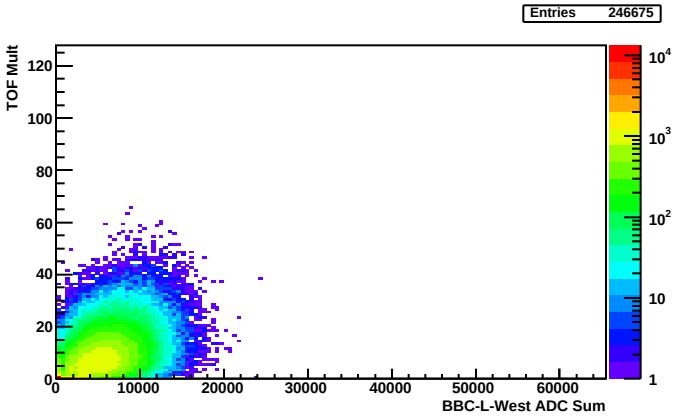

BBQ-VP003 (HI threshold)
Entries 3946800
ADC
QT Input Channel

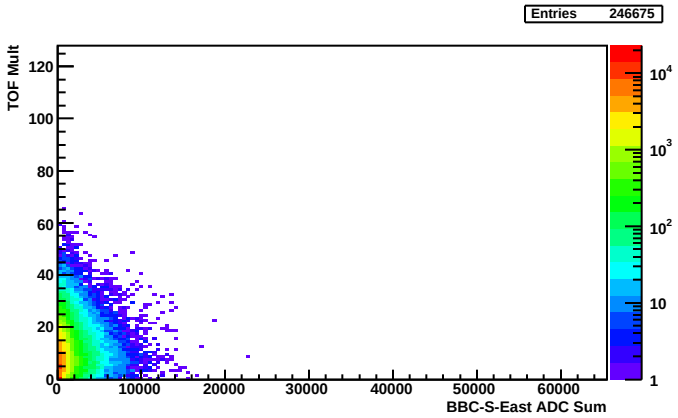

BBQ-VP003 (HI threshold)
Entries 3946800
TAC
QT Input Channel

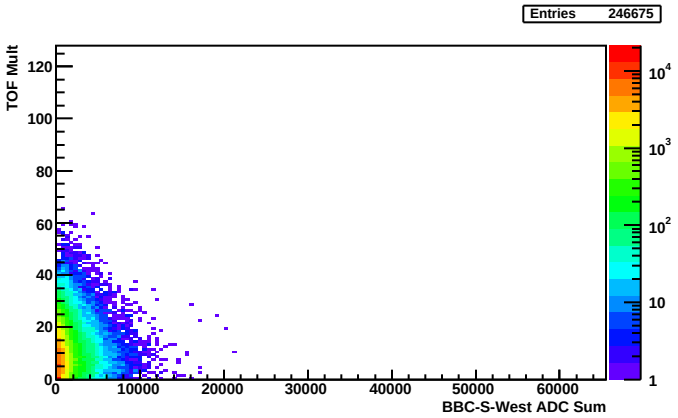

BBQ-VP004 (HI threshold)
Entries 3946800
ADC
QT Input Channel

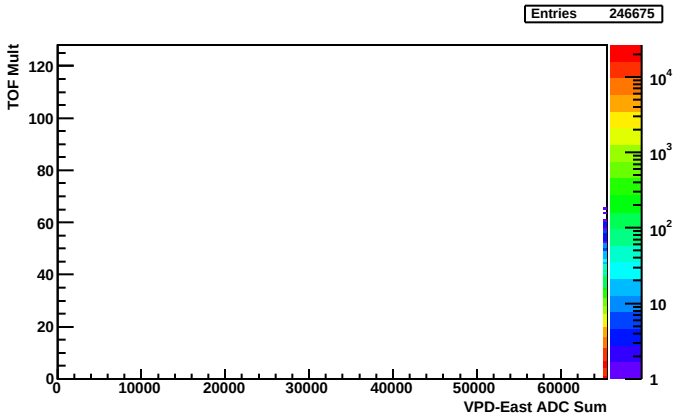

BBQ-VP004 (HI threshold)
Entries 3946800
TAC
QT Input Channel

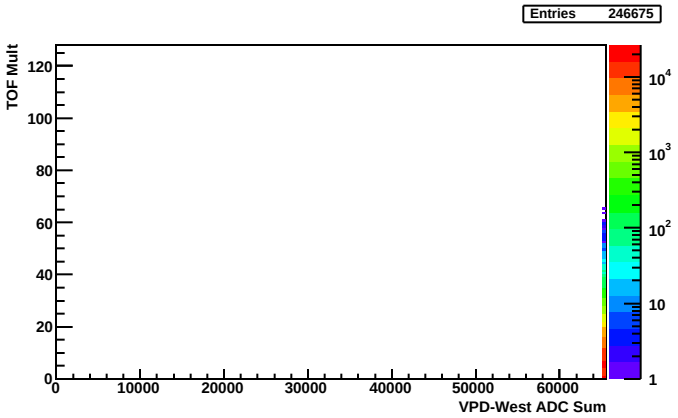

Entries 246675
TOF Mult
BBC-L-East ADC Sum

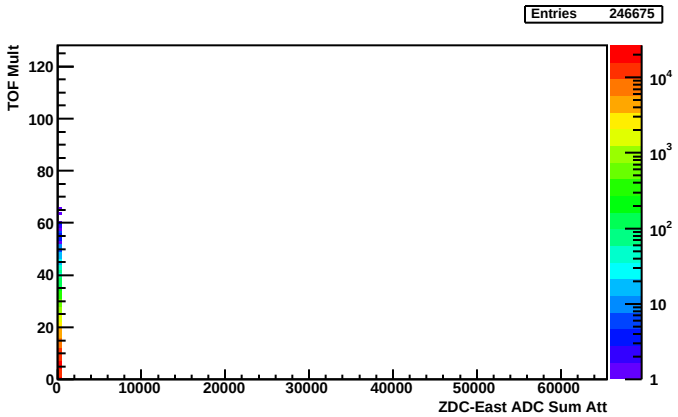

Entries 246675
TOF Mult
BBC-L-West ADC Sum

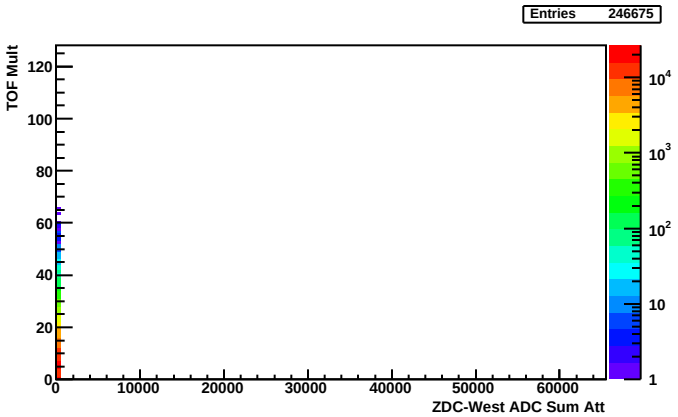

Entries 246675
TOF Mult
BBC-S-East ADC Sum

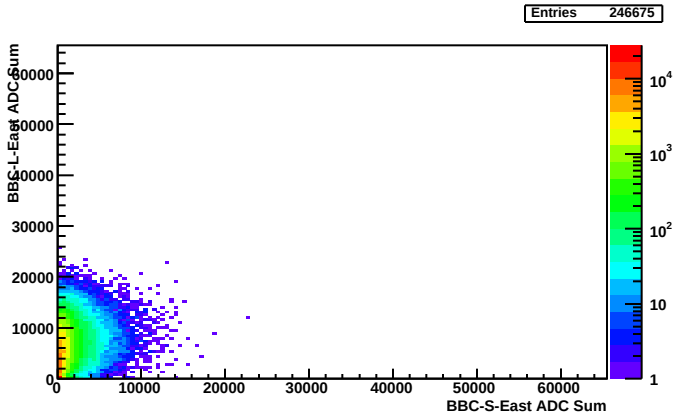

Entries 246675
TOF Mult
BBC-S-West ADC Sum

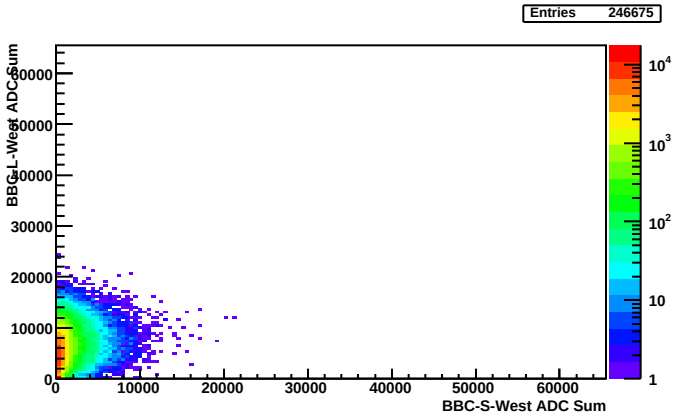

Entries 246675
TOF Mult
VPD-East ADC Sum

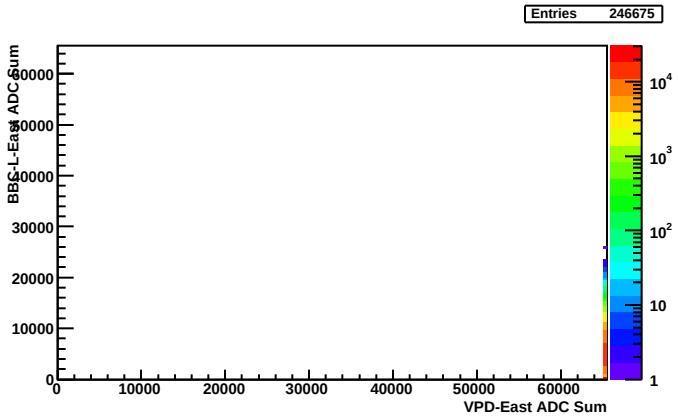

Entries 246675
TOF Mult
VPD-West ADC Sum

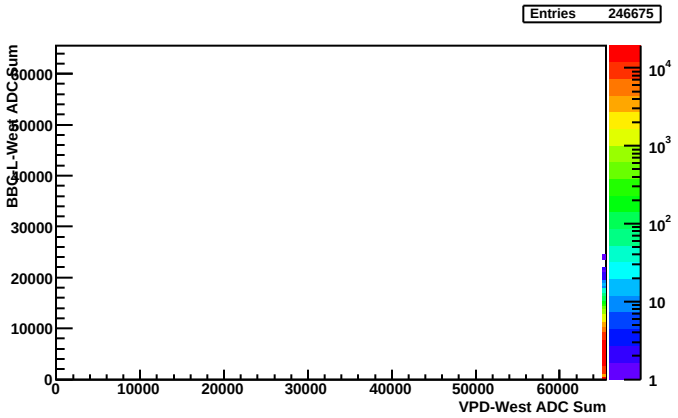

Entries 246675
TOF Mult
ZDC-East ADC Sum Att

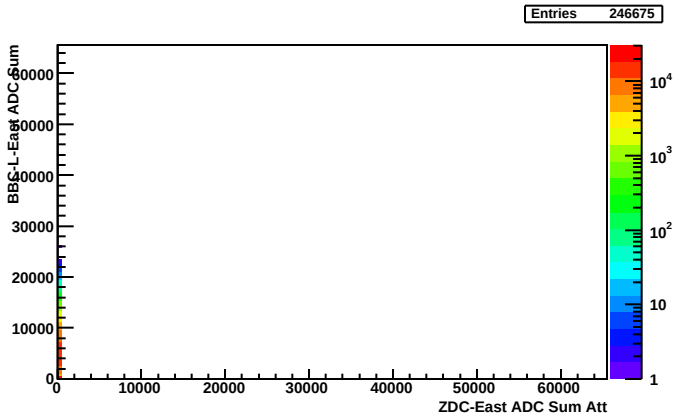

Entries 246675
TOF Mult
ZDC-West ADC Sum Att

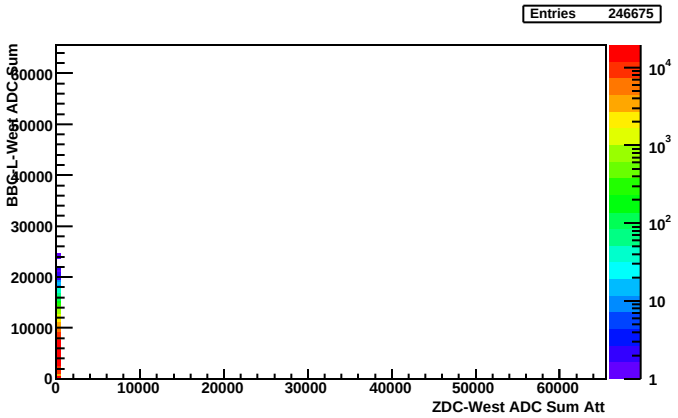

Entries 246675
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 246675
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 246675
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 246675
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 246675
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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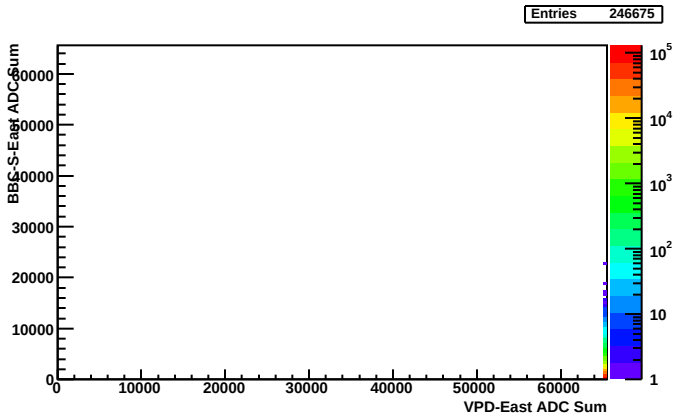
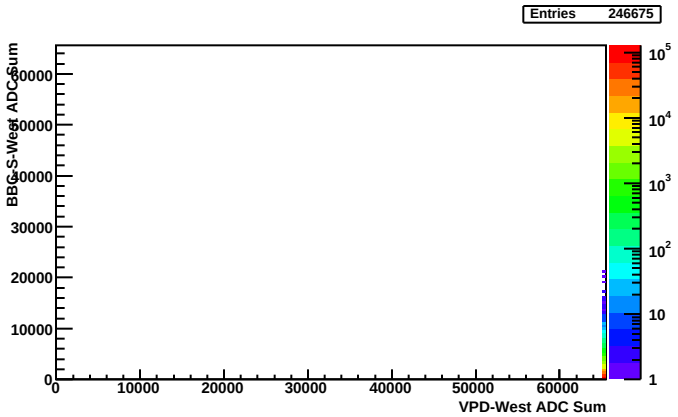
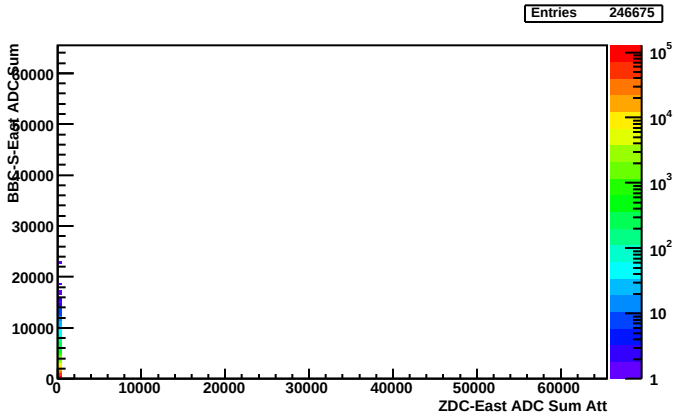
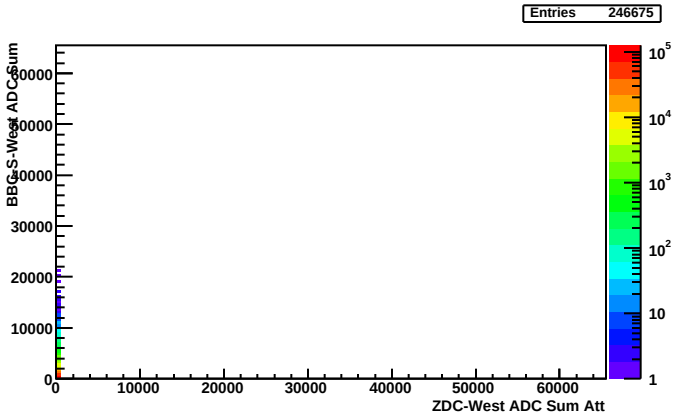
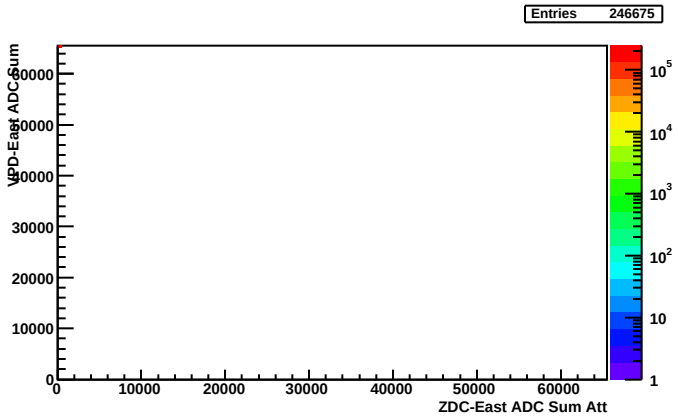
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

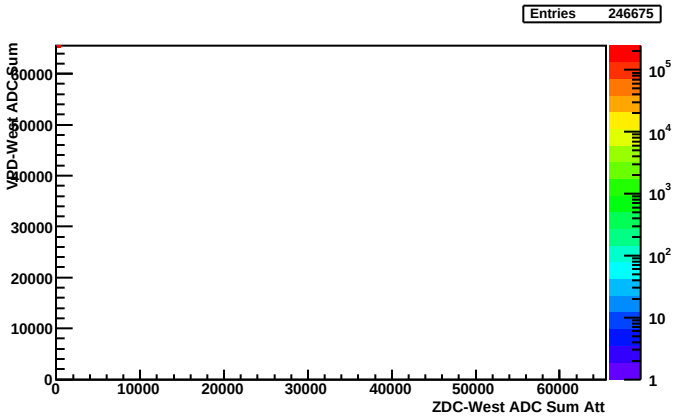

Entries 246675
BBC-S-East ADC Sum
VPD-East ADC Sum

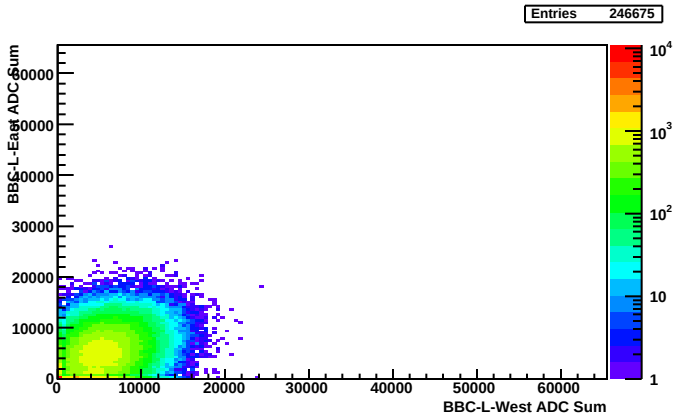

Entries 246675
BBC-S-West ADC Sum
VPD-West ADC Sum

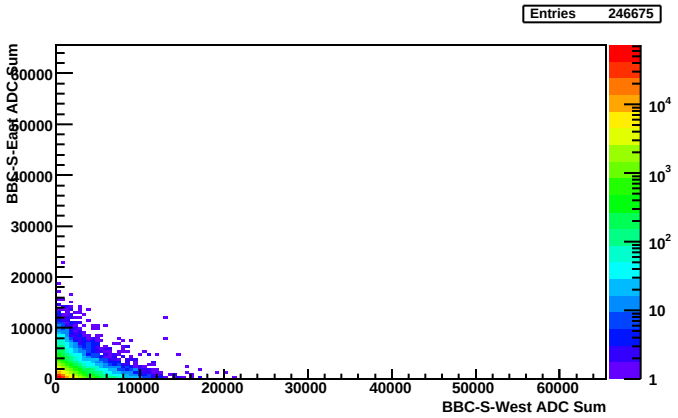

Entries 246675
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

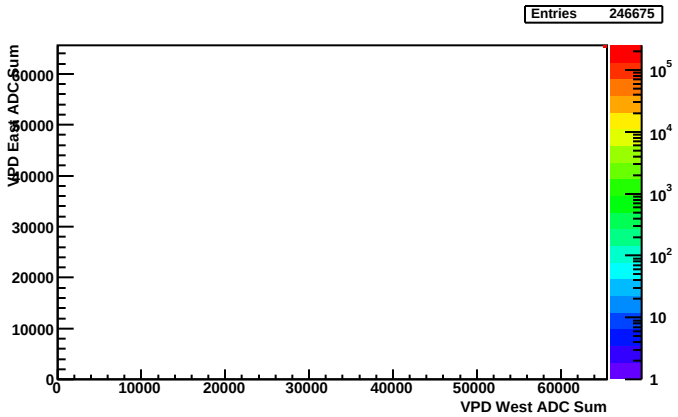

Entries 246675
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

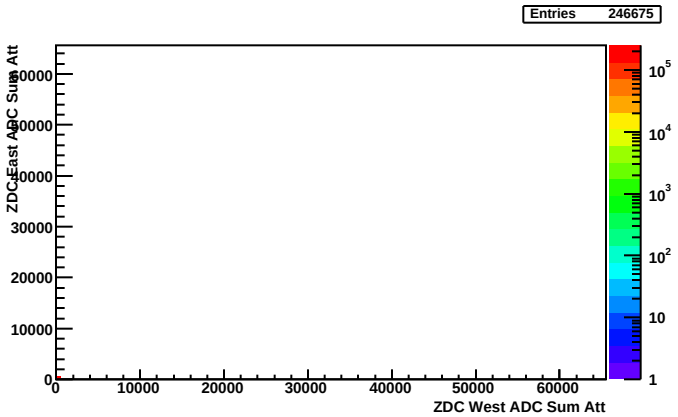

Entries 246675
VPD-East ADC Sum
ZDC-East ADC Sum Att

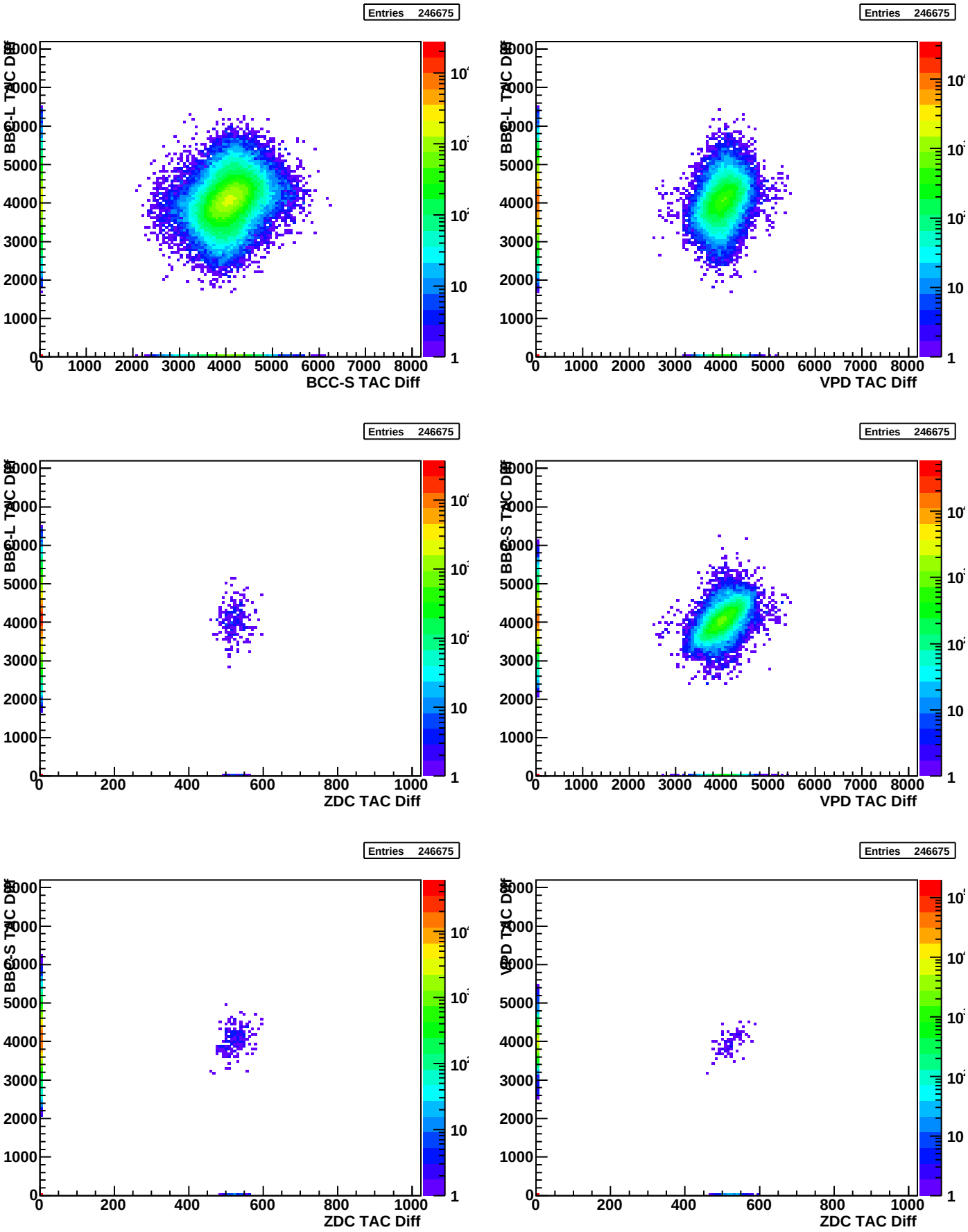

Entries 246675
VPD-West ADC Sum
ZDC-West ADC Sum Att

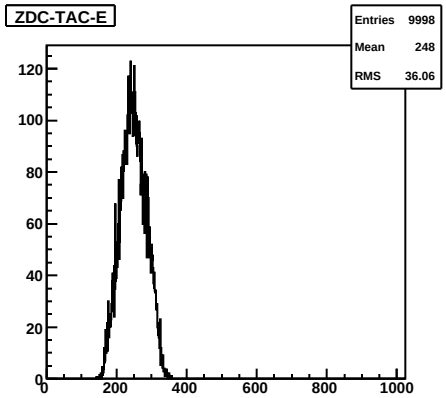

Entries 246675
BBC-L-East ADC Sum
BBC-L-West ADC Sum

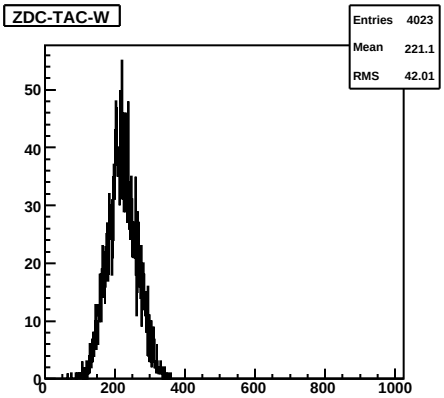

Entries 246675
BBC-S-East ADC Sum
BBC-S-West ADC Sum

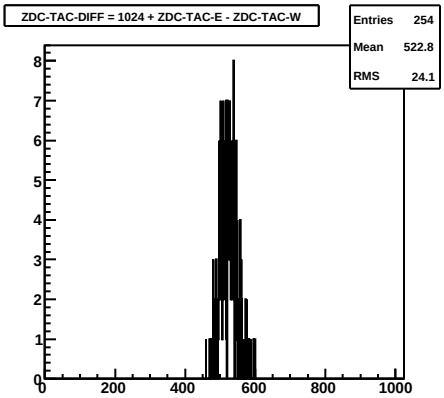

Entries 246675
VPD East ADC Sum
VPD West ADC Sum

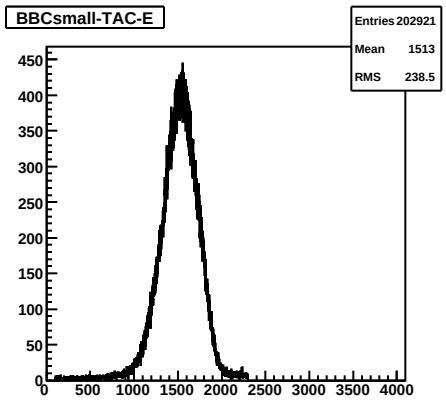

Entries 246675
ZDC East ADC Sum Att
ZDC West ADC Sum Att

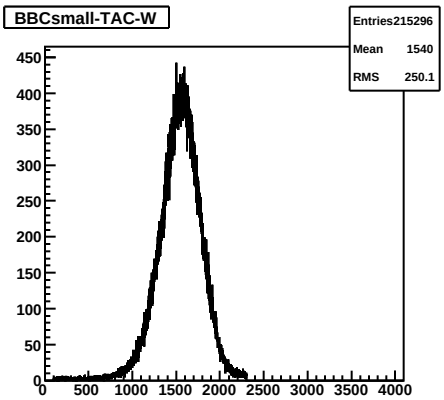

Entries 246675
BBC-L TAC Diff
BCC-S TAC Diff
Entries 246675
BBC-L TAC Diff
VPD TAC Diff
Entries 246675
BBC-L TAC Diff
ZDC TAC Diff
Entries 246675
BBC-S TAC Diff
VPD TAC Diff
Entries 246675
BBC-S TAC Diff
ZDC TAC Diff
Entries 246675
VPD TAC Diff
ZDC TAC Diff

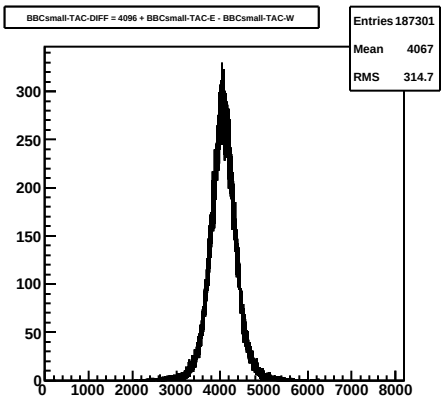

ZDC-TAC-E
Entries 9998
Mean 248
RMS 36.06

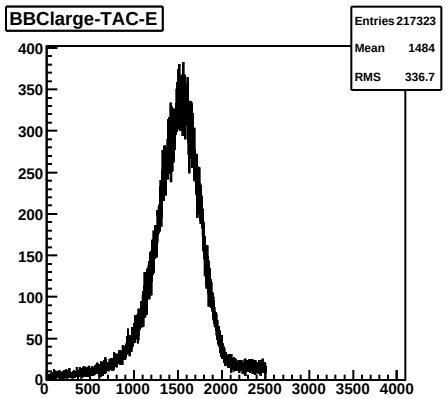

ZDC-TAC-W
Entries 4023
Mean 221.1
RMS 42.01

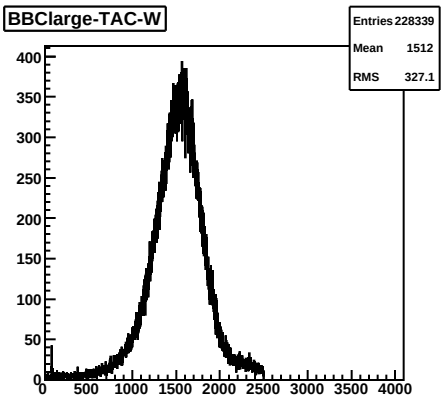

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 254
Mean 522.8
RMS 24.1

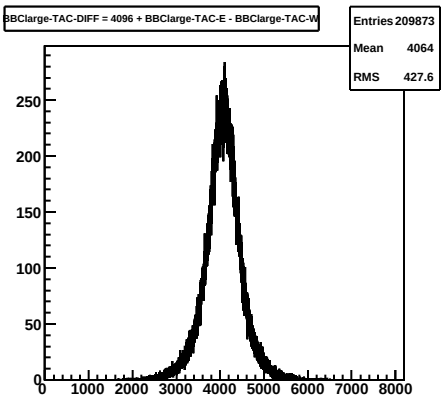

BBCsmall-TAC-E
Entries 202921
Mean 1513
RMS 238.5

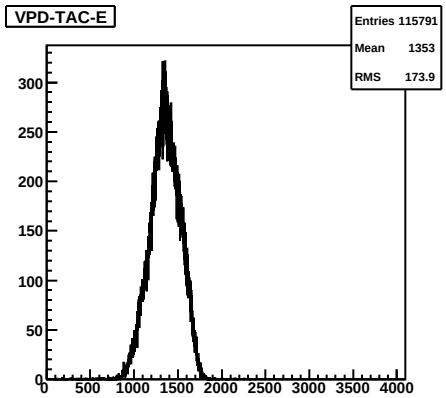

BBCsmall-TAC-W
Entries 215296
Mean 1540
RMS 250.1

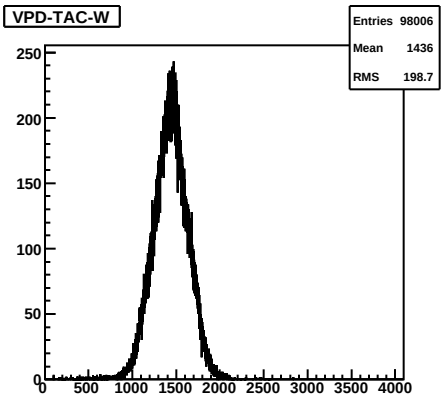

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 187301
Mean 4067
RMS 314.7

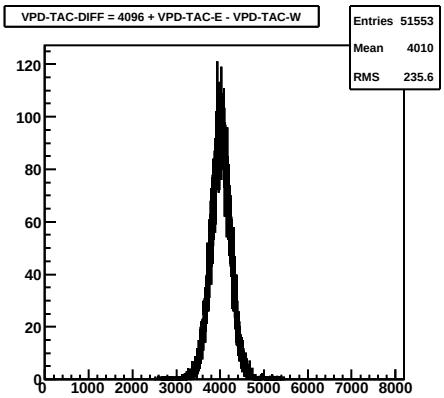

BBClarge-TAC-E
Entries 217323
Mean 1484
RMS 336.7

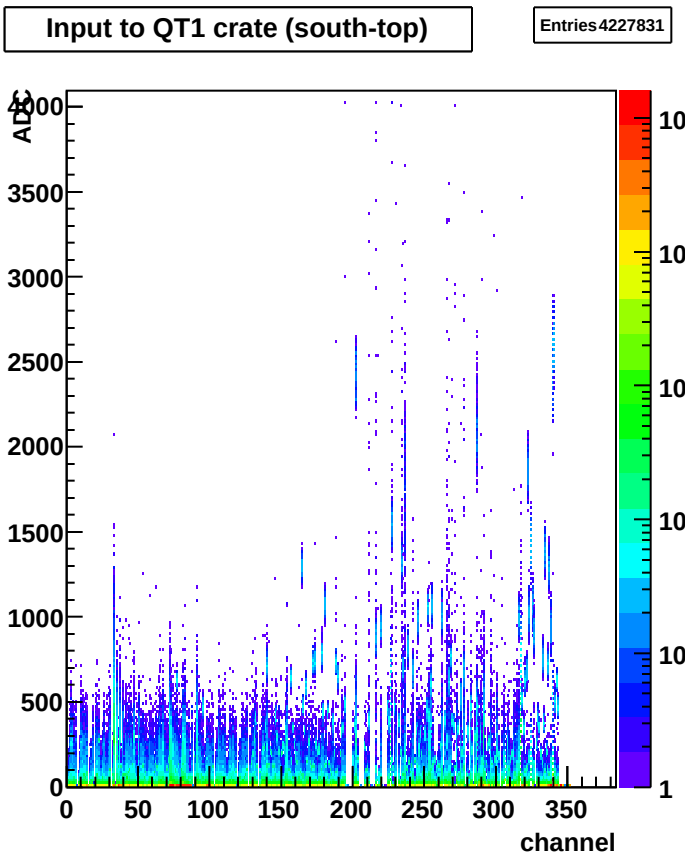

BBClarge-TAC-W
Entries 228339
Mean 1512
RMS 327.1

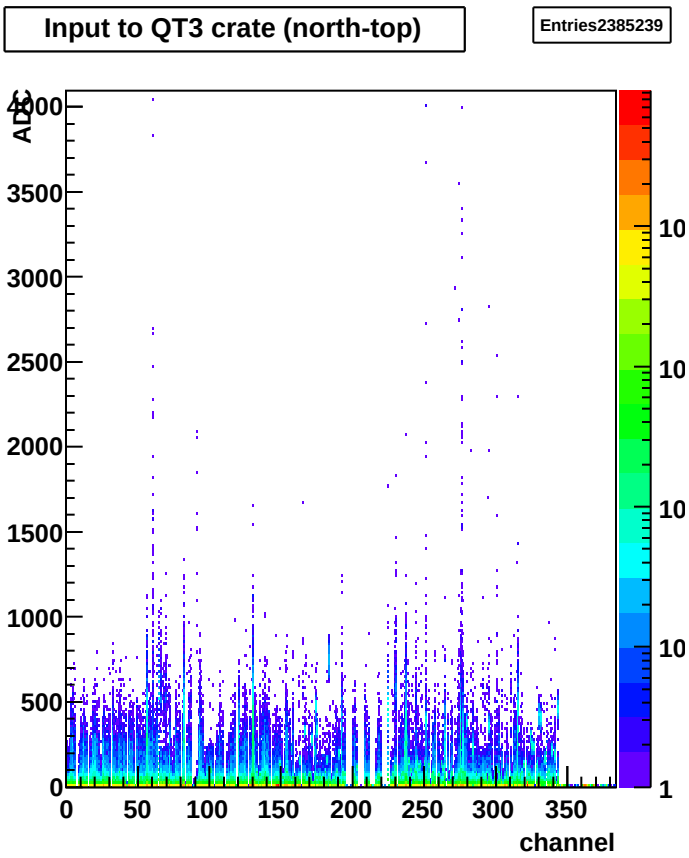

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 209873
Mean 4064
RMS 427.6

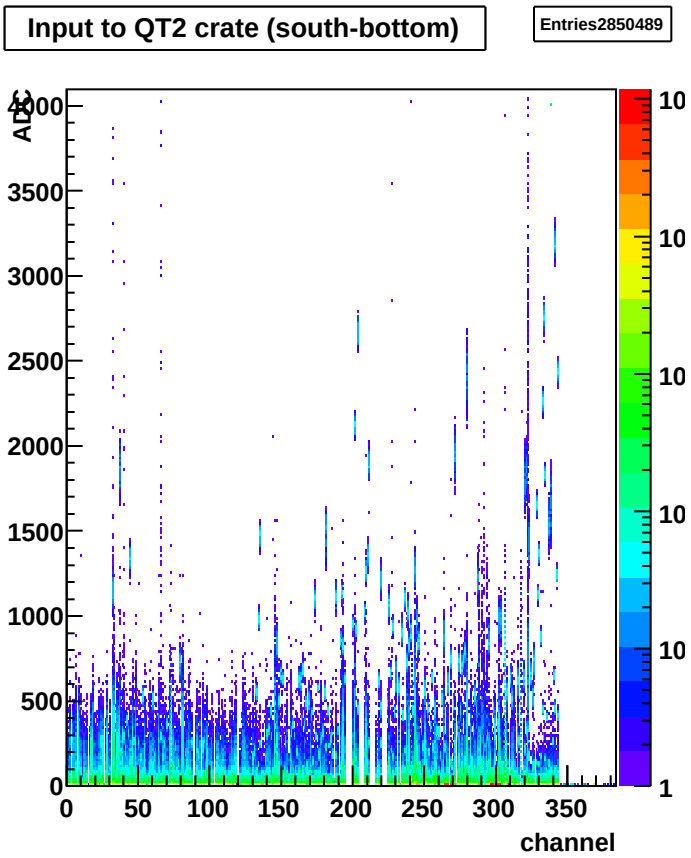

VPD-TAC-E
Entries 115791
Mean 1353
RMS 173.9

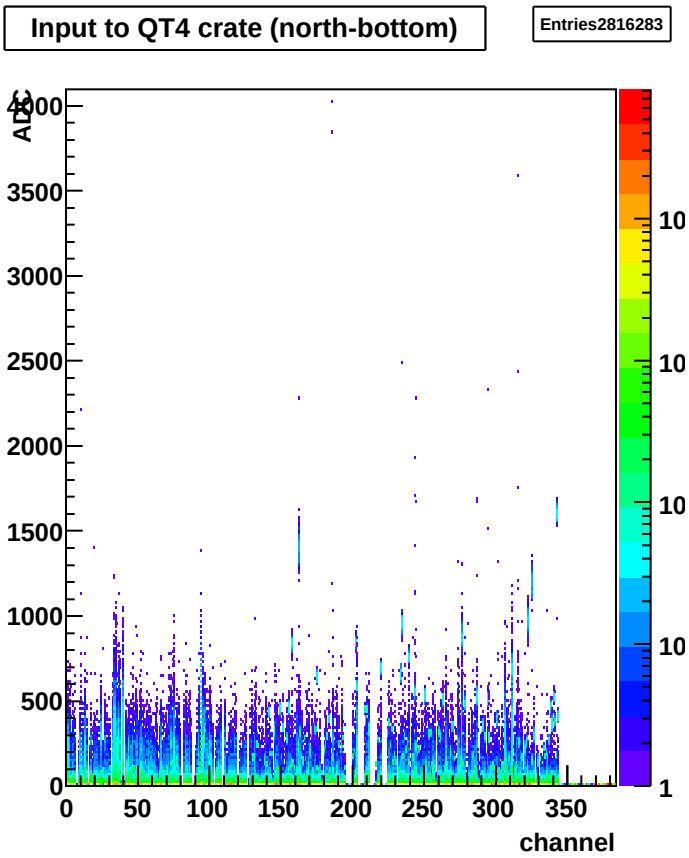

VPD-TAC-W
Entries 98006
Mean 1436
RMS 198.7

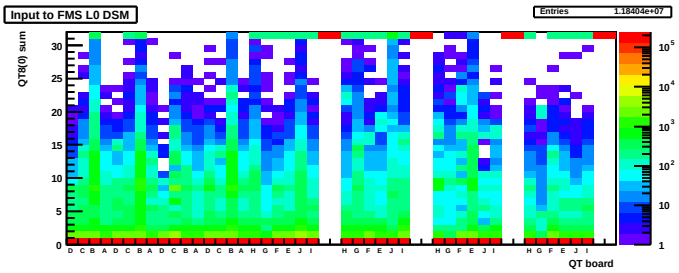

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 51553
Mean 4010
RMS 235.6

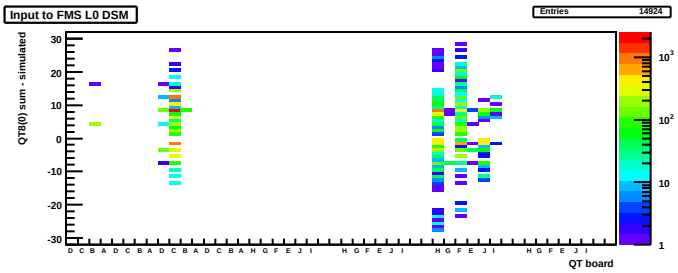

Input to QT1 crate (south-top)
Entries4227831
ADC
channel

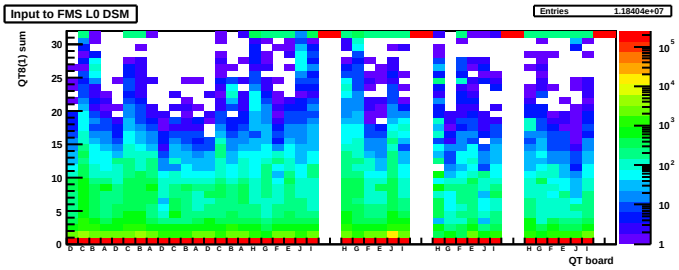

Input to QT3 crate (north-top)
Entries2385239
ADC
channel

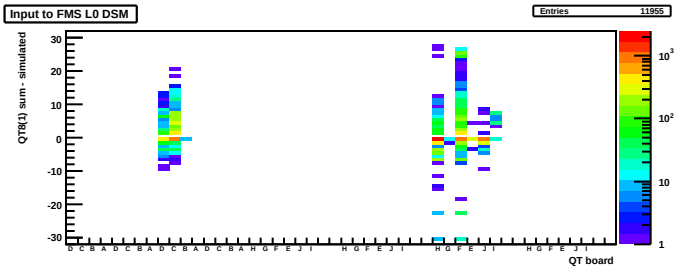

Input to QT2 crate (south-bottom)
Entries2850489
ADC
channel

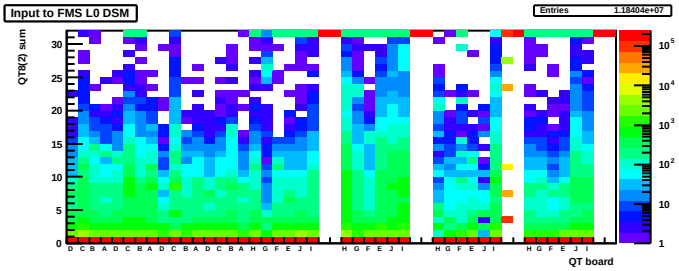

Input to QT4 crate (north-bottom)
Entries2816283
ADC
channel

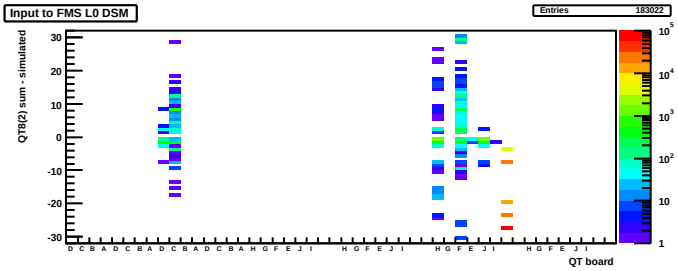

Input to FMS L0 DSM
Entries 1.18404e+07
QT8(0) sum
QT board

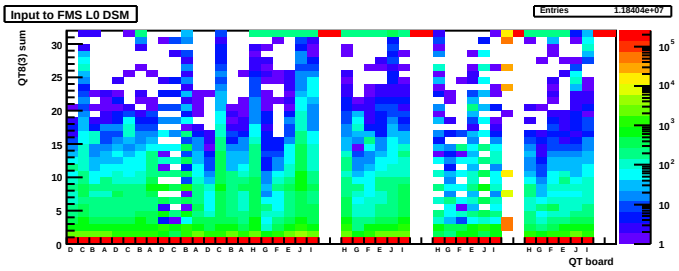

Input to FMS L0 DSM
Entries 14924
QT8(0) sum - simulated
QT board

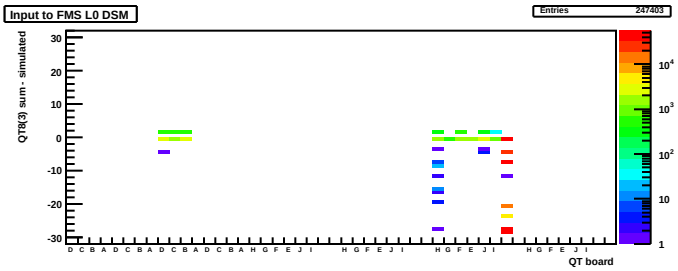

Input to FMS L0 DSM
Entries 1.18404e+07
QT8(1) sum
QT board

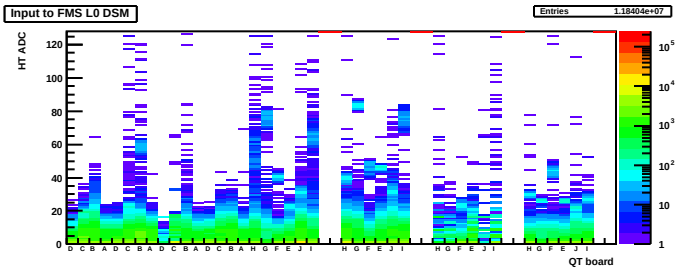

Input to FMS L0 DSM
Entries 11955
QT8(1) sum - simulated
QT board

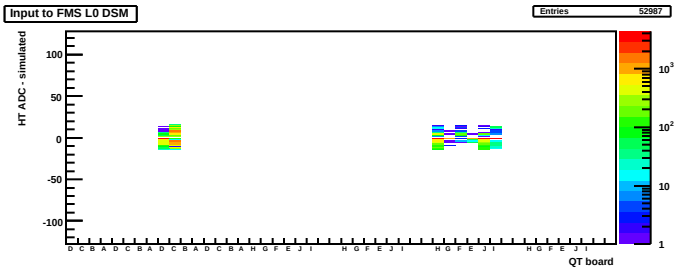

Input to FMS L0 DSM
Entries 1.18404e+07
QT8(2) sum
QT board

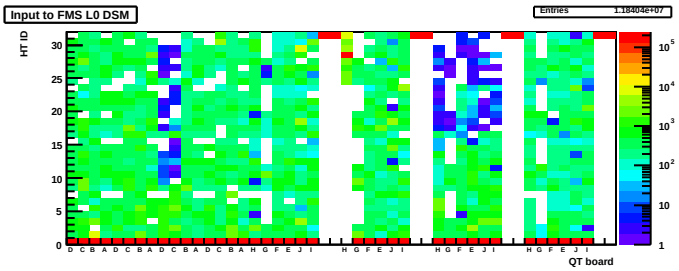

Input to FMS L0 DSM
Entries 183022
QT8(2) sum - simulated
QT board

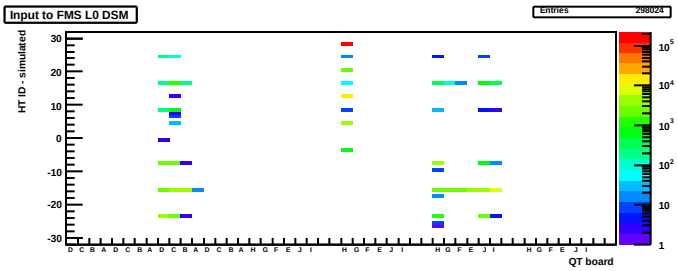

Input to FMS L0 DSM
Entries 1.18404e+07
QT8(3) sum
QT board

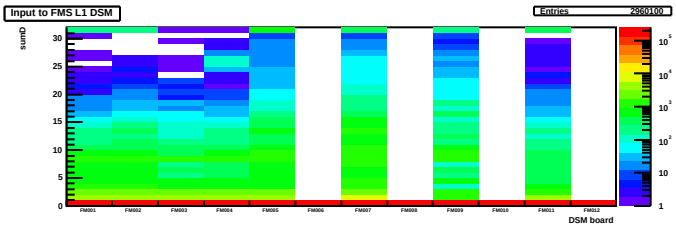

Input to FMS L0 DSM
Entries 247403
QT8(3) sum - simulated
QT board

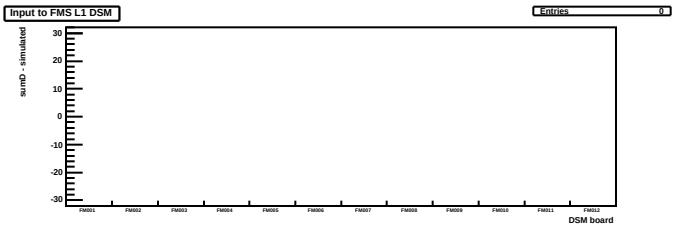

Input to FMS L0 DSM
Entries 1.18404e+07
HT ADC
QT board

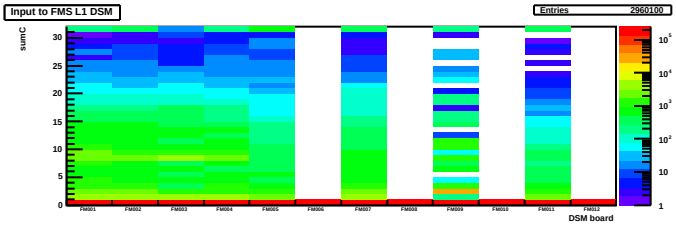

Input to FMS L0 DSM
Entries 52987
HT ADC - simulated
QT board

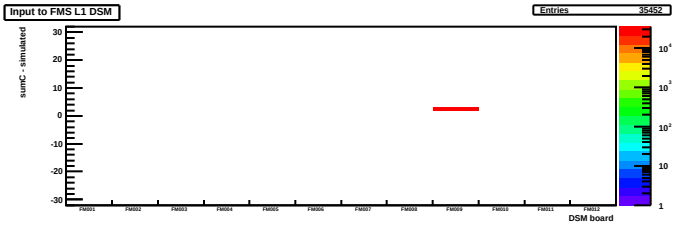

Input to FMS L0 DSM
Entries 1.18404e+07
HT ID
QT board

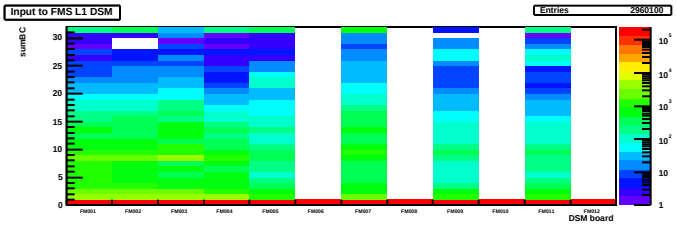

Input to FMS L0 DSM
Entries 298024
HT ID - simulated
QT board

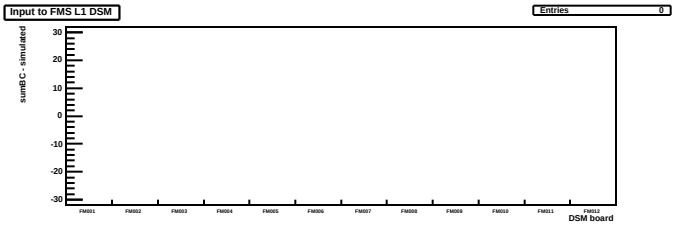

Input to FMS L1 DSM
Entries 2960100
sumD
DSM board

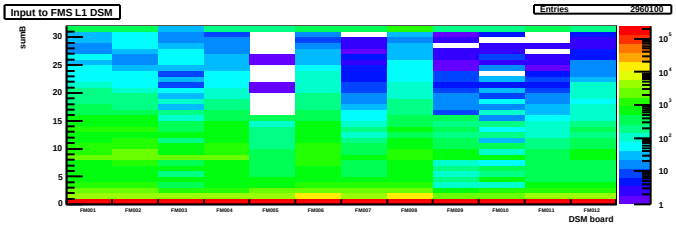

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

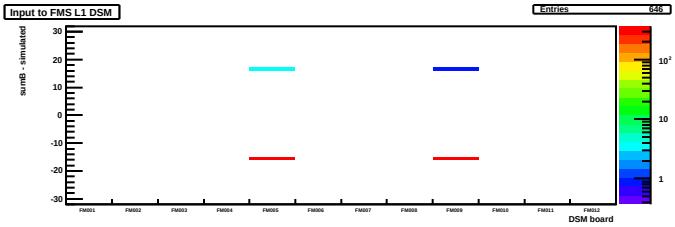

Input to FMS L1 DSM
Entries 2960100
sumC
DSM board

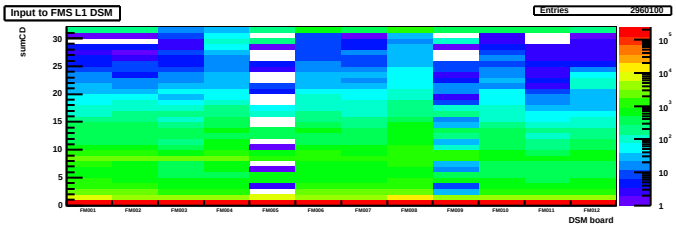

Input to FMS L1 DSM
Entries 35452
sumC - simulated
DSM board

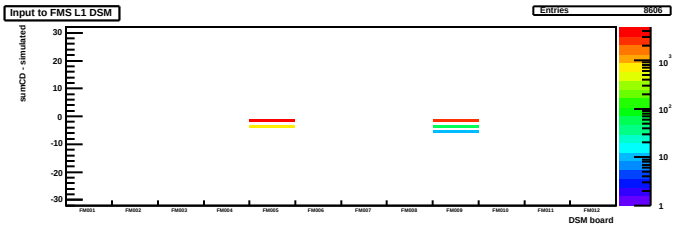

Input to FMS L1 DSM
Entries 2960100
sumBC
DSM board

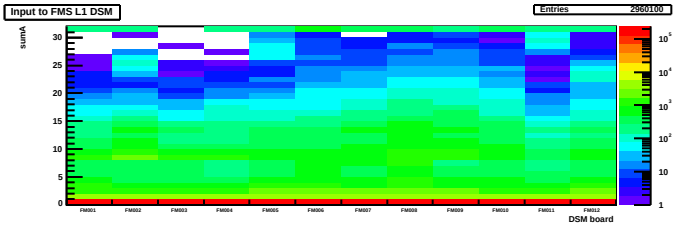

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

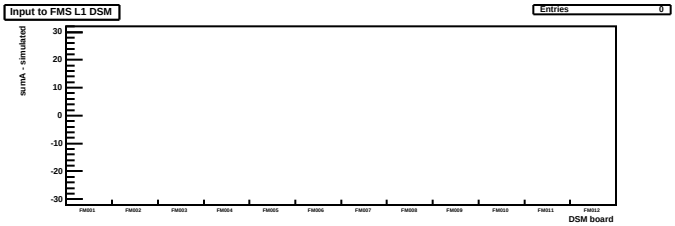

Input to FMS L1 DSM
Entries 2960100
sumB
DSM board

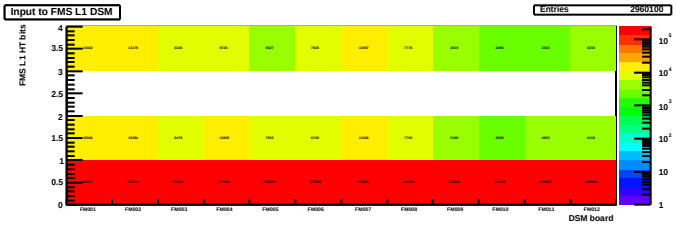

Input to FMS L1 DSM
Entries 646
sumB - simulated
DSM board

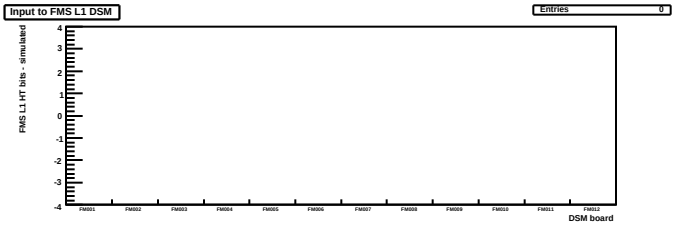

Input to FMS L1 DSM
Entries 2960100
sumCD
DSM board

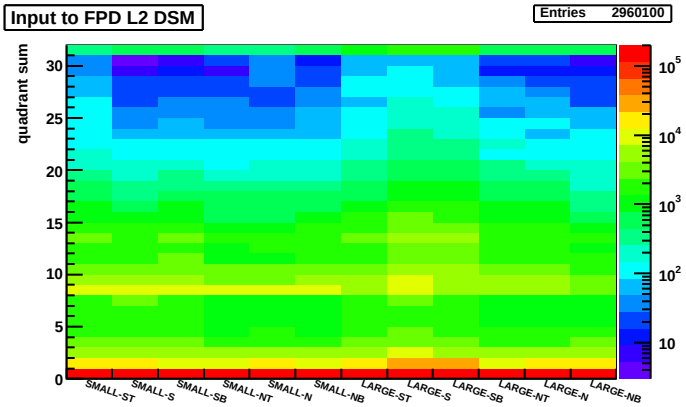

Input to FMS L1 DSM
Entries 8606
sumCD - simulated
DSM board

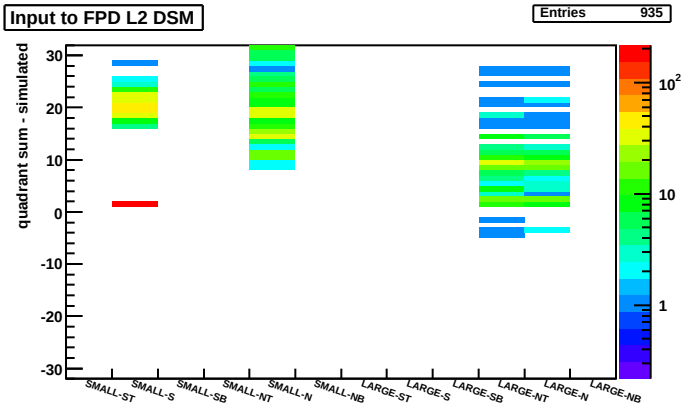

Input to FMS L1 DSM
Entries 2960100
sumA
DSM board

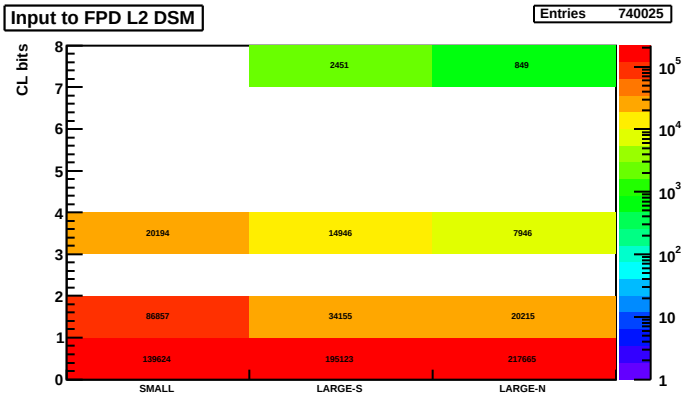

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

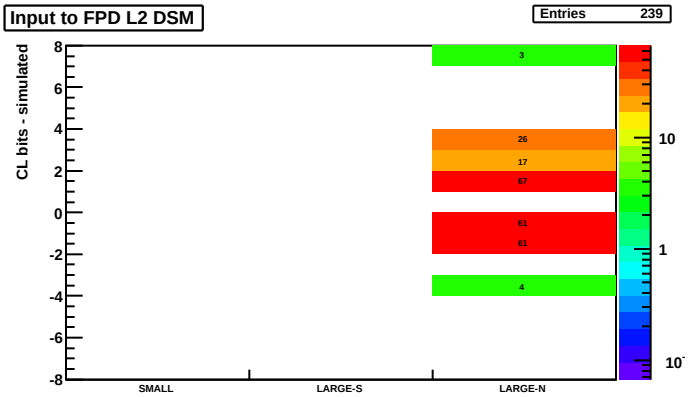

Input to FMS L1 DSM
Entries 2960100
FMS L1 HT bits
DSM board

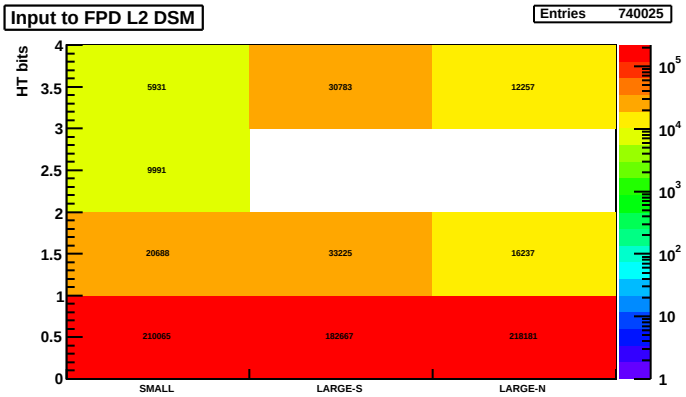

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

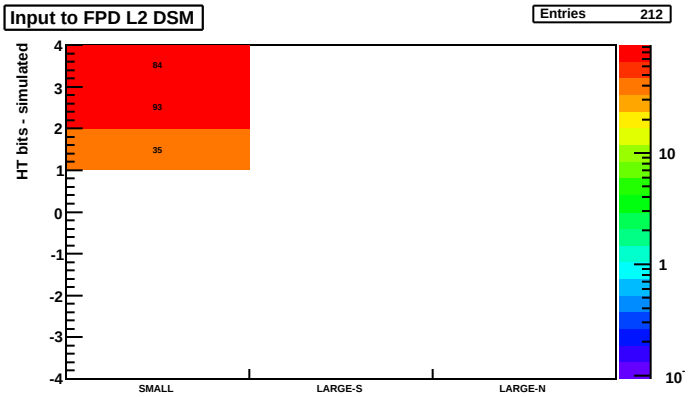

Input to FPD L2 DSM
Entries 2960100
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

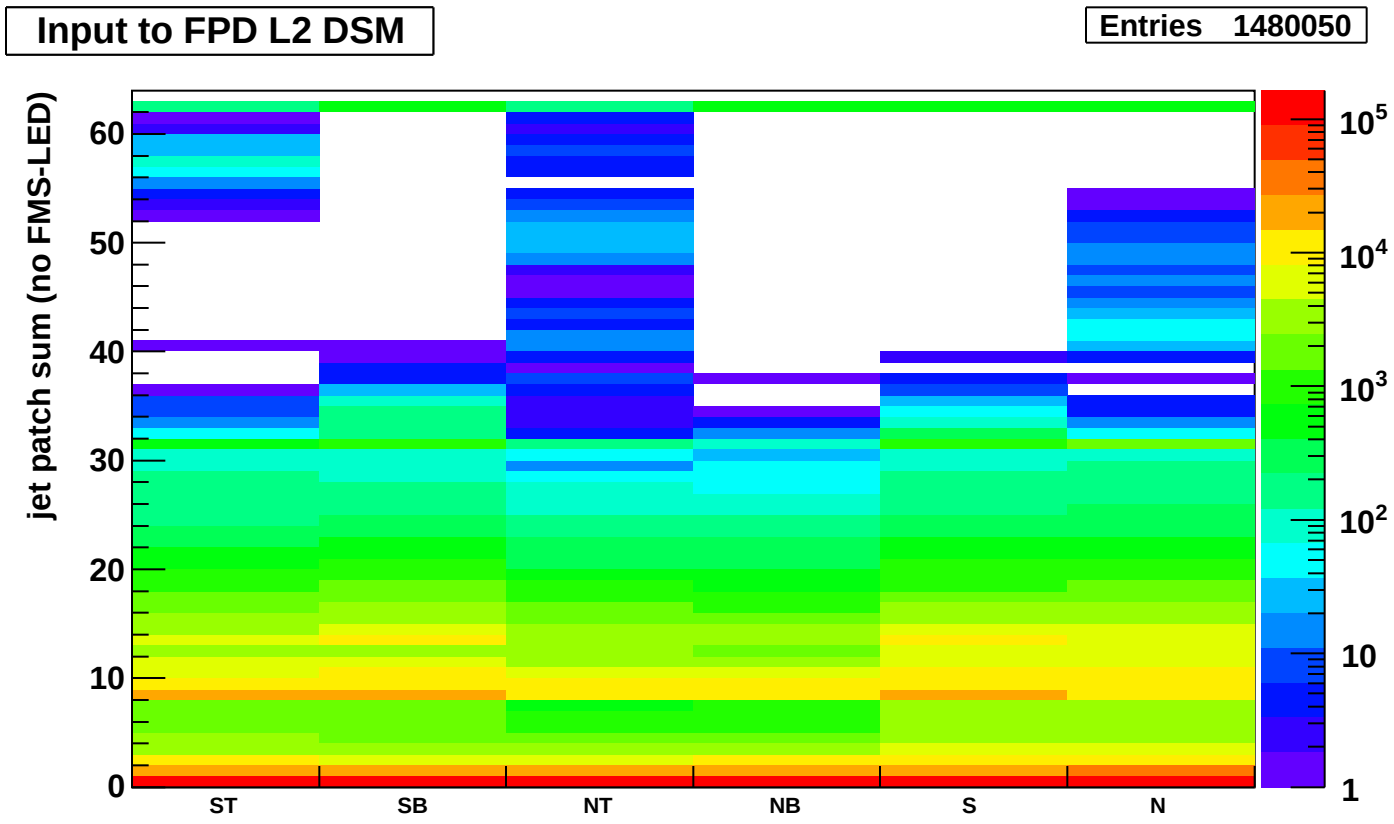

Input to FPD L2 DSM
Entries 935
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

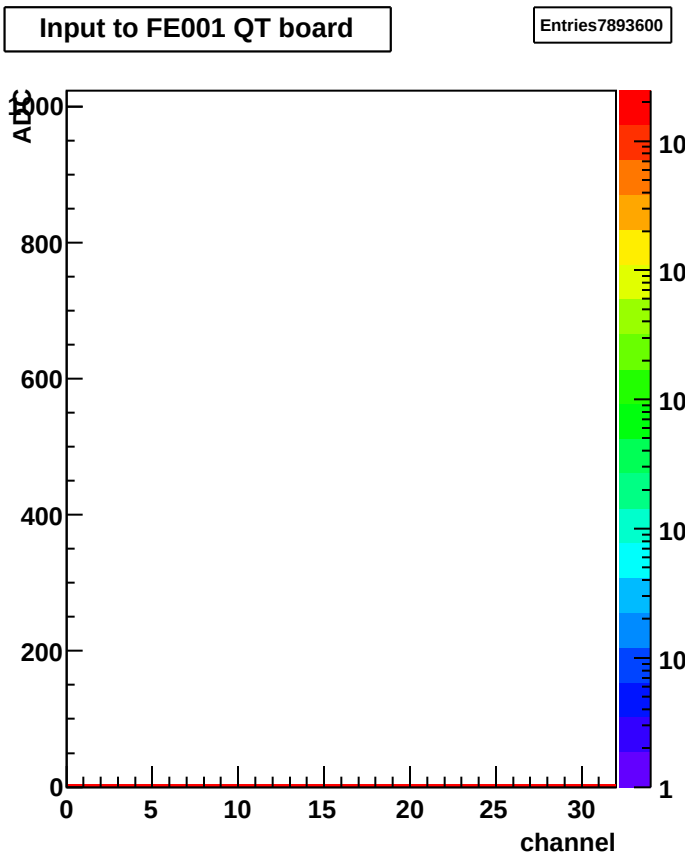

Input to FPD L2 DSM
Entries 740025
CL bits
2451
849
20194
14946
7946
86857
34155
20215
139624
195123
217665
SMALL
LARGE-S
LARGE-N

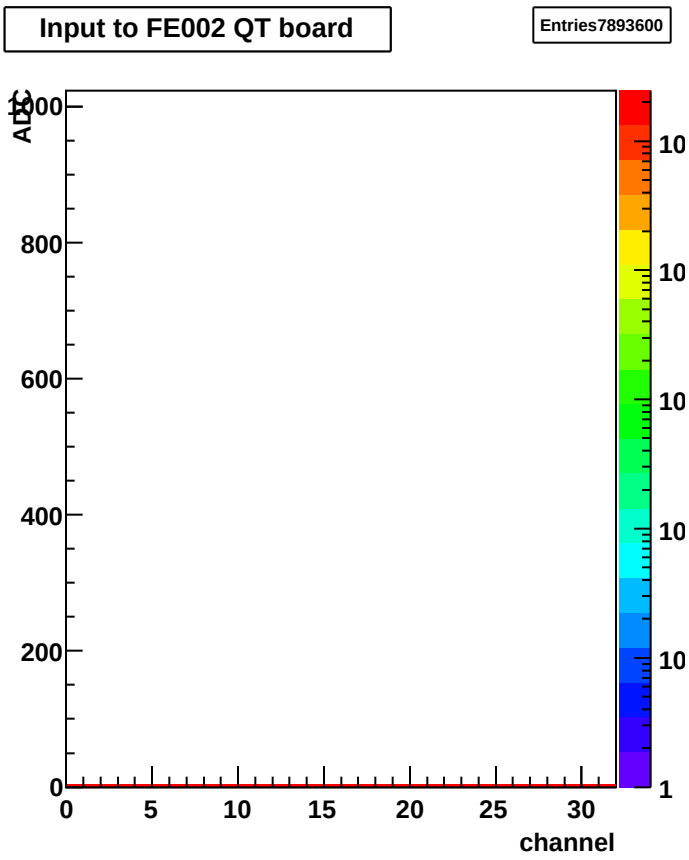

Input to FPD L2 DSM
Entries 239
CL bits - simulated
3
26
17
67
61
61
4
SMALL
LARGE-S
LARGE-N

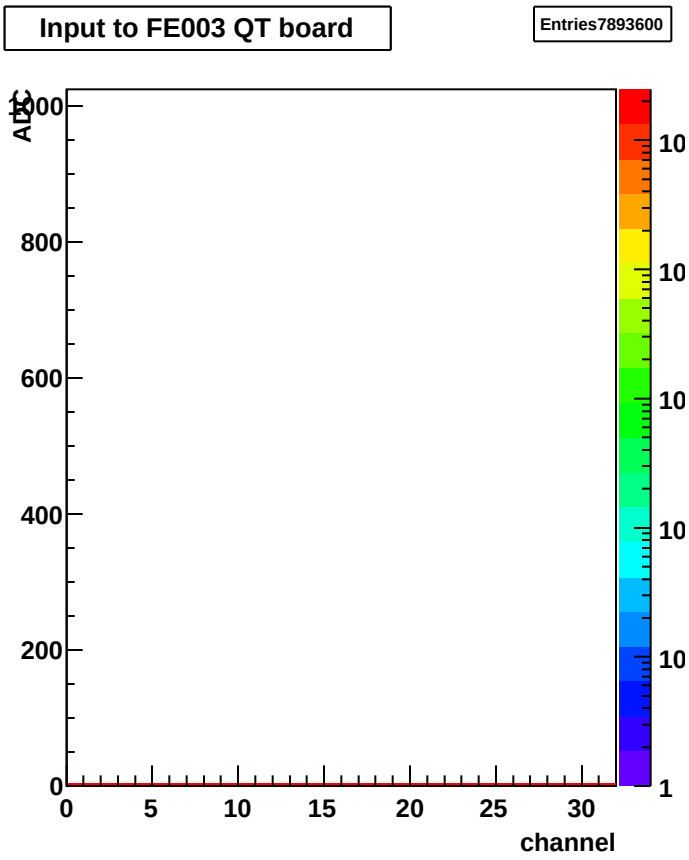

Input to FPD L2 DSM
Entries 740025
HT bits
5931
30783
12257
9991
20688
33225
16237
210065
182667
218181
SMALL
LARGE-S
LARGE-N

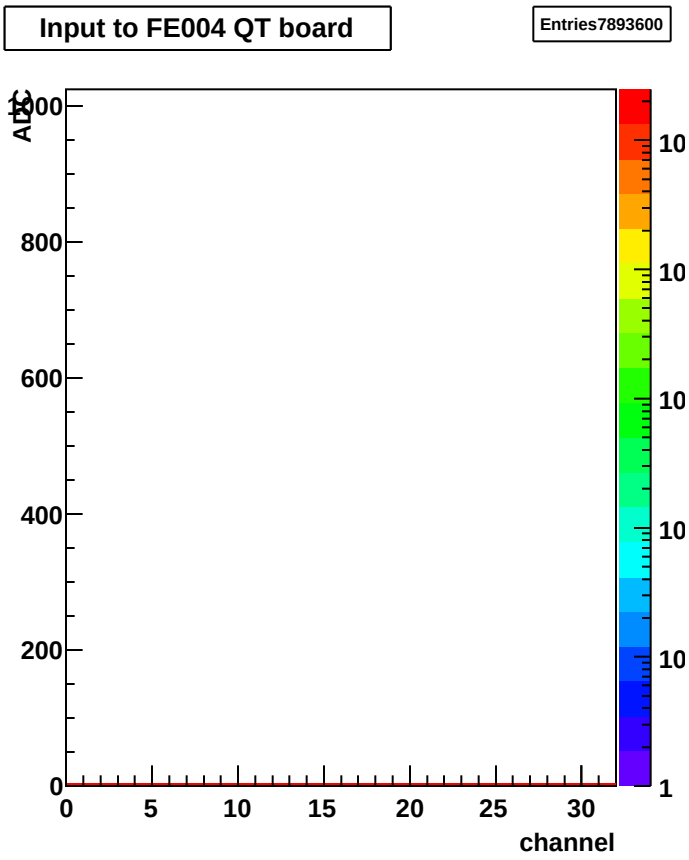

Input to FPD L2 DSM
Entries 212
HT bits - simulated
84
93
35
SMALL
LARGE-S
LARGE-N

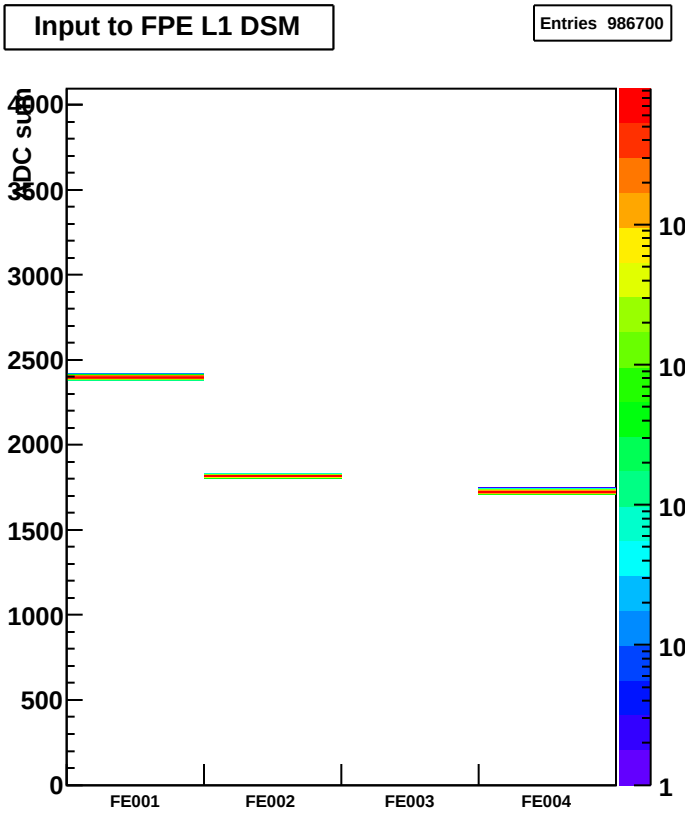

Input to FPD L2 DSM
Entries 1480050
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

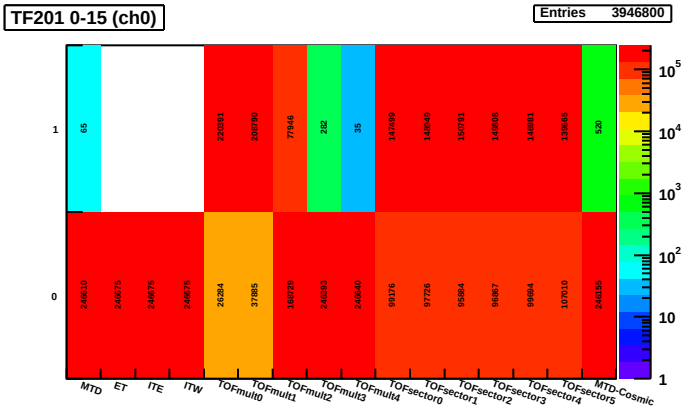

Input to FE001 QT board
Entries7893600
ADC
channel

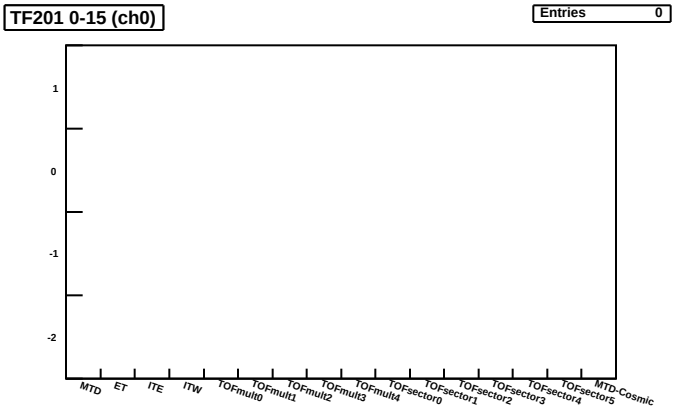

Input to FE002 QT board
Entries7893600
ADC
channel

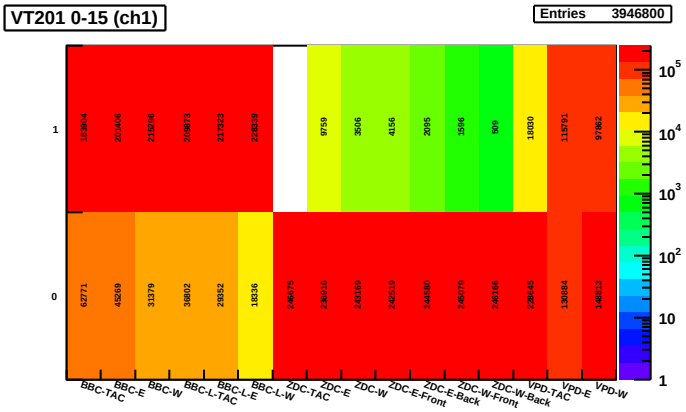

Input to FE003 QT board
Entries7893600
ADC
channel

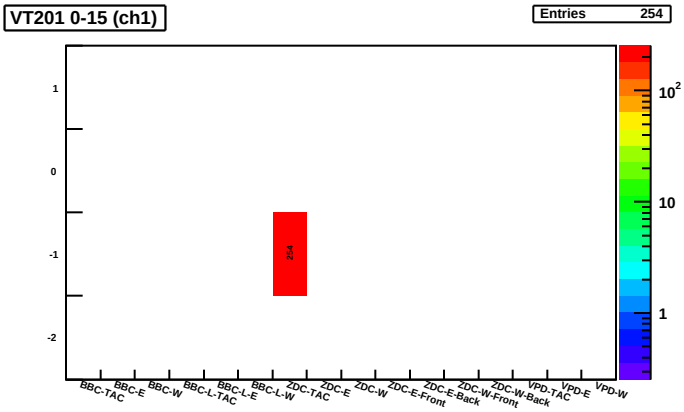

Input to FE004 QT board
Entries7893600
ADC
channel

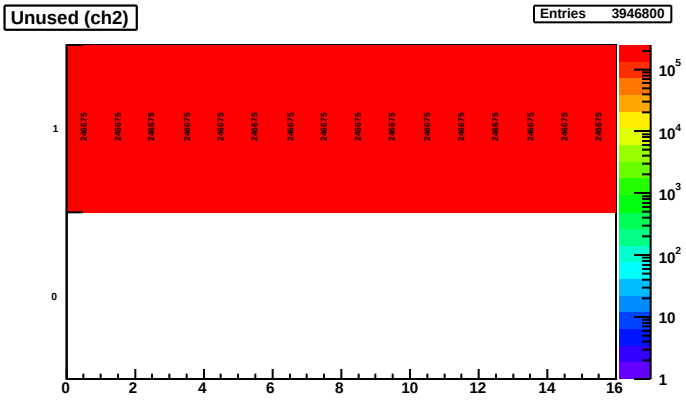

Input to FPE L1 DSM
Entries 986700
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

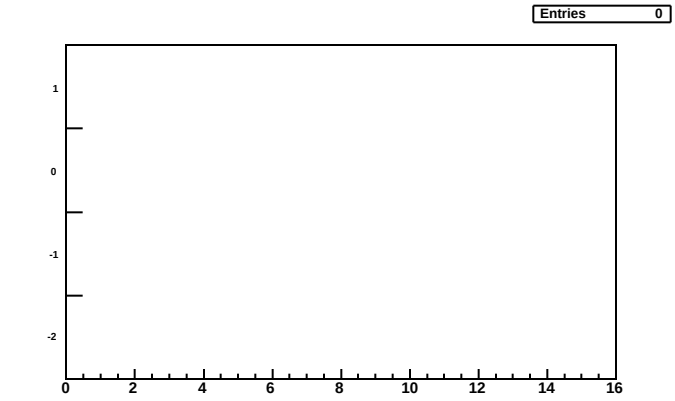

TF201 0-15 (ch0)
Entries 3946800
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 3946800
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 254
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 3946800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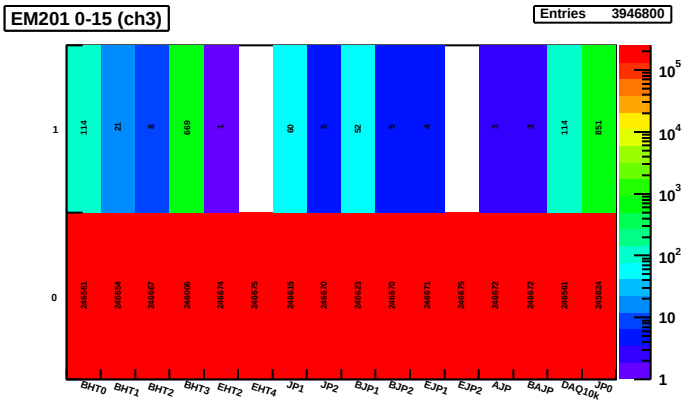
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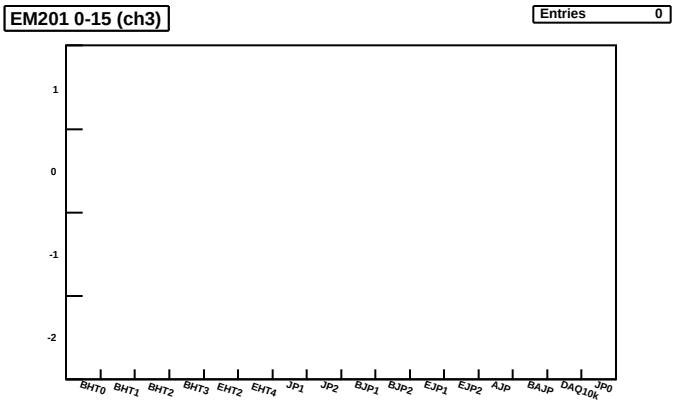
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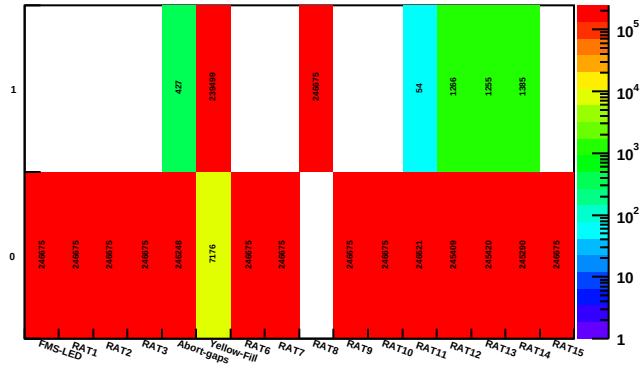
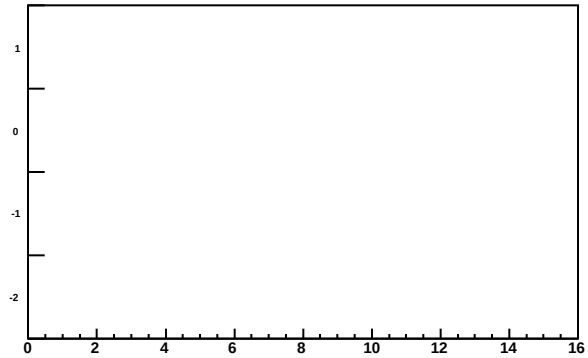
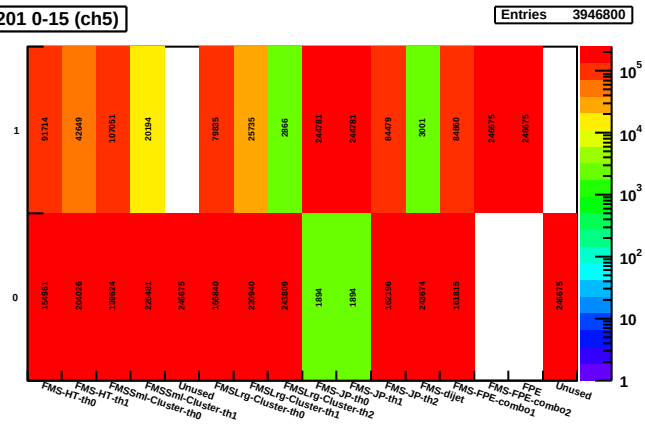
RAT board (ch4)





FP201 0-15 (ch5)



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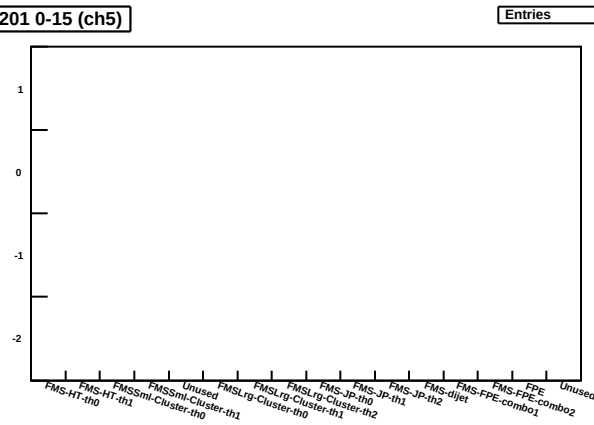

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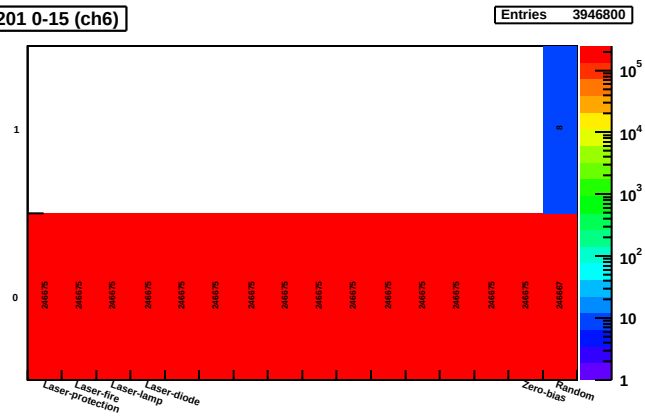

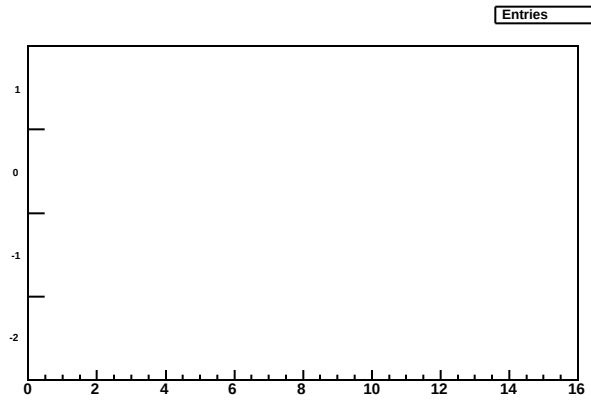
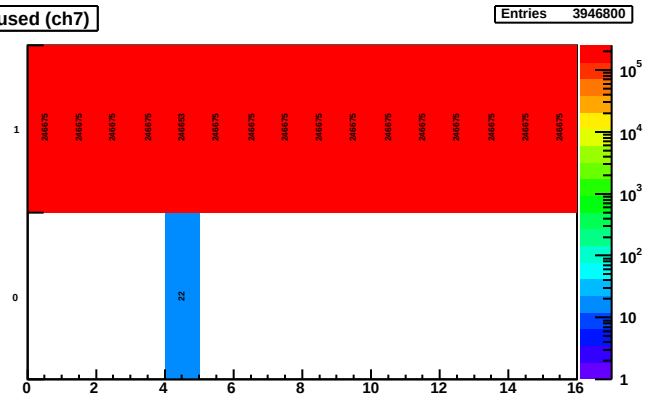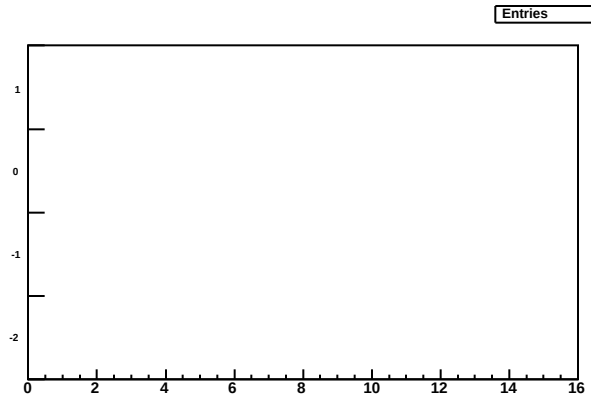
Unused (ch7)