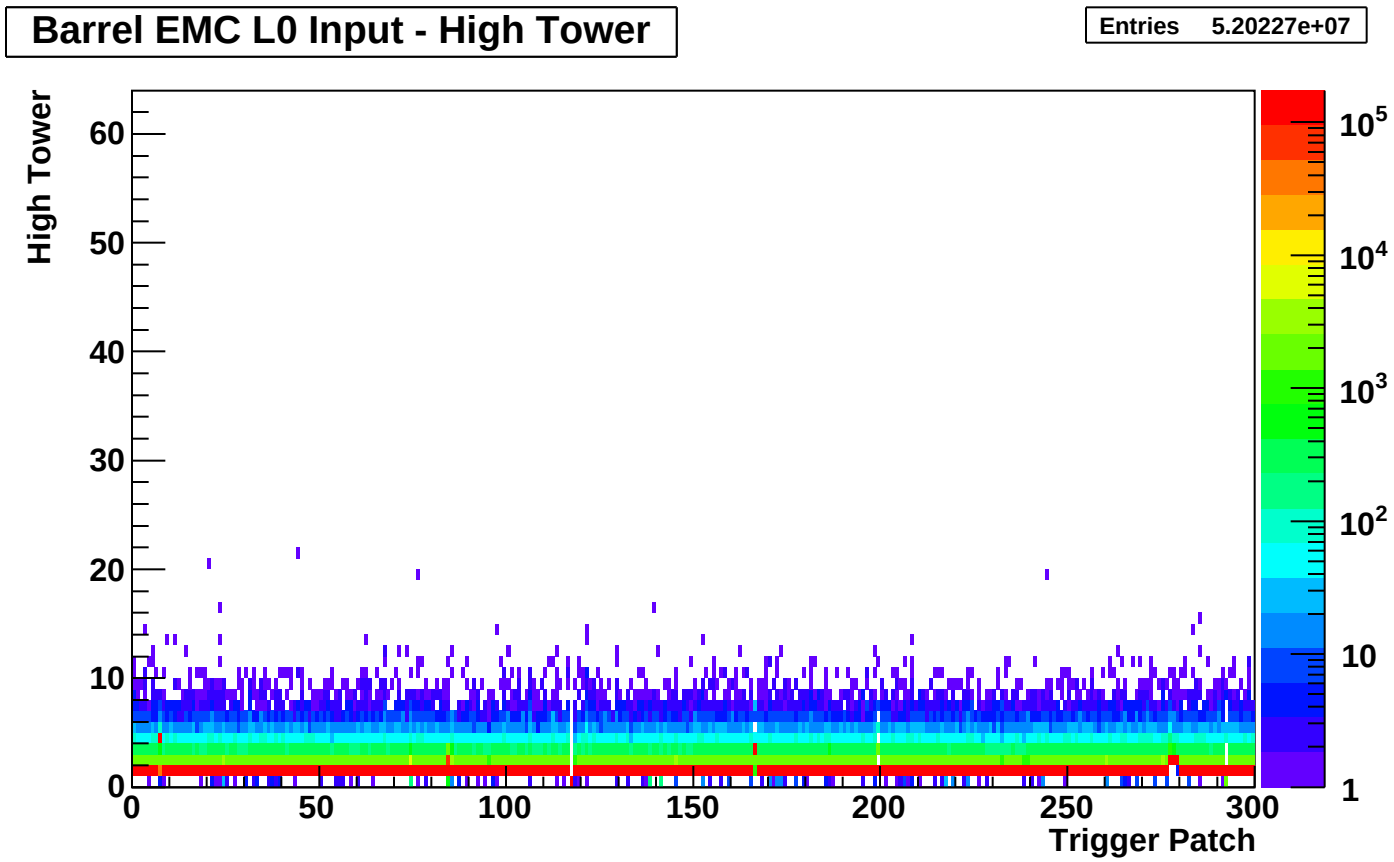
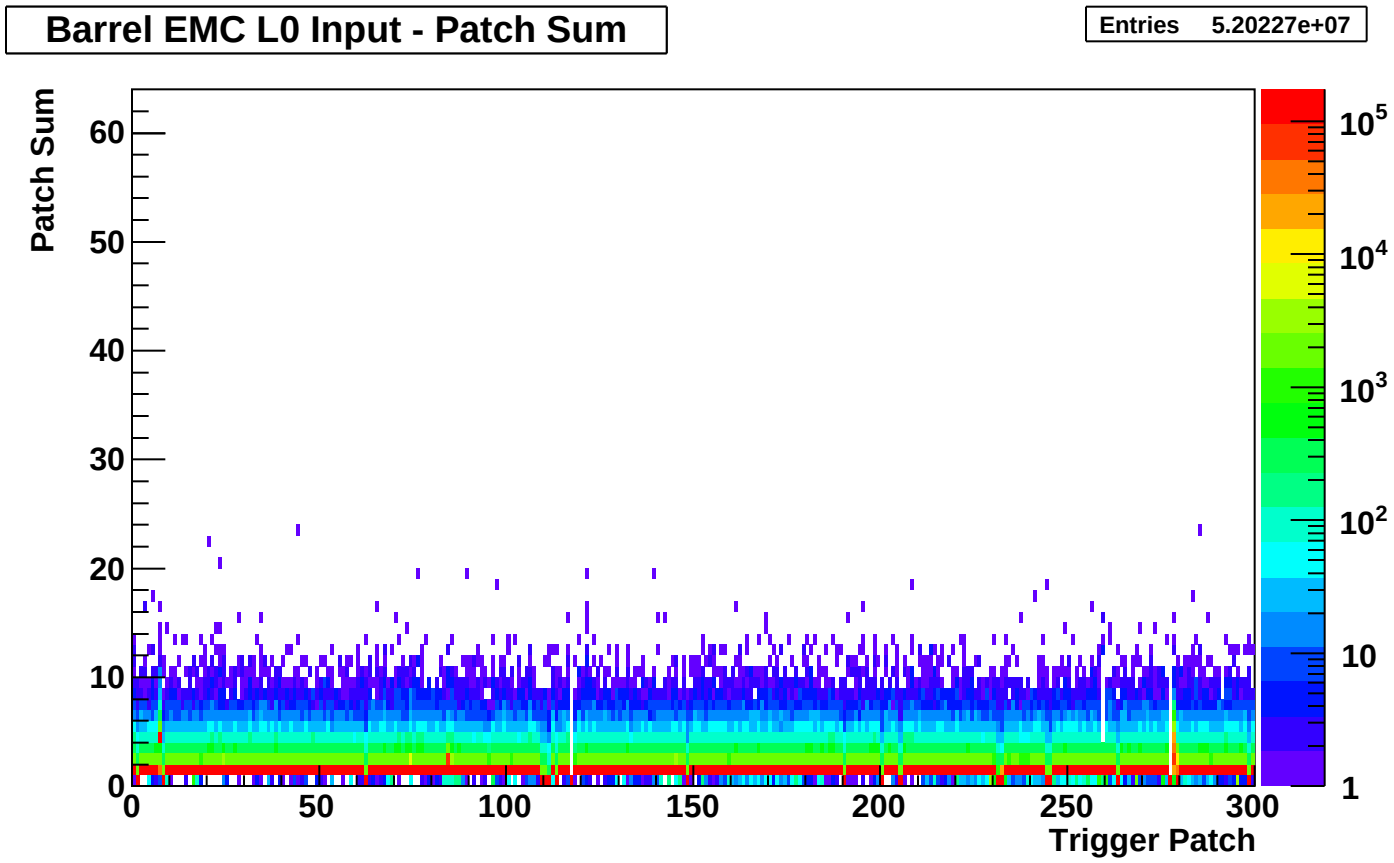
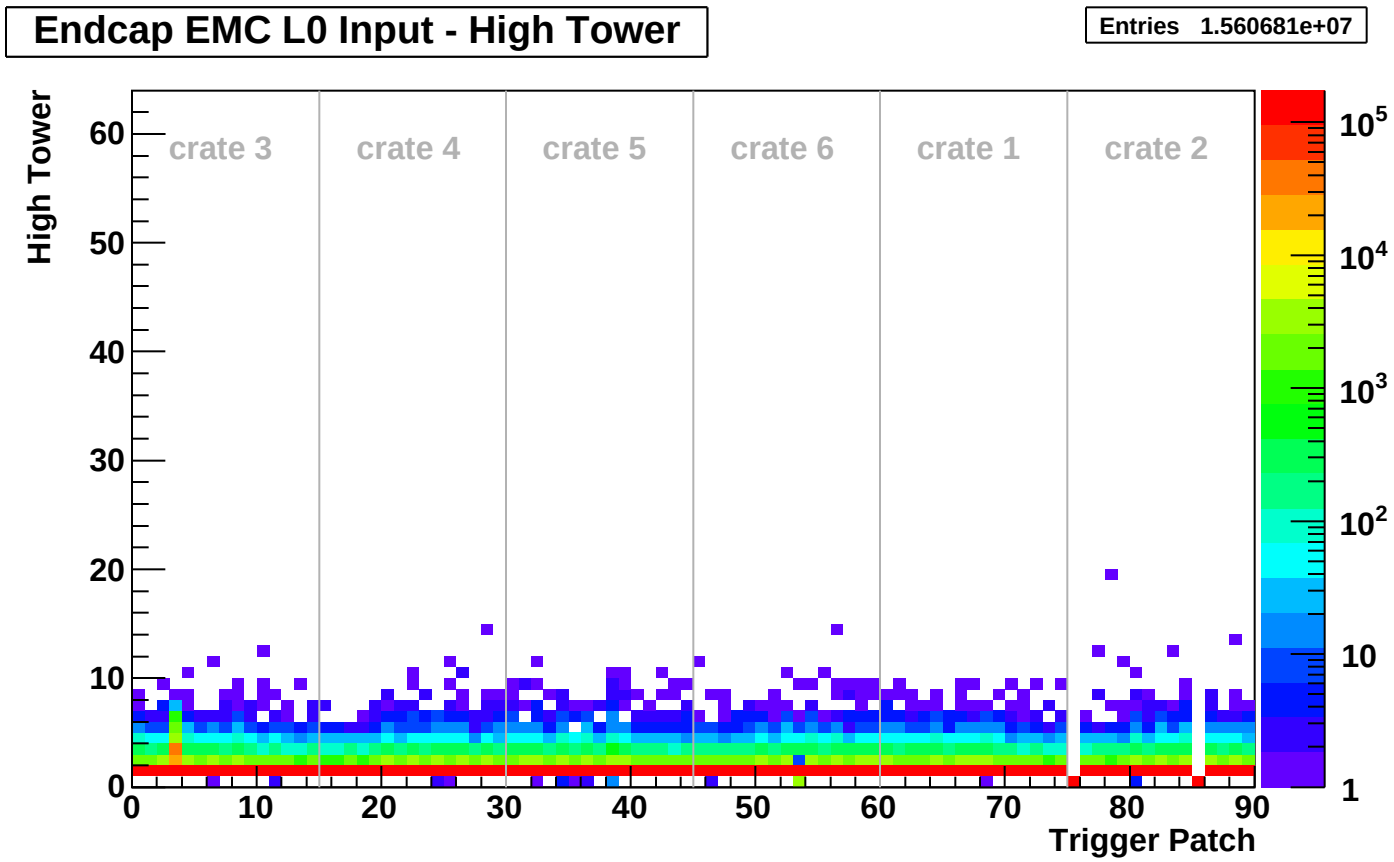
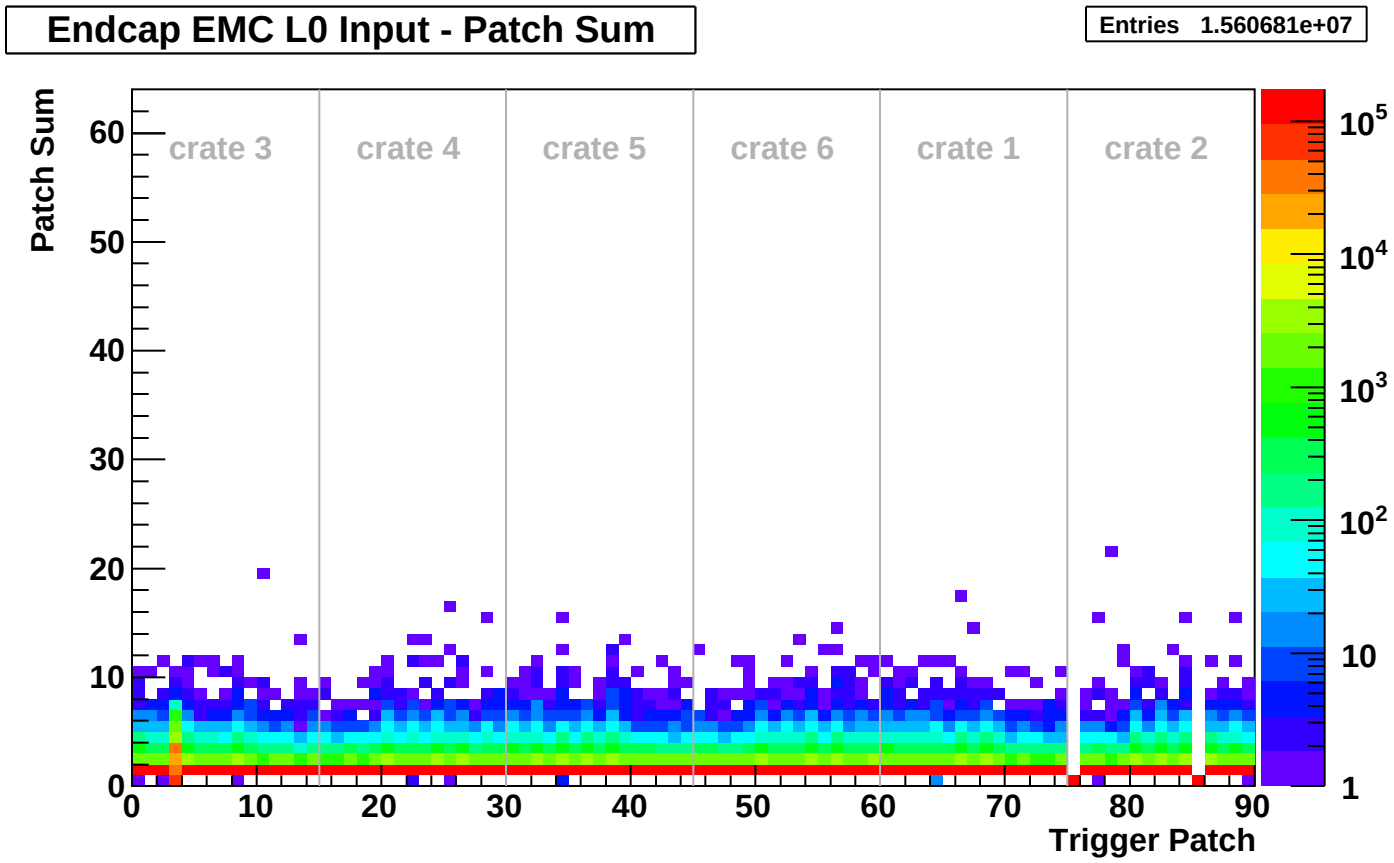
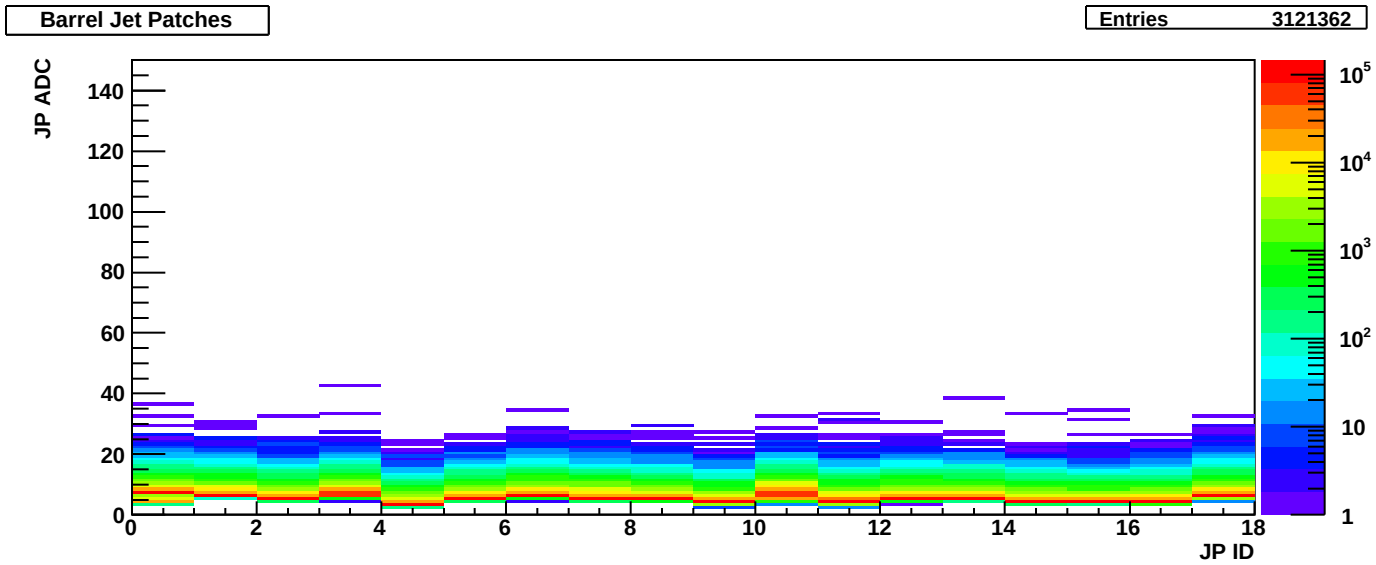
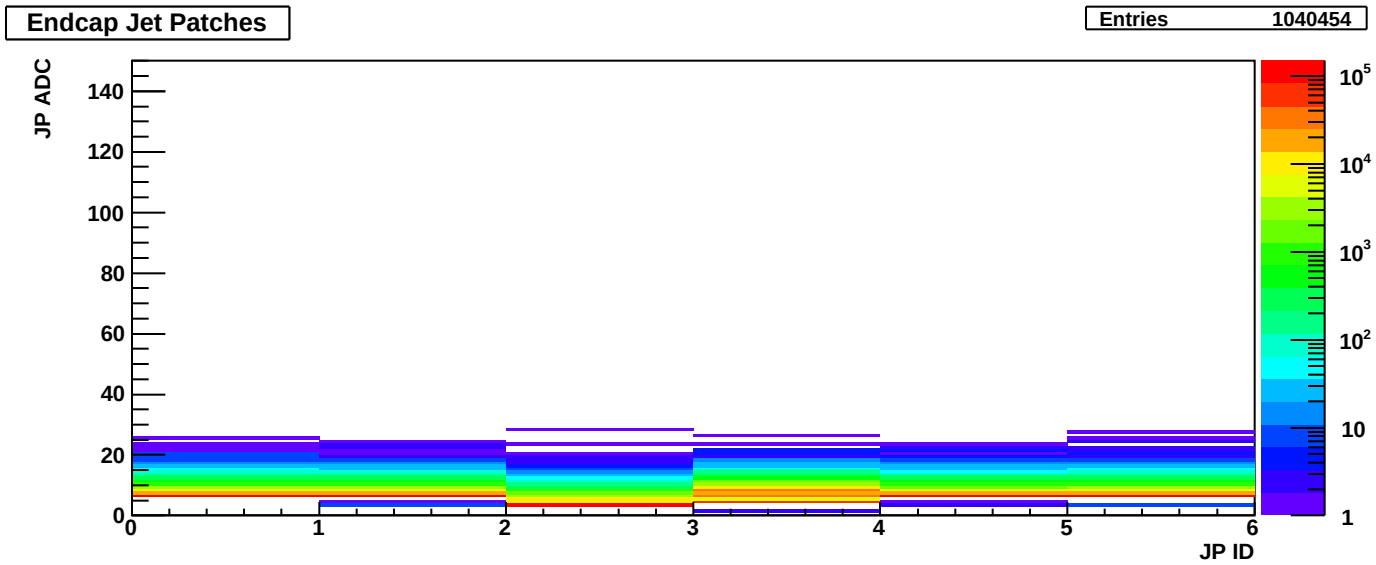

Barrel EMC L0 Input - High Tower
Entries 5.20227e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

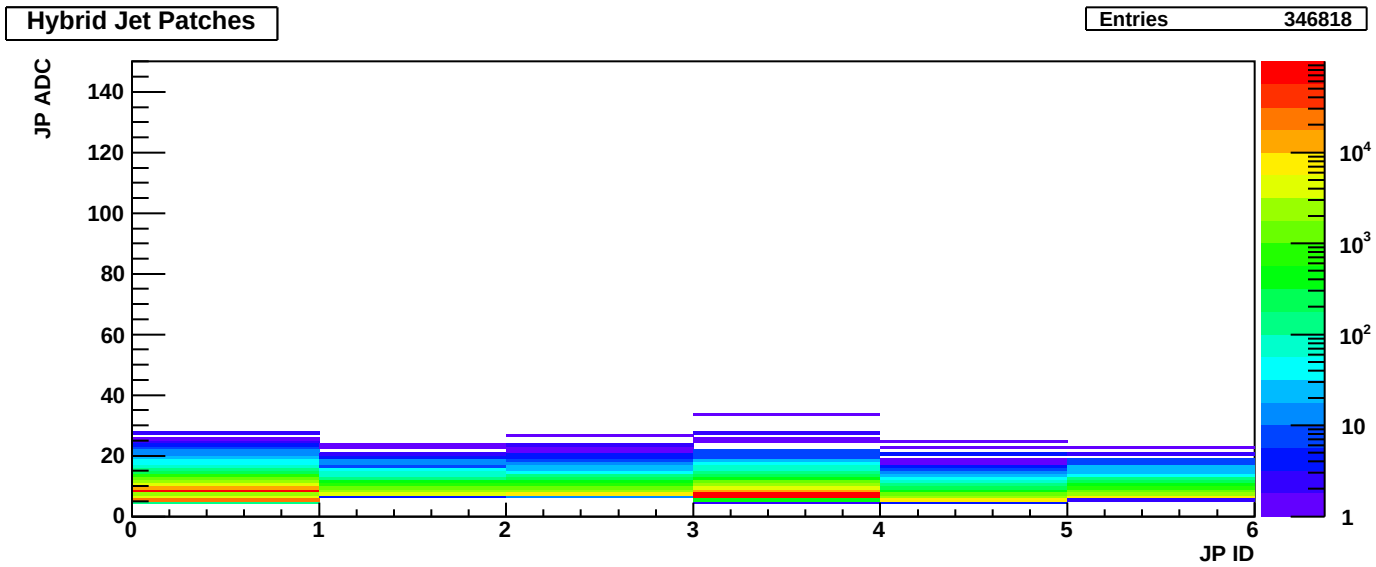

Barrel EMC L0 Input - Patch Sum
Entries 5.20227e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

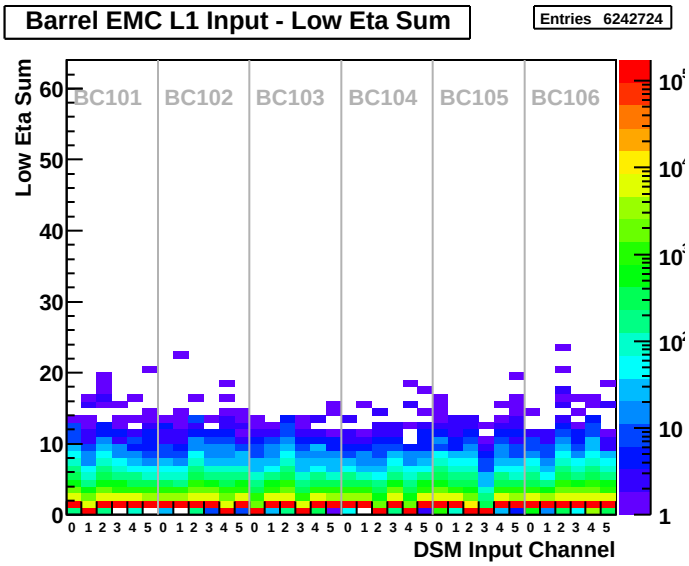

Endcap EMC L0 Input - High Tower
Entries 1.560681e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

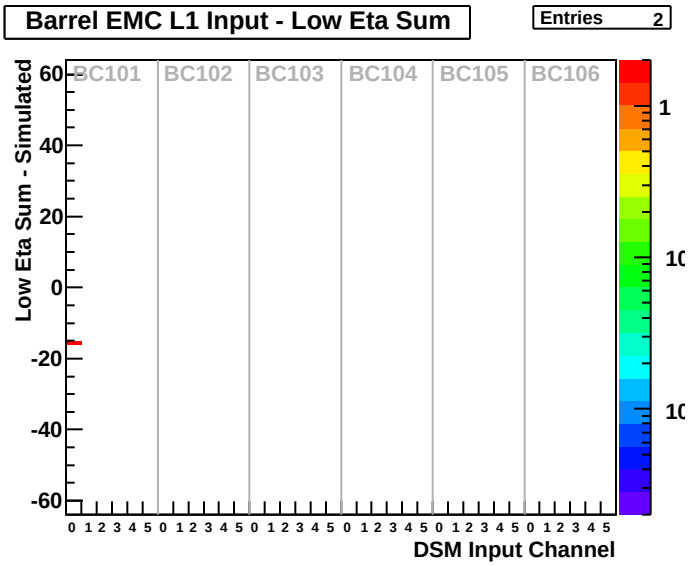

Endcap EMC L0 Input - Patch Sum
Entries 1.560681e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

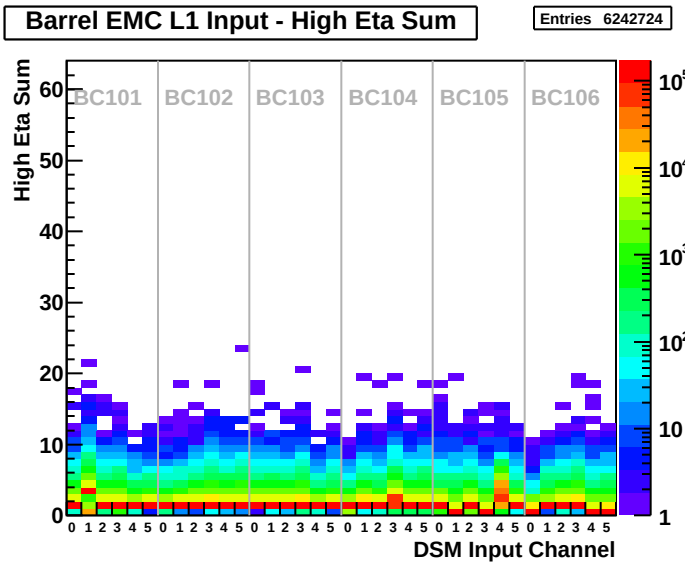

Barrel Jet Patches
Entries 3121362
JP ADC
JP ID

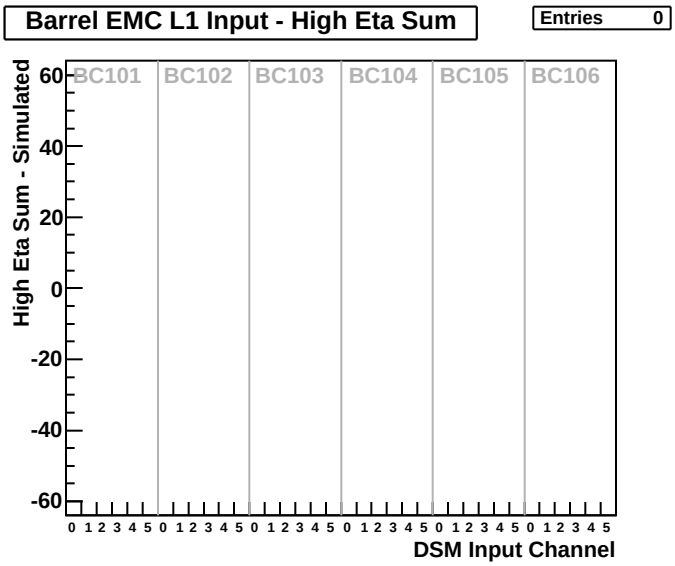

Endcap Jet Patches
Entries 1040454
JP ADC
JP ID

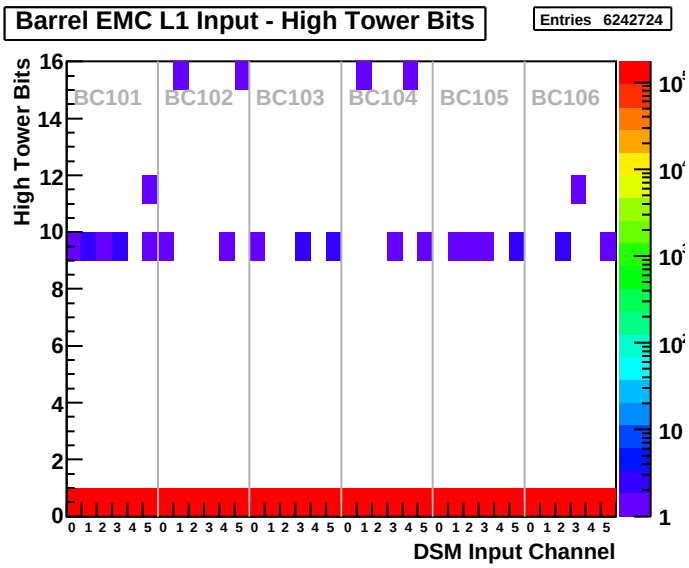

Hybrid Jet Patches
Entries 346818
JP ADC
JP ID

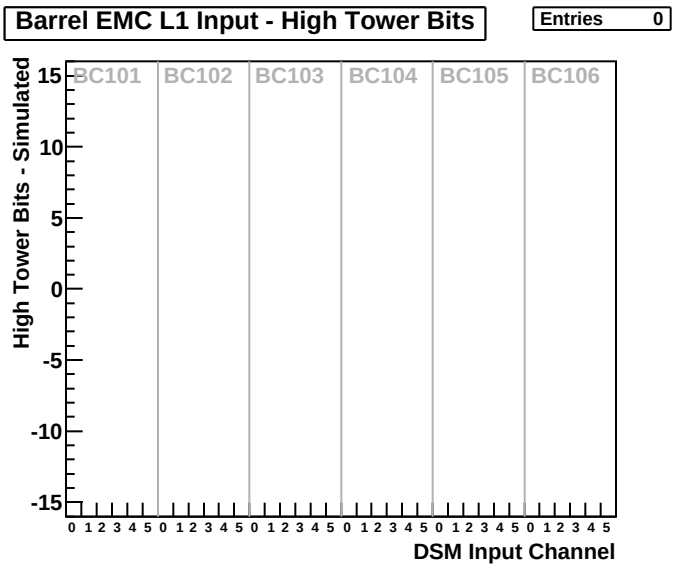

Barrel EMC L1 Input - Low Eta Sum
Entries 6242724
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

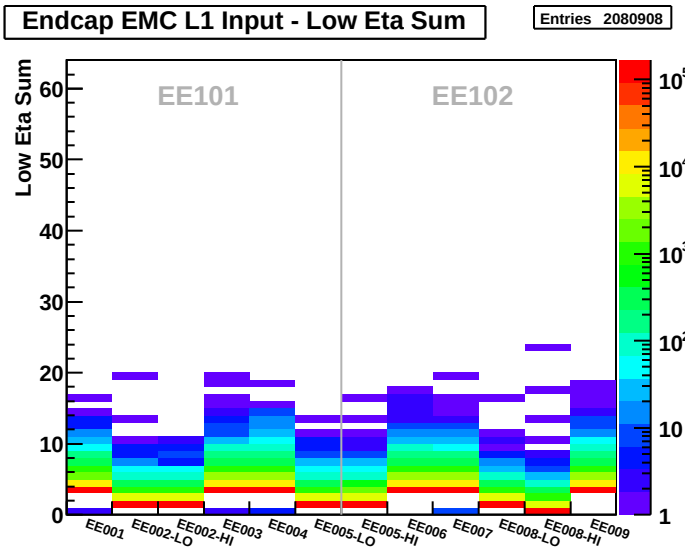

Barrel EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

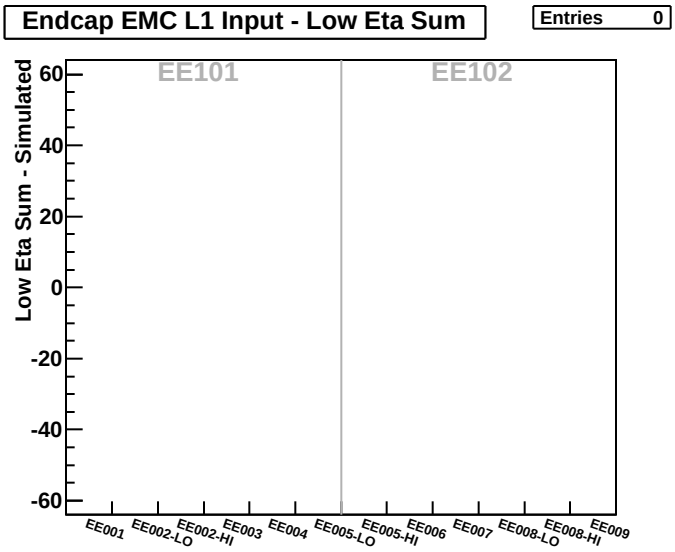

Barrel EMC L1 Input - High Eta Sum
Entries 6242724
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

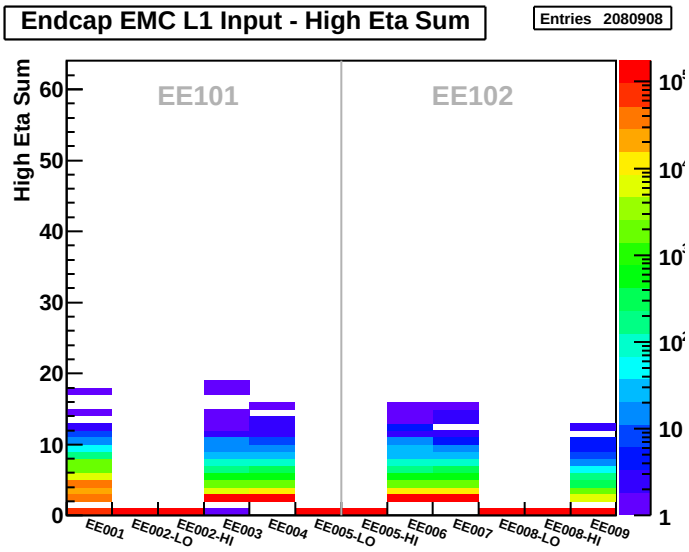

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

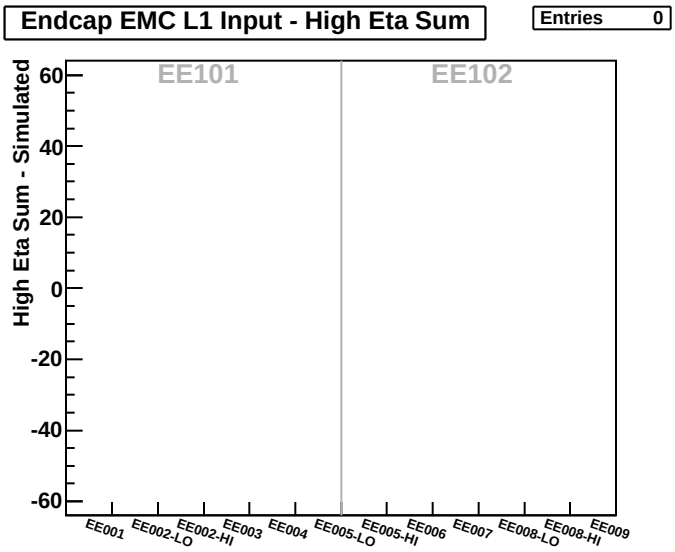

Barrel EMC L1 Input - High Tower Bits
Entries 6242724
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

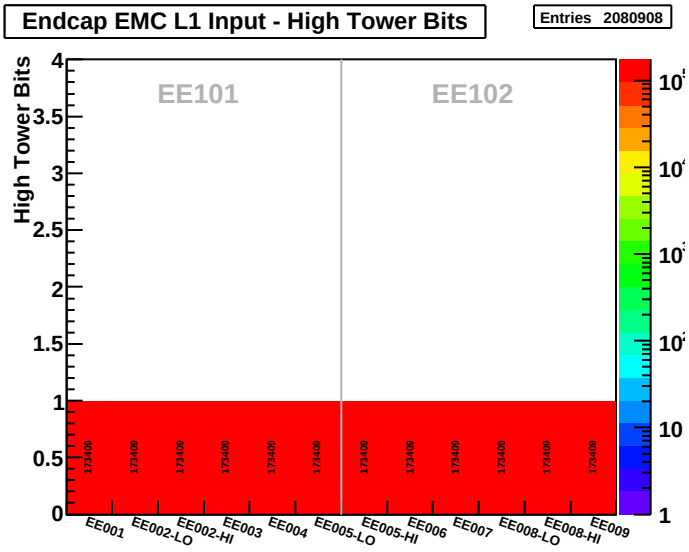

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

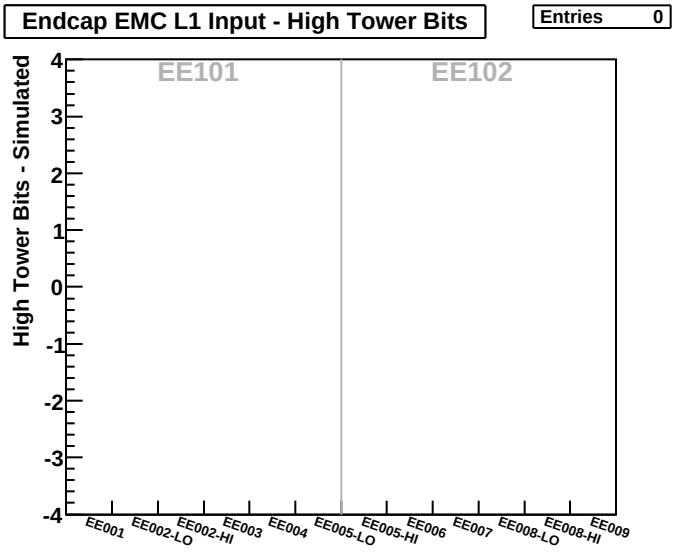

Endcap EMC L1 Input - Low Eta Sum
Entries 2080908
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

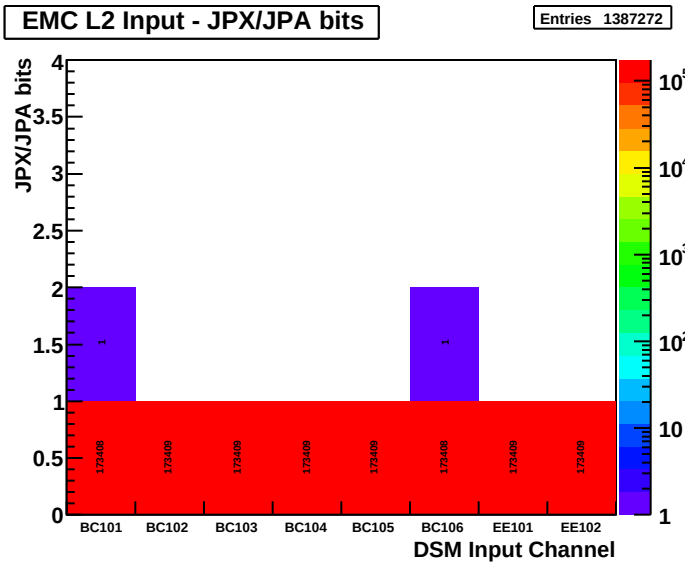

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

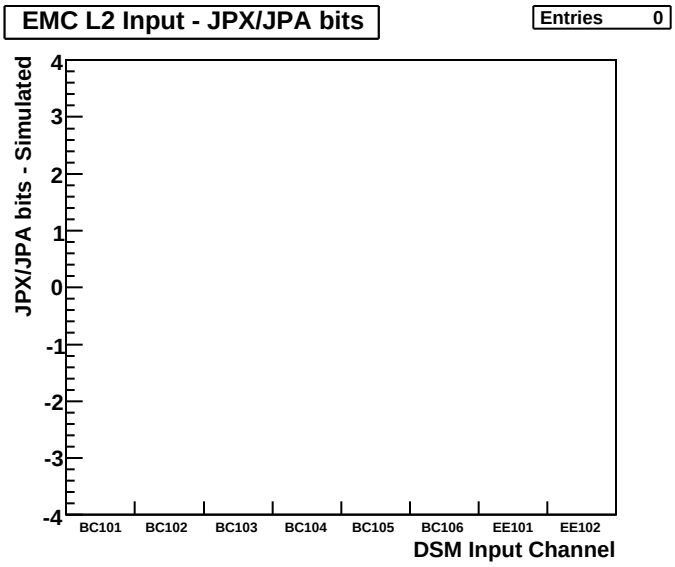

Endcap EMC L1 Input - High Eta Sum
Entries 2080908
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

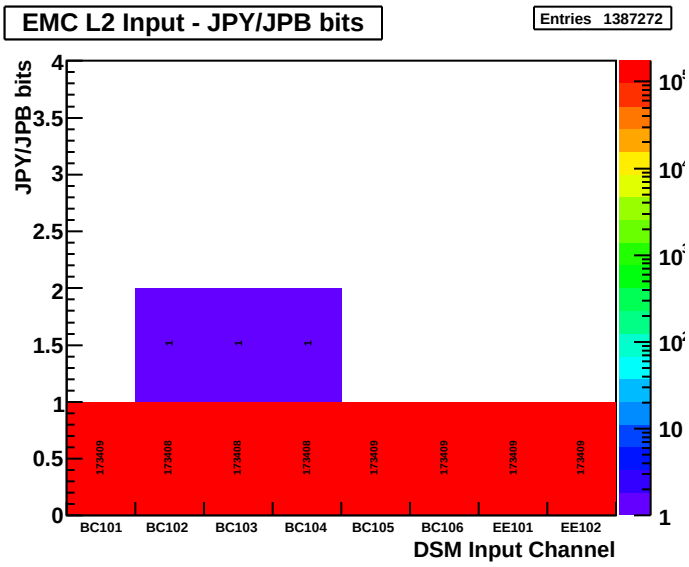

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

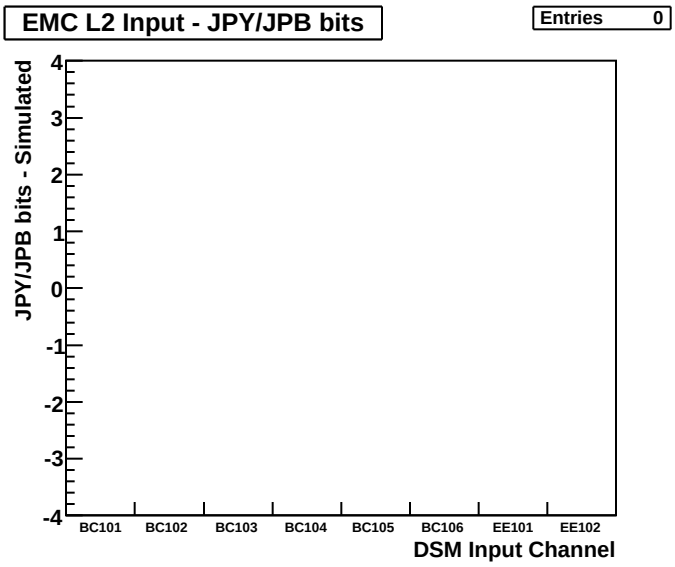

Endcap EMC L1 Input - High Tower Bits
Entries 2080908
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

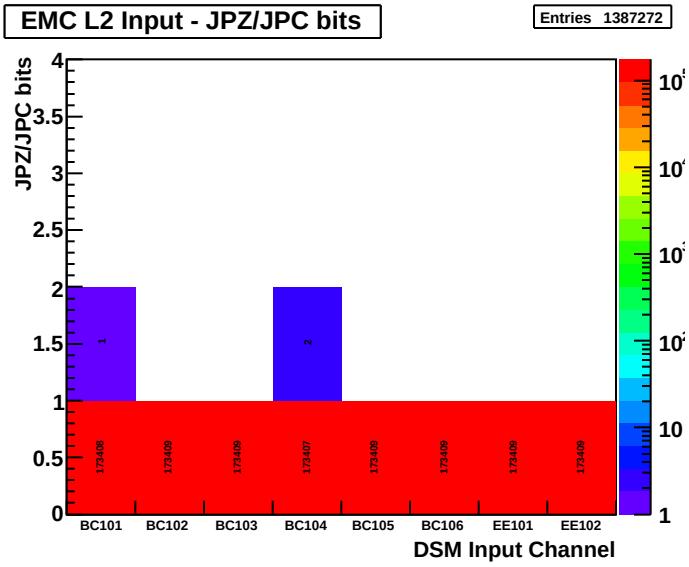

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

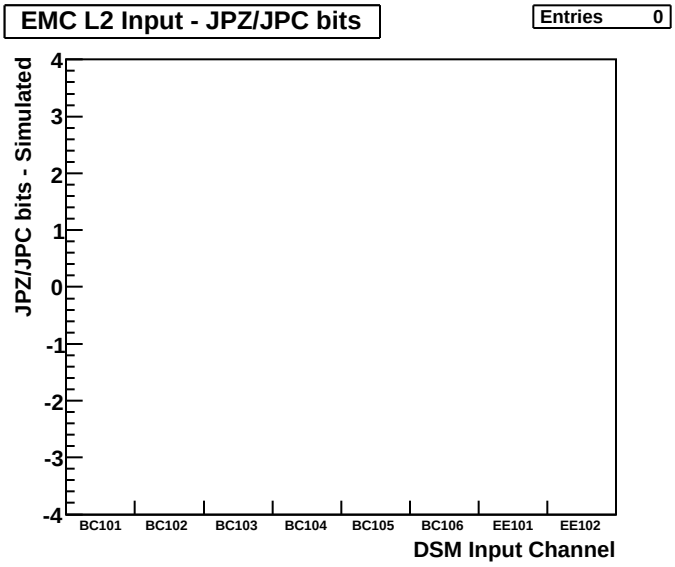

EMC L2 Input - JPX/JPA bits
Entries 1387272
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

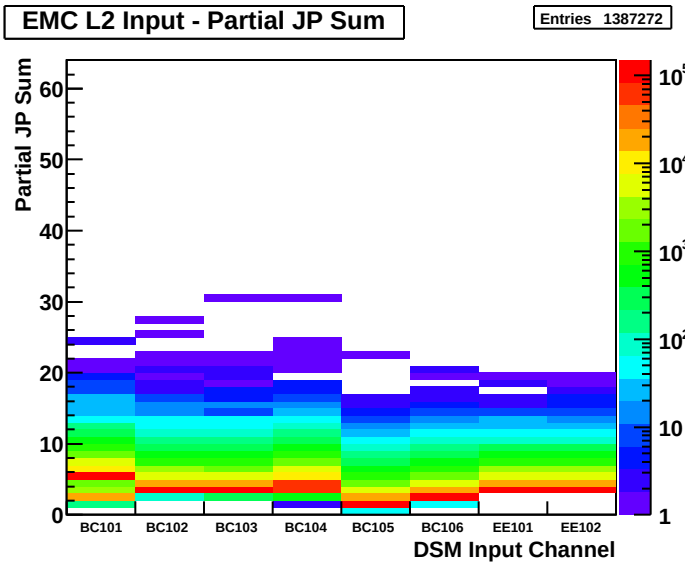

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

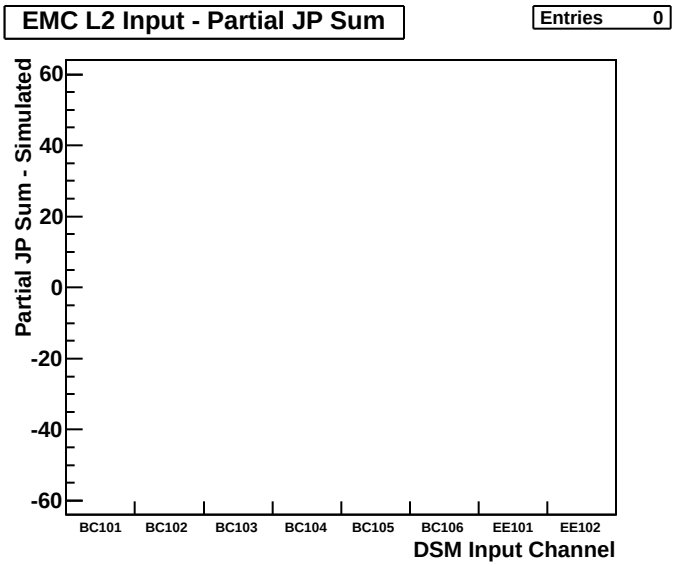

EMC L2 Input - JPY/JPB bits
Entries 1387272
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

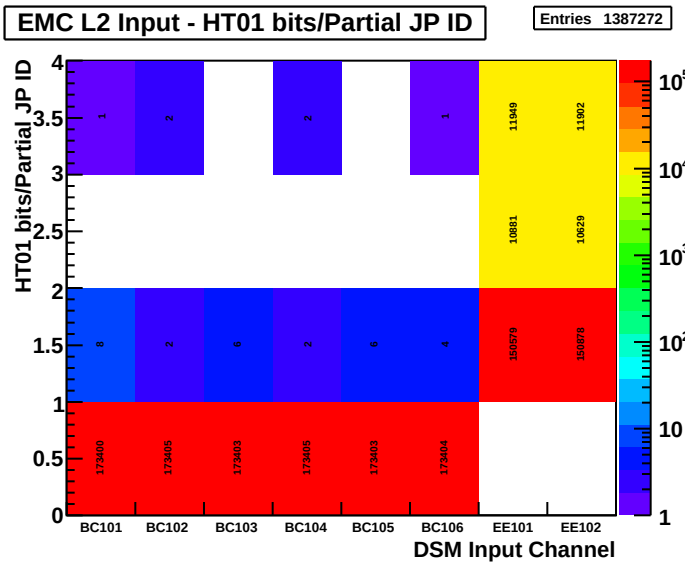

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

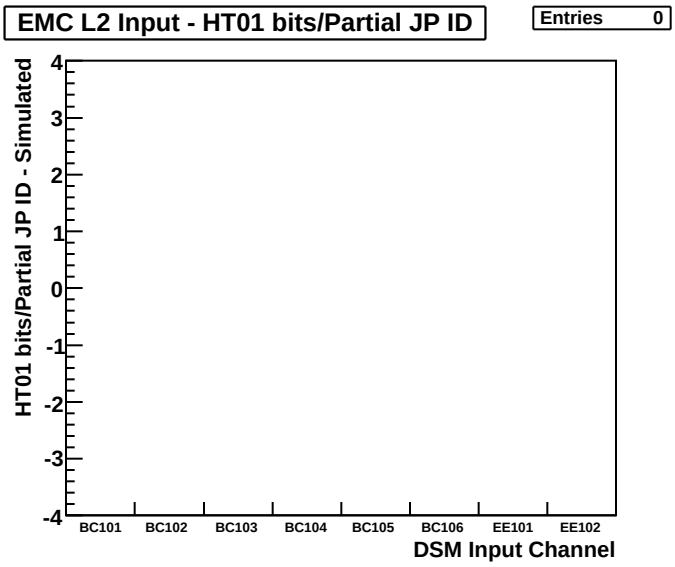

EMC L2 Input - JPZ/JPC bits
Entries 1387272
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

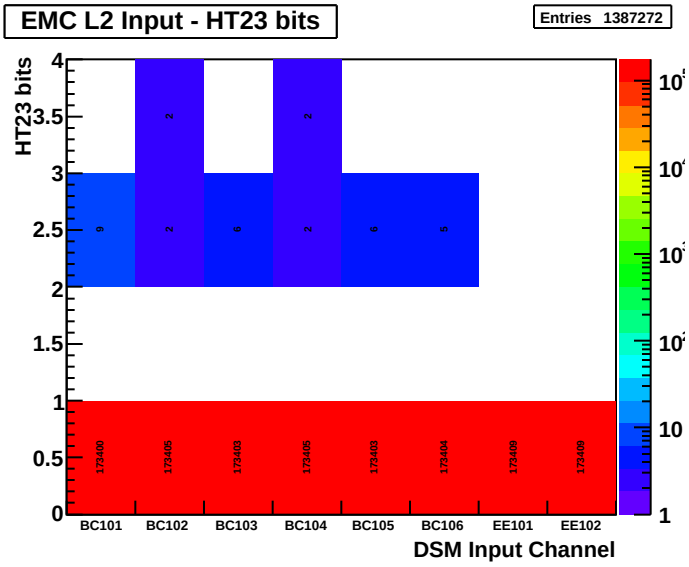

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

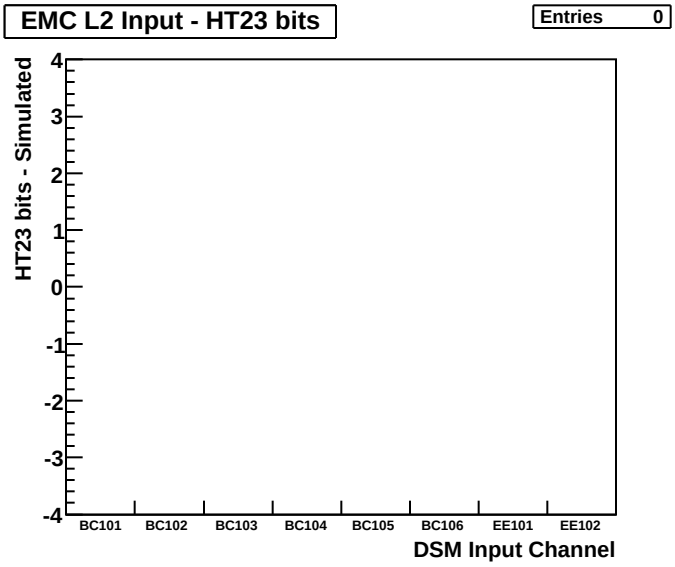

EMC L2 Input - Partial JP Sum
Entries 1387272
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

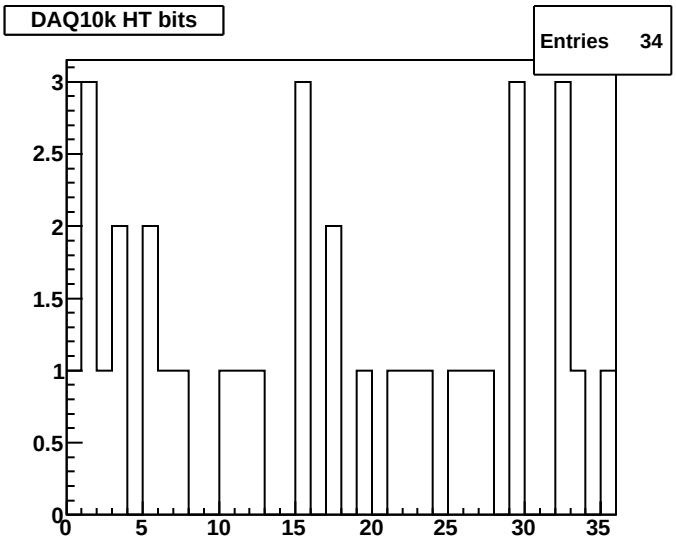

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

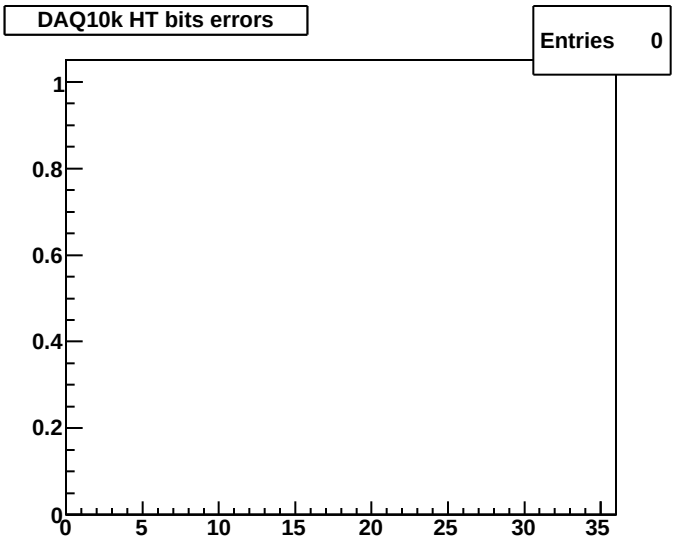

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1387272
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

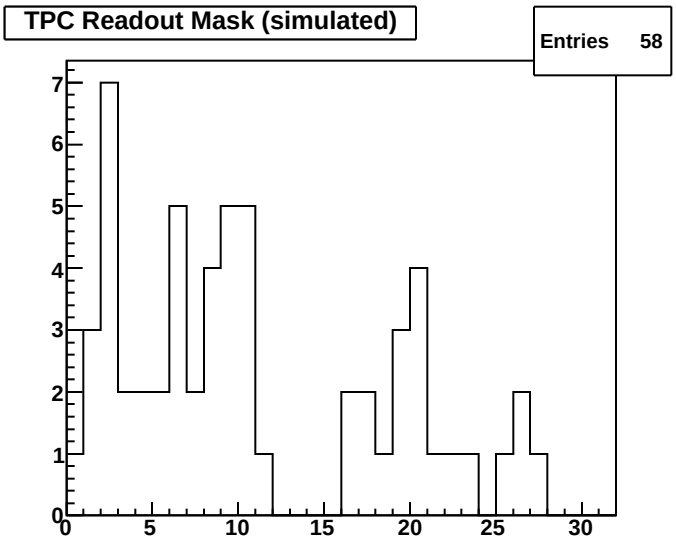

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

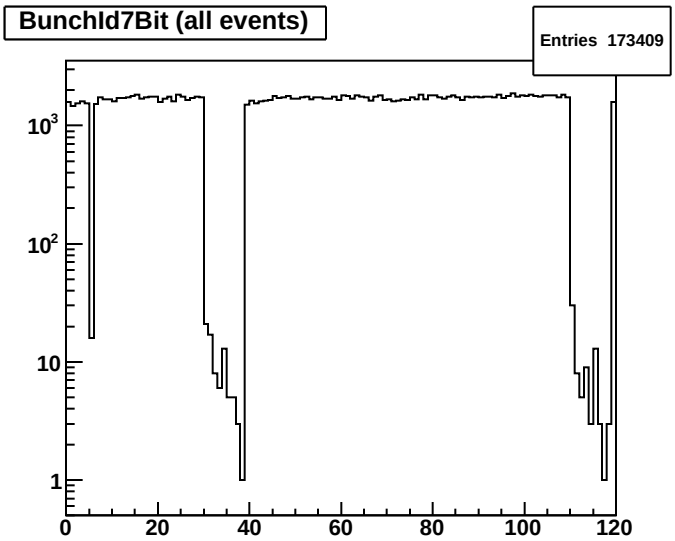

EMC L2 Input - HT23 bits
Entries 1387272
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

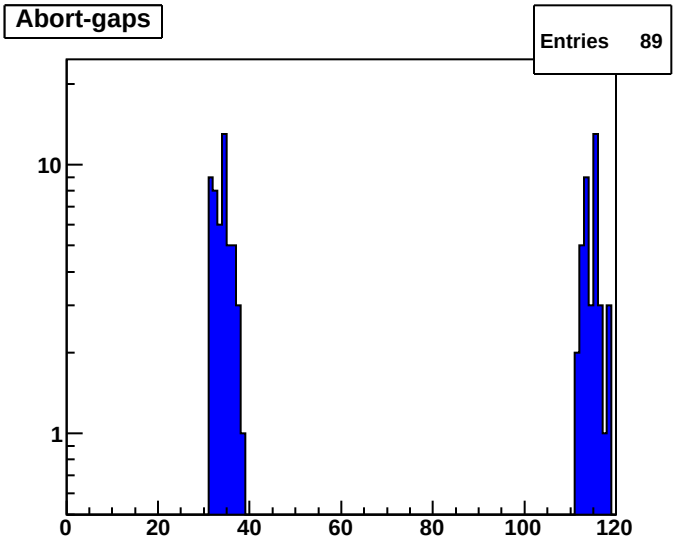

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

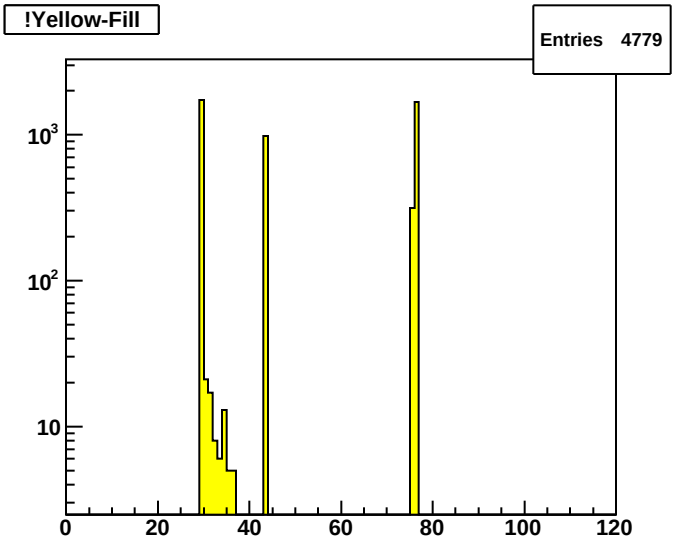

DAQ10k HT bits
Entries 34

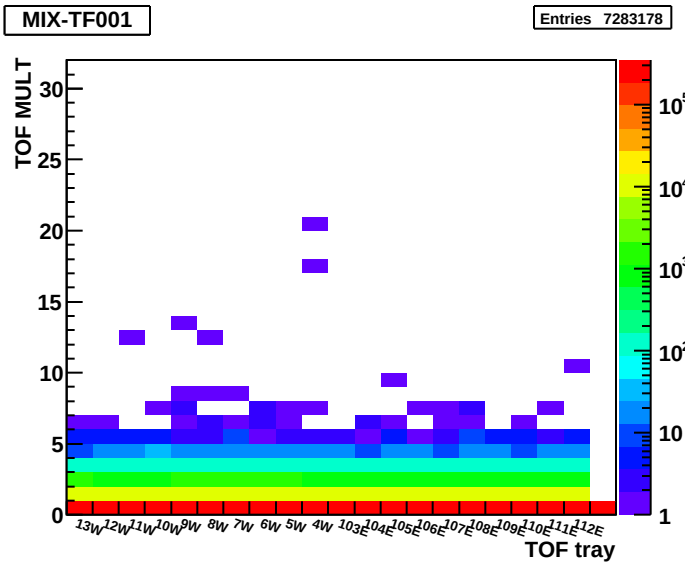

DAQ10k HT bits errors
Entries 0

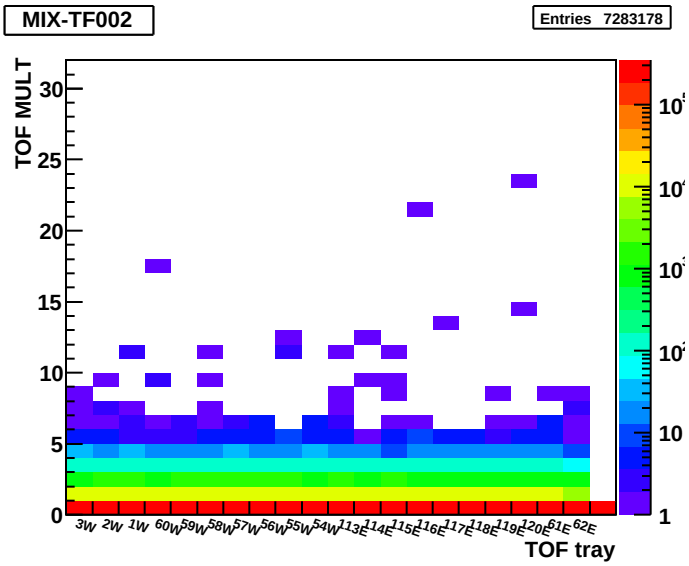

TPC Readout Mask (simulated)
Entries 58

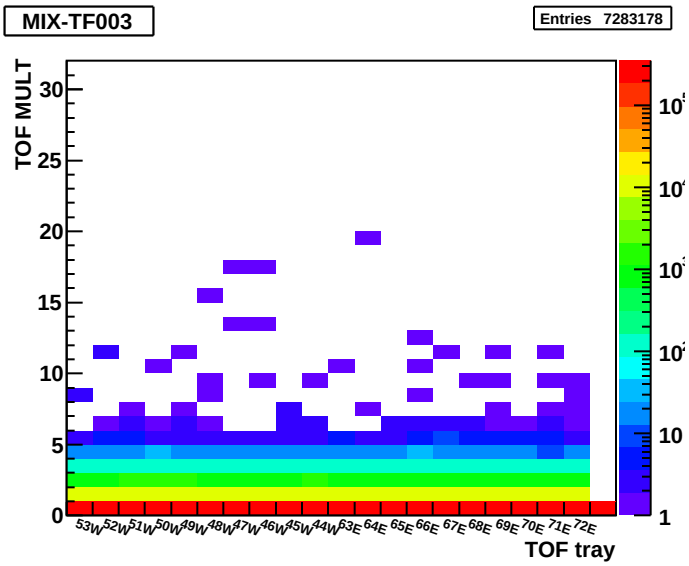

BunchId7Bit (all events)
Entries 173409

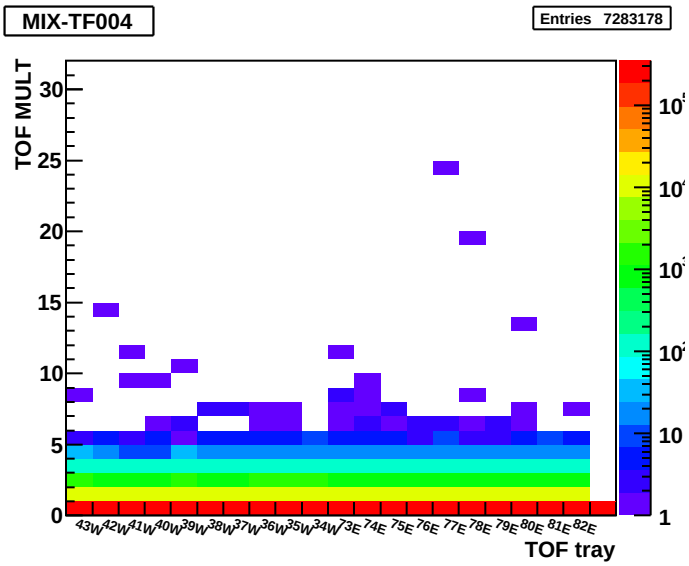

Abort-gaps
Entries 89

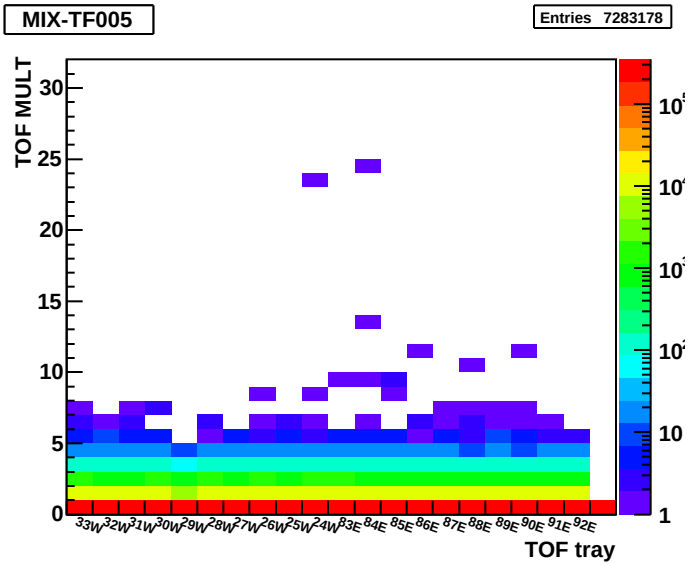

!Yellow-Fill
Entries 4779

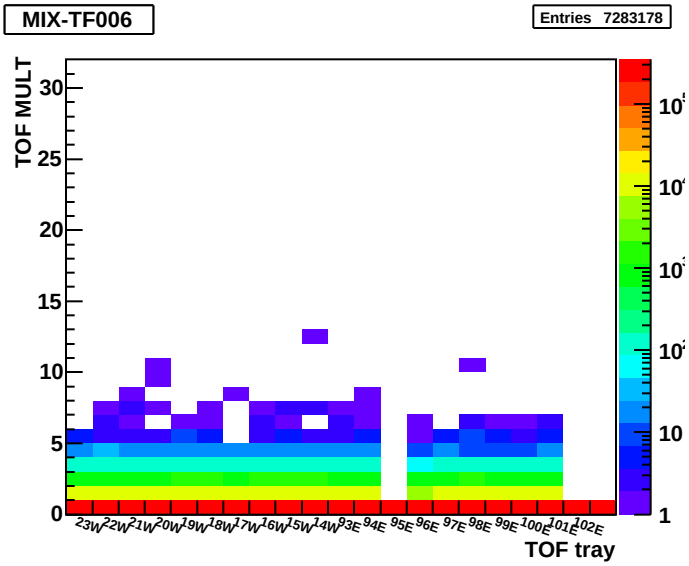

MIX-TF001
Entries 7283178
TOF MULT
TOF tray

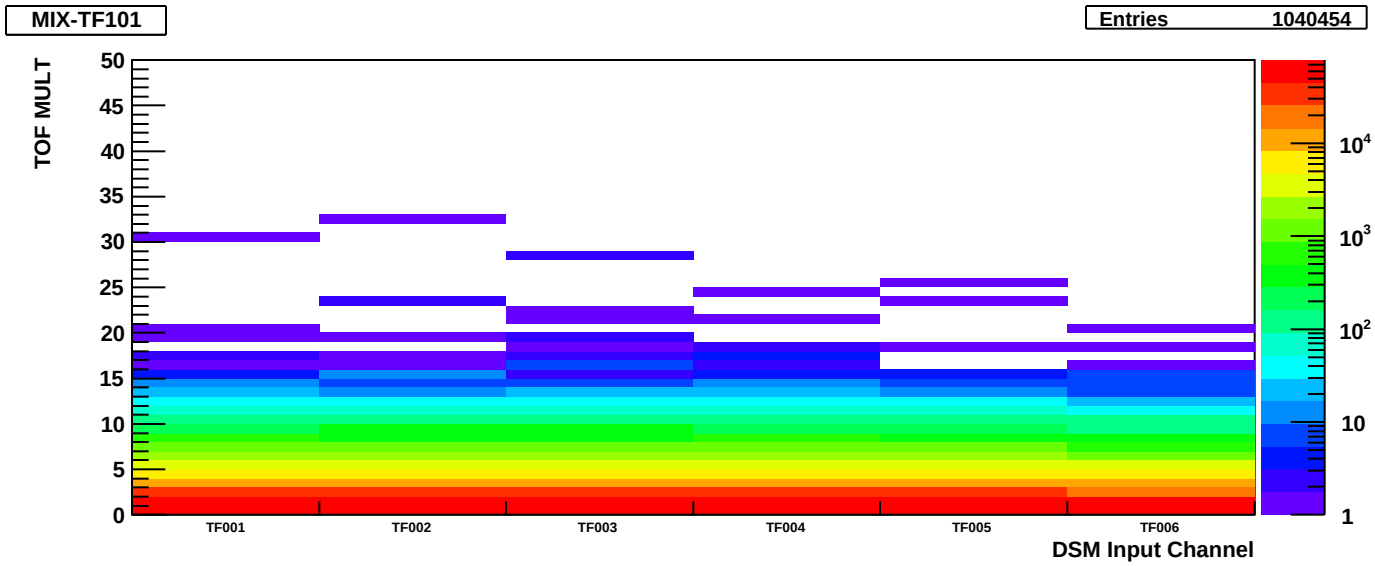

MIX-TF002
Entries 7283178
TOF MULT
TOF tray

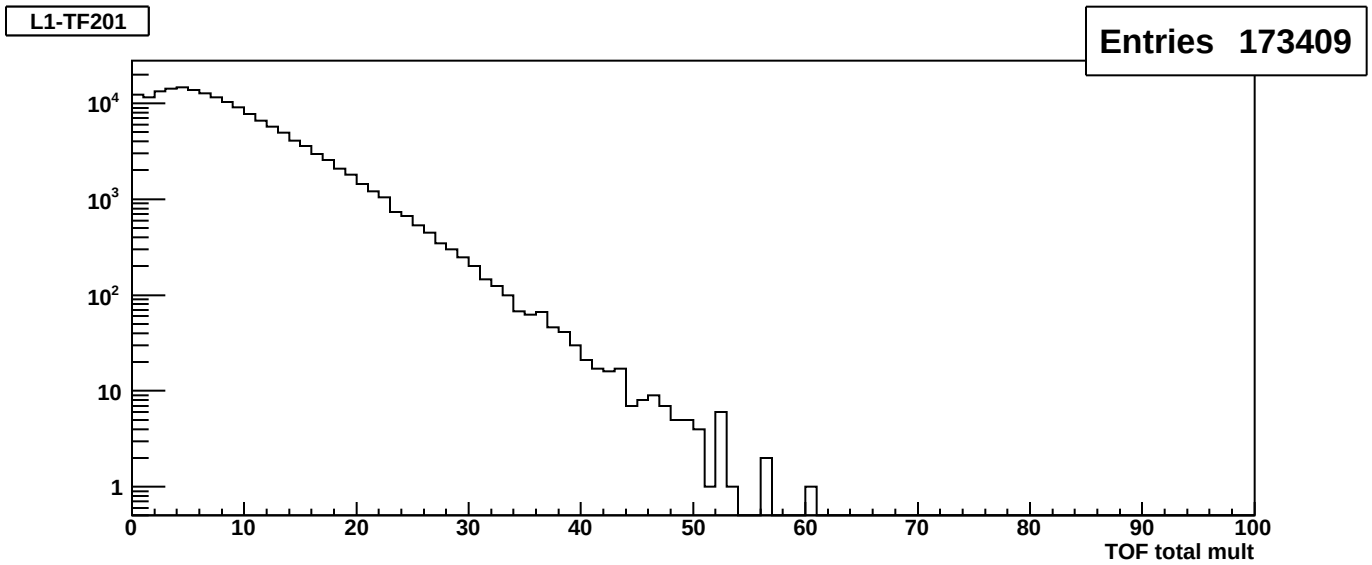

MIX-TF003
Entries 7283178
TOF MULT
TOF tray

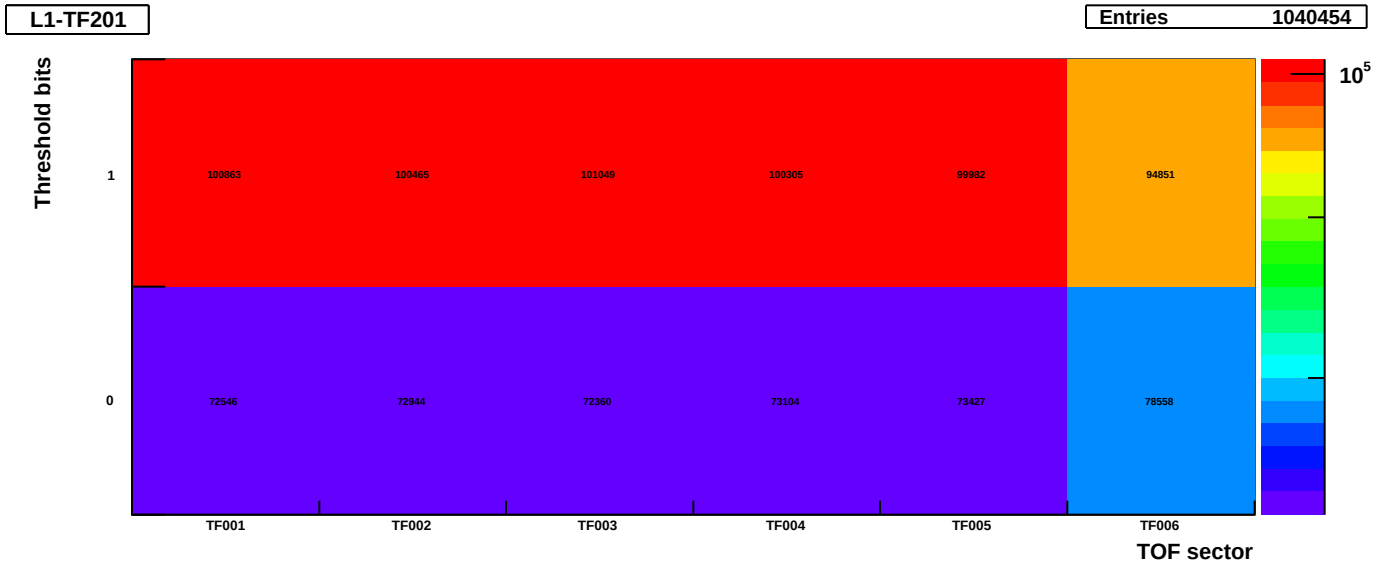

MIX-TF004
Entries 7283178
TOF MULT
TOF tray

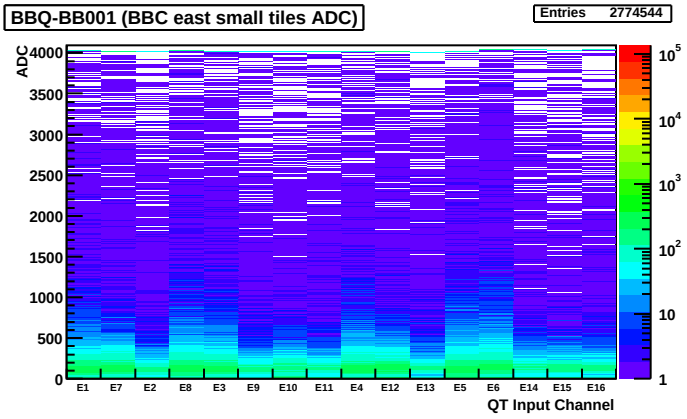

MIX-TF005
Entries 7283178
TOF MULT
TOF tray

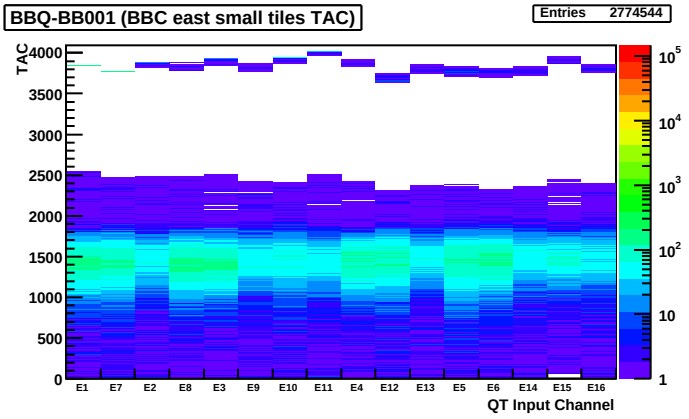

MIX-TF006
Entries 7283178
TOF MULT
TOF tray

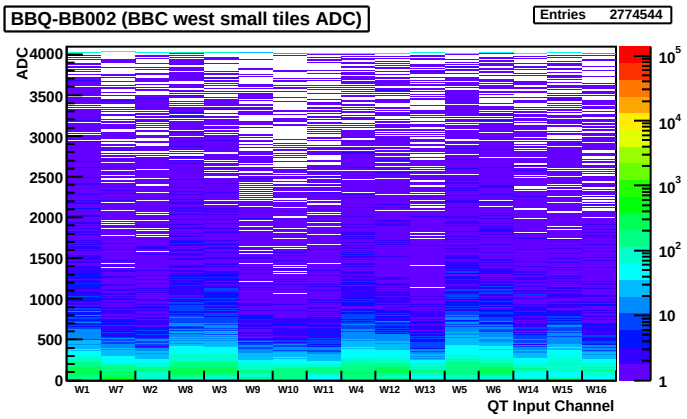

MIX-TF101
Entries 1040454
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

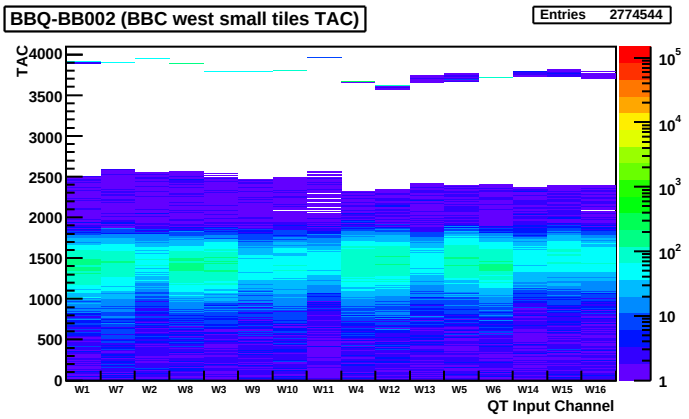

L1-TF201
Entries 173409
TOF total mult

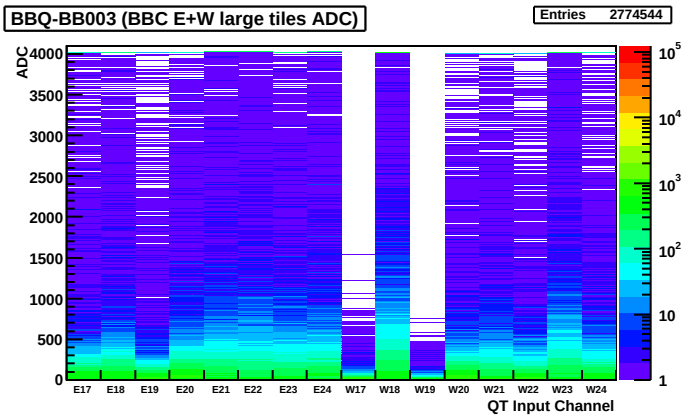

L1-TF201
Entries 1040454
Threshold bits
100863
100465
101049
100305
99982
94851
72546
72944
72360
73104
73427
78558
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

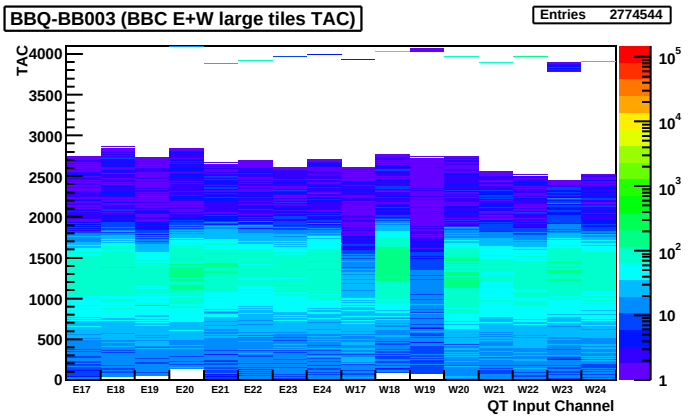

BBQ-BB001 (BBC east small tiles ADC)
Entries 2774544
ADC
QT Input Channel

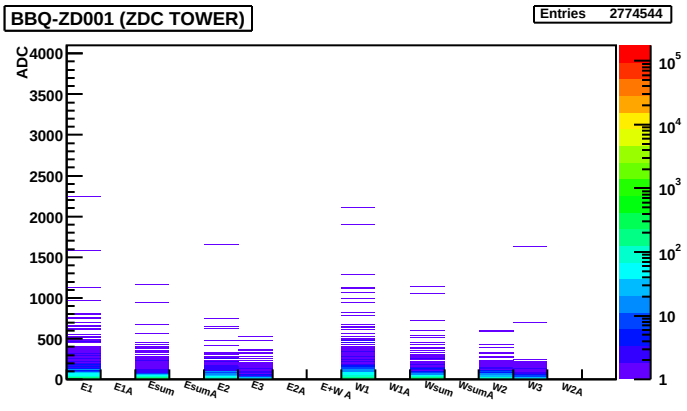

BBQ-BB001 (BBC east small tiles TAC)
Entries 2774544
TAC
QT Input Channel

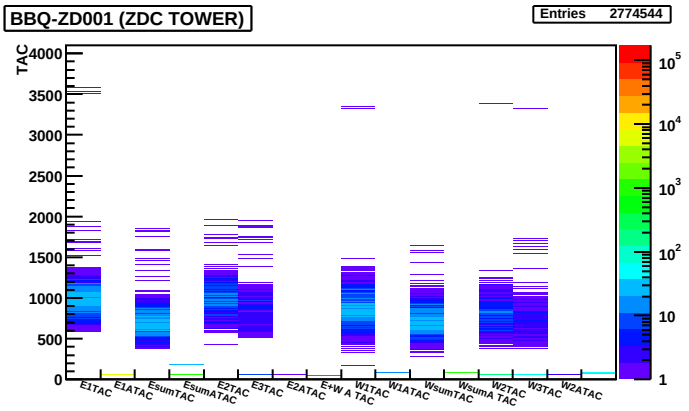

BBQ-BB002 (BBC west small tiles ADC)
Entries 2774544
ADC
QT Input Channel

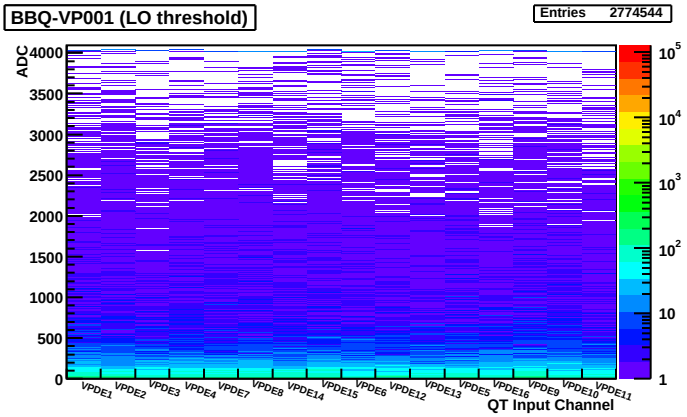

BBQ-BB002 (BBC west small tiles TAC)
Entries 2774544
TAC
QT Input Channel

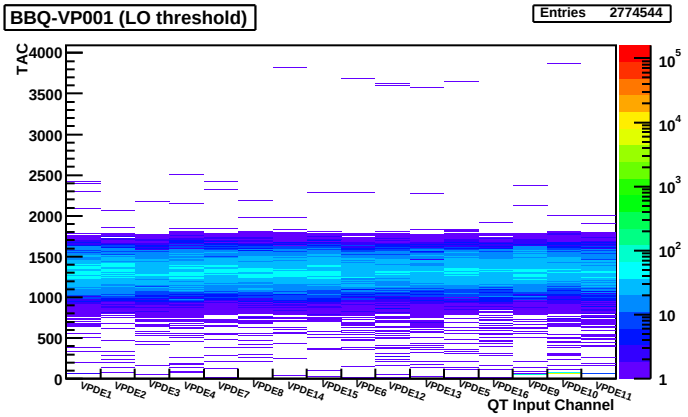

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2774544
ADC
QT Input Channel

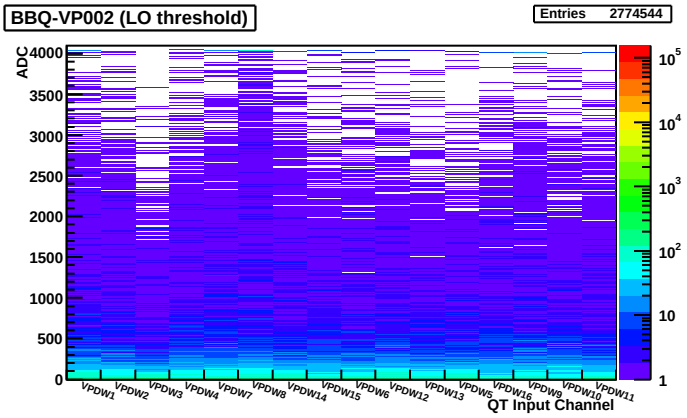

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2774544
TAC
QT Input Channel

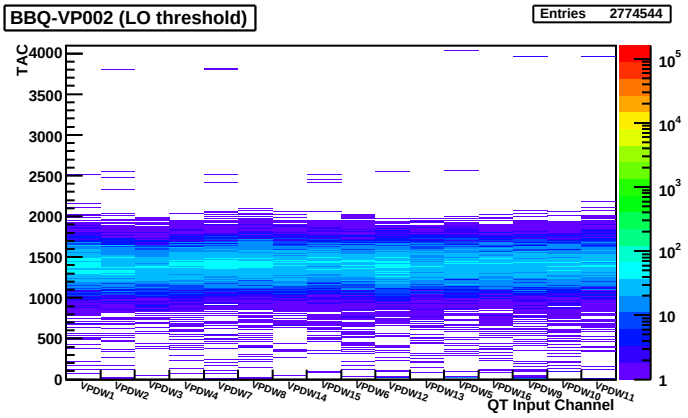

BBQ-ZD001 (ZDC TOWER)
Entries 2774544
ADC

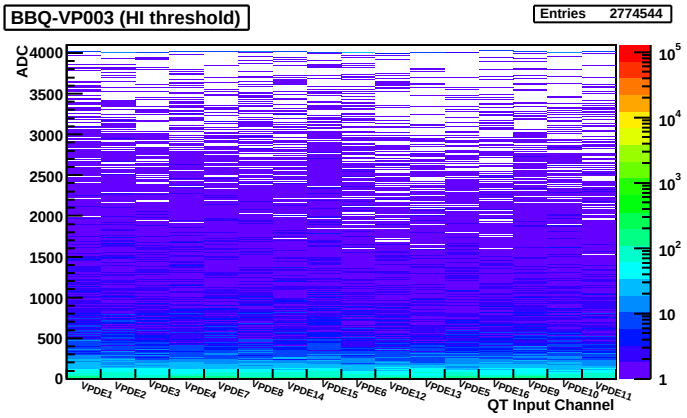

BBQ-ZD001 (ZDC TOWER)
Entries 2774544
TAC

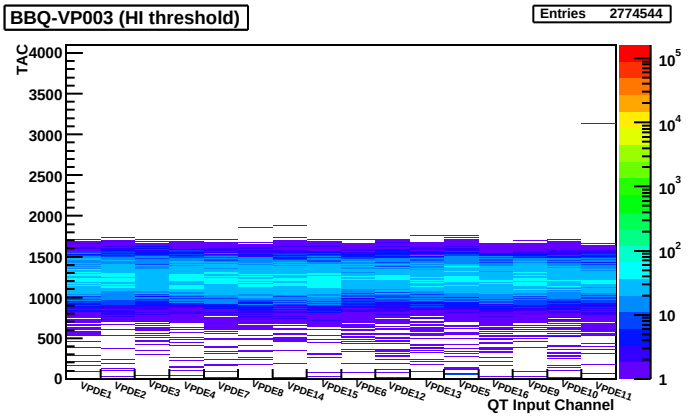

BBQ-VP001 (LO threshold)
Entries 2774544
ADC
QT Input Channel

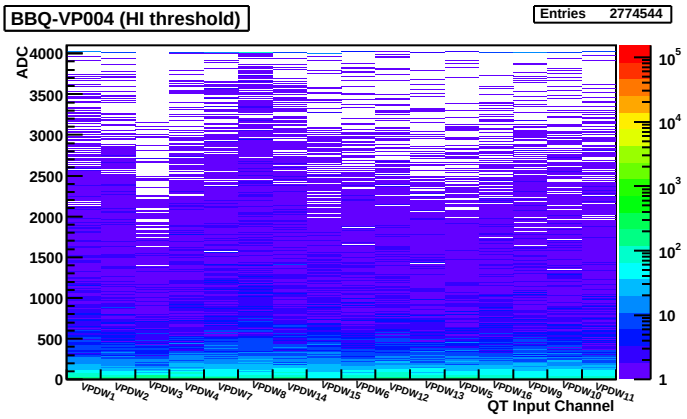

BBQ-VP001 (LO threshold)
Entries 2774544
TAC
QT Input Channel

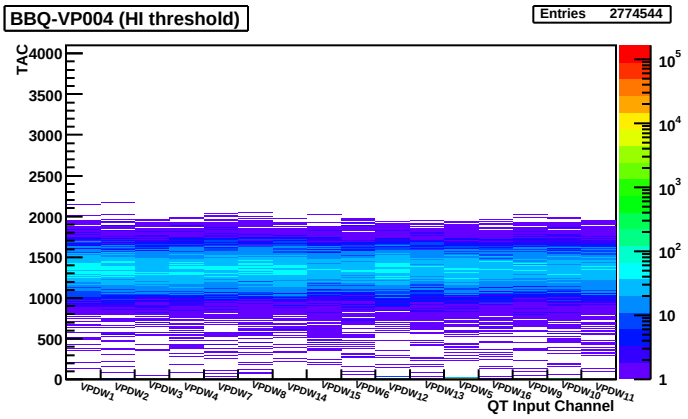

BBQ-VP002 (LO threshold)
Entries 2774544
ADC
QT Input Channel

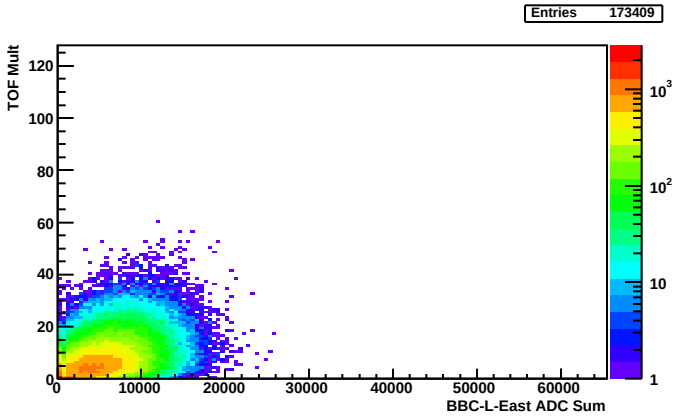

BBQ-VP002 (LO threshold)
Entries 2774544
TAC
QT Input Channel

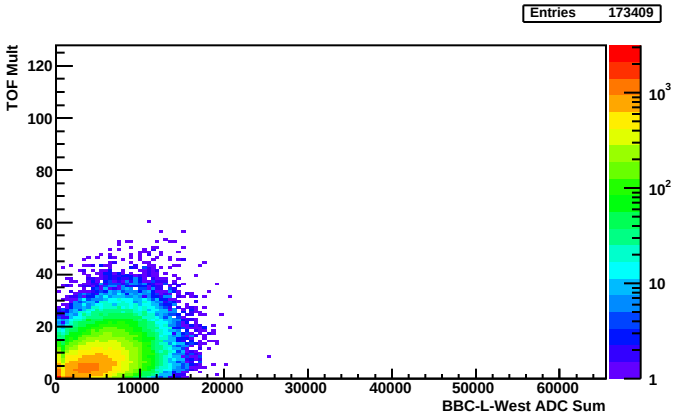

BBQ-VP003 (HI threshold)
Entries 2774544
ADC
QT Input Channel

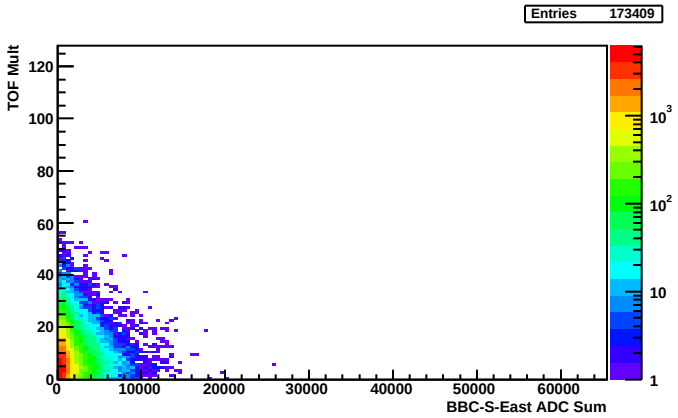

BBQ-VP003 (HI threshold)
Entries 2774544
TAC
QT Input Channel

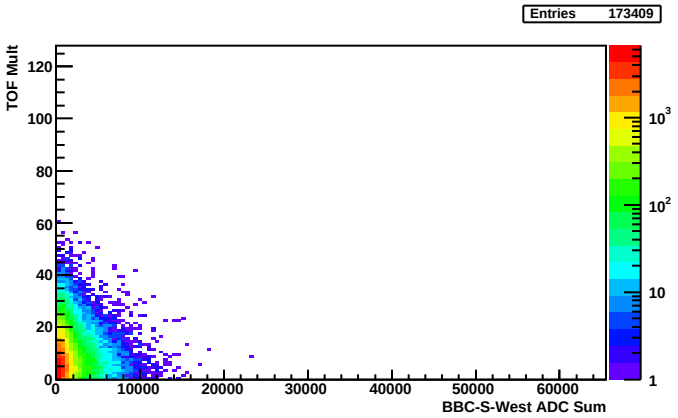

BBQ-VP004 (HI threshold)
Entries 2774544
ADC
QT Input Channel

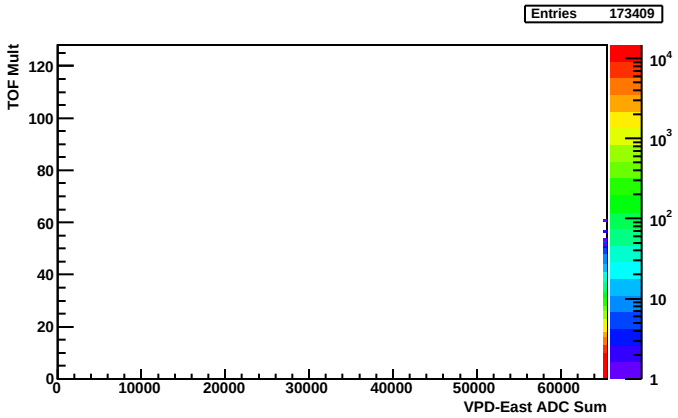

BBQ-VP004 (HI threshold)
Entries 2774544
TAC
QT Input Channel

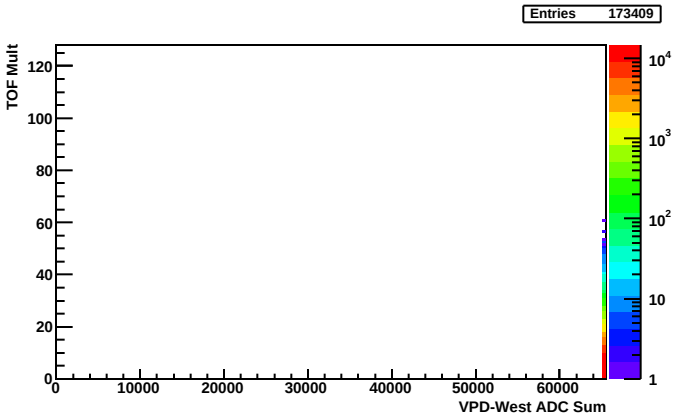

Entries 173409
TOF Mult
BBC-L-East ADC Sum

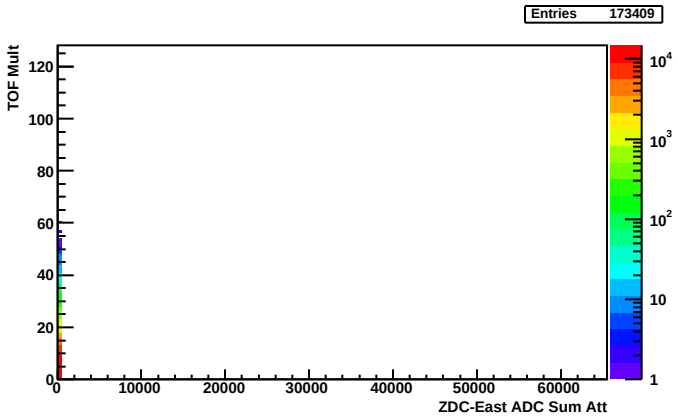

Entries 173409
TOF Mult
BBC-L-West ADC Sum

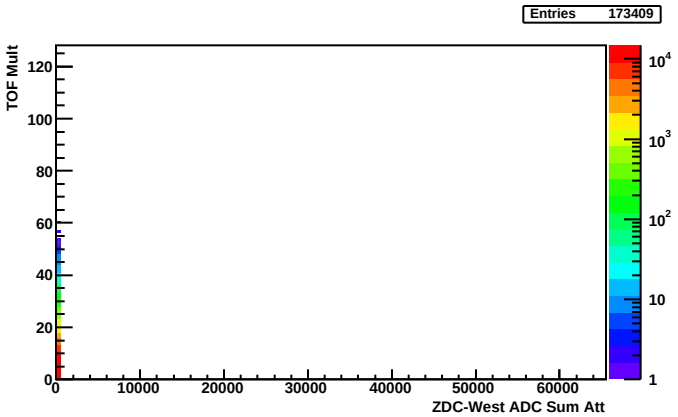

Entries 173409
TOF Mult
BBC-S-East ADC Sum

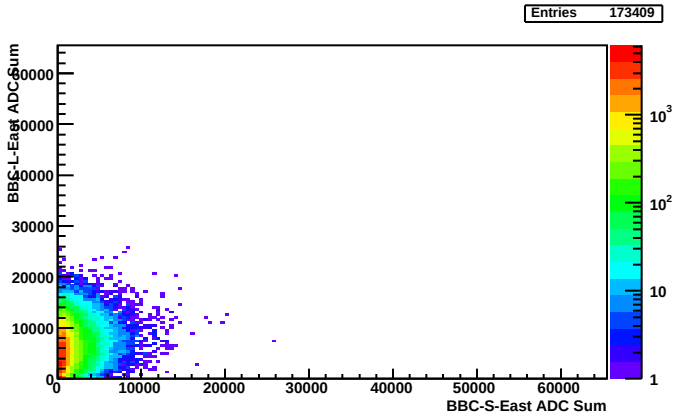

Entries 173409
TOF Mult
BBC-S-West ADC Sum

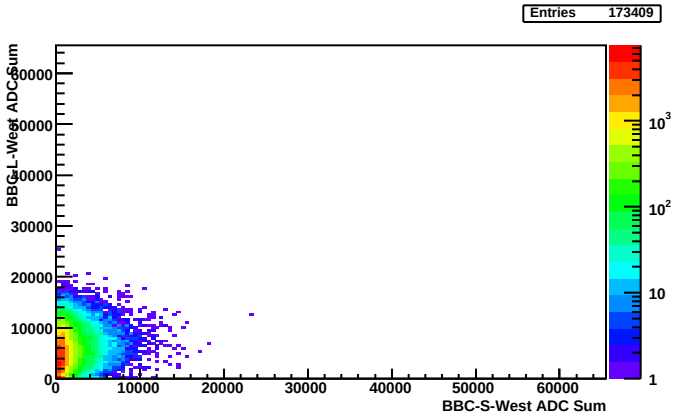

Entries 173409
TOF Mult
VPD-East ADC Sum

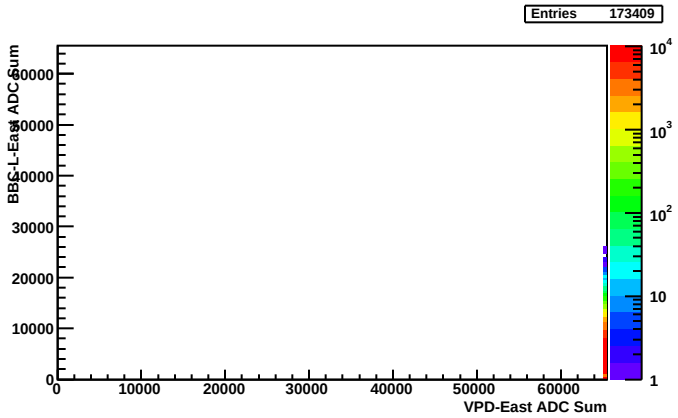

Entries 173409
TOF Mult
VPD-West ADC Sum

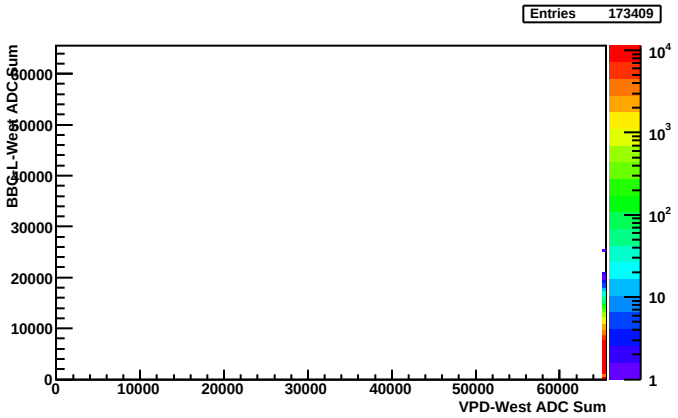

Entries 173409
TOF Mult
ZDC-East ADC Sum Att

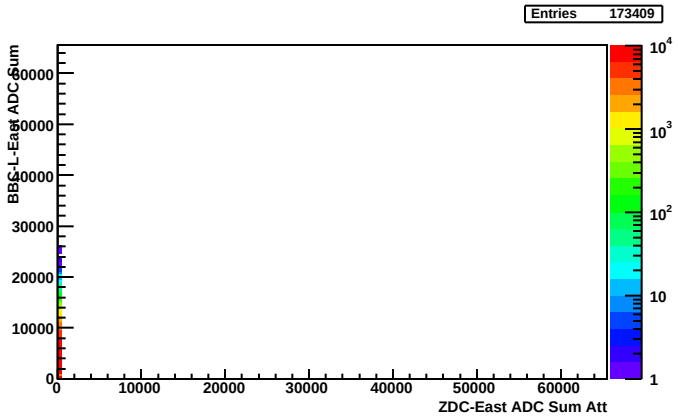

Entries 173409
TOF Mult
ZDC-West ADC Sum Att

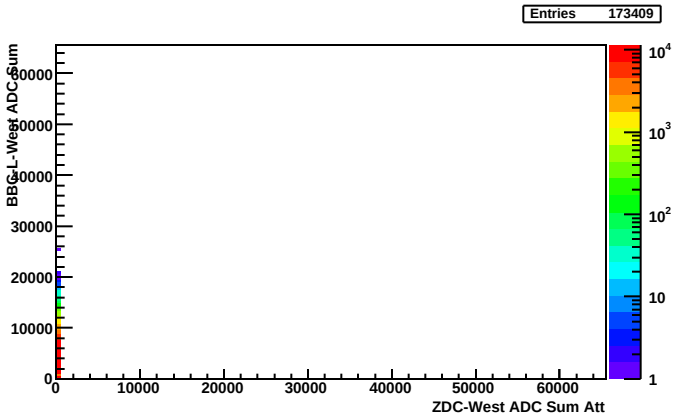

Entries 173409
BBC-L-East ADC Sum
BBC-S-East ADC Sum

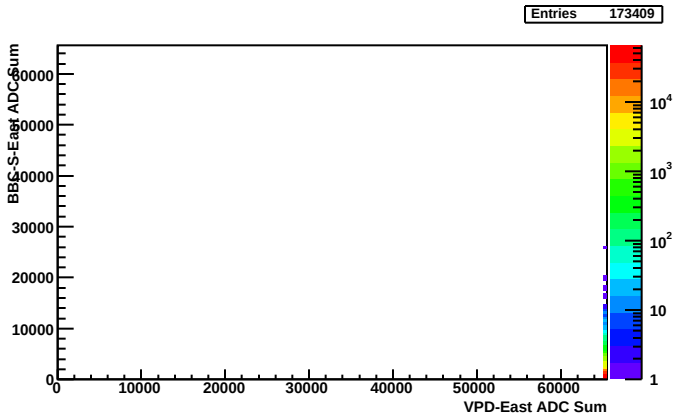

Entries 173409
BBC-L-West ADC Sum
BBC-S-West ADC Sum

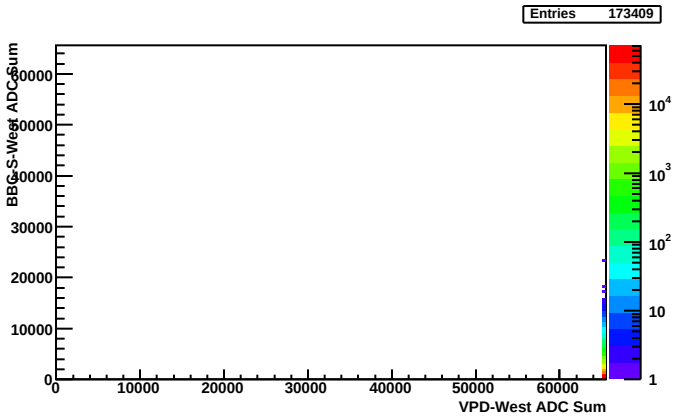

Entries 173409
BBC-L-East ADC Sum
VPD-East ADC Sum

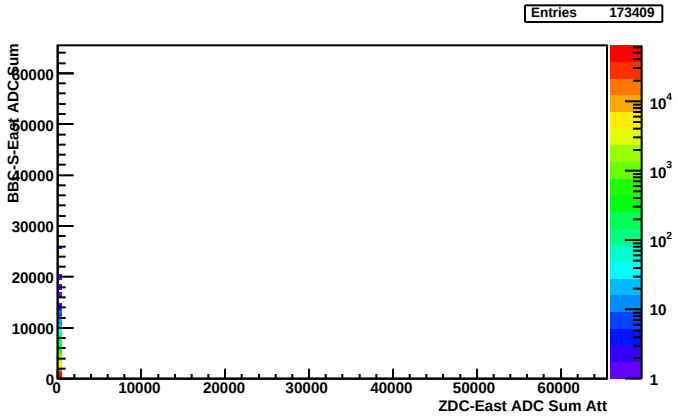

Entries 173409
BBC-L-West ADC Sum
VPD-West ADC Sum

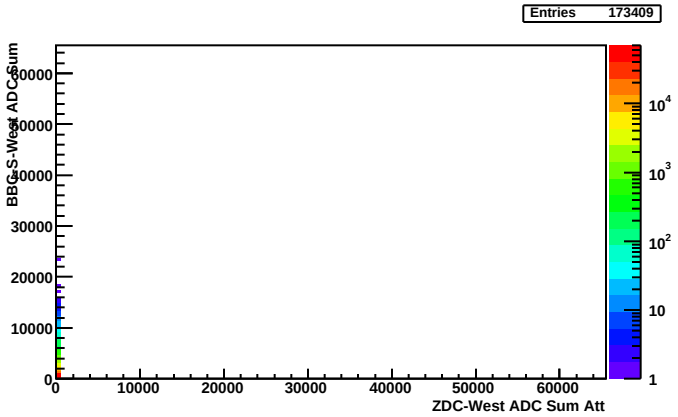

Entries 173409
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

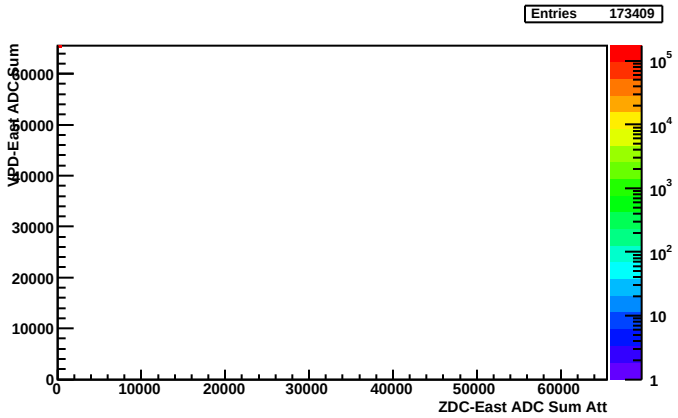

Entries 173409
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

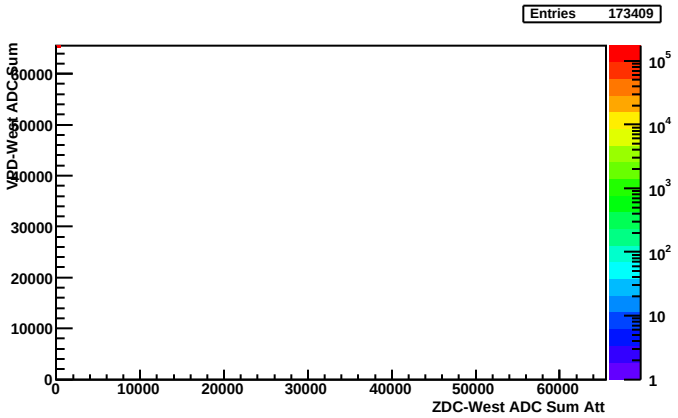

Entries 173409
BBC-S-East ADC Sum
VPD-East ADC Sum

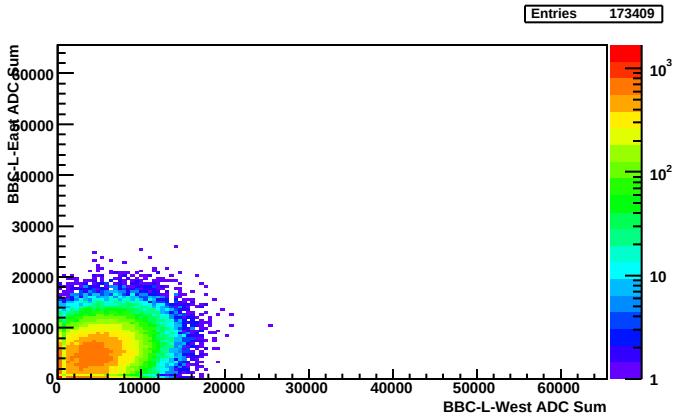

Entries 173409
BBC-S-West ADC Sum
VPD-West ADC Sum

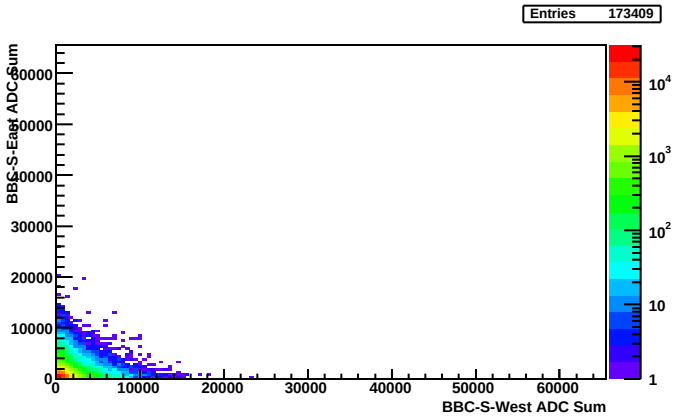

Entries 173409
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

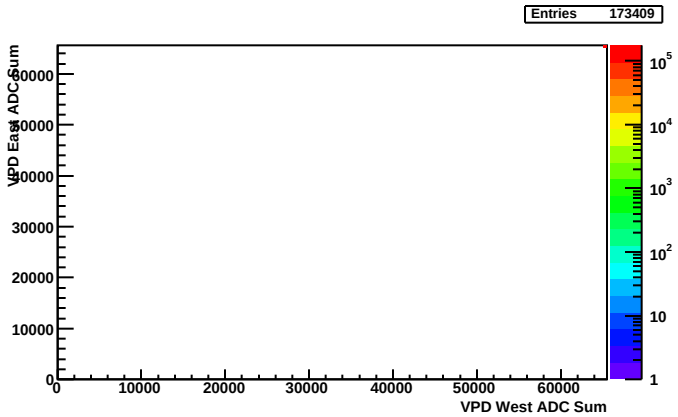

Entries 173409
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

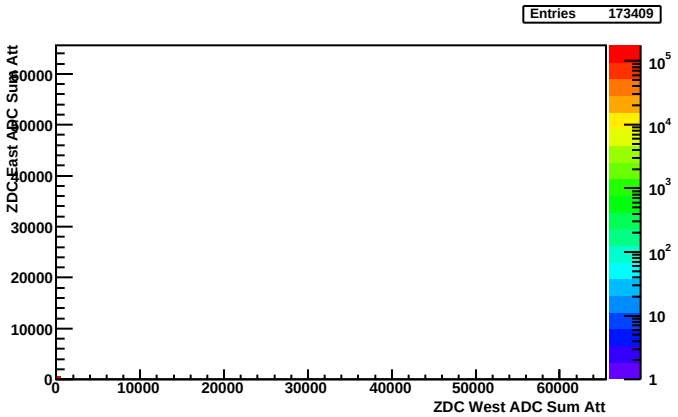

Entries 173409
VPD-East ADC Sum
ZDC-East ADC Sum Att

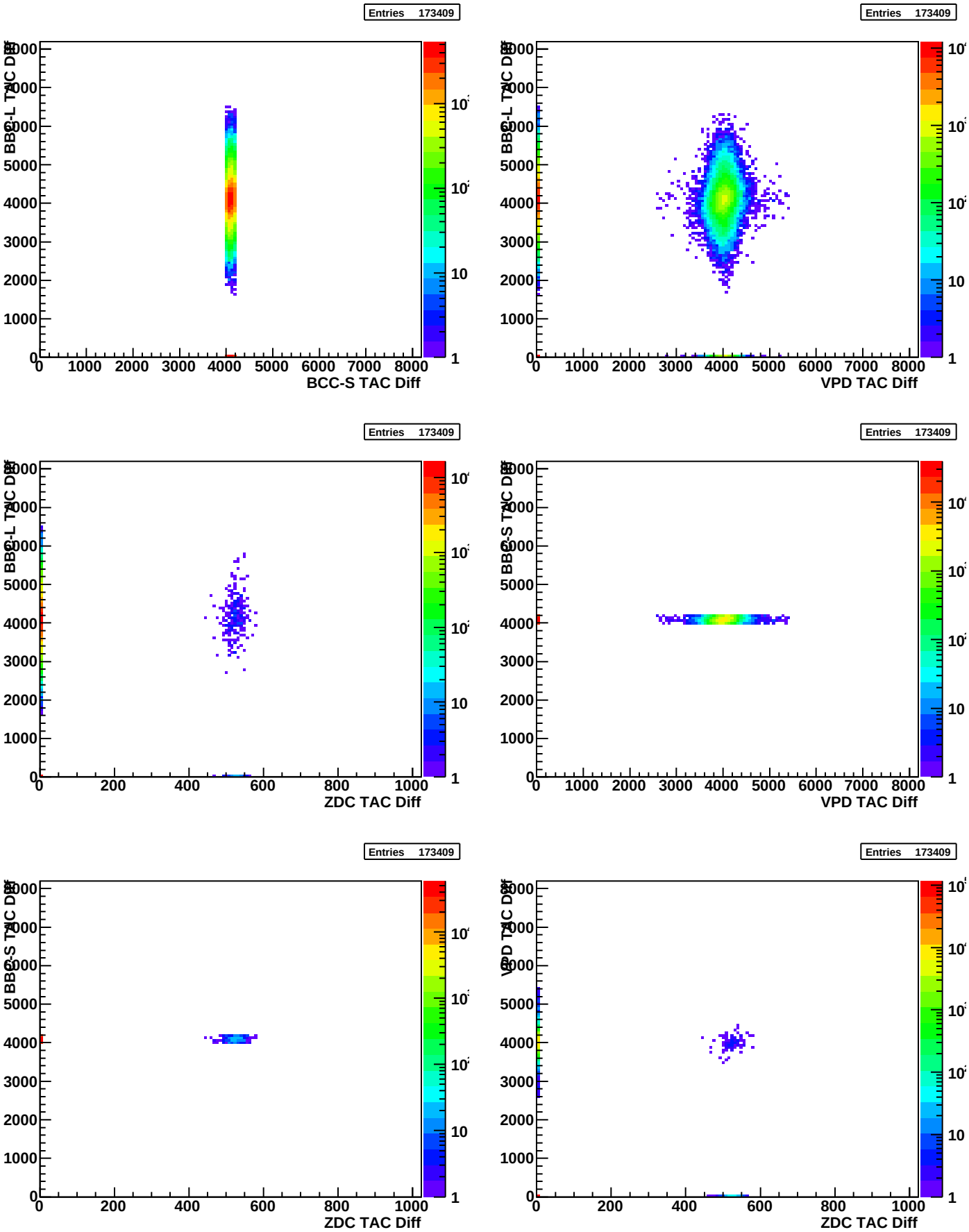

Entries 173409
VPD-West ADC Sum
ZDC-West ADC Sum Att

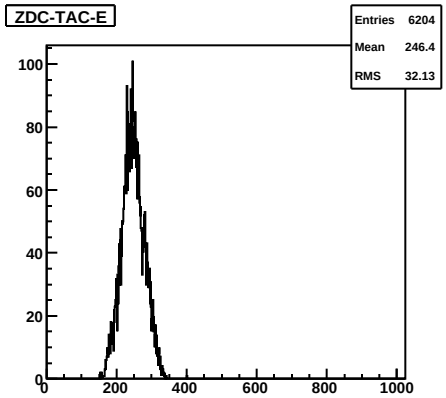

Entries 173409
BBC-L-East ADC Sum
BBC-L-West ADC Sum

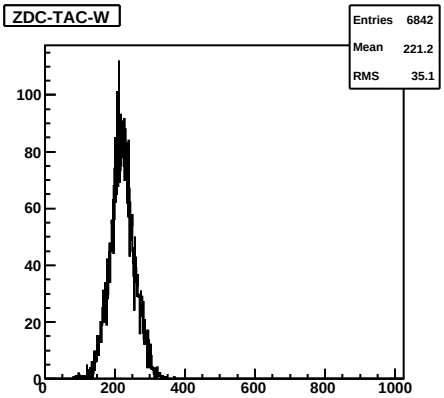

Entries 173409
BBC-S-East ADC Sum
BBC-S-West ADC Sum

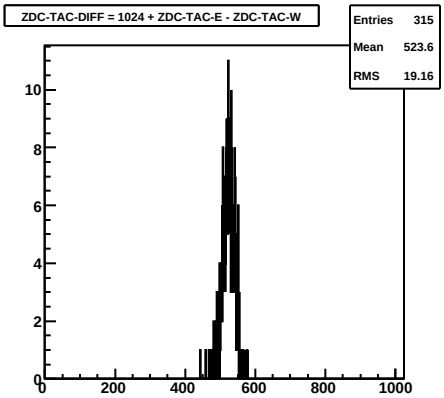

Entries 173409
VPD East ADC Sum
VPD West ADC Sum

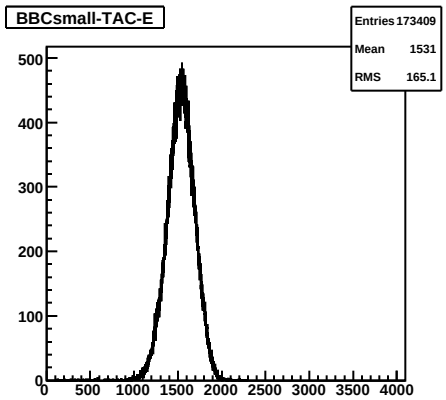

Entries 173409
ZDC East ADC Sum Att
ZDC West ADC Sum Att

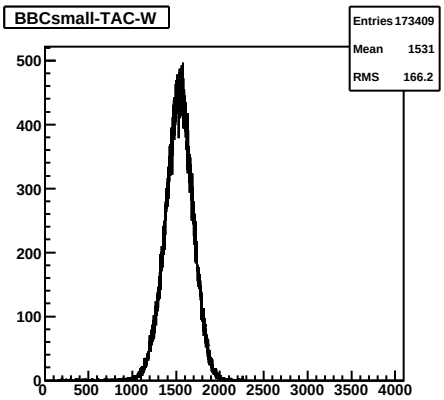

Entries 173409
BBC-L TAC Diff
BCC-S TAC Diff
Entries 173409
BBC-L TAC Diff
VPD TAC Diff
Entries 173409
BBC-L TAC Diff
ZDC TAC Diff
Entries 173409
BBC-S TAC Diff
VPD TAC Diff
Entries 173409
BBC-S TAC Diff
ZDC TAC Diff
Entries 173409
VPD TAC Diff
ZDC TAC Diff

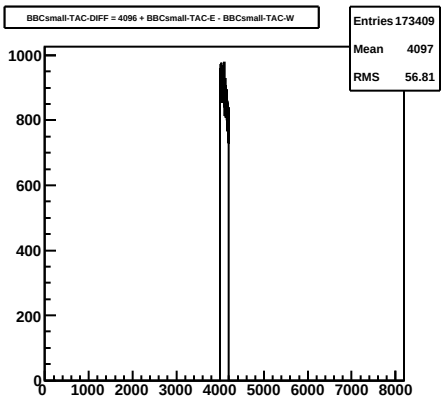

ZDC-TAC-E
Entries 6204
Mean 246.4
RMS 32.13

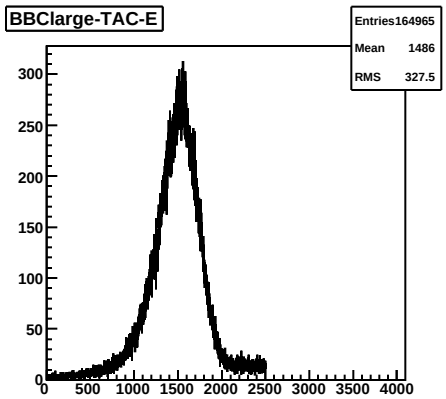

ZDC-TAC-W
Entries 6842
Mean 221.2
RMS 35.1

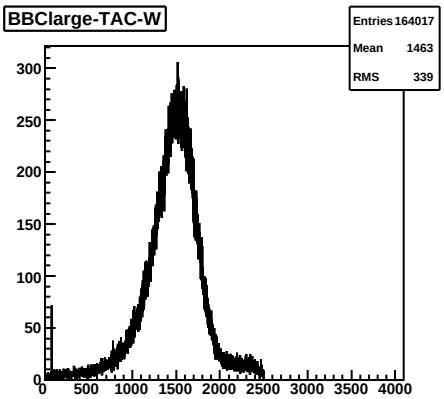

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 315
Mean 523.6
RMS 19.16

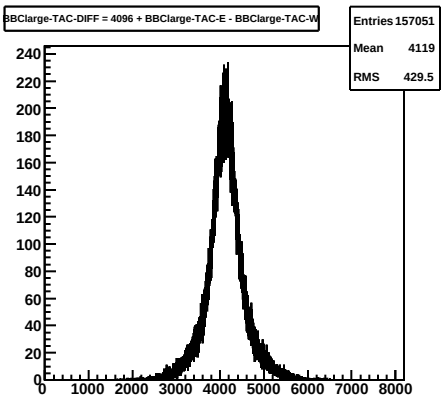

BBCsmall-TAC-E
Entries 173409
Mean 1531
RMS 165.1

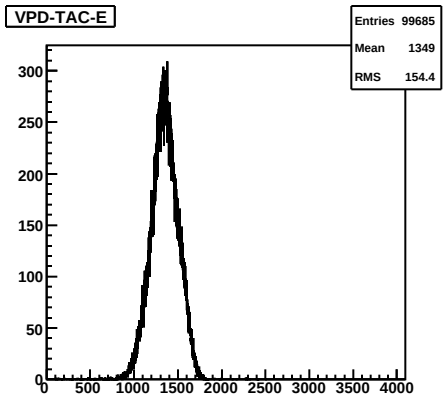

BBCsmall-TAC-W
Entries 173409
Mean 1531
RMS 166.2

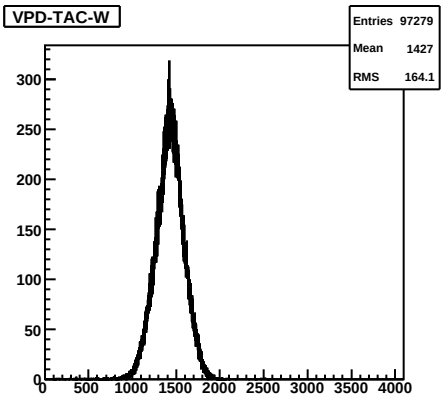

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 173409
Mean 4097
RMS 56.81

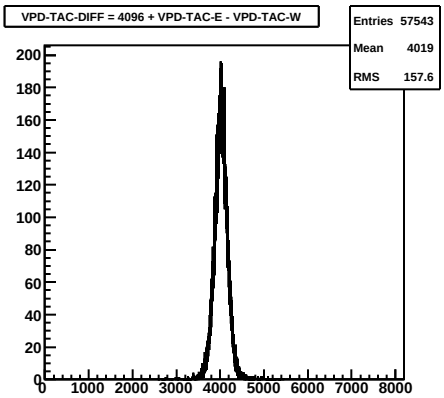

BBClarge-TAC-E
Entries 164965
Mean 1486
RMS 327.5

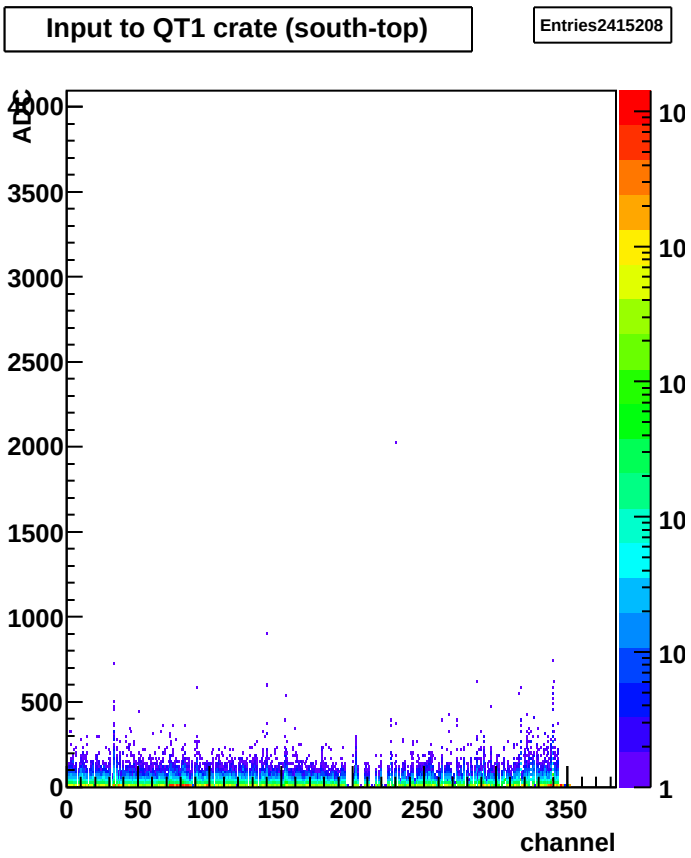

BBClarge-TAC-W
Entries 164017
Mean 1463
RMS 339

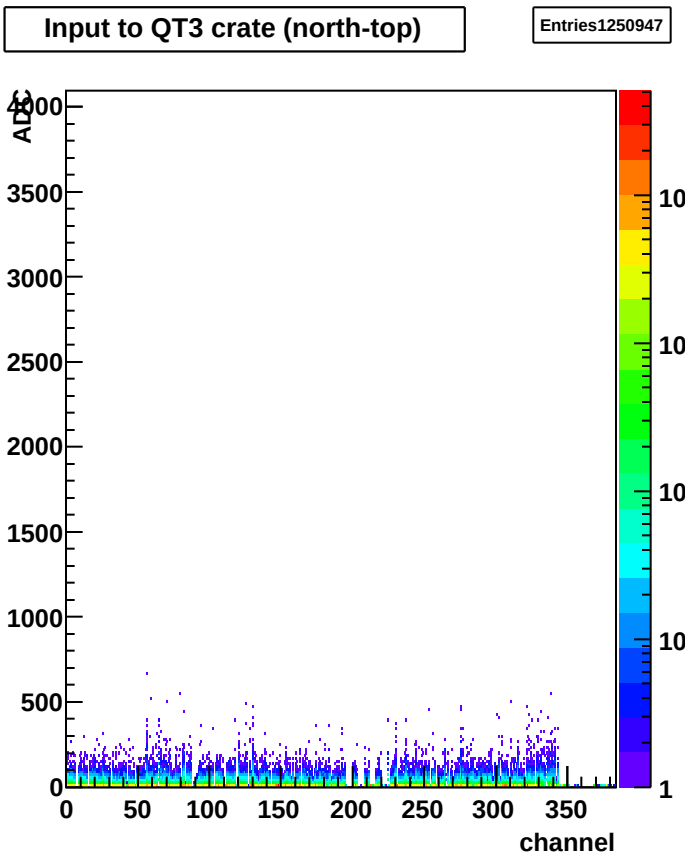

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 157051
Mean 4119
RMS 429.5

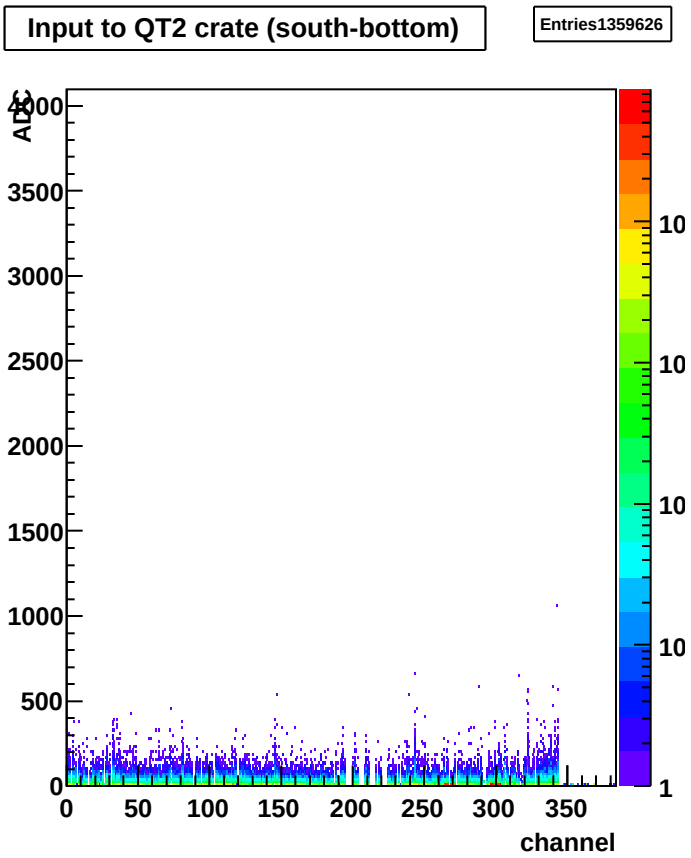

VPD-TAC-E
Entries 99685
Mean 1349
RMS 154.4

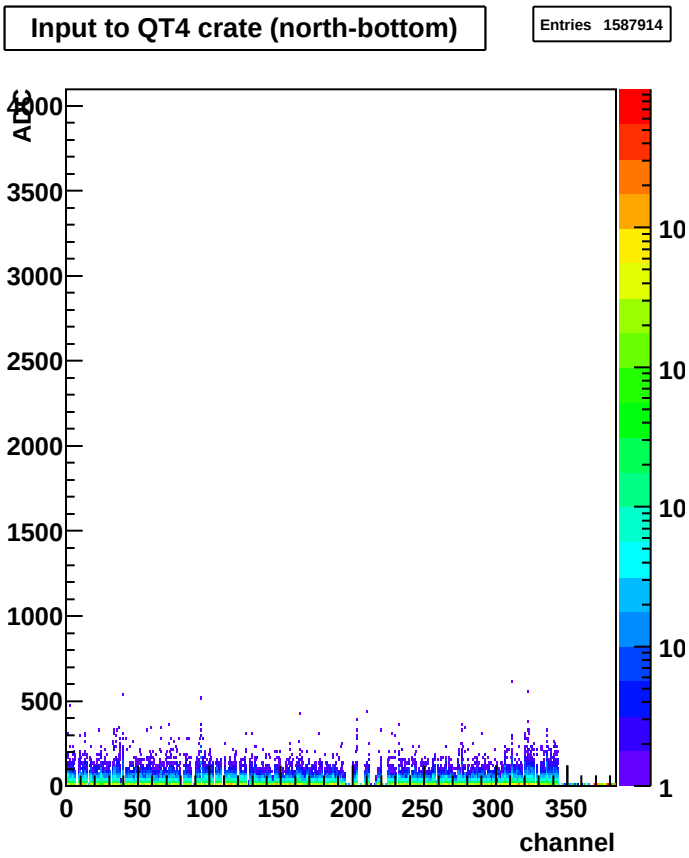

VPD-TAC-W
Entries 97279
Mean 1427
RMS 164.1

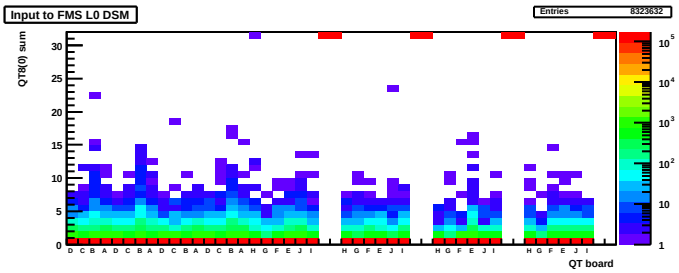

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 57543
Mean 4019
RMS 157.6

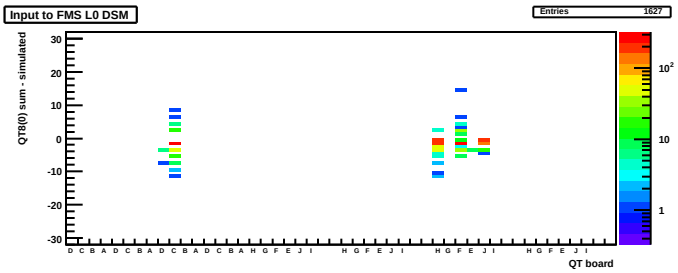

Input to QT1 crate (south-top)
Entries2415208
ADC
channel

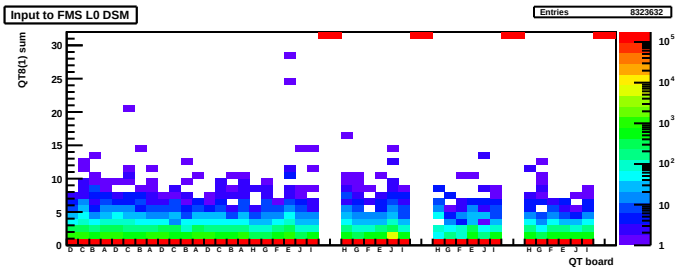

Input to QT3 crate (north-top)
Entries1250947
ADC
channel

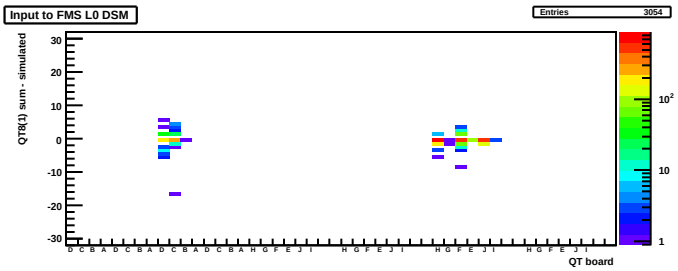

Input to QT2 crate (south-bottom)
Entries1359626
ADC
channel

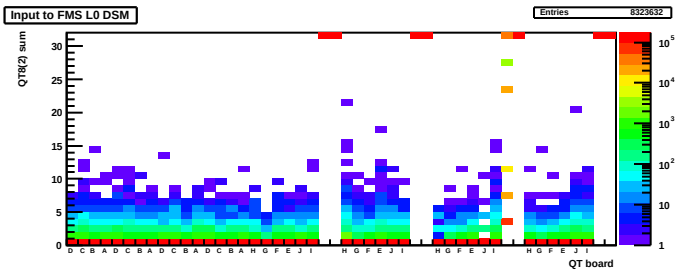

Input to QT4 crate (north-bottom)
Entries 1587914
ADC
channel

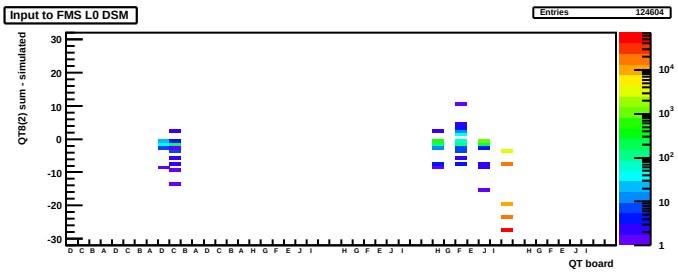

Input to FMS L0 DSM
Entries 8323632
QT8(0) sum
QT board

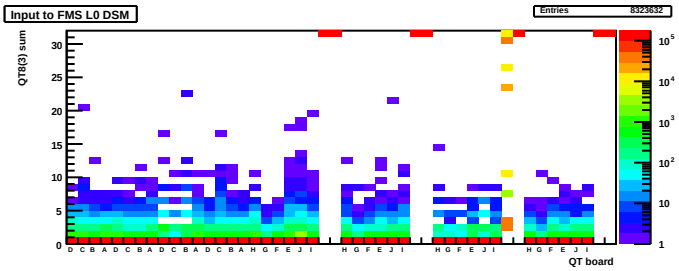

Input to FMS L0 DSM
Entries 1627
QT8(0) sum - simulated
QT board

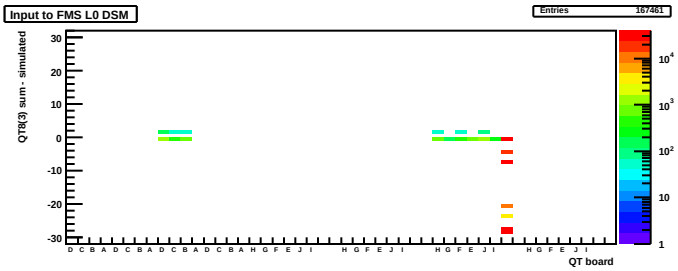

Input to FMS L0 DSM
Entries 8323632
QT8(1) sum
QT board

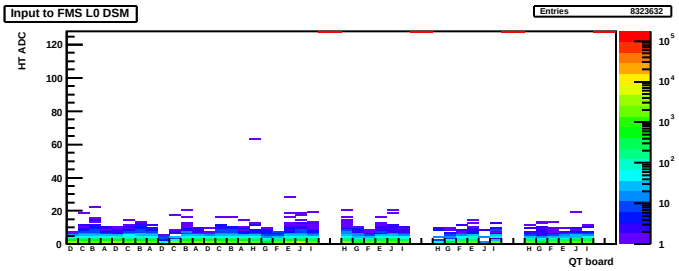

Input to FMS L0 DSM
Entries 3054
QT8(1) sum - simulated
QT board

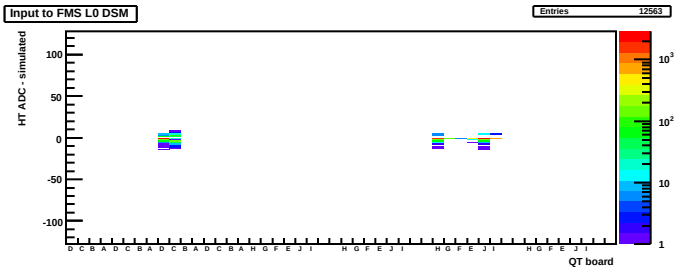

Input to FMS L0 DSM
Entries 8323632
QT8(2) sum
QT board

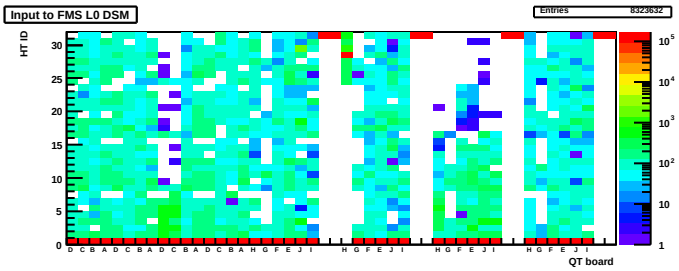

Input to FMS L0 DSM
Entries 124604
QT8(2) sum - simulated
QT board

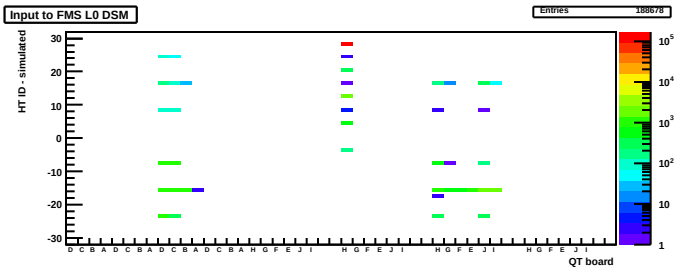

Input to FMS L0 DSM
Entries 8323632
QT8(3) sum
QT board

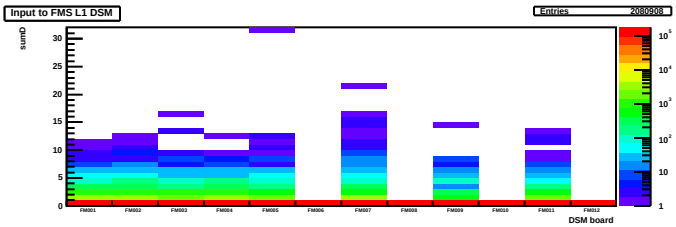

Input to FMS L0 DSM
Entries 167461
QT8(3) sum - simulated
QT board

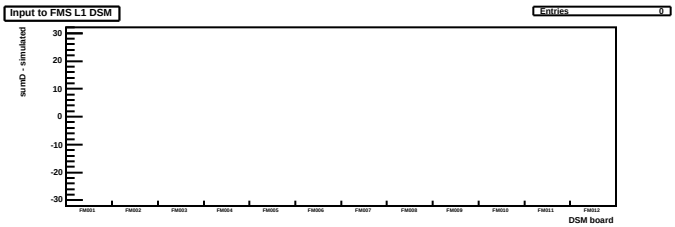

Input to FMS L0 DSM
Entries 8323632
HT ADC
QT board

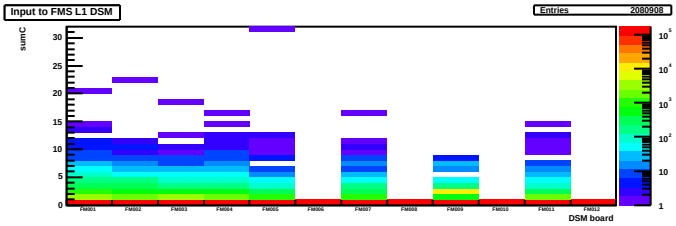

Input to FMS L0 DSM
Entries 12563
HT ADC - simulated
QT board

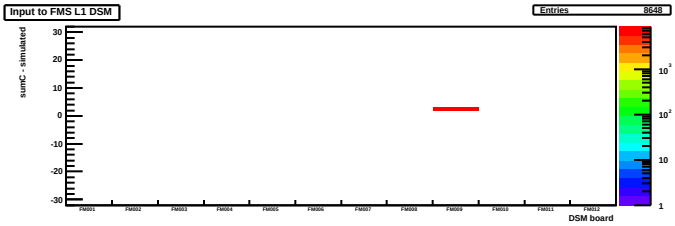

Input to FMS L0 DSM
Entries 8323632
HT ID
QT board

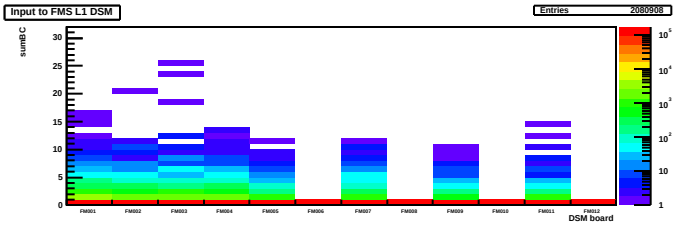

Input to FMS L0 DSM
Entries 188678
HT ID - simulated
QT board

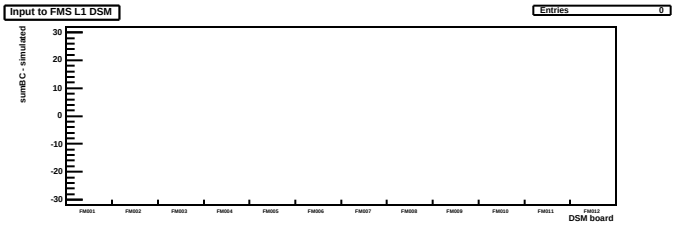

Input to FMS L1 DSM
Entries 2080908
sumD
DSM board

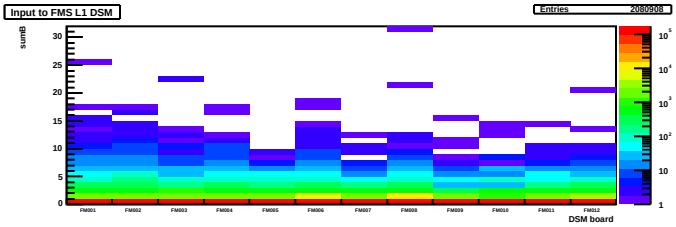

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

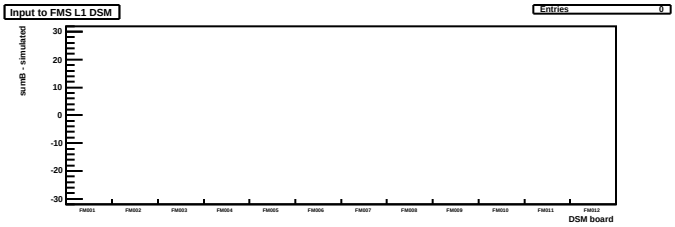

Input to FMS L1 DSM
Entries 2080908
sumC
DSM board

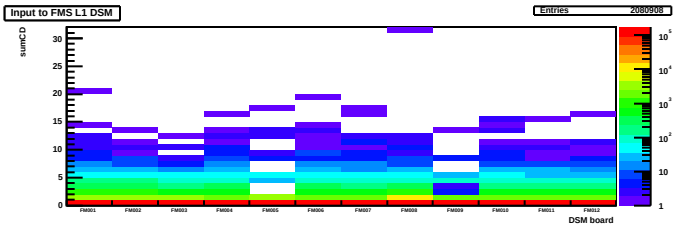

Input to FMS L1 DSM
Entries 8648
sumC - simulated
DSM board

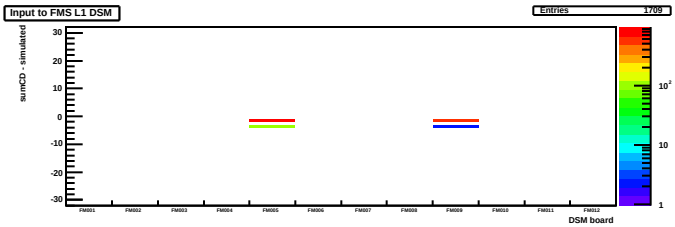

Input to FMS L1 DSM
Entries 2080908
sumBC
DSM board

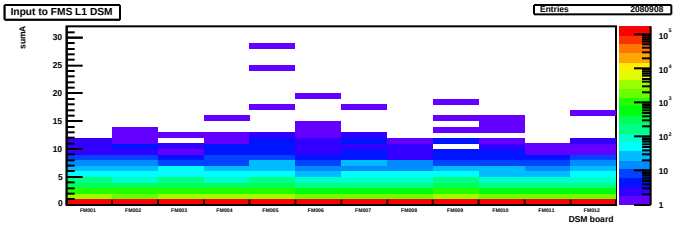

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

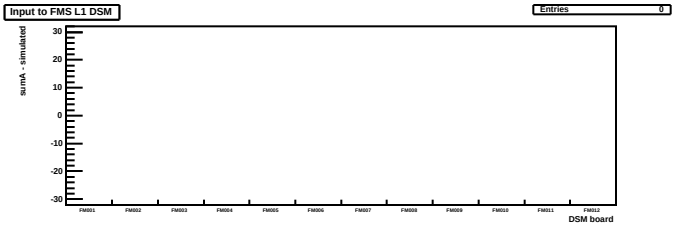

Input to FMS L1 DSM
Entries 2080908
sumB
DSM board

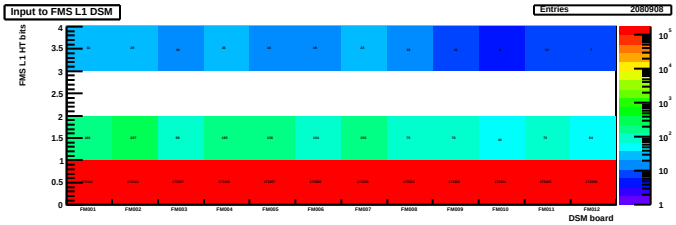

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

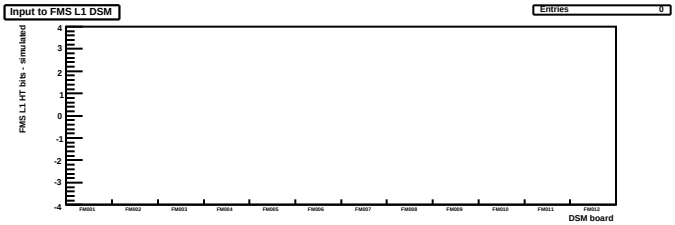

Input to FMS L1 DSM
Entries 2080908
sumCD
DSM board

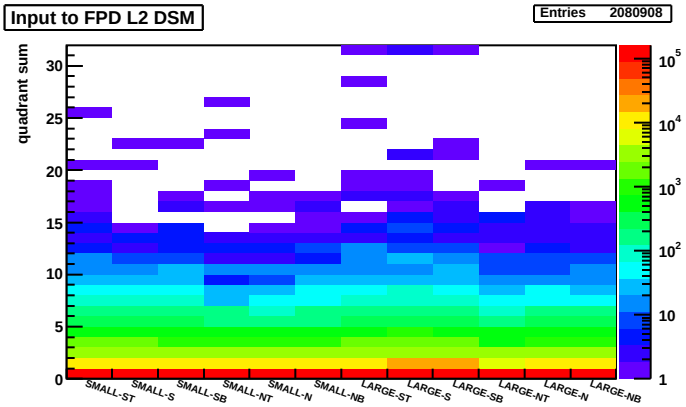

Input to FMS L1 DSM
Entries 1709
sumCD - simulated
DSM board

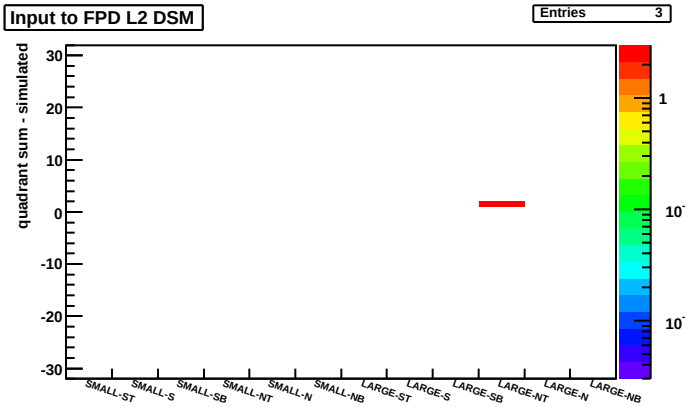

Input to FMS L1 DSM
Entries 2080908
sumA
DSM board

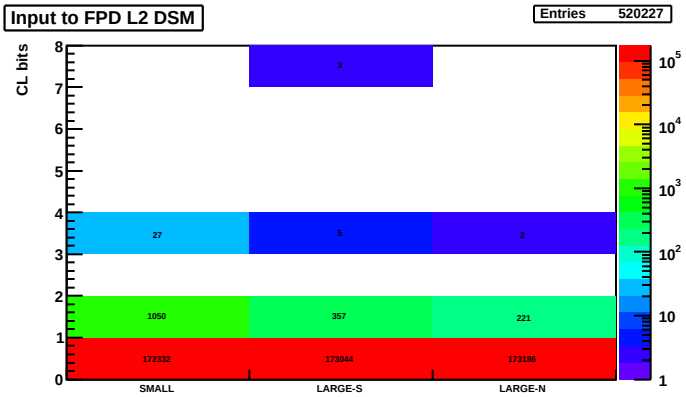

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

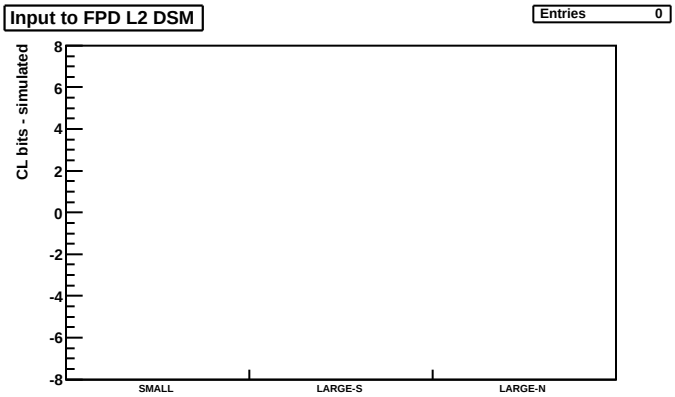

Input to FMS L1 DSM
Entries 2080908
FMS L1 HT bits
DSM board

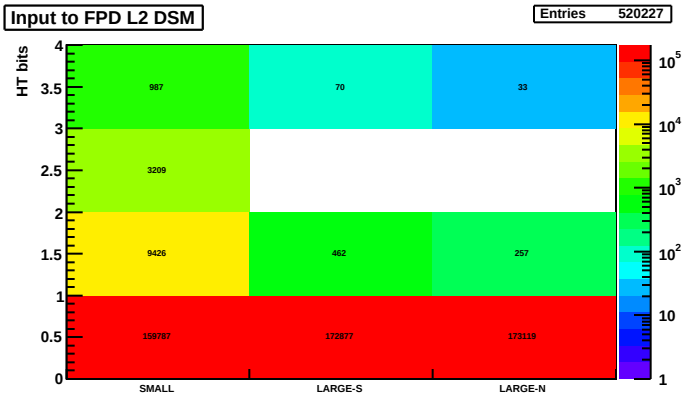

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

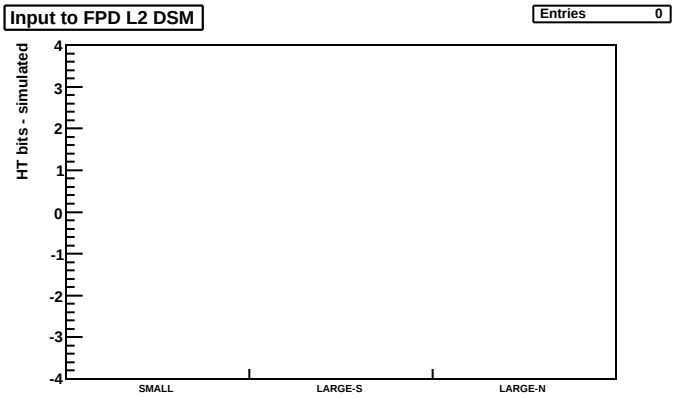

Input to FPD L2 DSM
Entries 2080908
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

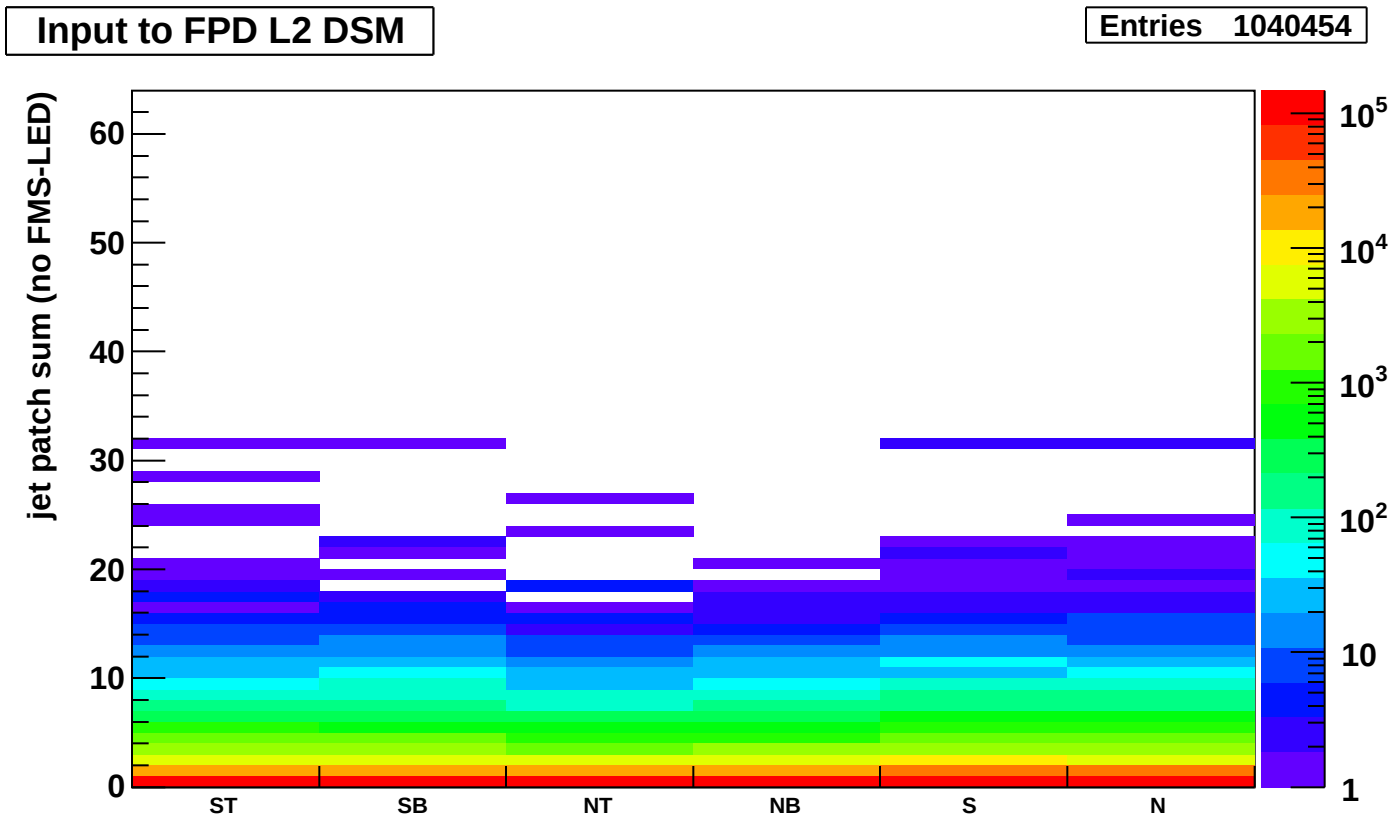

Input to FPD L2 DSM
Entries 3
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

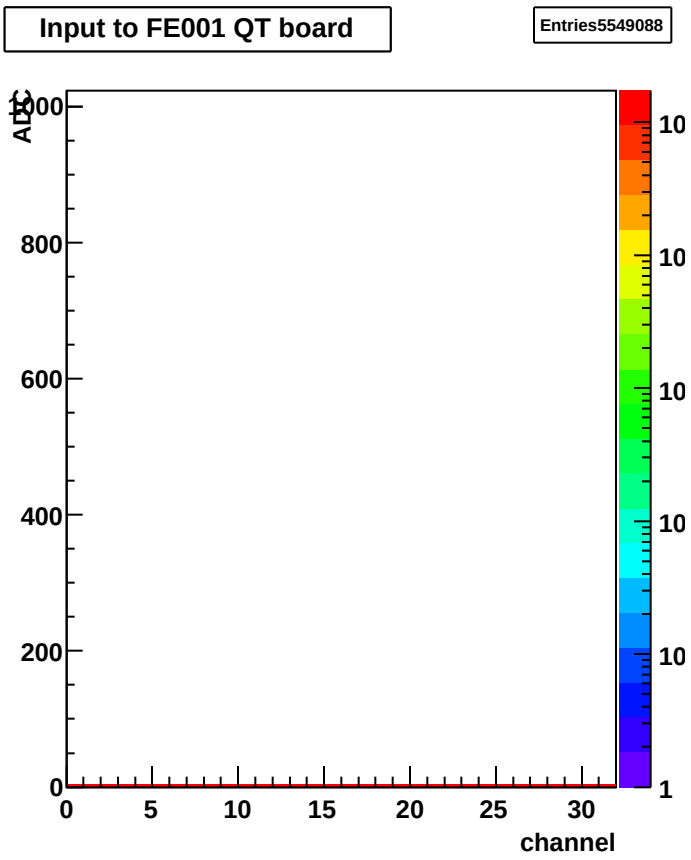

Input to FPD L2 DSM
Entries 520227
CL bits
3
27
5
2
1050
357
221
172332
173044
173186
SMALL
LARGE-S
LARGE-N

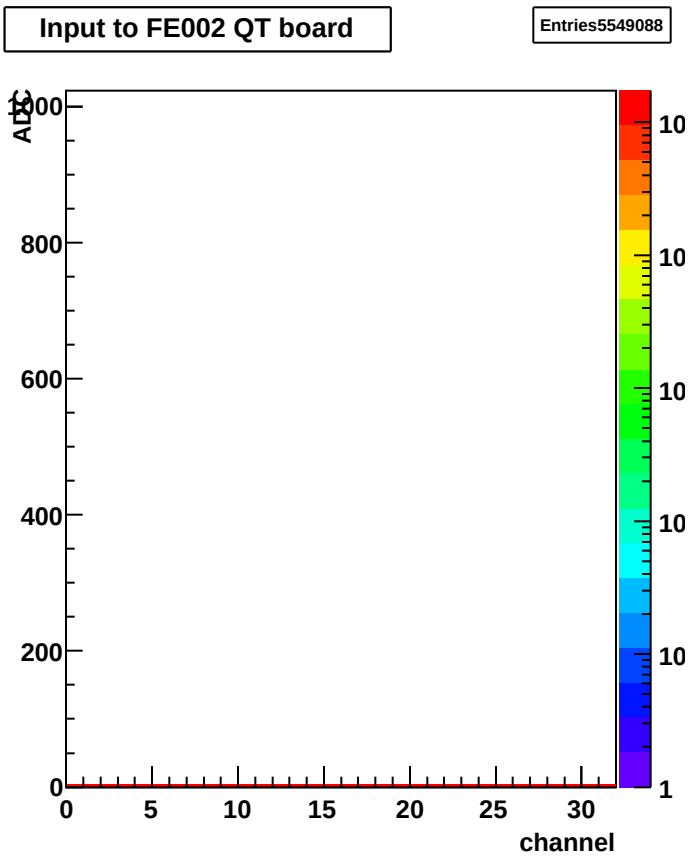

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

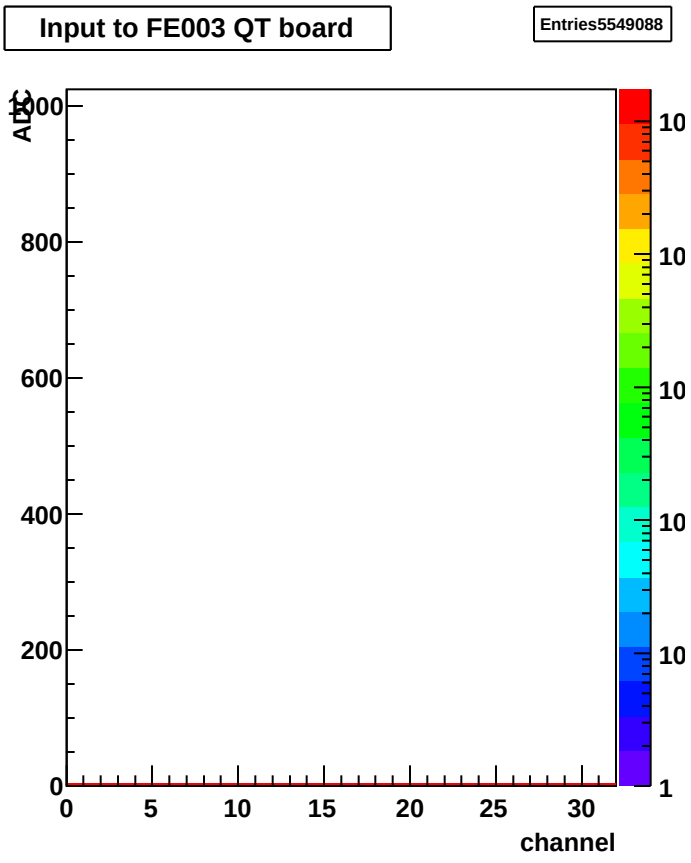

Input to FPD L2 DSM
Entries 520227
HT bits
987
70
33
3209
9426
462
257
159787
172877
173119
SMALL
LARGE-S
LARGE-N

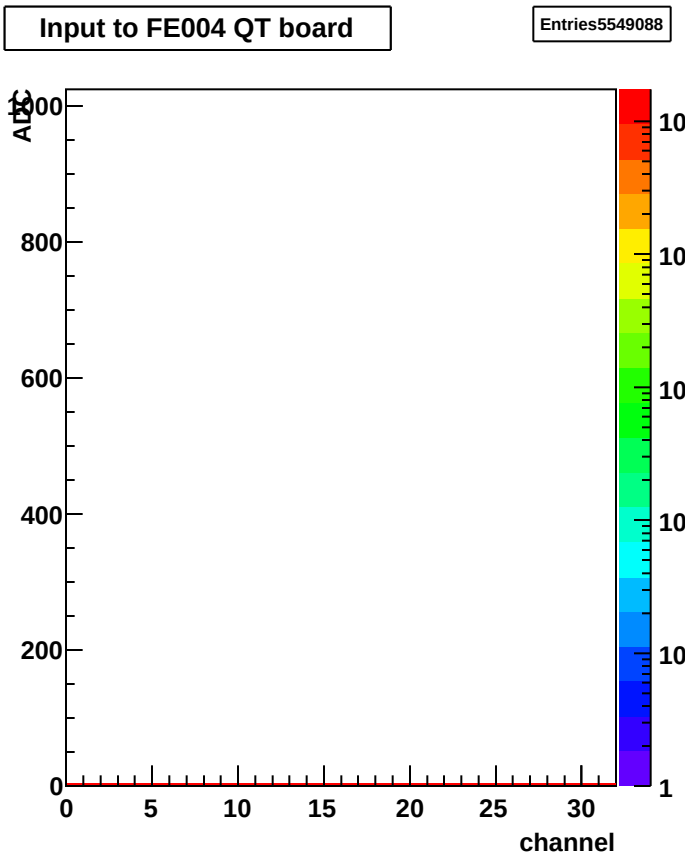

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

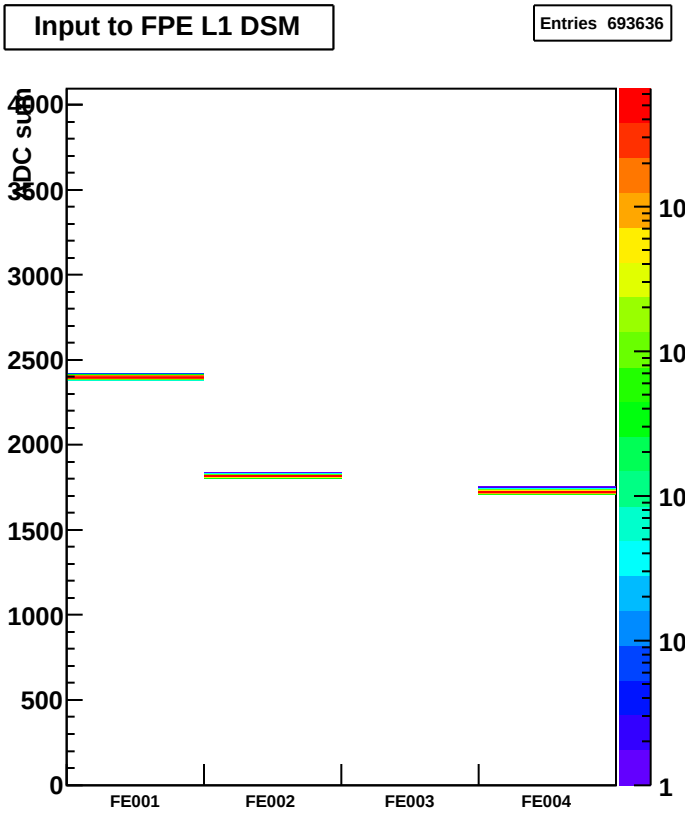

Input to FPD L2 DSM
Entries 1040454
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

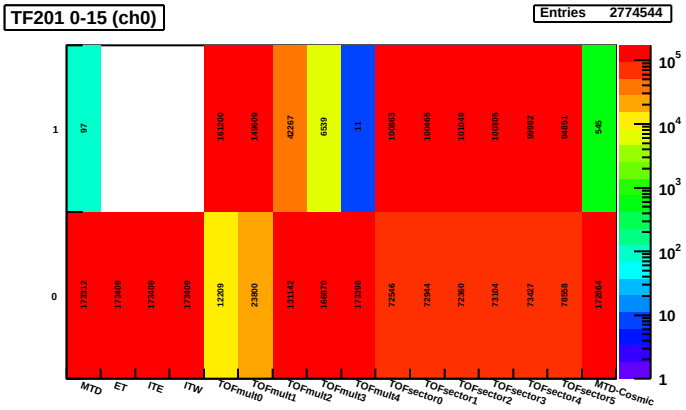

Input to FE001 QT board
Entries5549088
ADC
channel

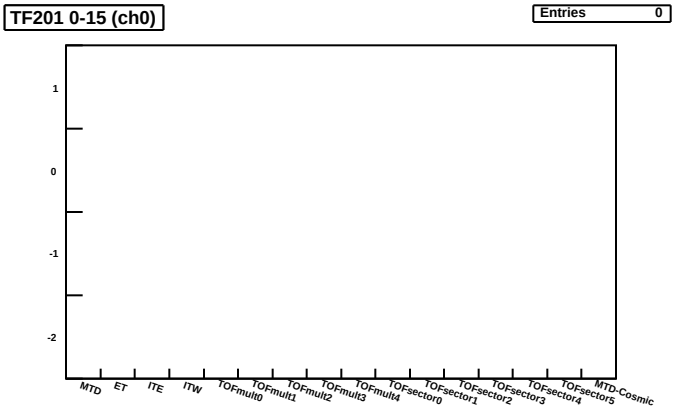

Input to FE002 QT board
Entries5549088
ADC
channel

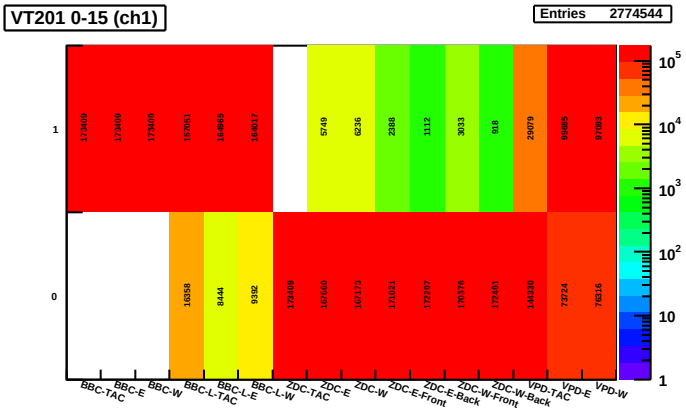

Input to FE003 QT board
Entries5549088
ADC
channel

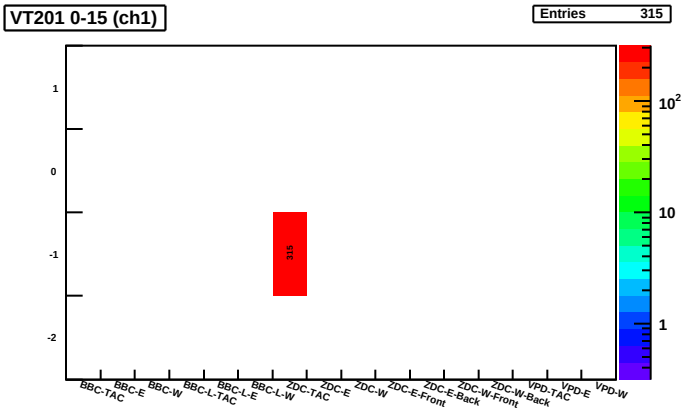

Input to FE004 QT board
Entries5549088
ADC
channel

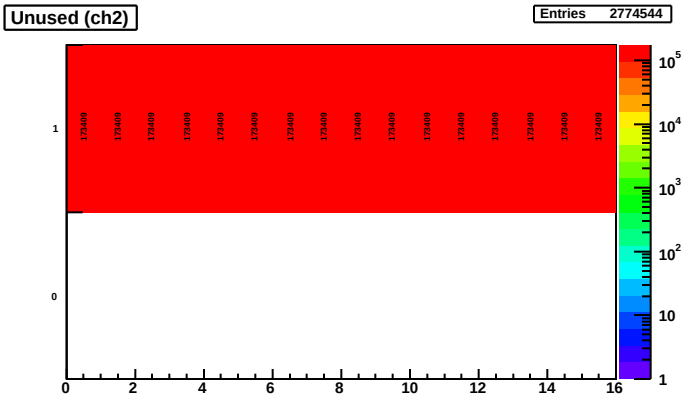

Input to FPE L1 DSM
Entries 693636
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

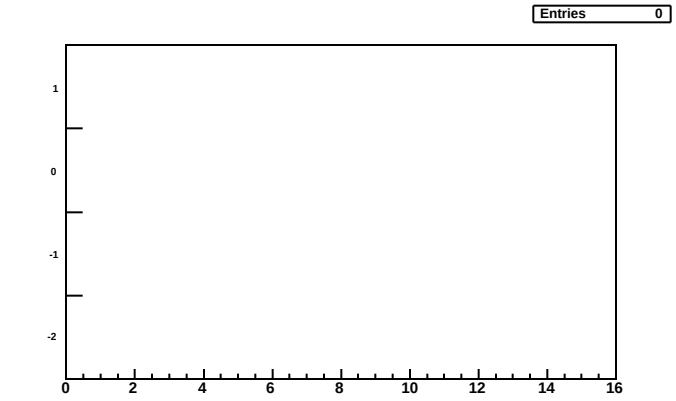

TF201 0-15 (ch0)
Entries 2774544
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

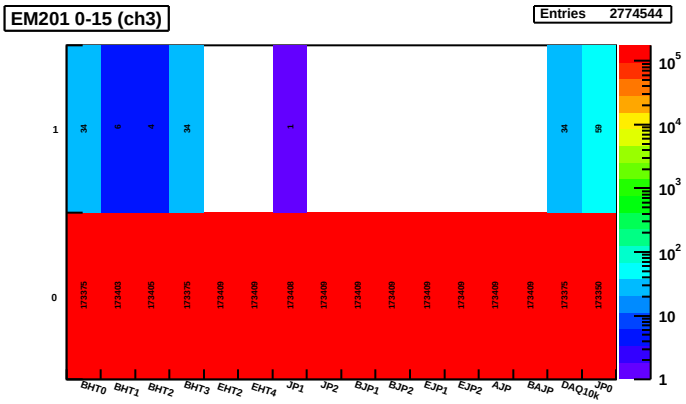

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

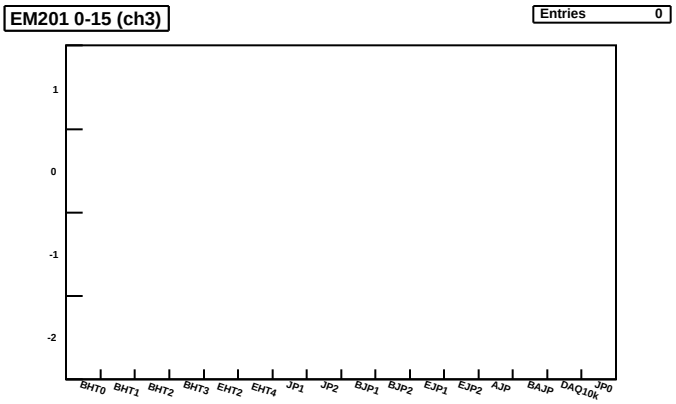

VT201 0-15 (ch1)
Entries 2774544
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 315
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 2774544


Entries 0


EM201 0-15 (ch3)
Entries 2774544
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0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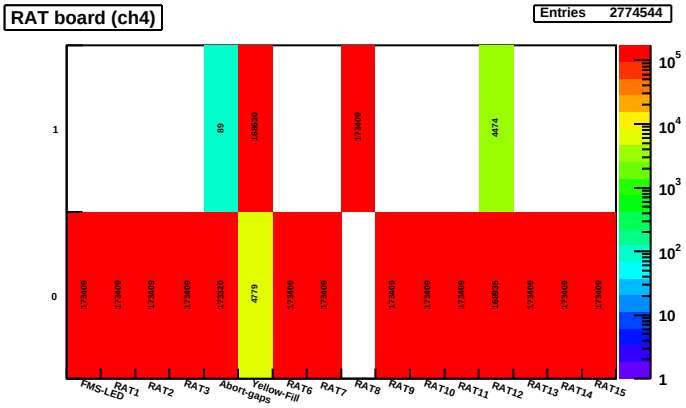

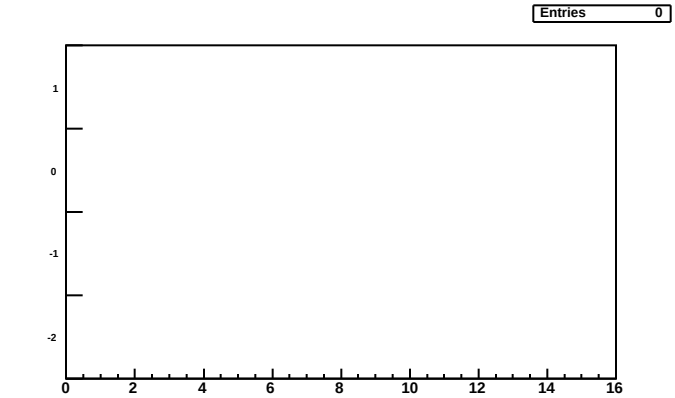

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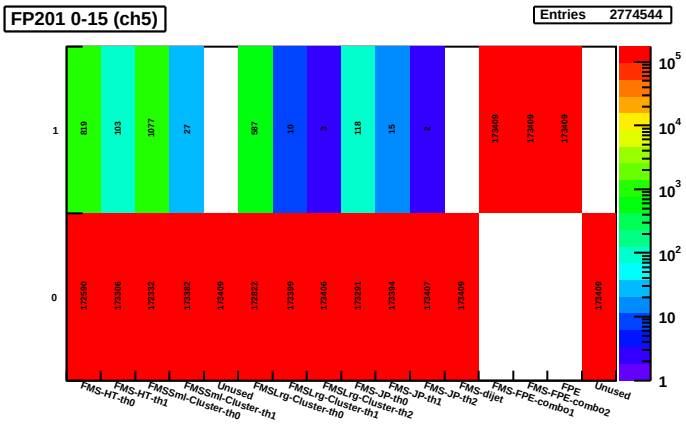

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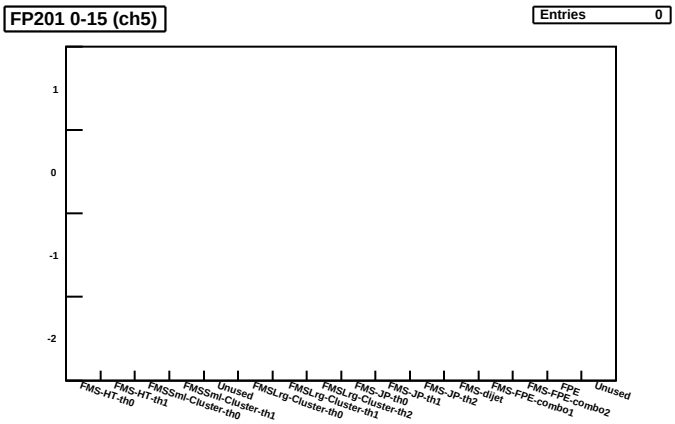

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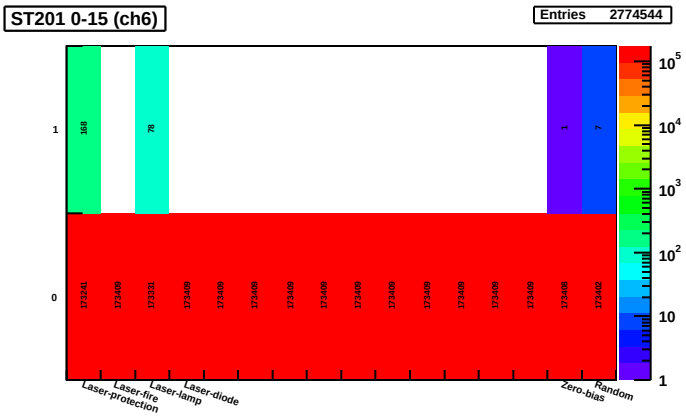

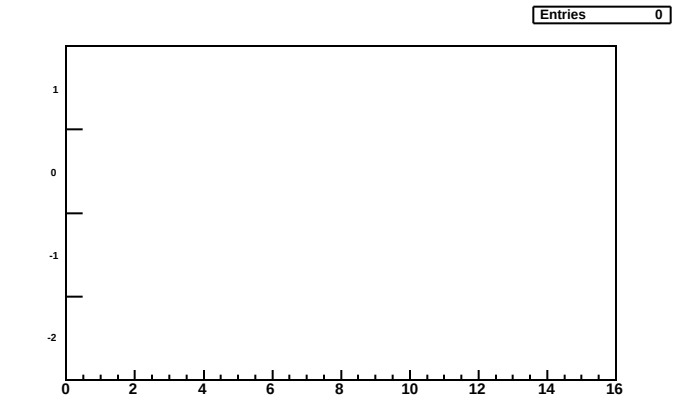
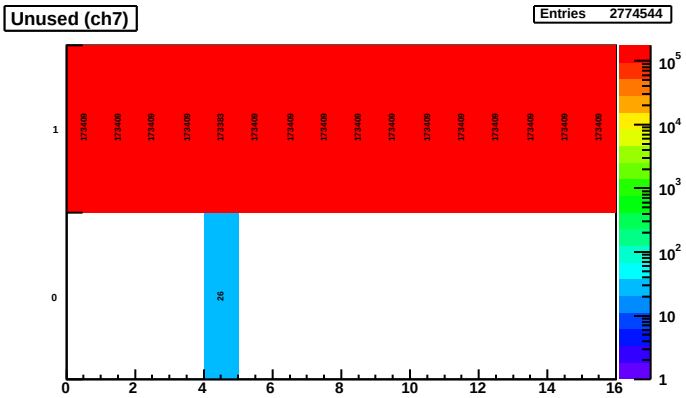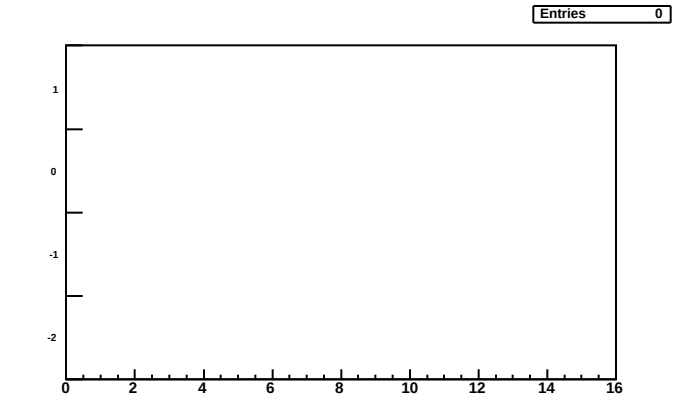
Unused (ch7)