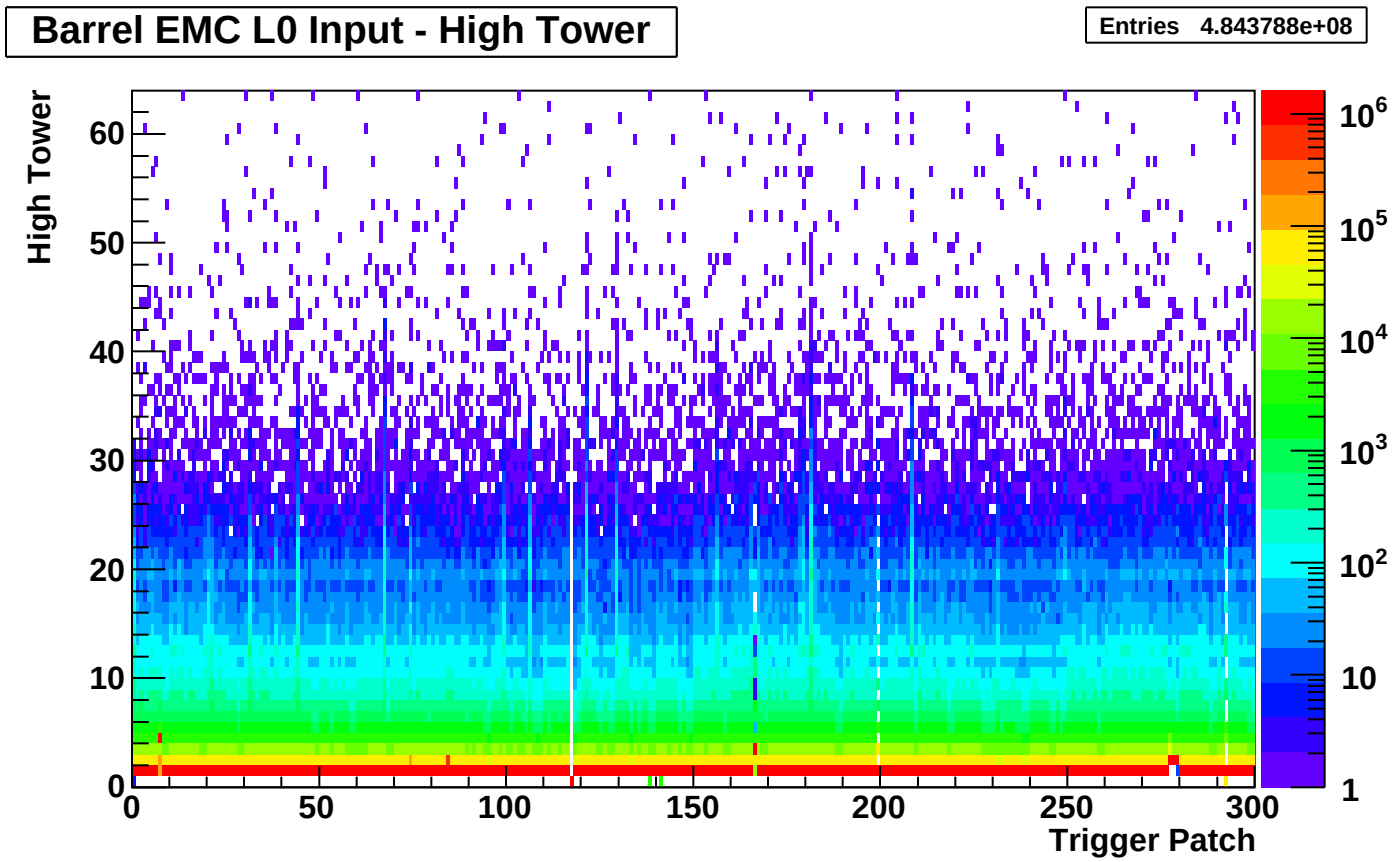
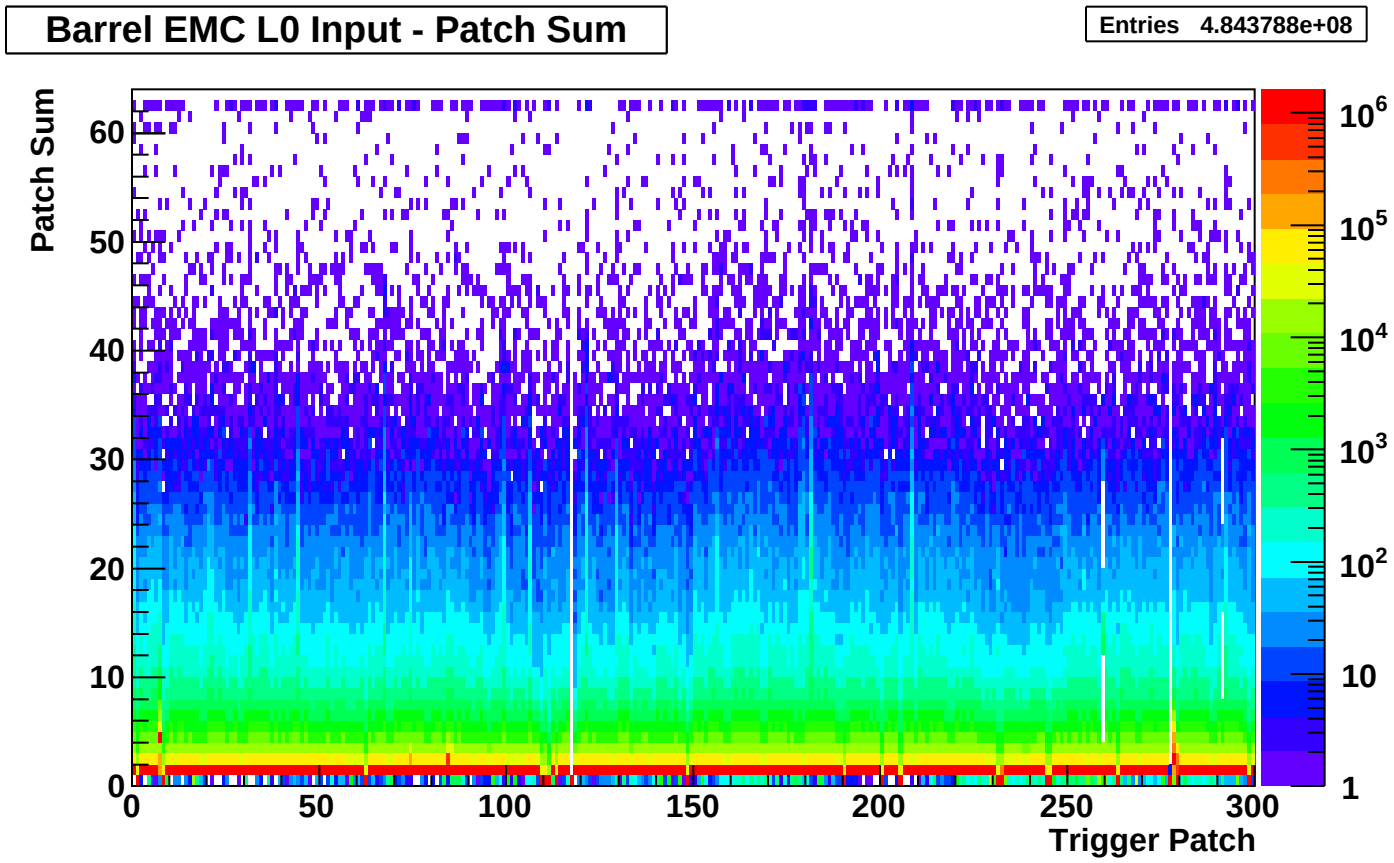
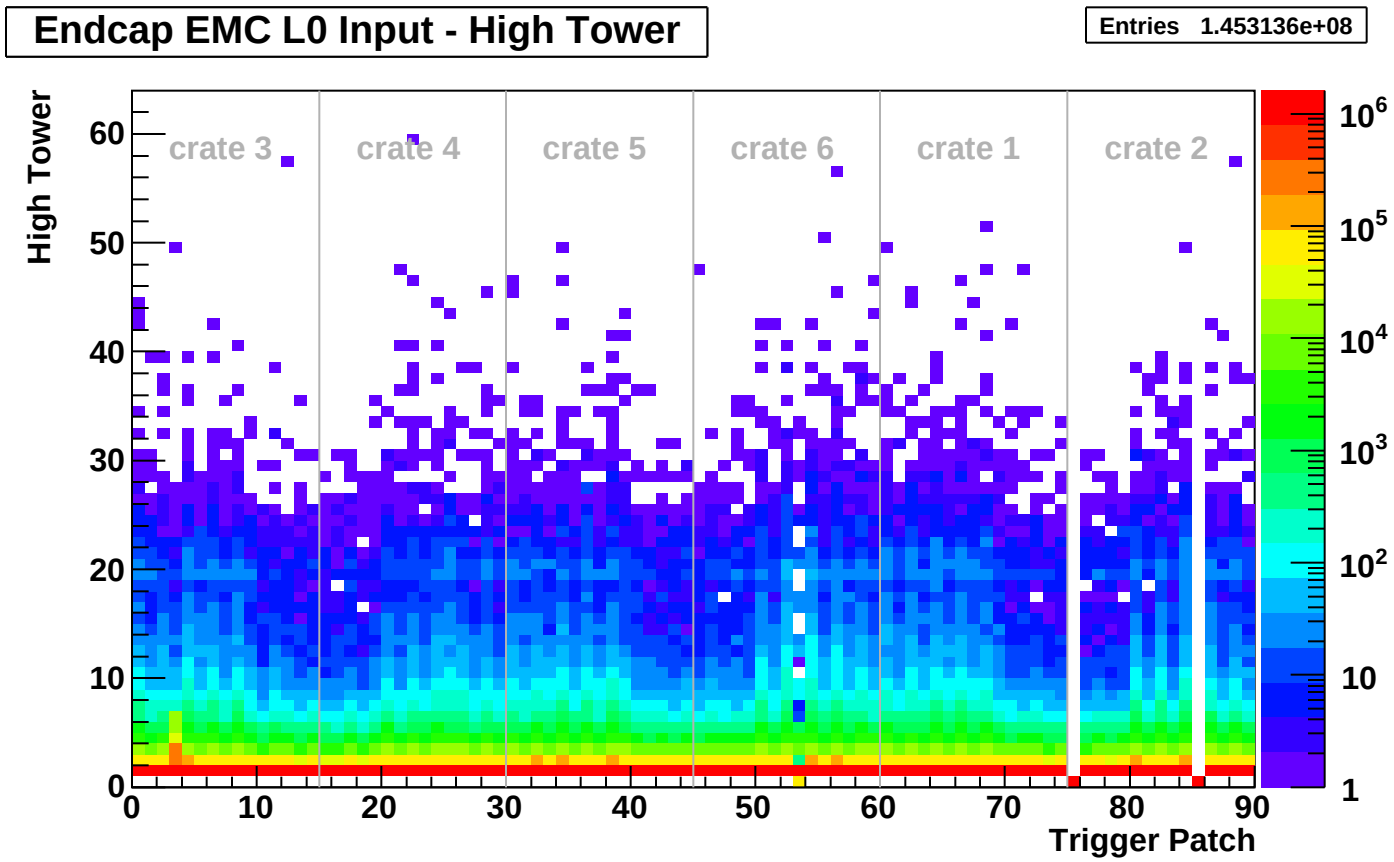
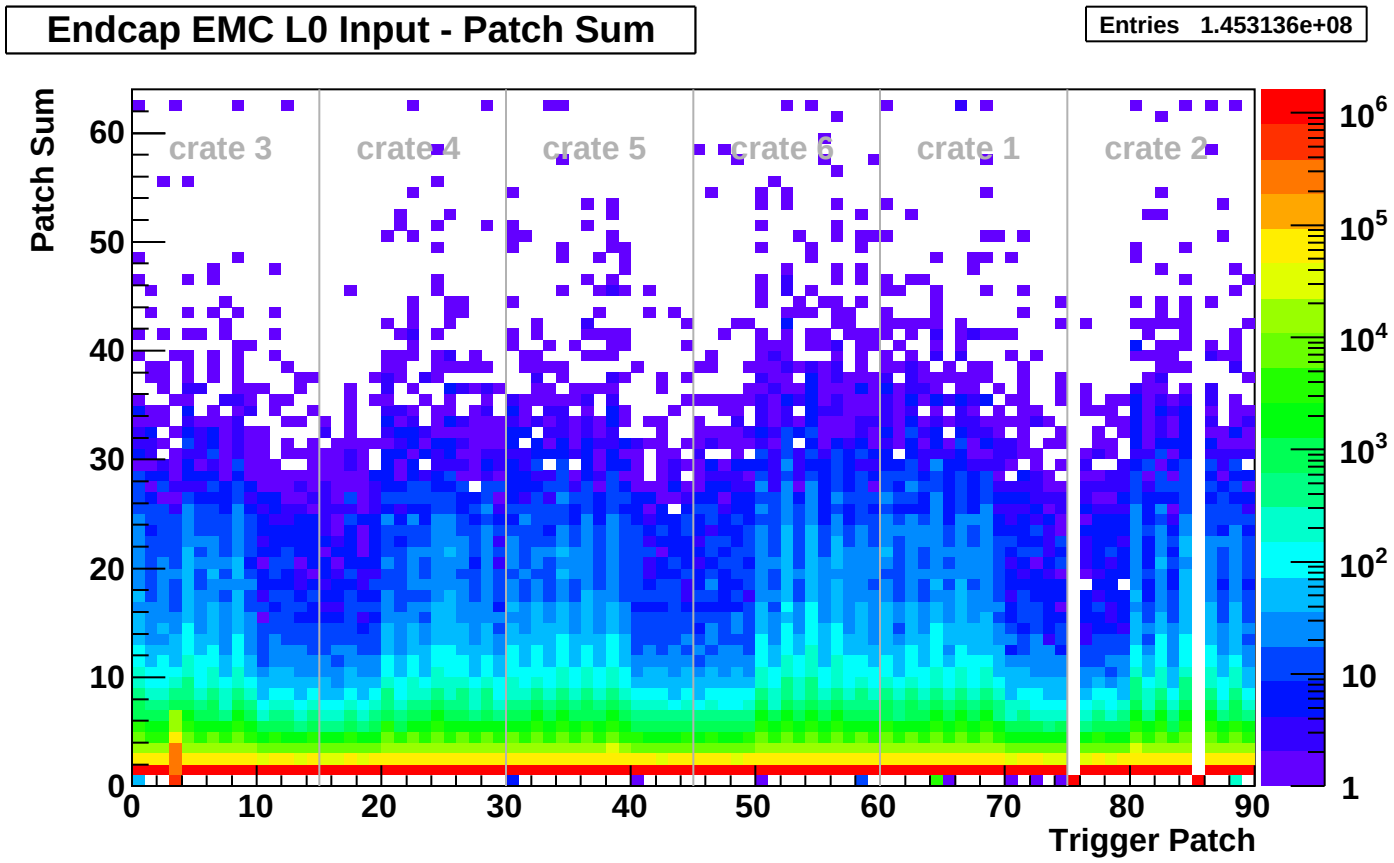
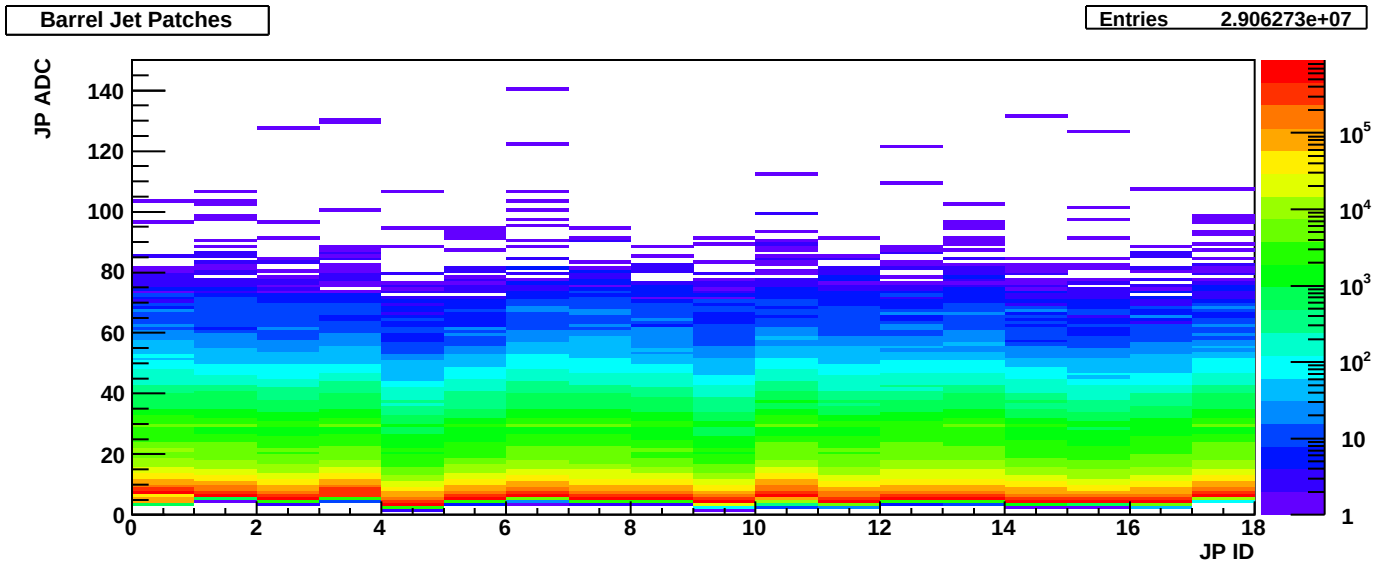
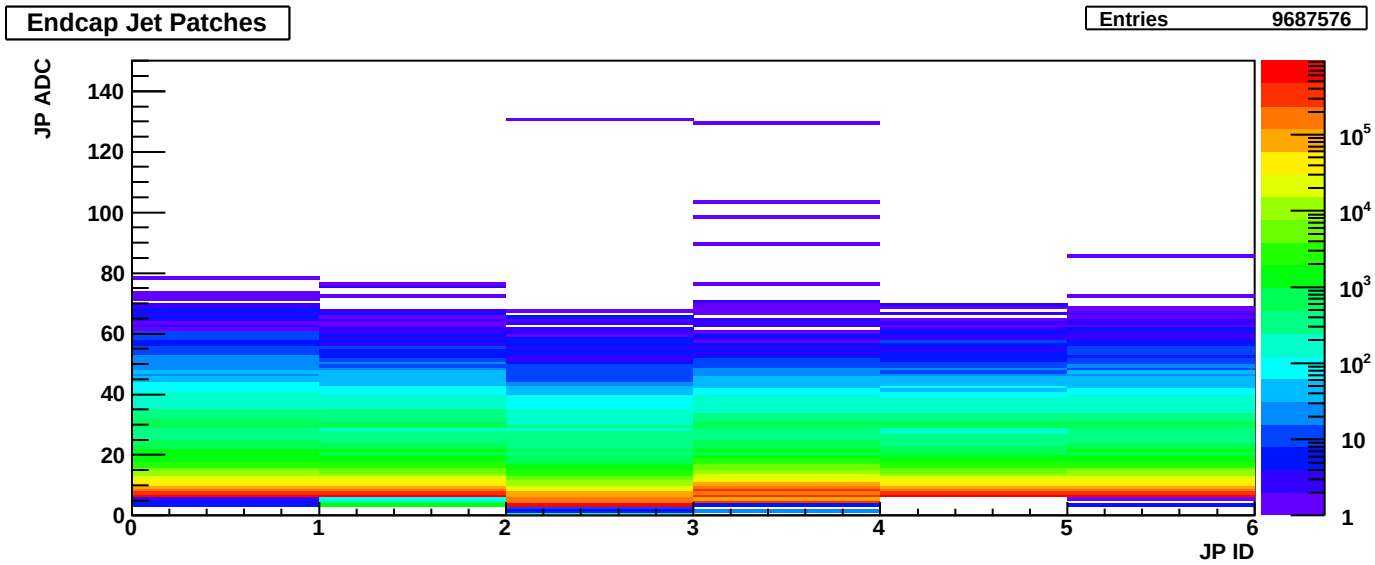

Barrel EMC L0 Input - High Tower
Entries 4.843788e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

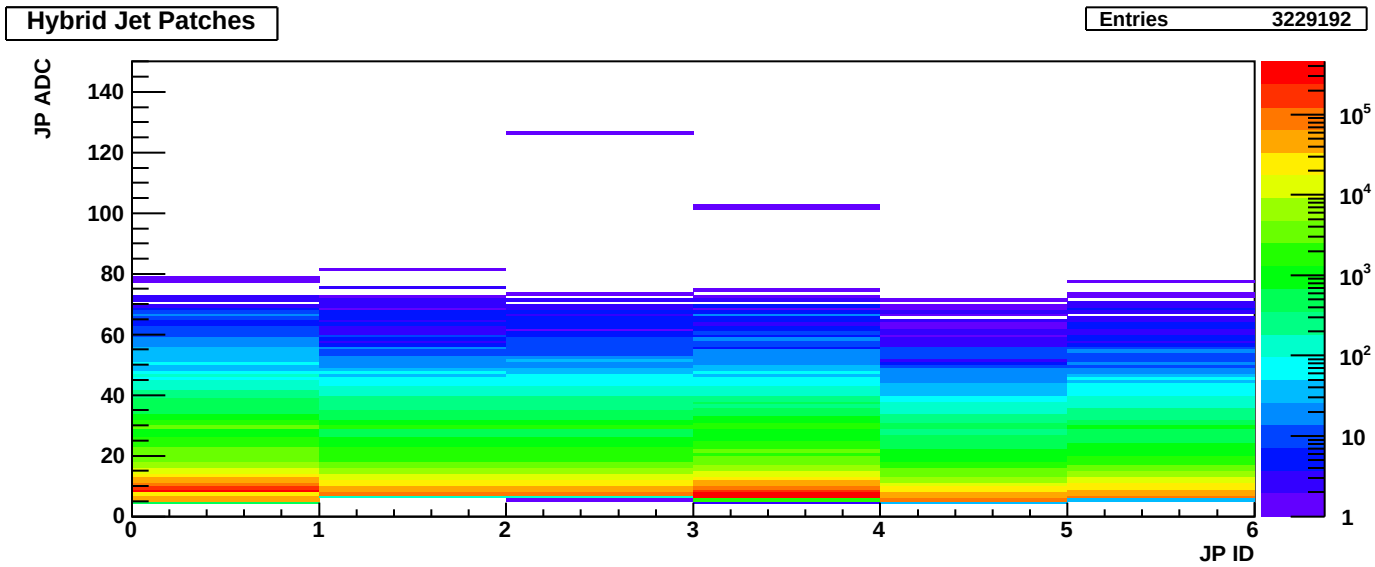

Barrel EMC L0 Input - Patch Sum
Entries 4.843788e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

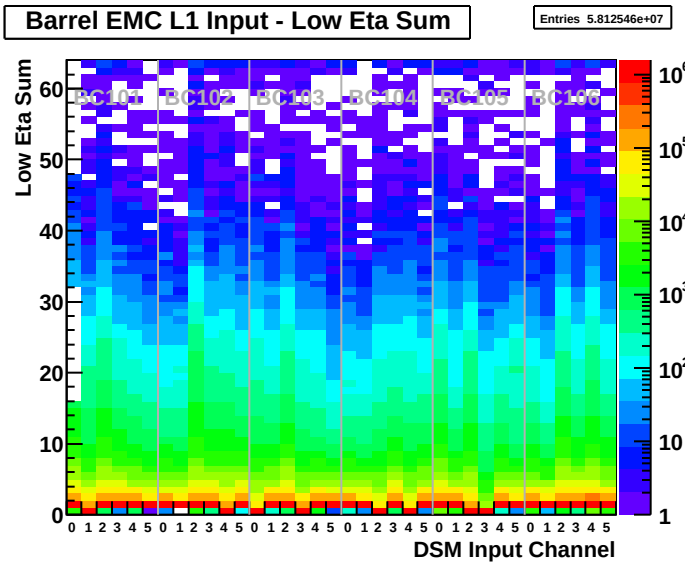

Endcap EMC L0 Input - High Tower
Entries 1.453136e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

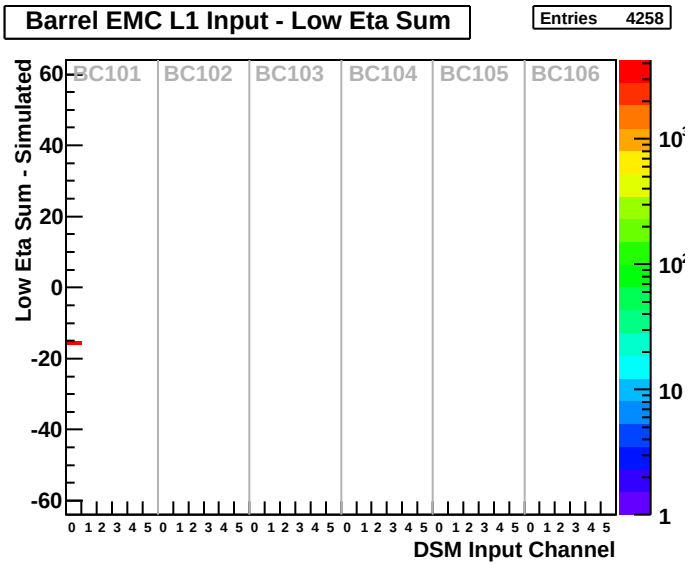

Endcap EMC L0 Input - Patch Sum
Entries 1.453136e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

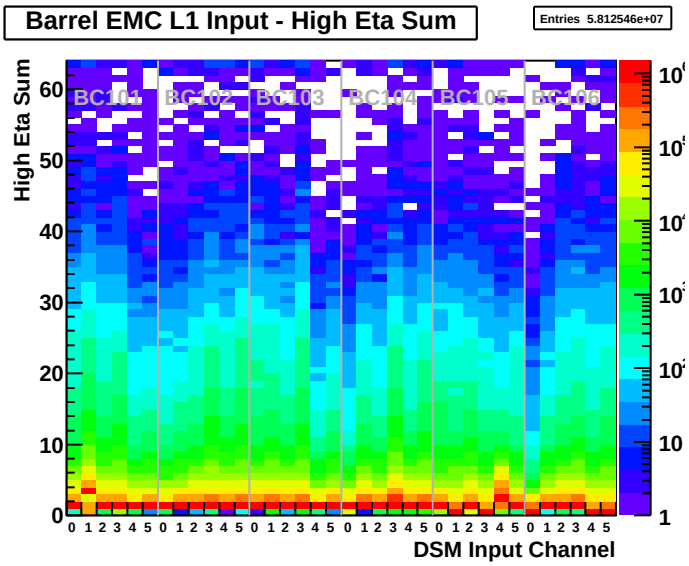

Barrel Jet Patches
Entries 2.906273e+07
JP ADC
JP ID

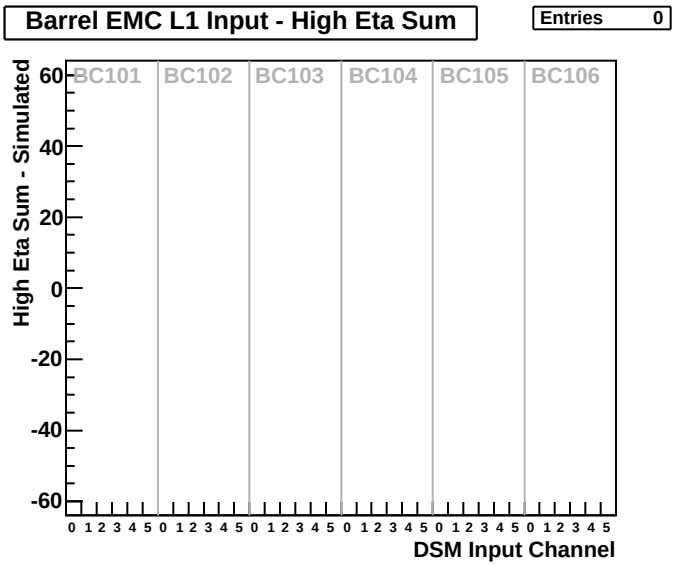

Endcap Jet Patches
Entries 9687576
JP ADC
JP ID

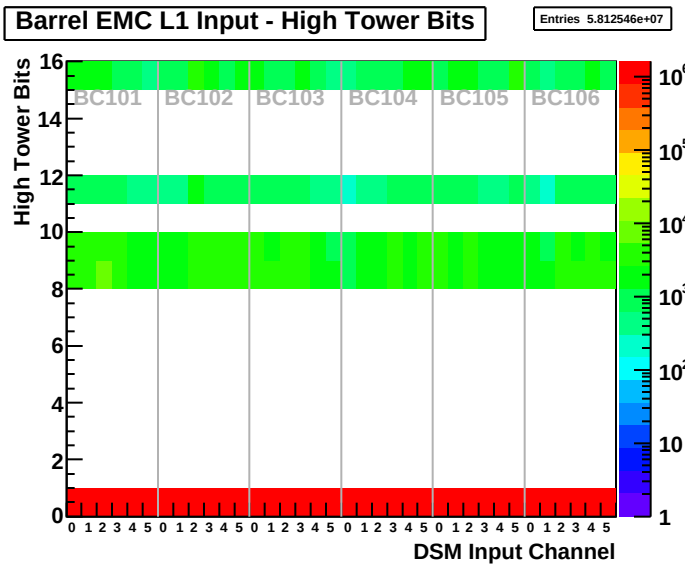

Hybrid Jet Patches
Entries 3229192
JP ADC
JP ID

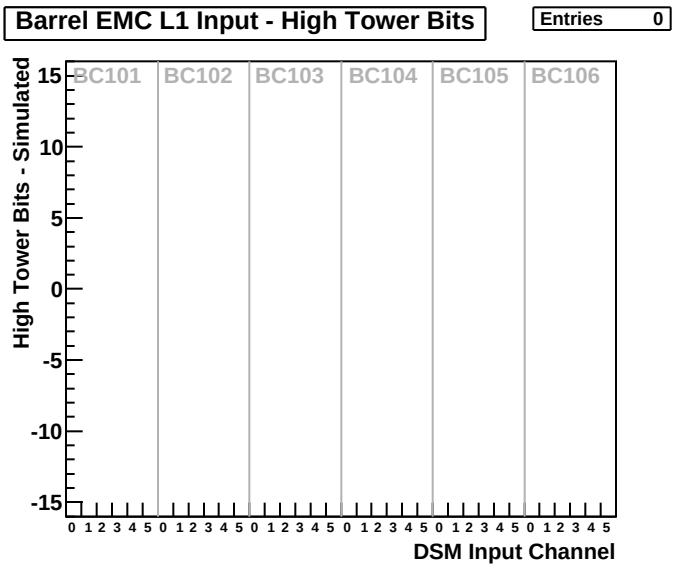

Barrel EMC L1 Input - Low Eta Sum
Entries 5.812546e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

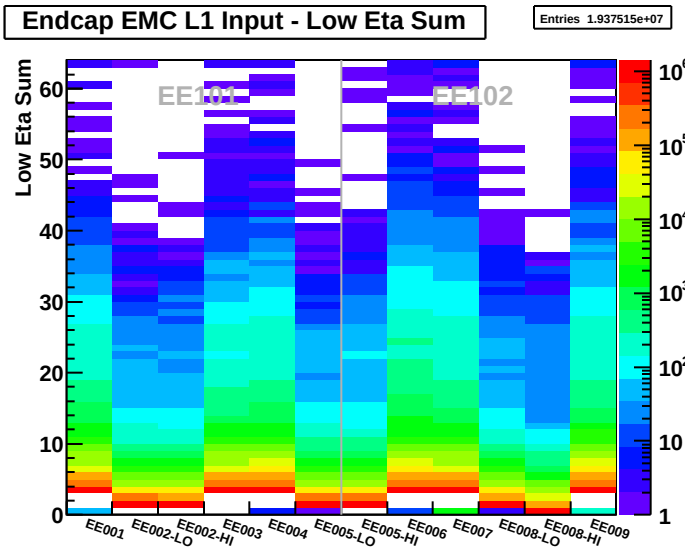

Barrel EMC L1 Input - Low Eta Sum
Entries 4258
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

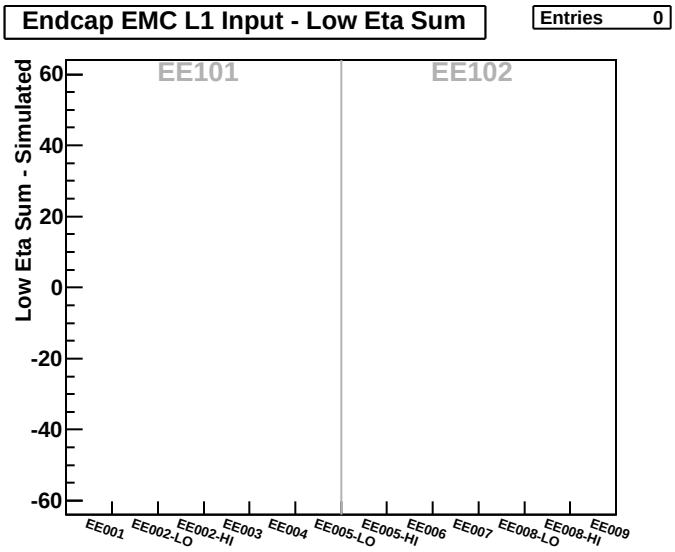

Barrel EMC L1 Input - High Eta Sum
Entries 5.812546e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

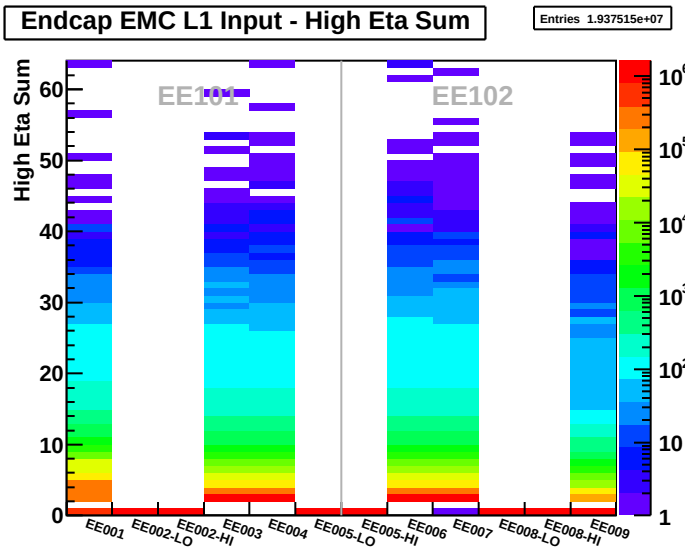

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

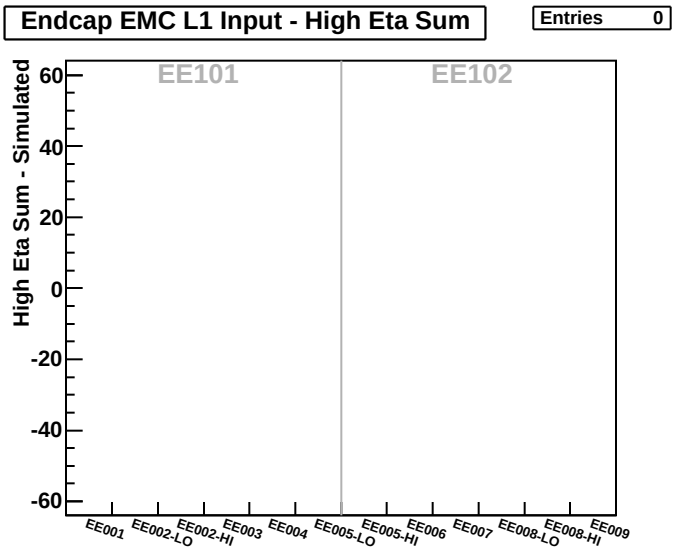

Barrel EMC L1 Input - High Tower Bits
Entries 5.812546e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

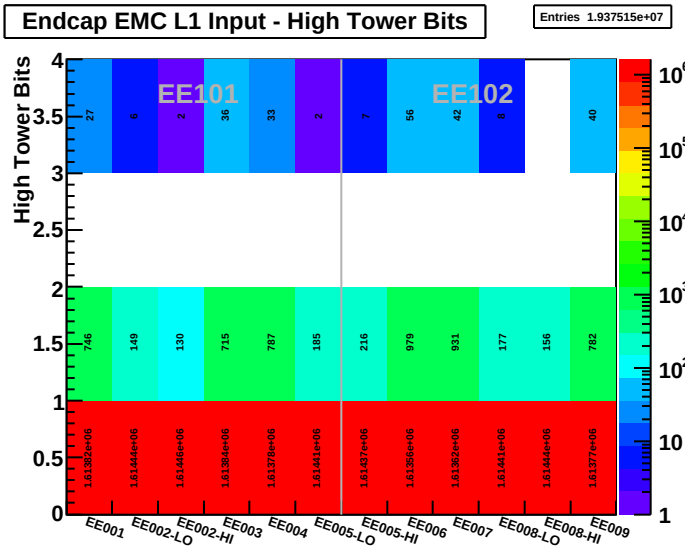

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

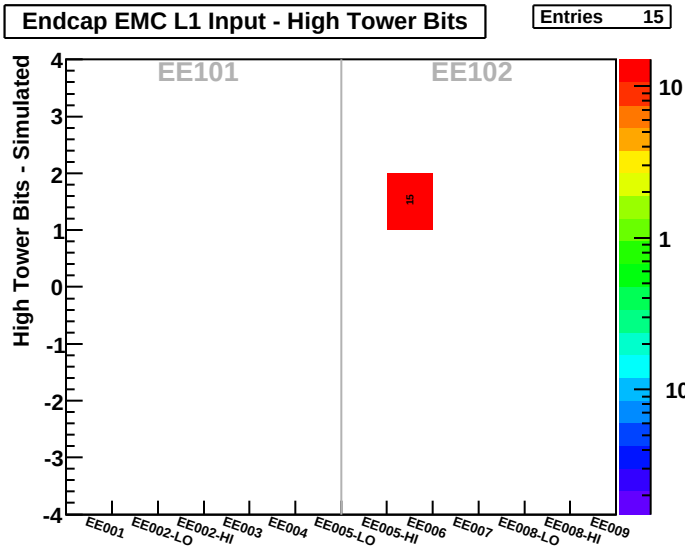

Endcap EMC L1 Input - Low Eta Sum
Entries 1.937515e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

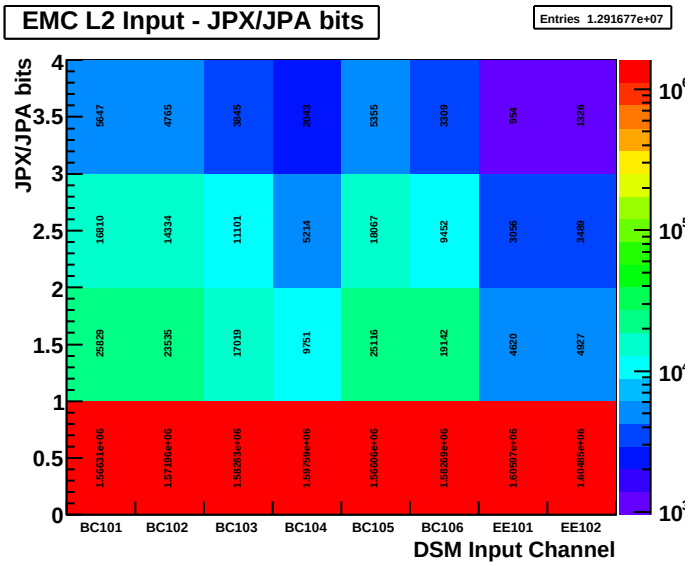

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

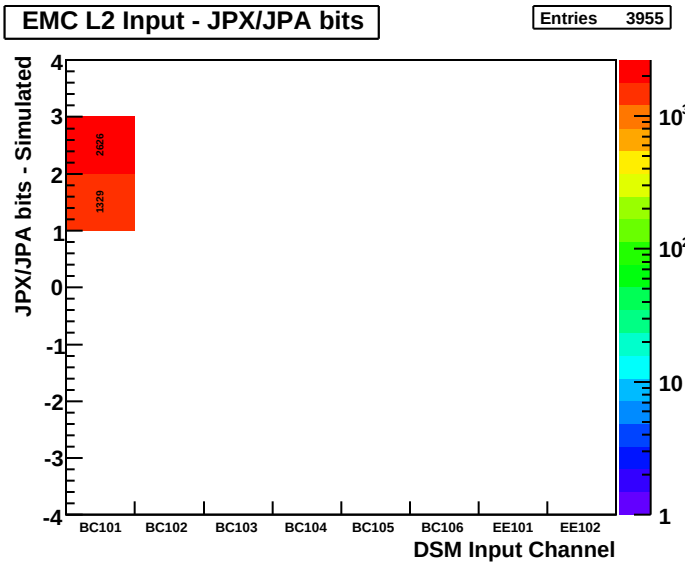

Endcap EMC L1 Input - High Eta Sum
Entries 1.937515e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

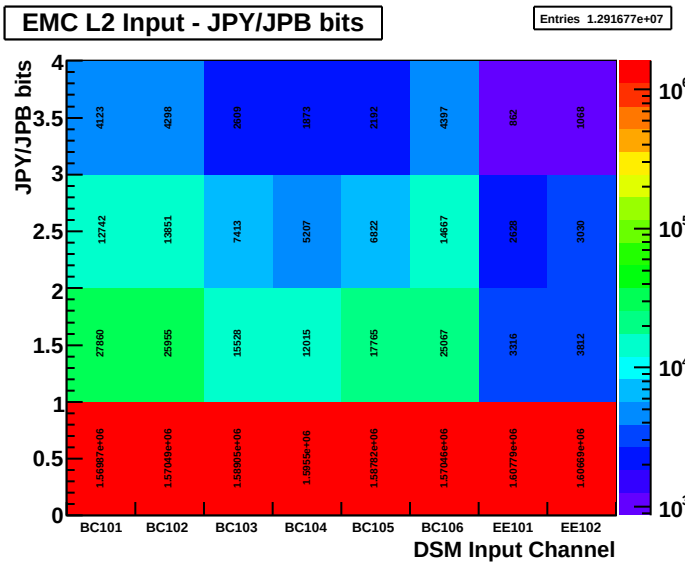

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

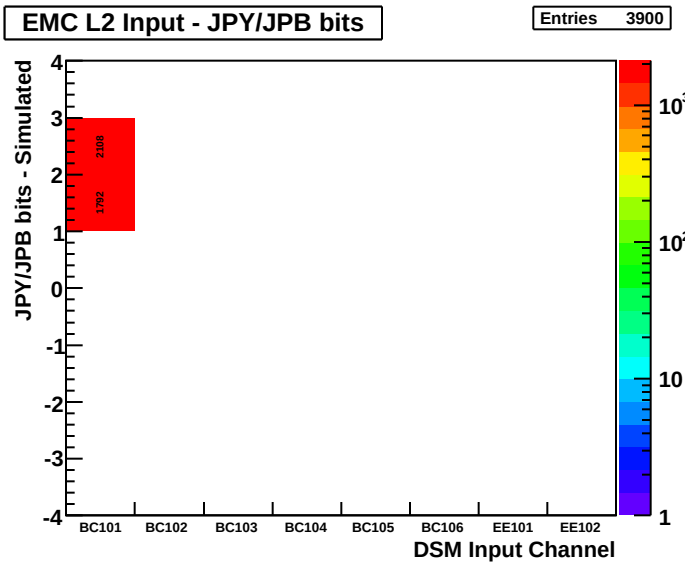

Endcap EMC L1 Input - High Tower Bits
Entries 1.937515e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

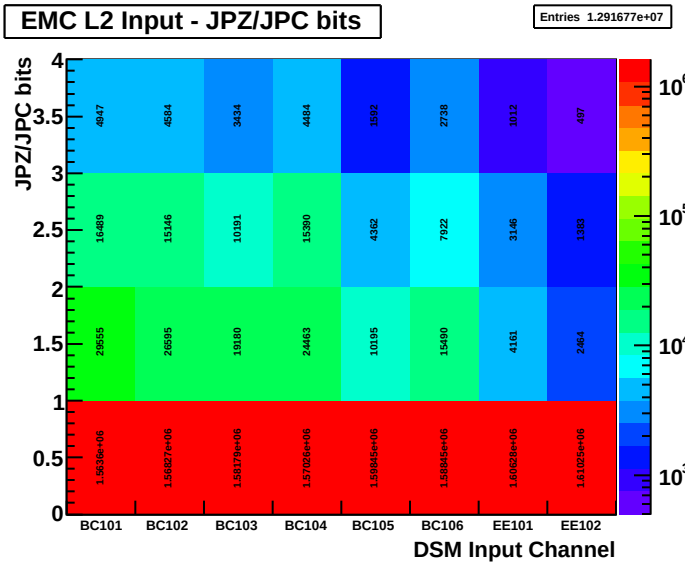

Endcap EMC L1 Input - High Tower Bits
Entries 15
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

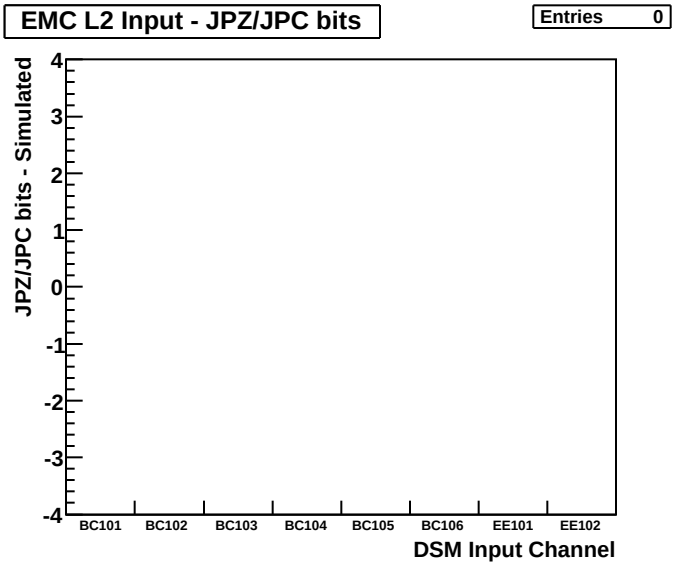

EMC L2 Input - JPX/JPA bits
Entries 1.291677e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5647 4765 3845 2043 5355 3309 954 1326
16810 14334 11101 5214 18067 9452 3056 3489
25829 23535 17019 9751 25116 19142 4620 4927
1.56631e+06 1.57196e+06 1.58233e+06 1.59759e+06 1.56606e+06 1.58269e+06 1.60997e+06 1.60485e+06

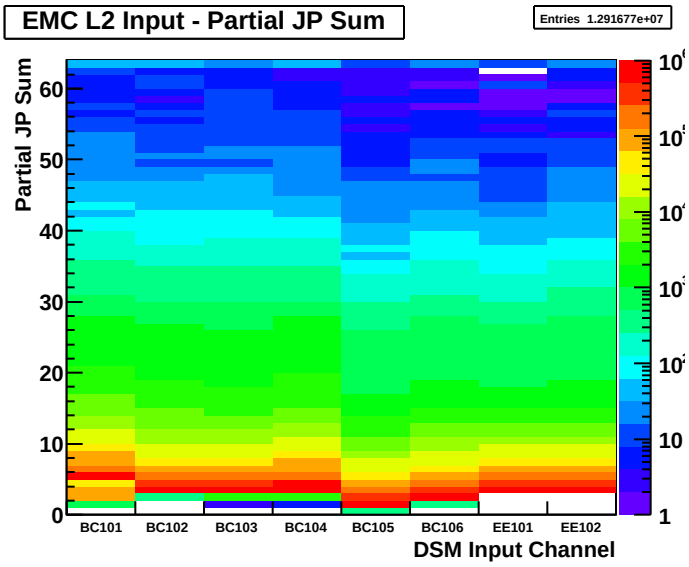

EMC L2 Input - JPX/JPA bits
Entries 3955
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2626
1329

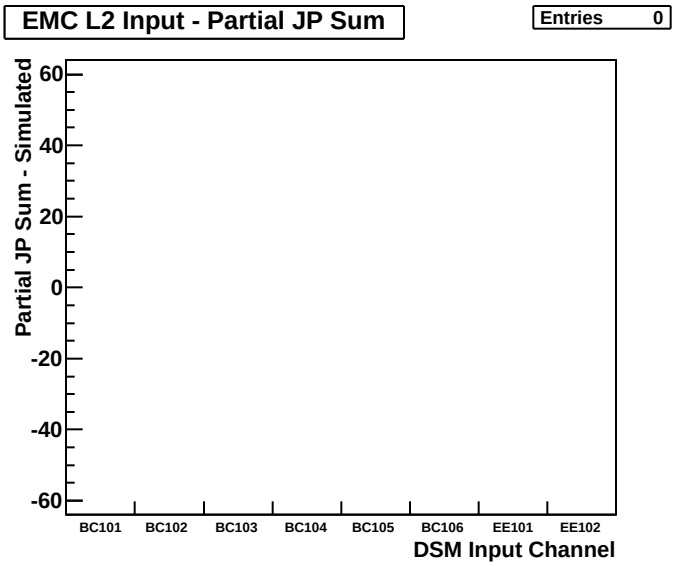

EMC L2 Input - JPY/JPB bits
Entries 1.291677e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4123 4298 2609 1873 2192 4397 862 1068
12742 13951 7413 5207 6822 14667 2628 3030
27860 25955 15528 12015 17765 25067 3316 3812
1.56987e+06 1.57049e+06 1.58905e+06 1.59555e+06 1.58782e+06 1.57046e+06 1.60779e+06 1.60669e+06

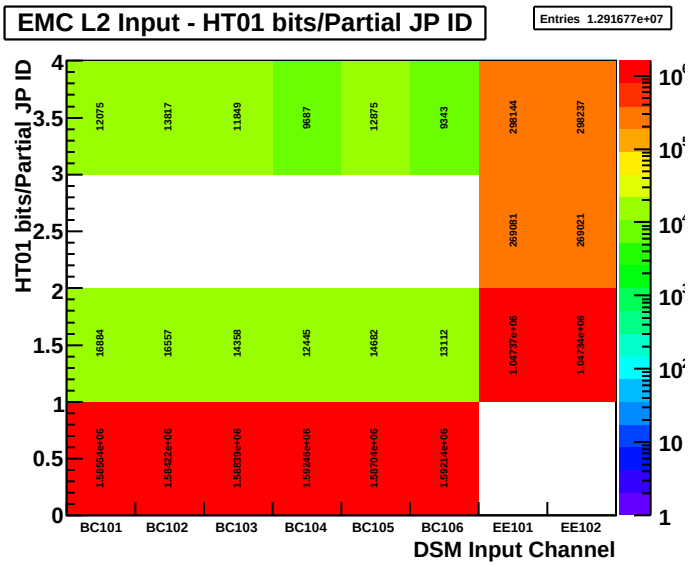

EMC L2 Input - JPY/JPB bits
Entries 3900
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2108
1792

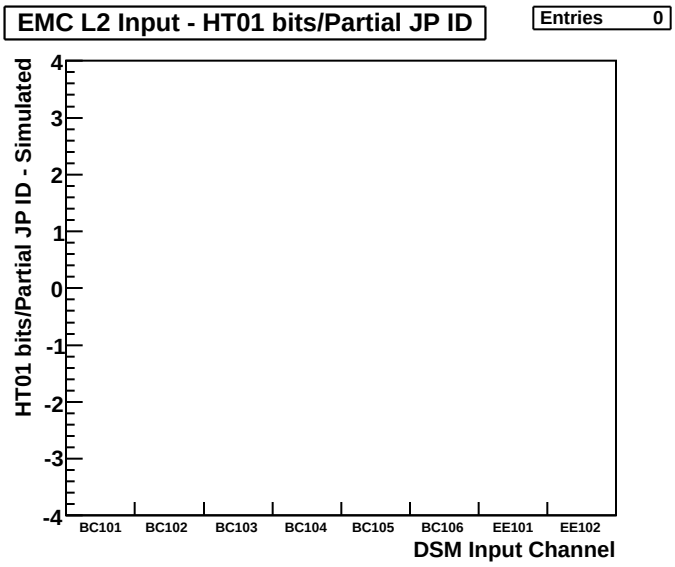

EMC L2 Input - JPZ/JPC bits
Entries 1.291677e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4947 4584 3434 4484 1592 2738 1012 497
16489 15146 10191 15390 4362 7922 3146 1383
29555 26595 19180 24463 10195 15490 4161 2464
1.56396e+06 1.56827e+06 1.58179e+06 1.57026e+06 1.59845e+06 1.58845e+06 1.60628e+06 1.61025e+06

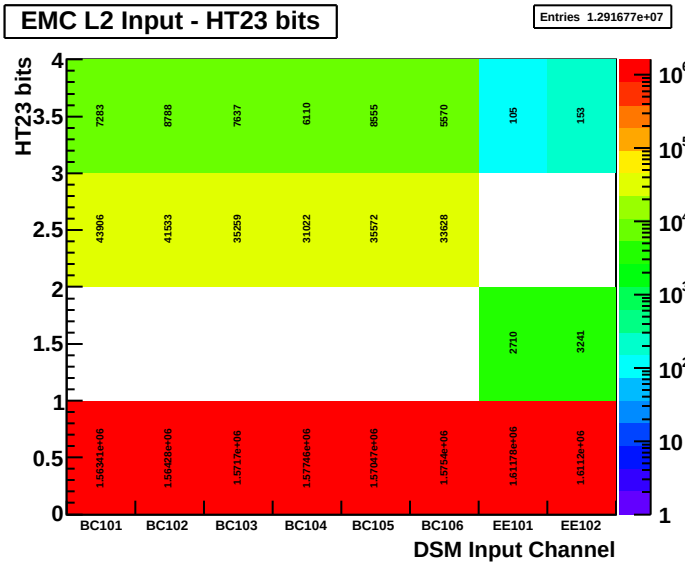

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

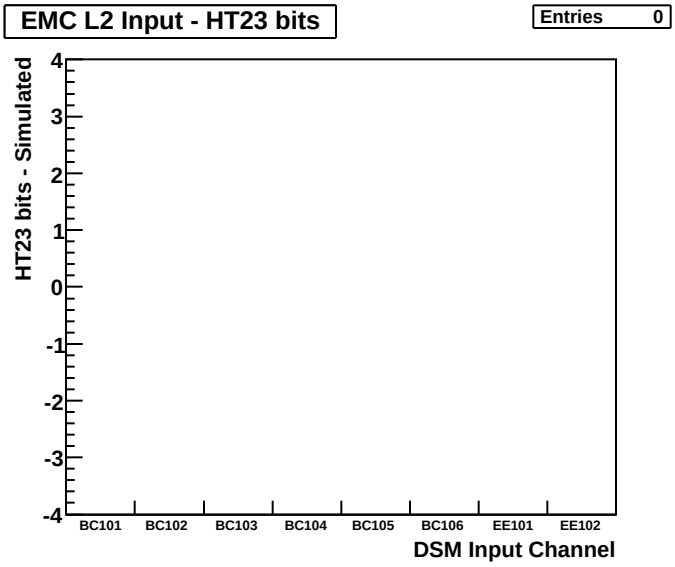

EMC L2 Input - Partial JP Sum
Entries 1.291677e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

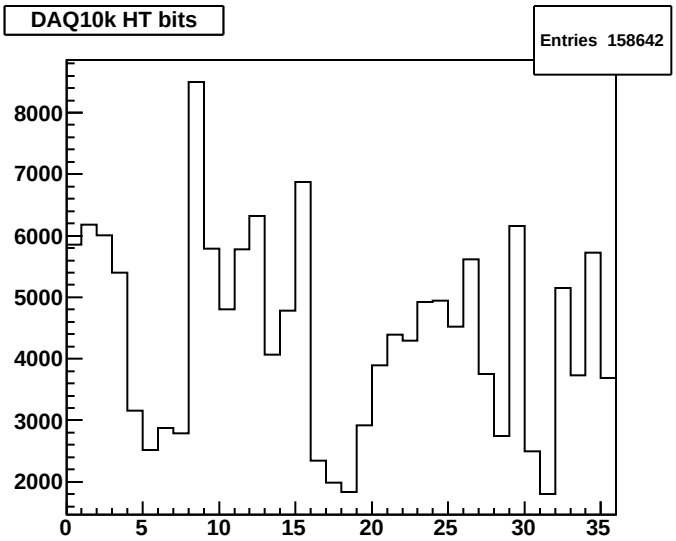

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

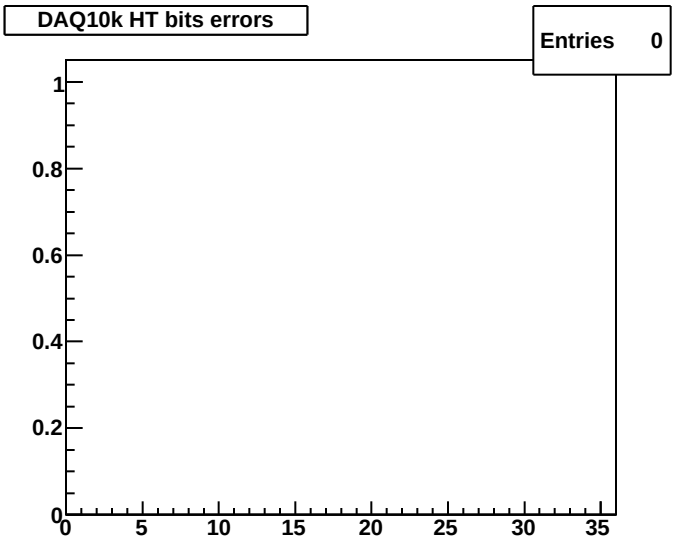

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.291677e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

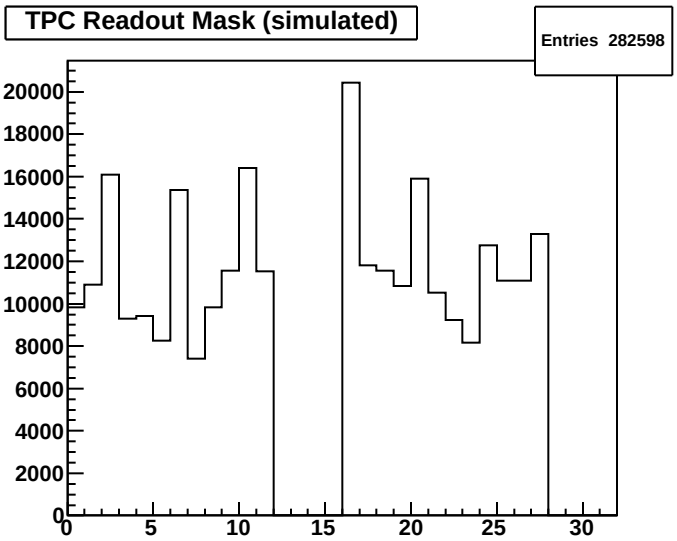

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

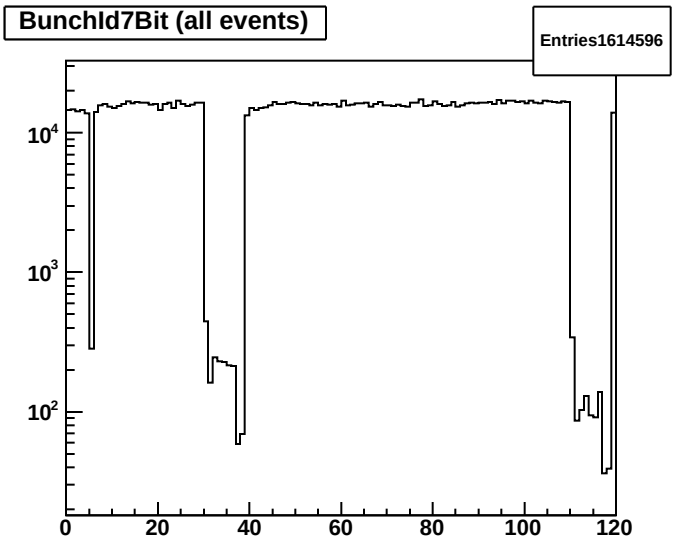

EMC L2 Input - HT23 bits
Entries 1.291677e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

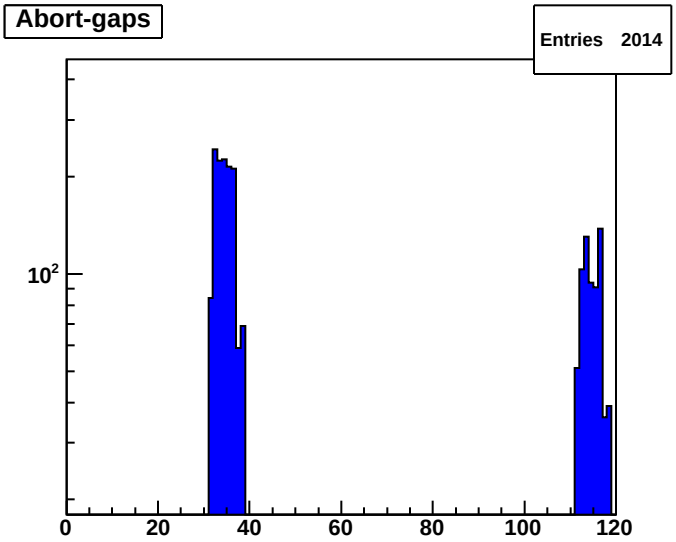

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

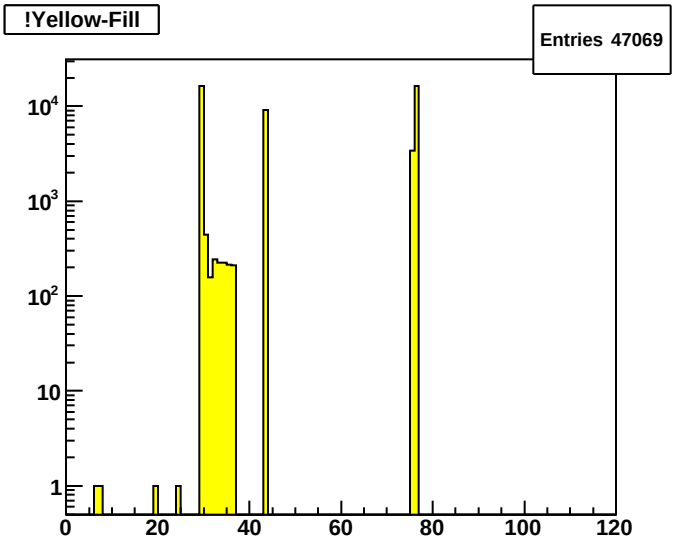

DAQ10k HT bits
Entries 158642

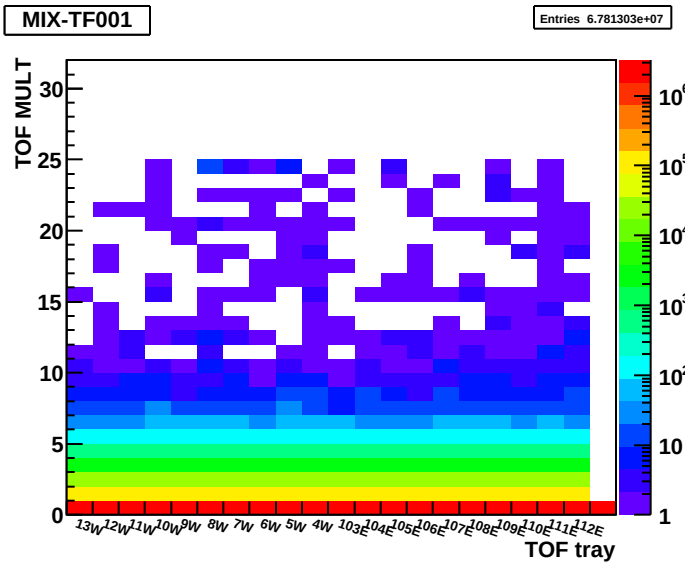

DAQ10k HT bits errors
Entries 0

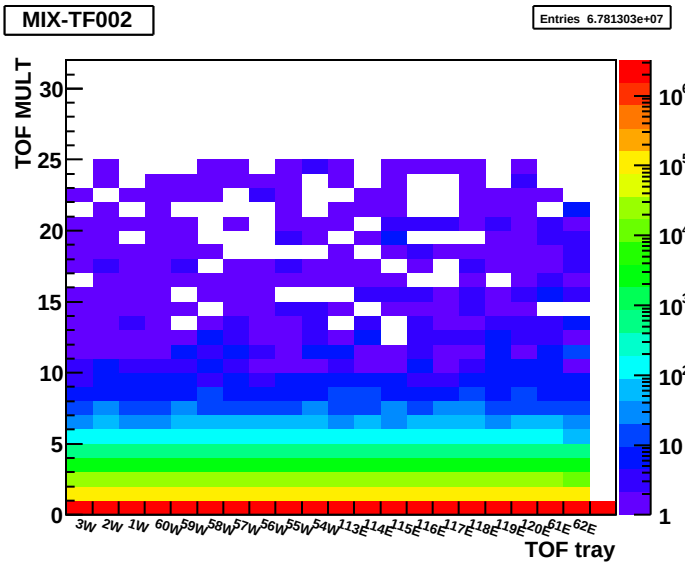

TPC Readout Mask (simulated)
Entries 282598

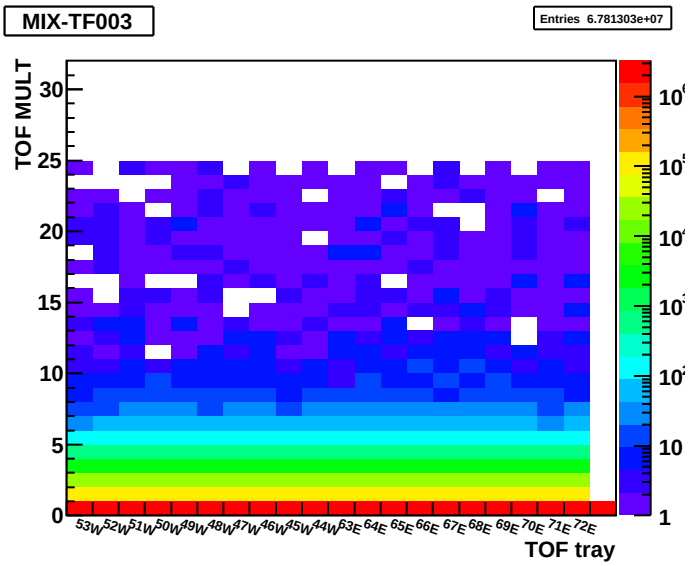

BunchId7Bit (all events)
Entries1614596

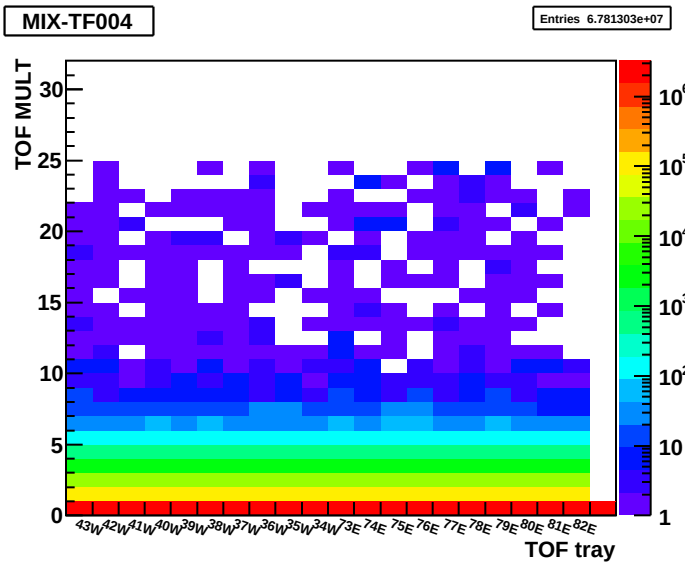

Abort-gaps
Entries 2014

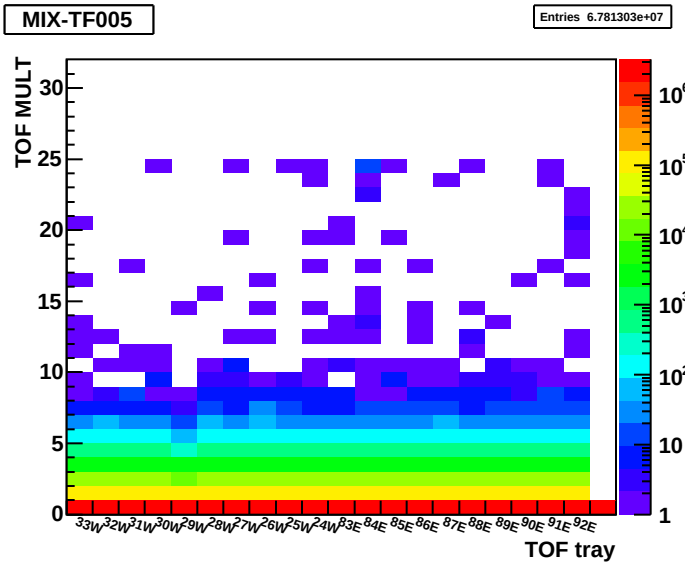

!Yellow-Fill
Entries 47069

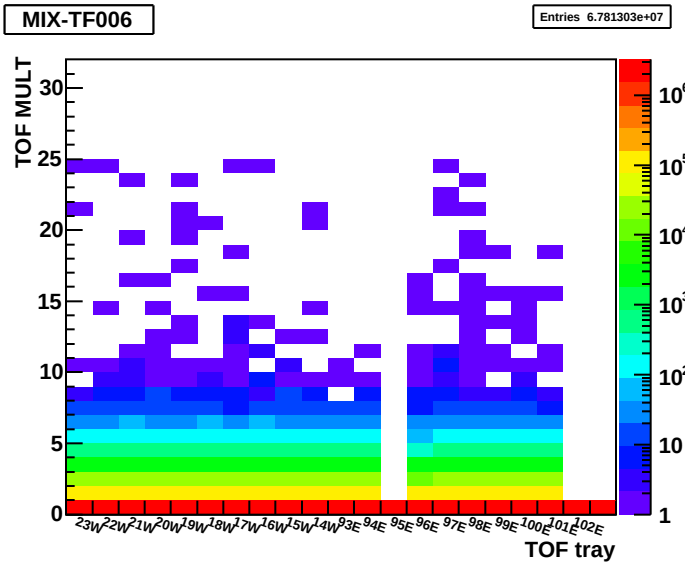

MIX-TF001
Entries 6.781303e+07
TOF MULT
TOF tray

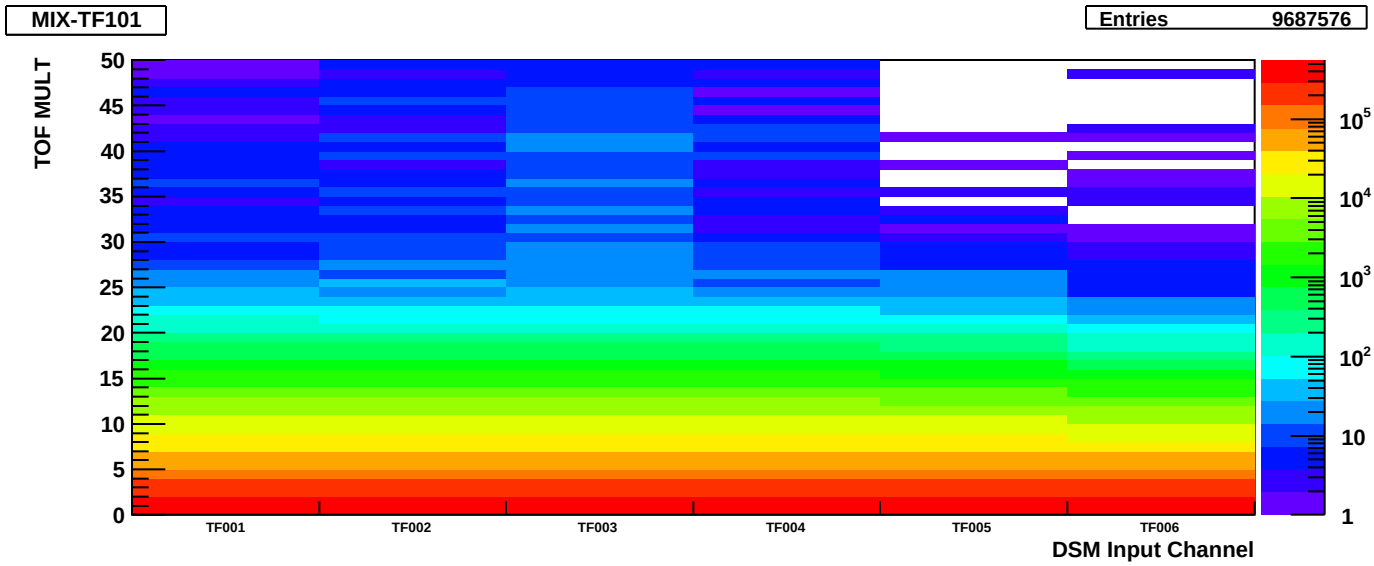

MIX-TF002
Entries 6.781303e+07
TOF MULT
TOF tray

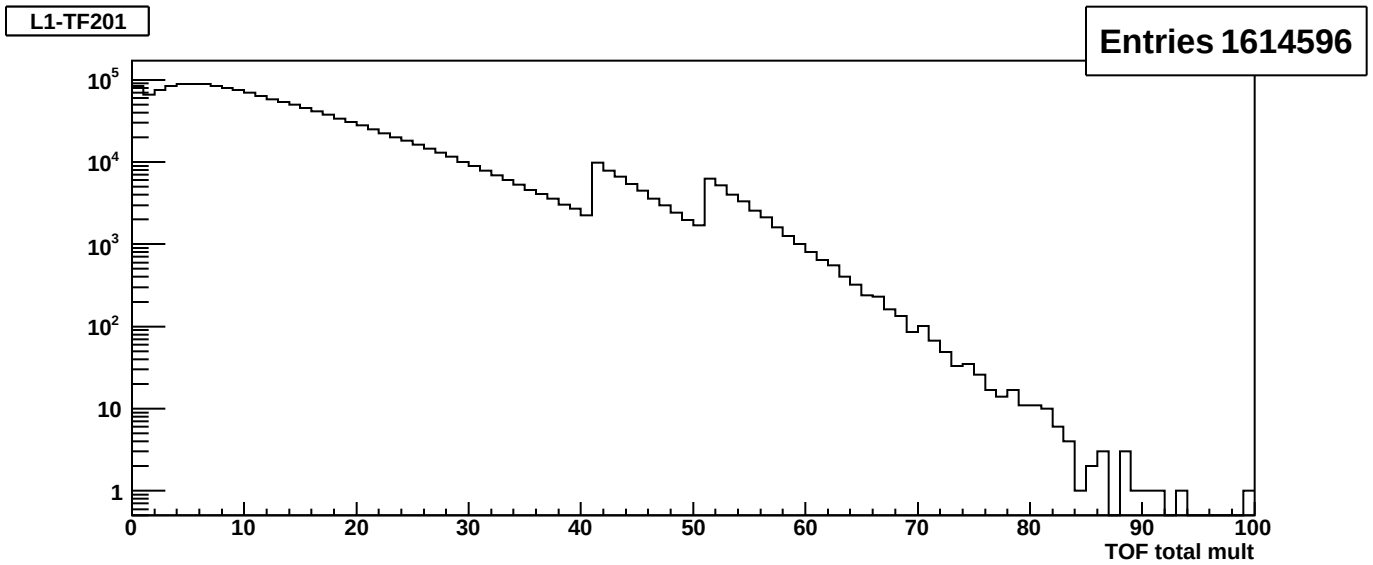

MIX-TF003
Entries 6.781303e+07
TOF MULT
TOF tray

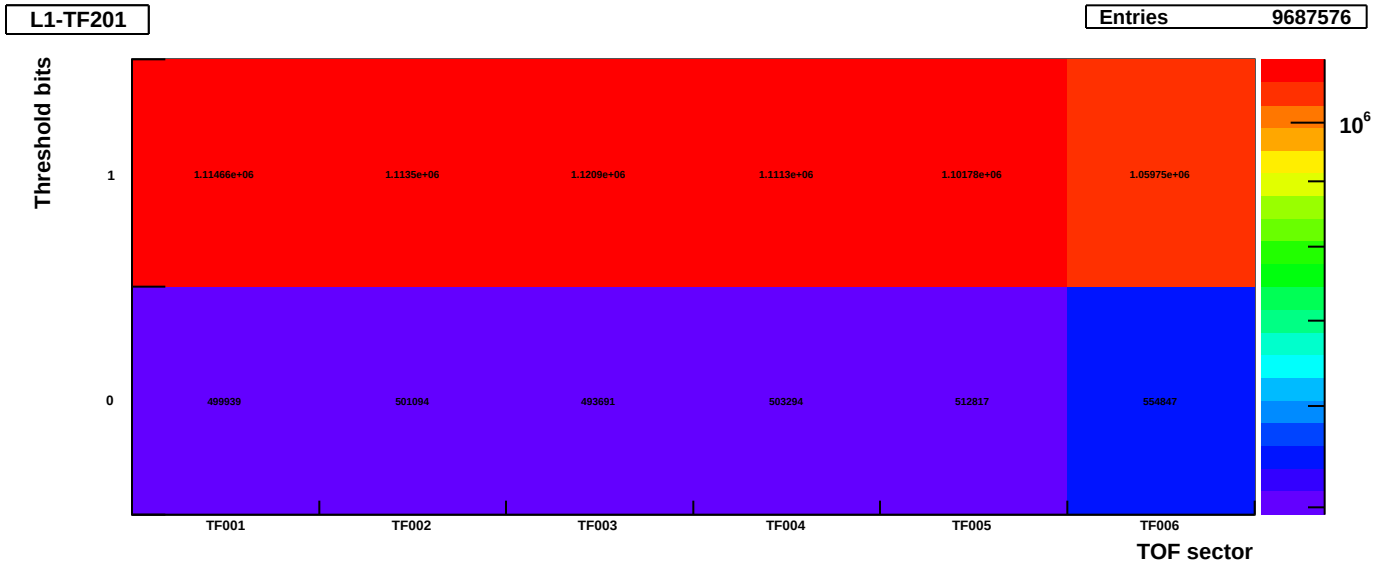

MIX-TF004
Entries 6.781303e+07
TOF MULT
TOF tray

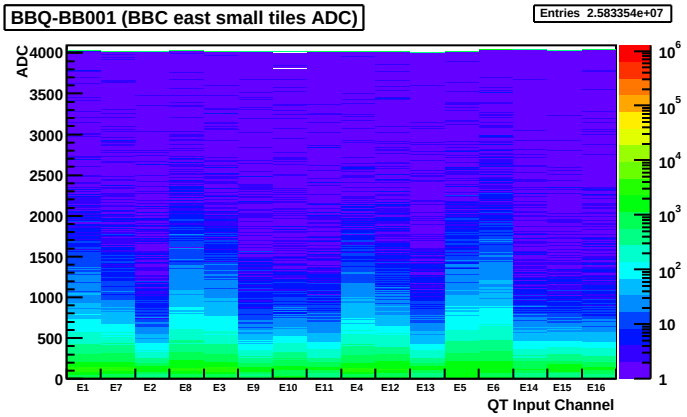

MIX-TF005
Entries 6.781303e+07
TOF MULT
TOF tray

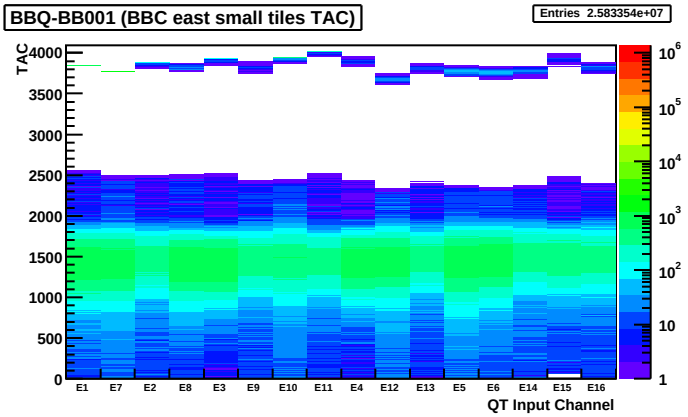

MIX-TF006
Entries 6.781303e+07
TOF MULT
TOF tray

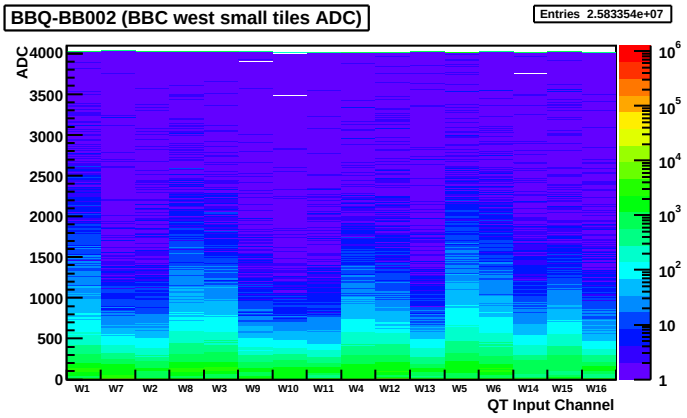

MIX-TF101
Entries 9687576
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

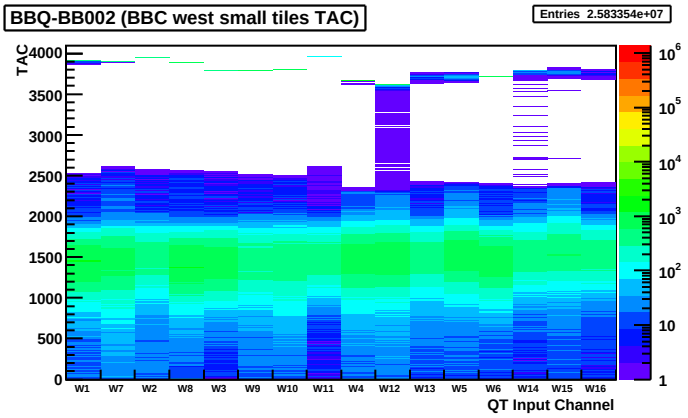

L1-TF201
Entries 1614596
TOF total mult

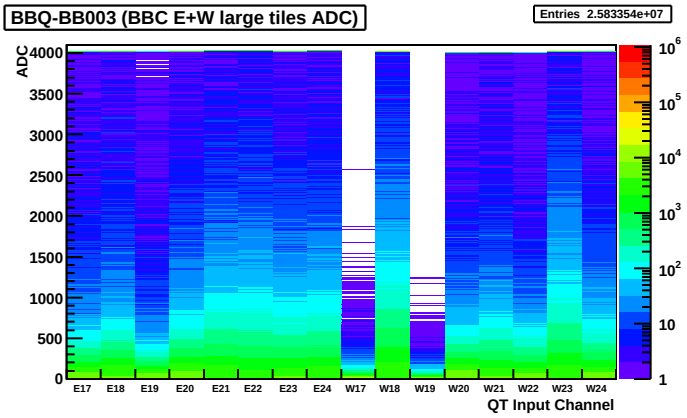

L1-TF201
Entries 9687576
Threshold bits
1.11466e+06
1.1135e+06
1.1209e+06
1.1113e+06
1.10178e+06
1.05975e+06
499939
501094
493691
503294
512817
554847
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

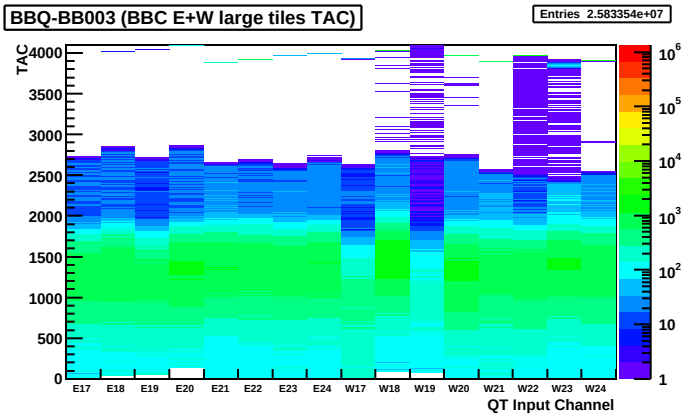

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.583354e+07
ADC
QT Input Channel

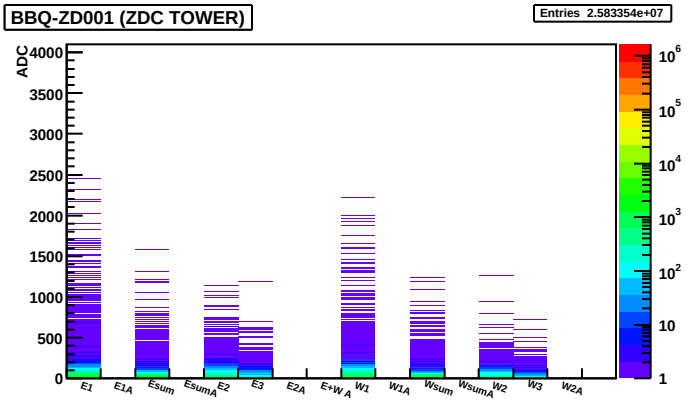

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.583354e+07
TAC
QT Input Channel

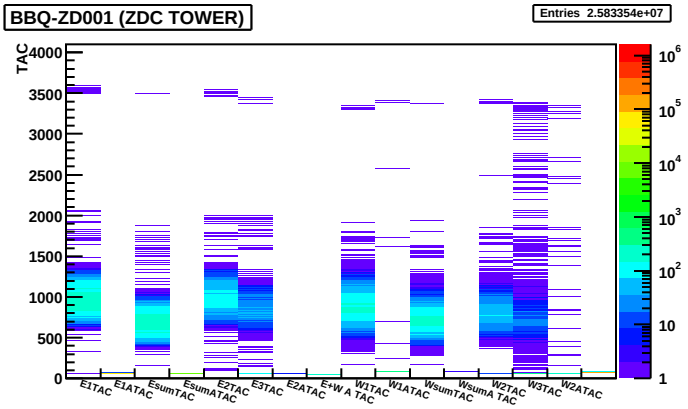

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.583354e+07
ADC
QT Input Channel

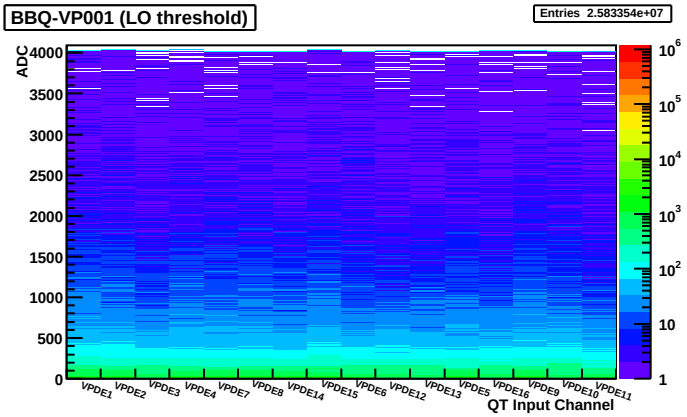

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.583354e+07
TAC
QT Input Channel

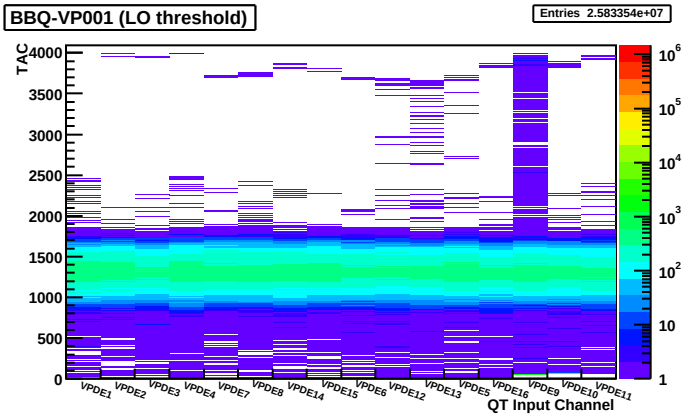

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.583354e+07
ADC
QT Input Channel

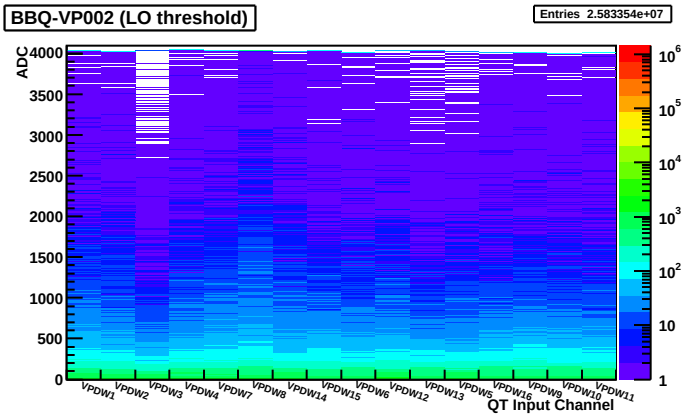

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.583354e+07
TAC
QT Input Channel

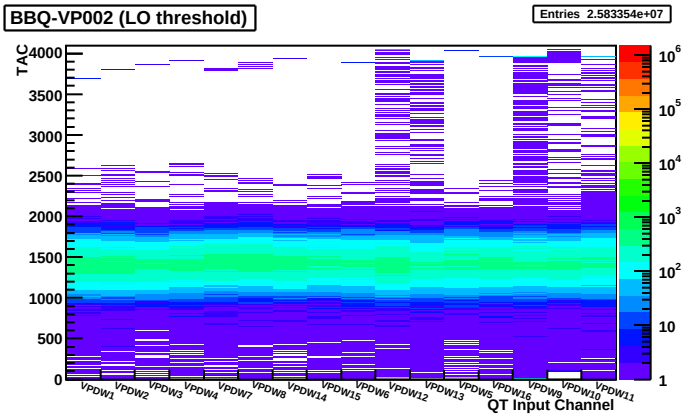

BBQ-ZD001 (ZDC TOWER)
Entries 2.583354e+07
ADC

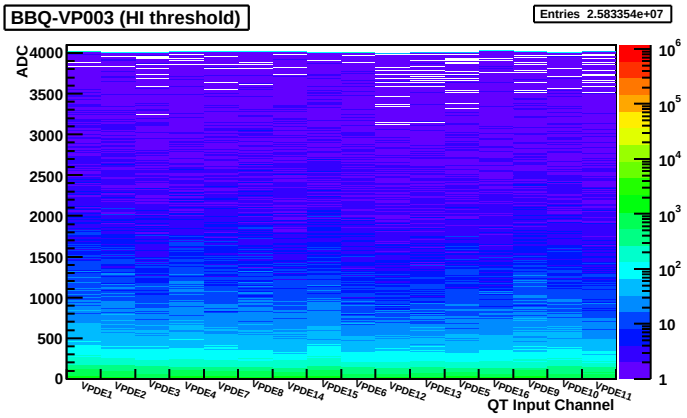

BBQ-ZD001 (ZDC TOWER)
Entries 2.583354e+07
TAC

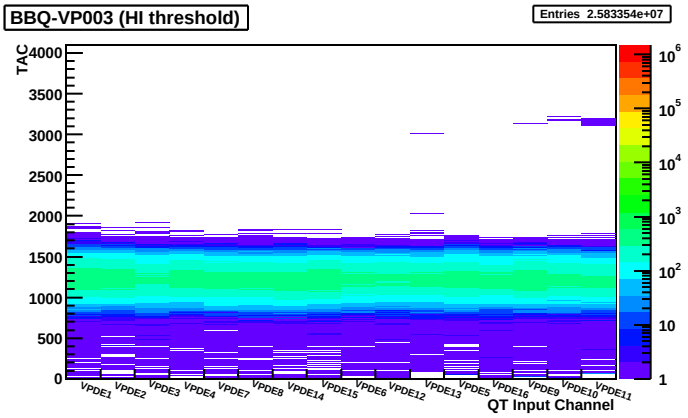

BBQ-VP001 (LO threshold)
Entries 2.583354e+07
ADC
QT Input Channel

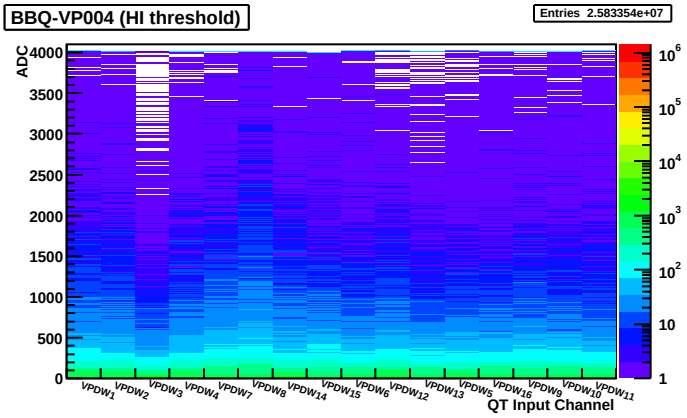

BBQ-VP001 (LO threshold)
Entries 2.583354e+07
TAC
QT Input Channel

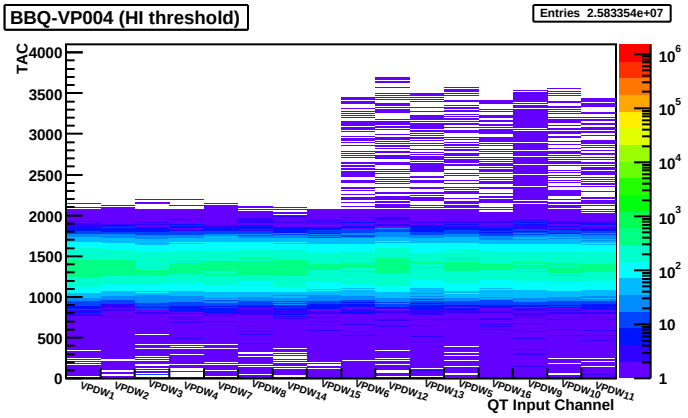

BBQ-VP002 (LO threshold)
Entries 2.583354e+07
ADC
QT Input Channel

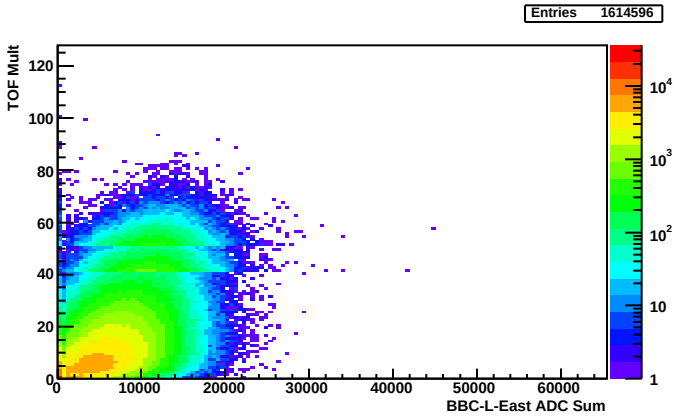

BBQ-VP002 (LO threshold)
Entries 2.583354e+07
TAC
QT Input Channel

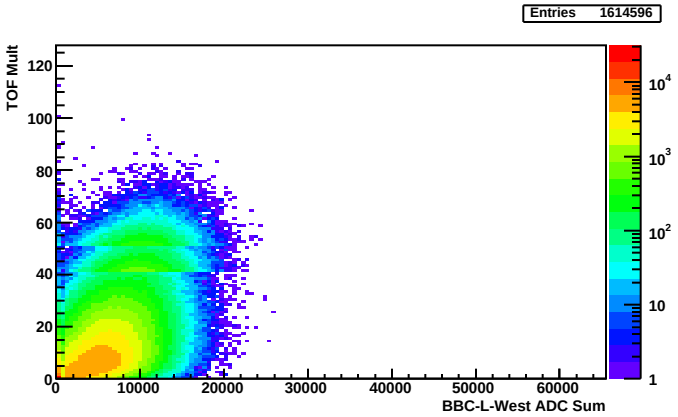

BBQ-VP003 (HI threshold)
Entries 2.583354e+07
ADC
QT Input Channel

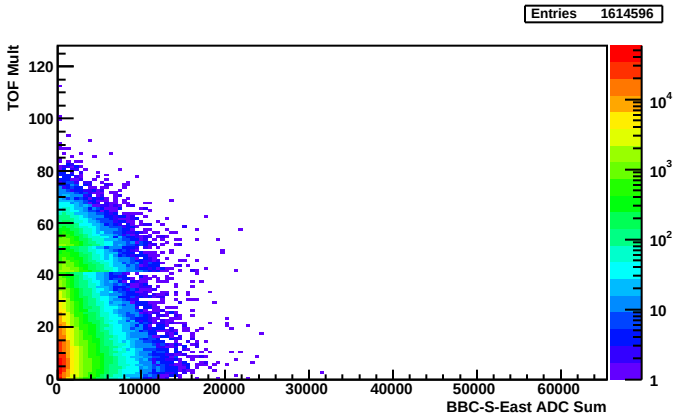

BBQ-VP003 (HI threshold)
Entries 2.583354e+07
TAC
QT Input Channel

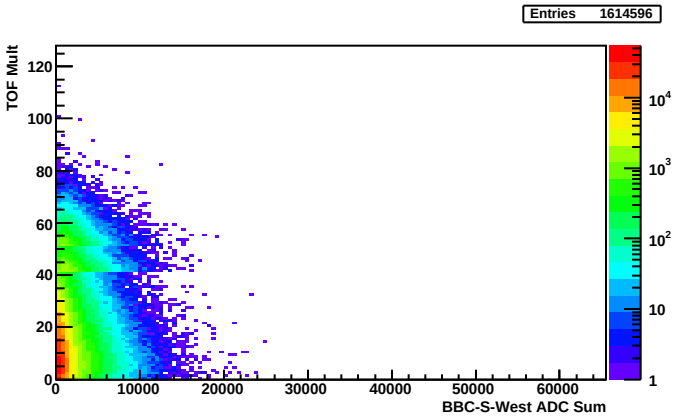

BBQ-VP004 (HI threshold)
Entries 2.583354e+07
ADC
QT Input Channel

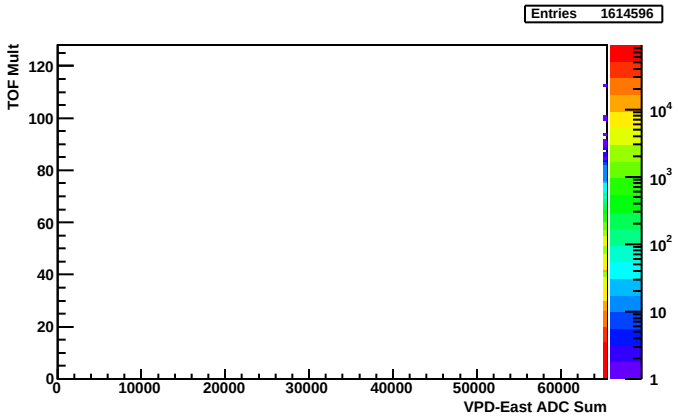

BBQ-VP004 (HI threshold)
Entries 2.583354e+07
TAC
QT Input Channel

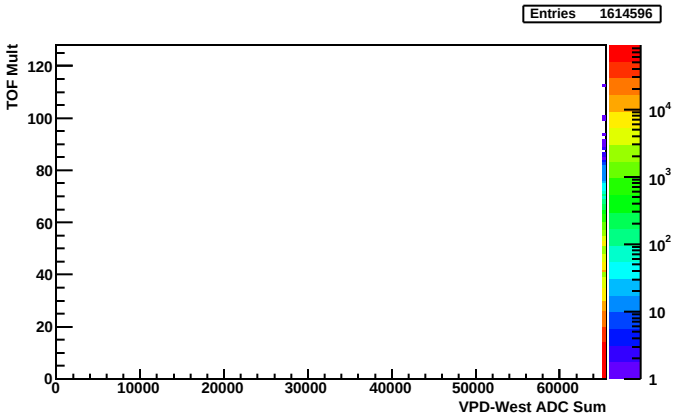

Entries 1614596
TOF Mult
BBC-L-East ADC Sum

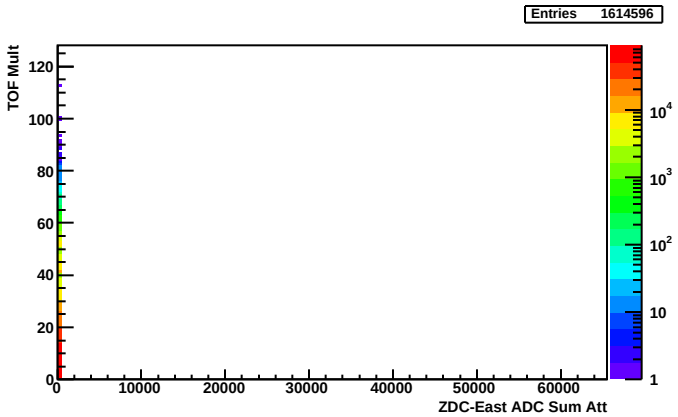

Entries 1614596
TOF Mult
BBC-L-West ADC Sum

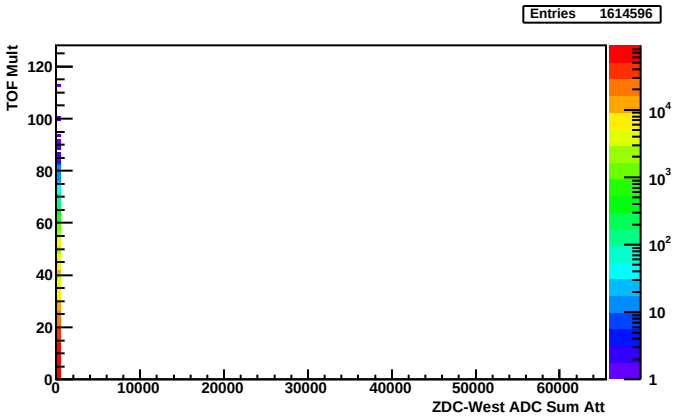

Entries 1614596
TOF Mult
BBC-S-East ADC Sum

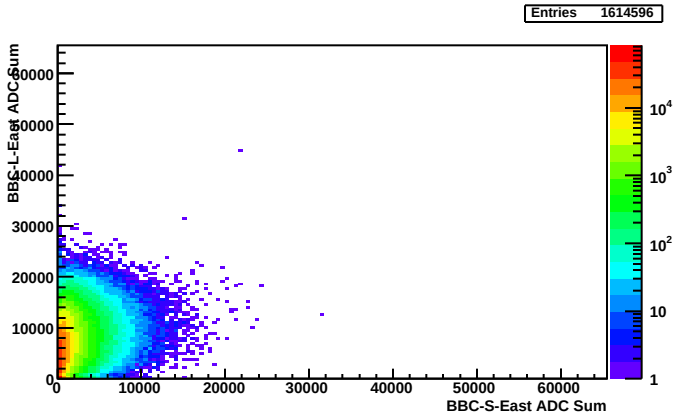

Entries 1614596
TOF Mult
BBC-S-West ADC Sum

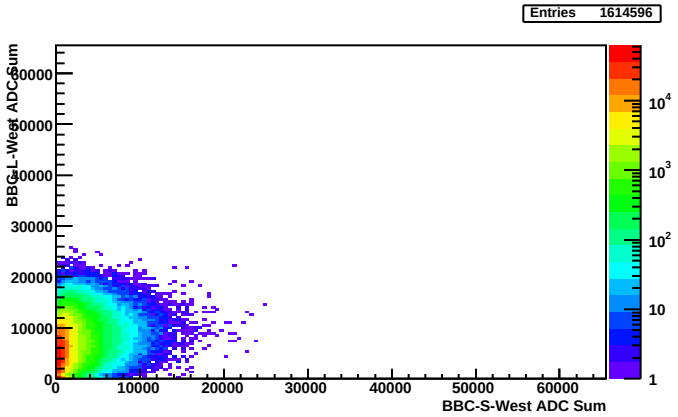

Entries 1614596
TOF Mult
VPD-East ADC Sum

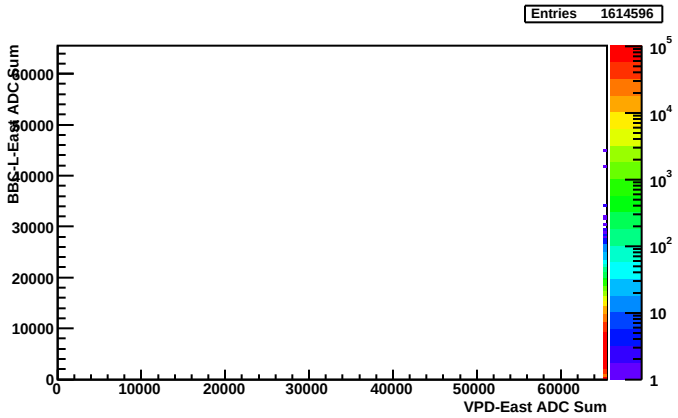

Entries 1614596
TOF Mult
VPD-West ADC Sum

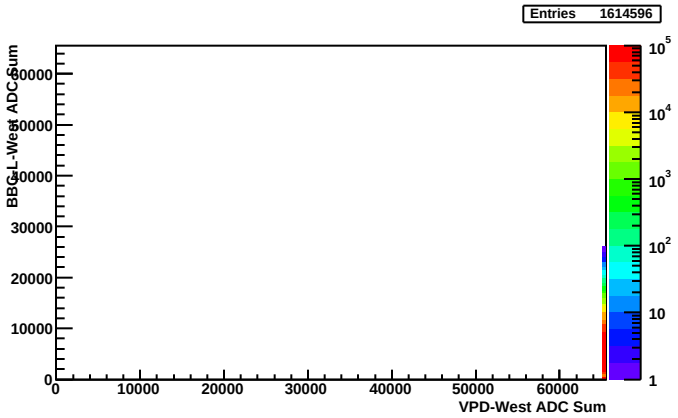

Entries 1614596
TOF Mult
ZDC-East ADC Sum Att

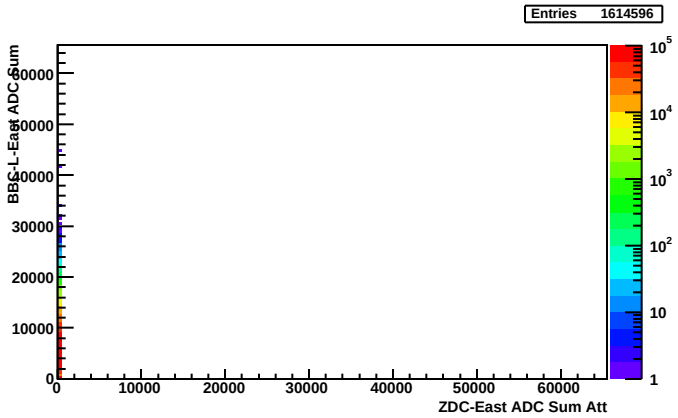

Entries 1614596
TOF Mult
ZDC-West ADC Sum Att

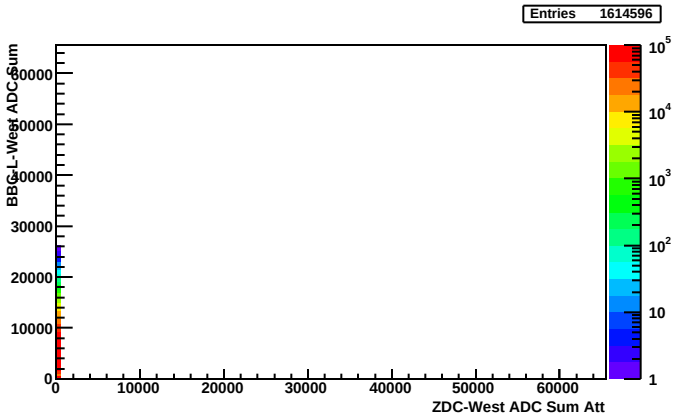

Entries 1614596
BBC-L-East ADC Sum
BBC-S-East ADC Sum

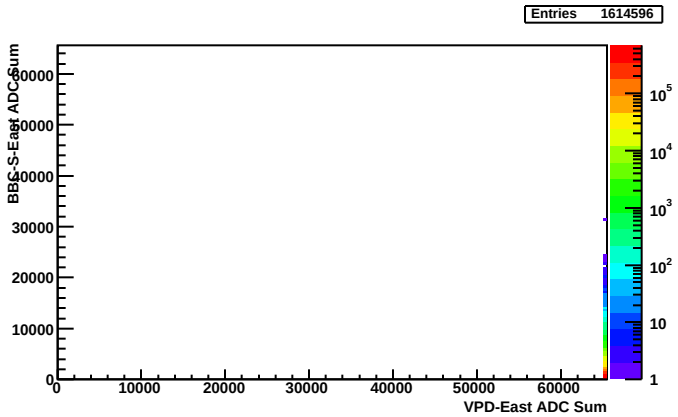

Entries 1614596
BBC-L-West ADC Sum
BBC-S-West ADC Sum

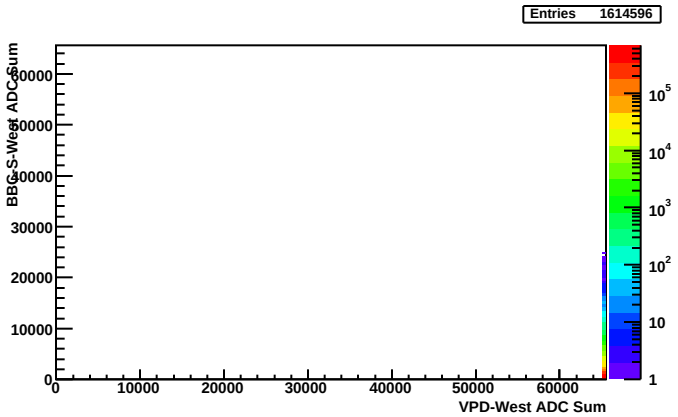

Entries 1614596
BBC-L-East ADC Sum
VPD-East ADC Sum

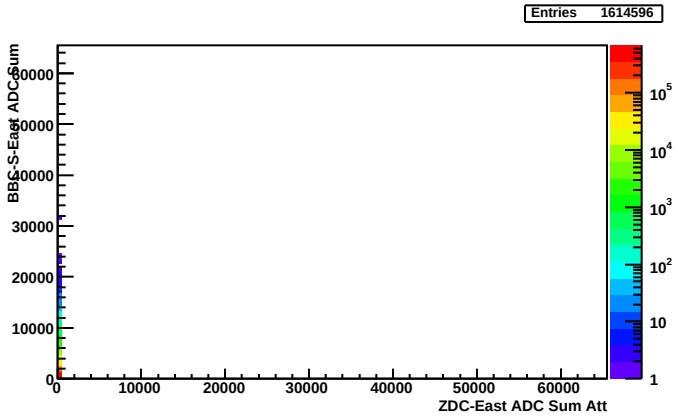

Entries 1614596
BBC-L-West ADC Sum
VPD-West ADC Sum

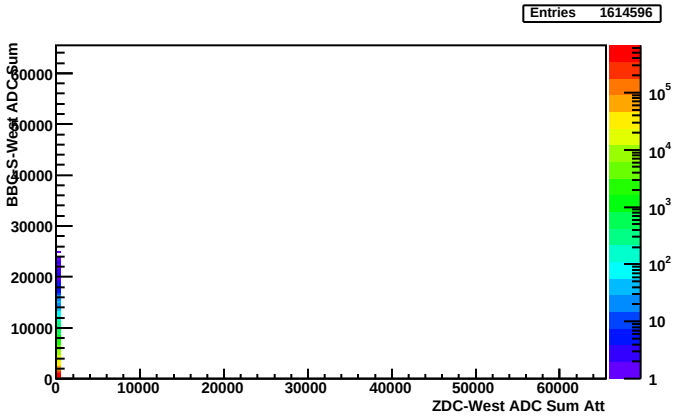

Entries 1614596
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

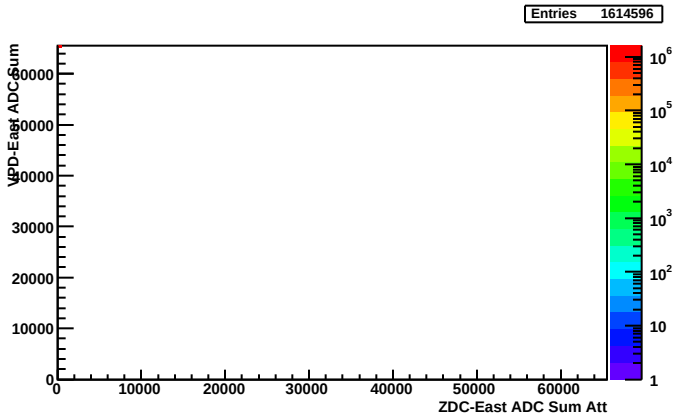

Entries 1614596
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

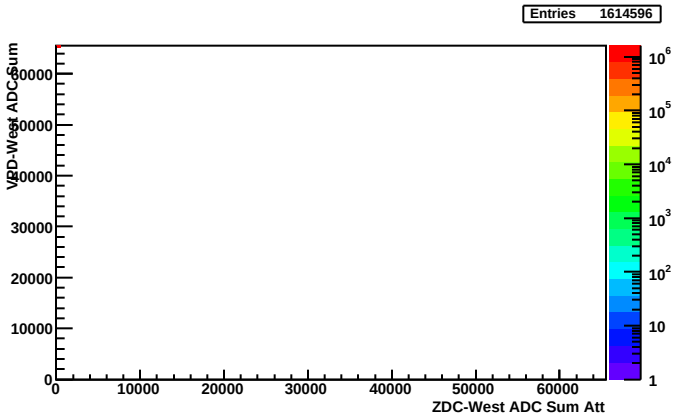

Entries 1614596
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 1614596
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 1614596
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 1614596
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1614596
VPD-East ADC Sum
ZDC-East ADC Sum Att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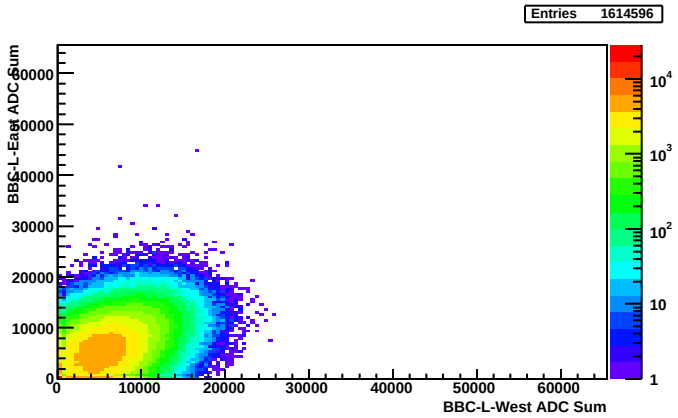
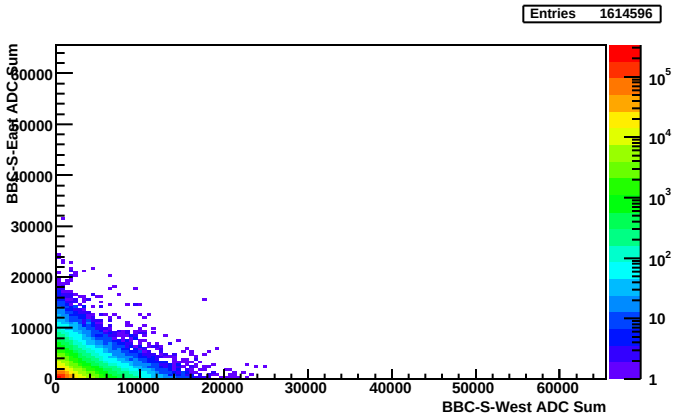
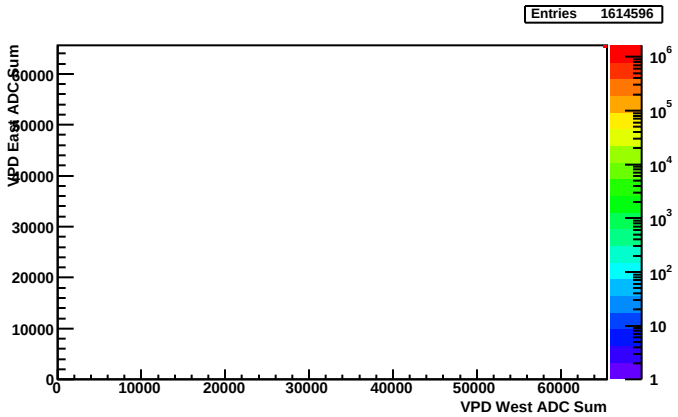
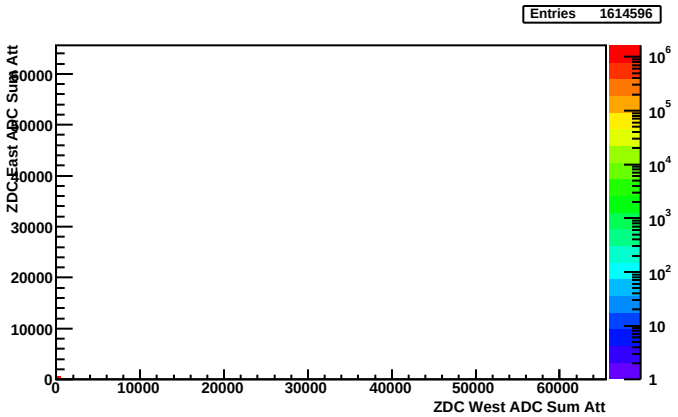
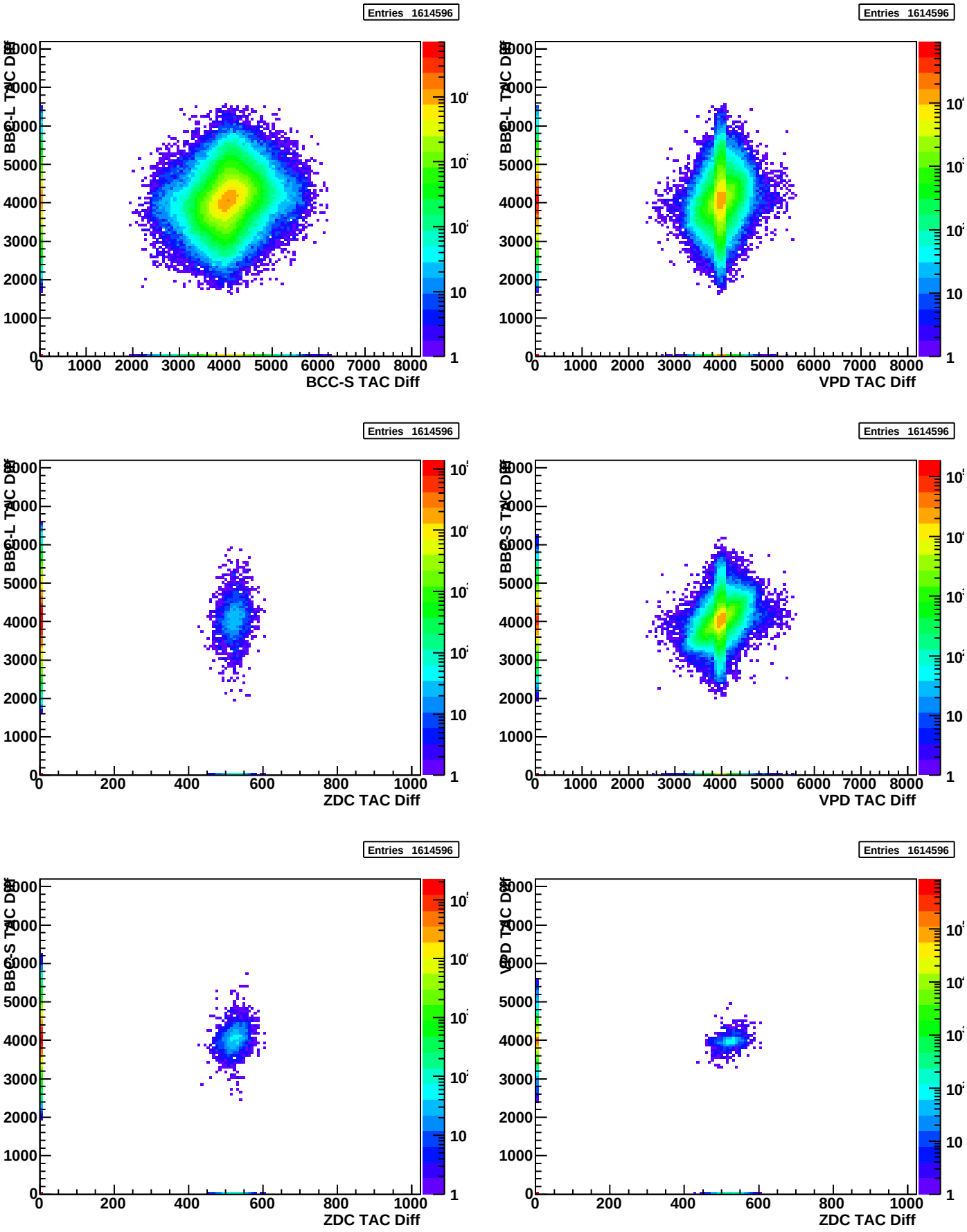
VPD-West ADC Sum
ZDC-West ADC Sum Att

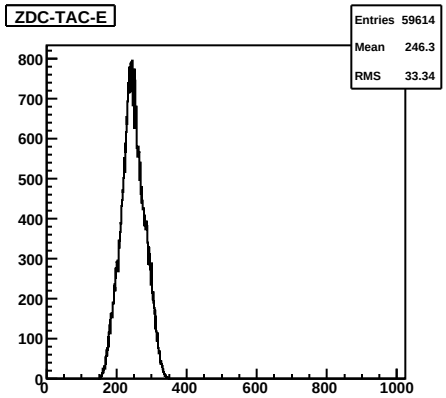

Entries 1614596
BBC-L-East ADC Sum
BBC-L-West ADC Sum

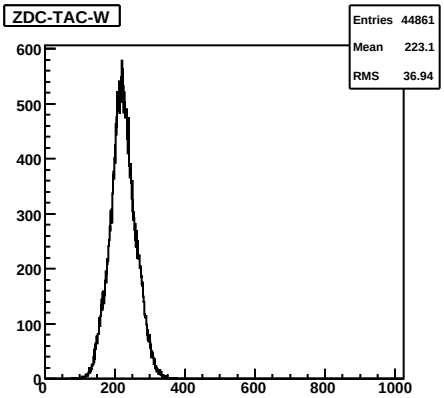

Entries 1614596
BBC-S-East ADC Sum
BBC-S-West ADC Sum

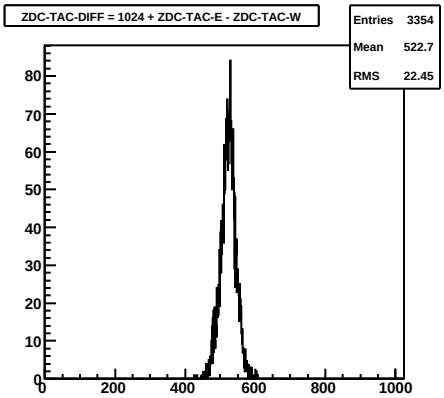

Entries 1614596
VPD East ADC Sum
VPD West ADC Sum

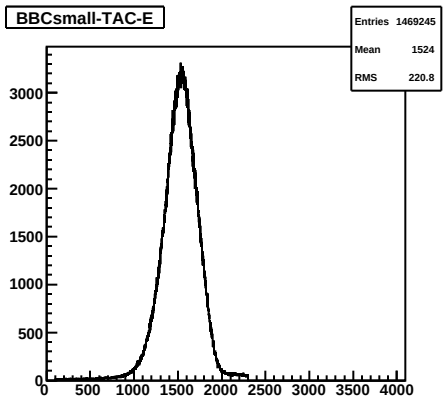

Entries 1614596
ZDC East ADC Sum Att
ZDC West ADC Sum Att

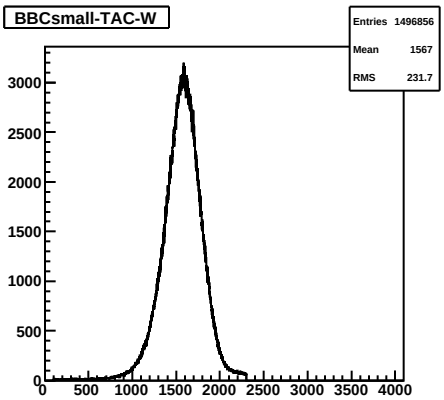

Entries 1614596
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1614596
BBC-L TAC Diff
VPD TAC Diff
Entries 1614596
BBC-L TAC Diff
ZDC TAC Diff
Entries 1614596
BBC-S TAC Diff
VPD TAC Diff
Entries 1614596
BBC-S TAC Diff
ZDC TAC Diff
Entries 1614596
VPD TAC Diff
ZDC TAC Diff

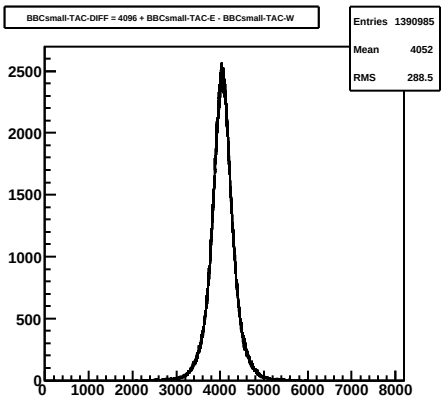

ZDC-TAC-E
Entries 59614
Mean 246.3
RMS 33.34

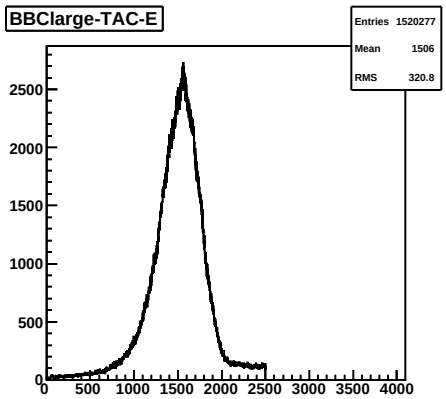

ZDC-TAC-W
Entries 44861
Mean 223.1
RMS 36.94

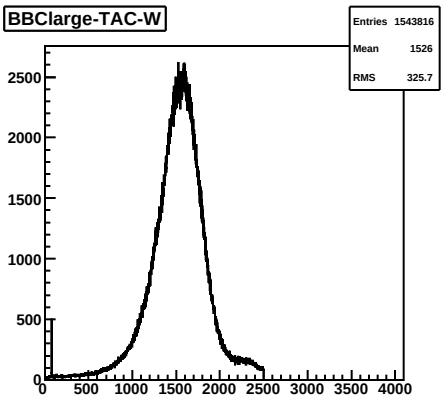

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3354
Mean 522.7
RMS 22.45

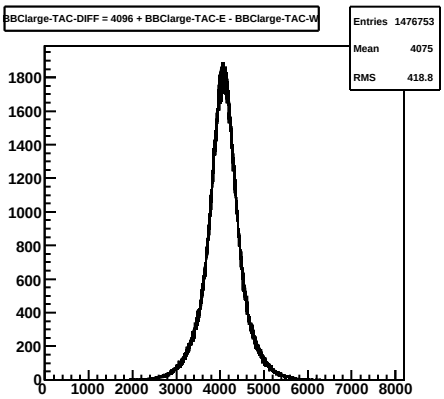

BBCsmall-TAC-E
Entries 1469245
Mean 1524
RMS 220.8

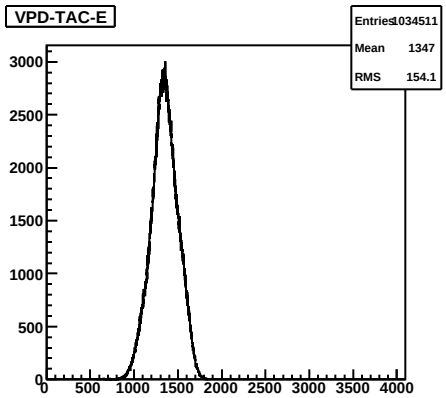

BBCsmall-TAC-W
Entries 1496856
Mean 1567
RMS 231.7

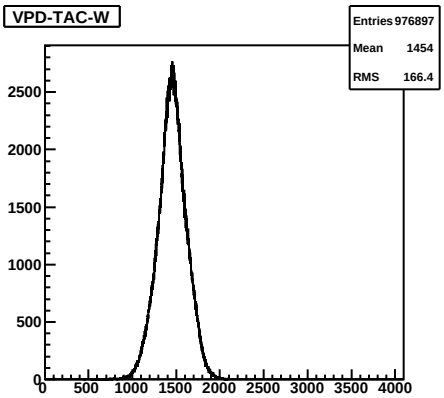

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1390985
Mean 4052
RMS 288.5

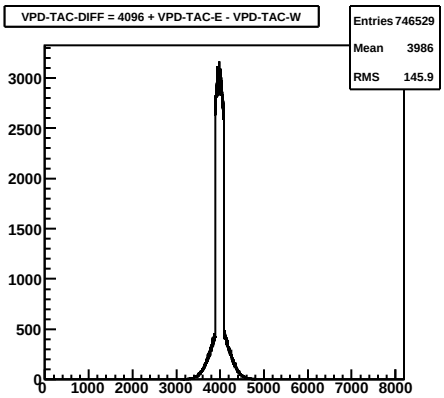

BBClarge-TAC-E
Entries 1520277
Mean 1506
RMS 320.8

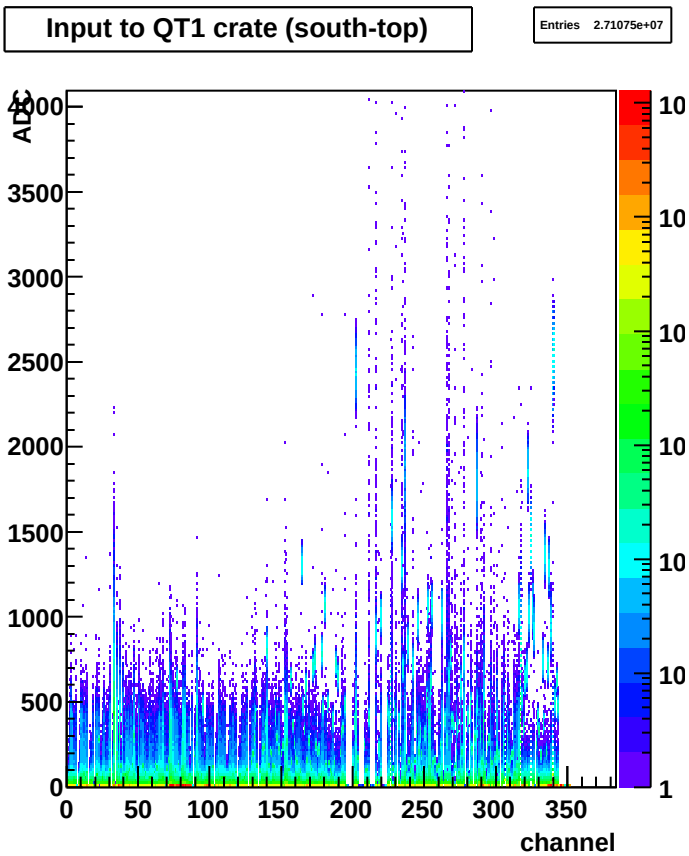

BBClarge-TAC-W
Entries 1543816
Mean 1526
RMS 325.7

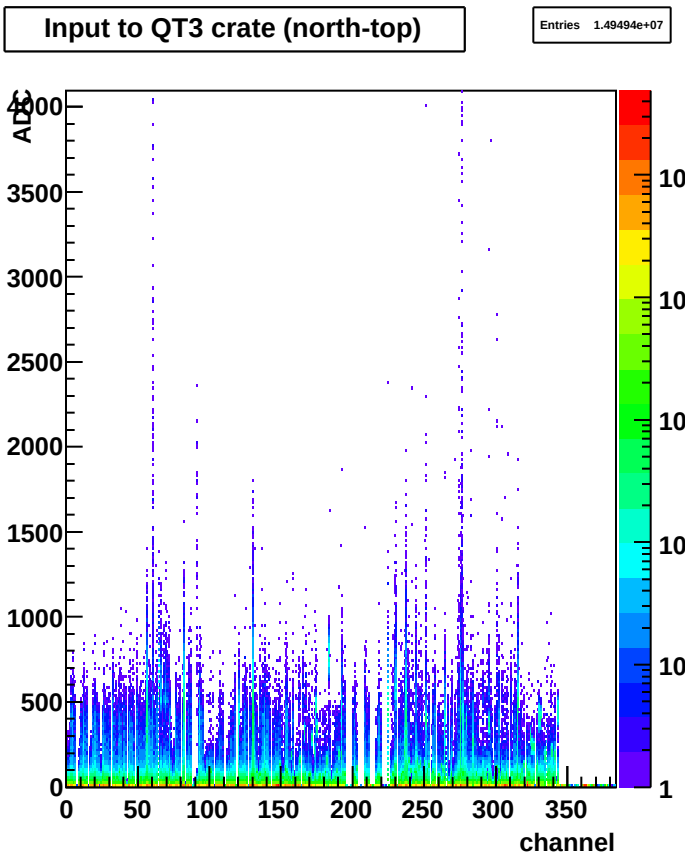

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1476753
Mean 4075
RMS 418.8

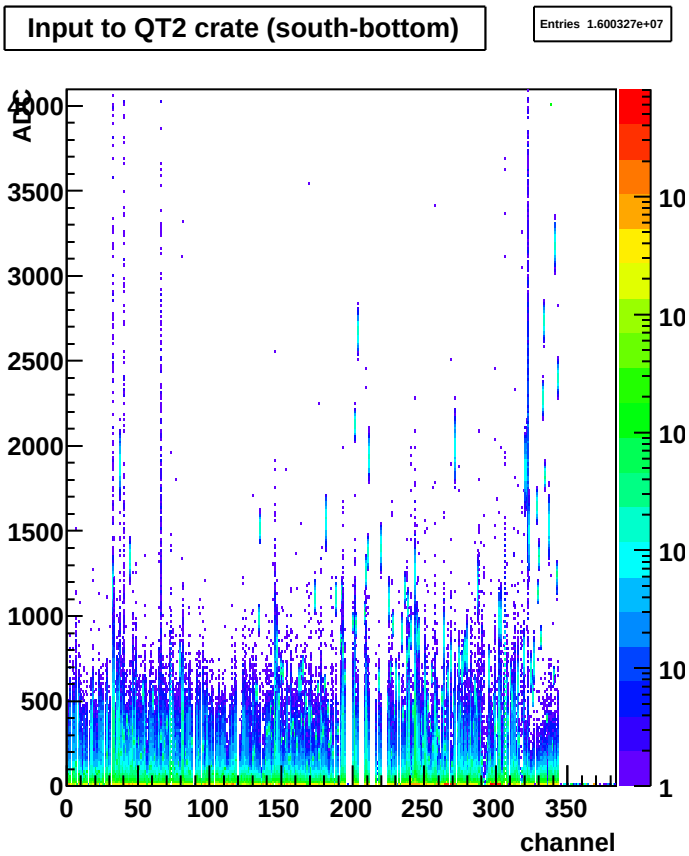

VPD-TAC-E
Entries 1034511
Mean 1347
RMS 154.1

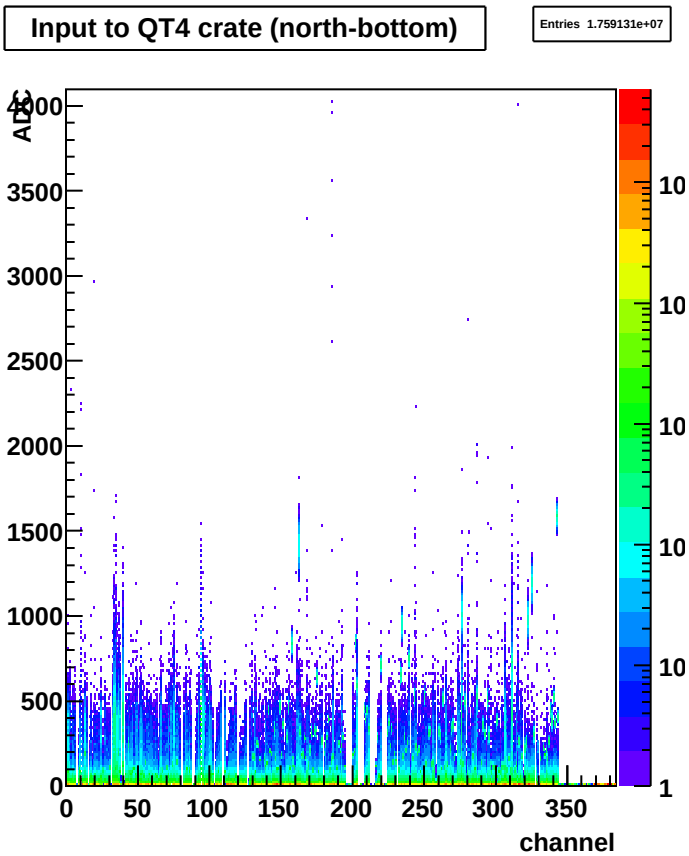

VPD-TAC-W
Entries 976897
Mean 1454
RMS 166.4

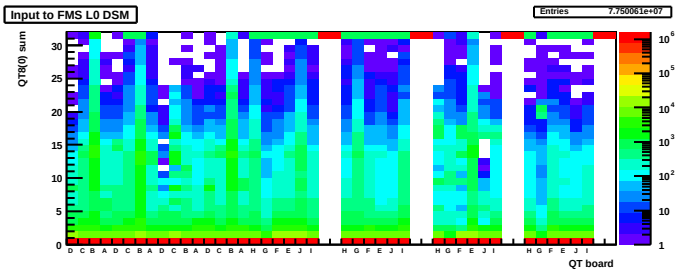

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 746529
Mean 3986
RMS 145.9

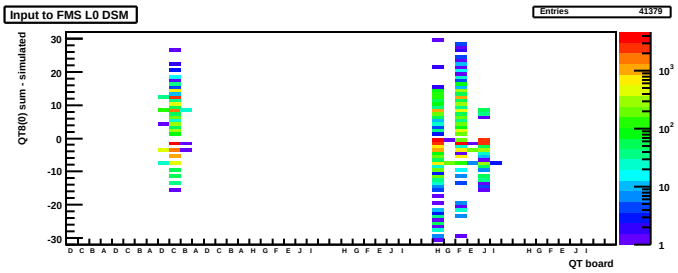

Input to QT1 crate (south-top)
Entries 2.71075e+07
ADC
channel

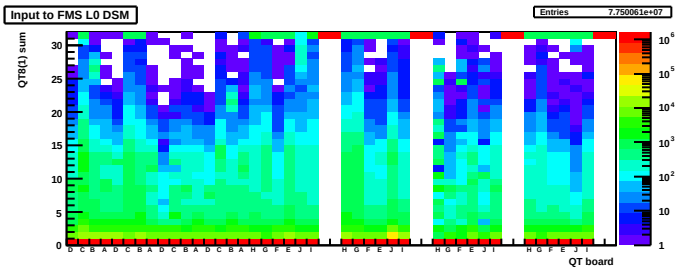

Input to QT3 crate (north-top)
Entries 1.49494e+07
ADC
channel

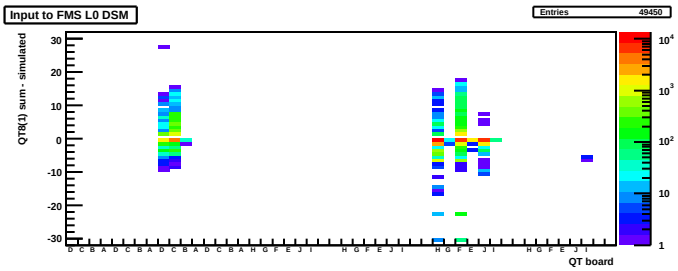

Input to QT2 crate (south-bottom)
Entries 1.600327e+07
ADC
channel

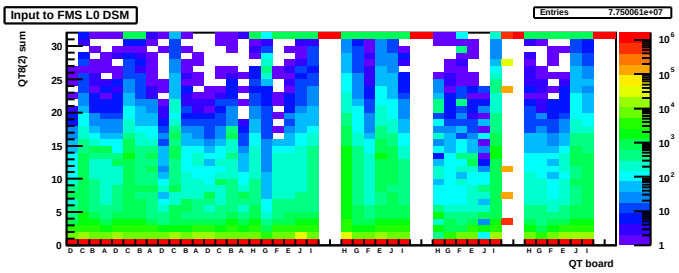

Input to QT4 crate (north-bottom)
Entries 1.759131e+07
ADC
channel

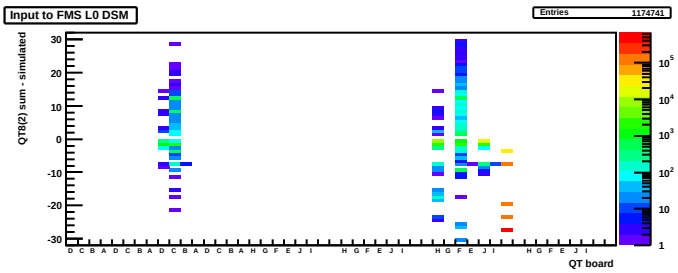

Input to FMS L0 DSM
Entries 7.750061e+07
QT8(0) sum
QT board

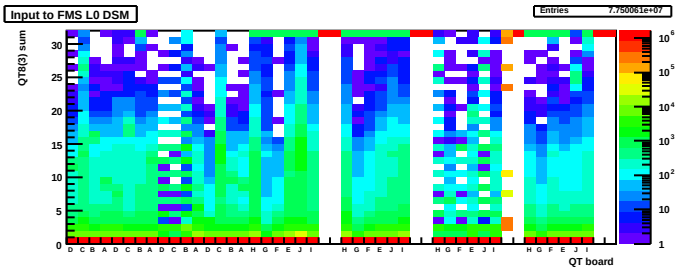

Input to FMS L0 DSM
Entries 41379
QT8(0) sum - simulated
QT board

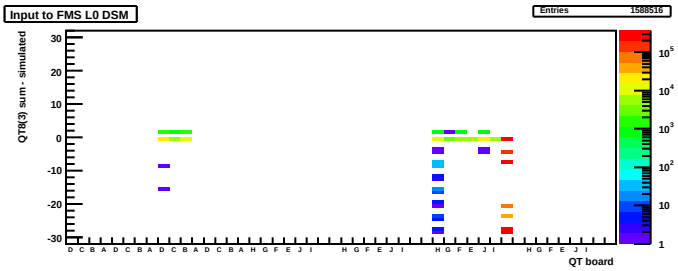

Input to FMS L0 DSM
Entries 7.750061e+07
QT8(1) sum
QT board

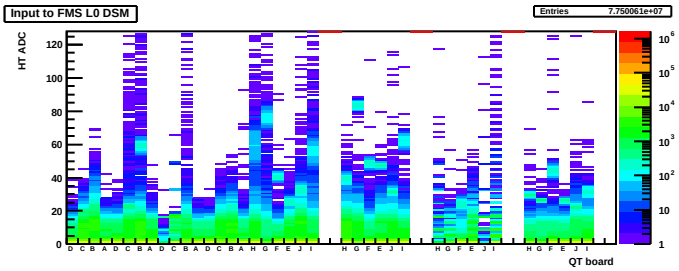

Input to FMS L0 DSM
Entries 49450
QT8(1) sum - simulated
QT board

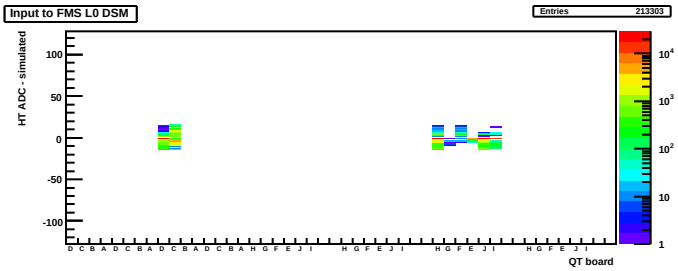

Input to FMS L0 DSM
Entries 7.750061e+07
QT8(2) sum
QT board

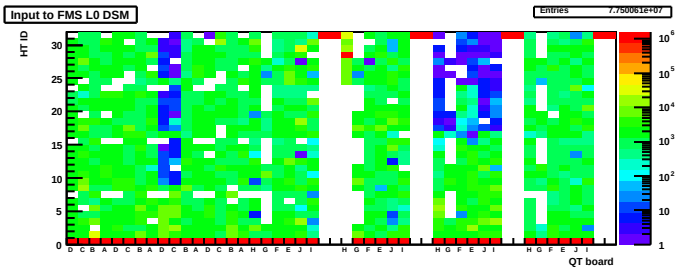

Input to FMS L0 DSM
Entries 1174741
QT8(2) sum - simulated
QT board

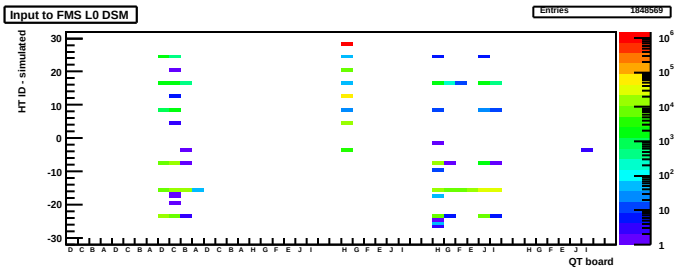

Input to FMS L0 DSM
Entries 7.750061e+07
QT8(3) sum
QT board

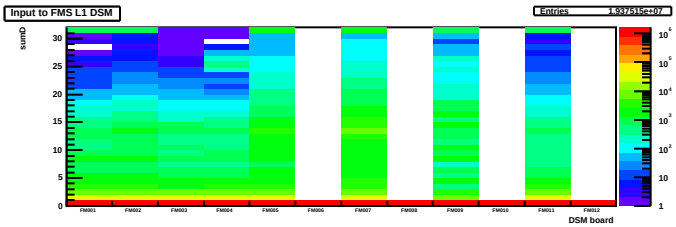

Input to FMS L0 DSM
Entries 1588516
QT8(3) sum - simulated
QT board

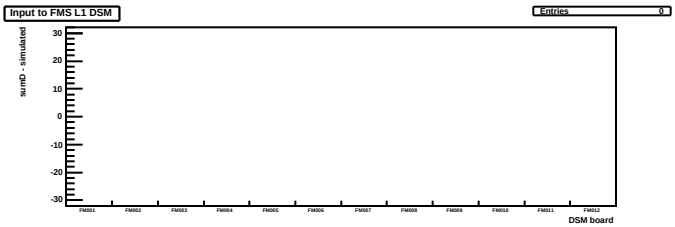

Input to FMS L0 DSM
Entries 7.750061e+07
HT ADC
QT board

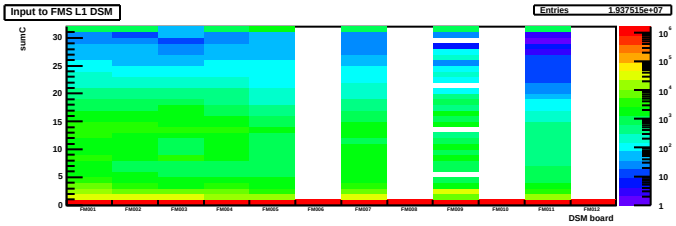

Input to FMS L0 DSM
Entries 213303
HT ADC - simulated
QT board

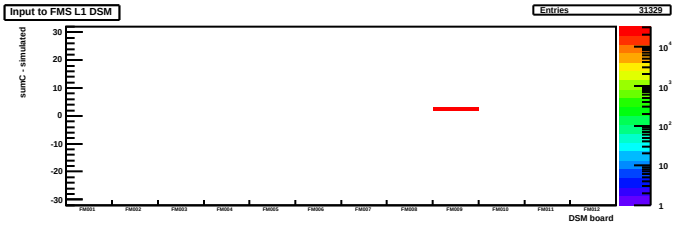

Input to FMS L0 DSM
Entries 7.750061e+07
HT ID
QT board

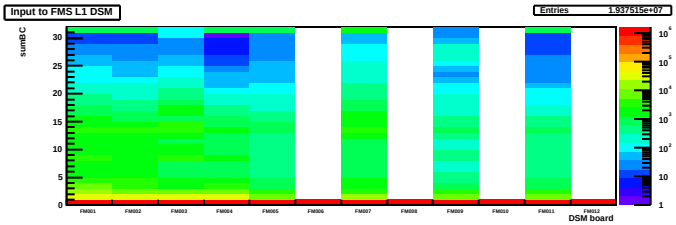

Input to FMS L0 DSM
Entries 1848569
HT ID - simulated
QT board

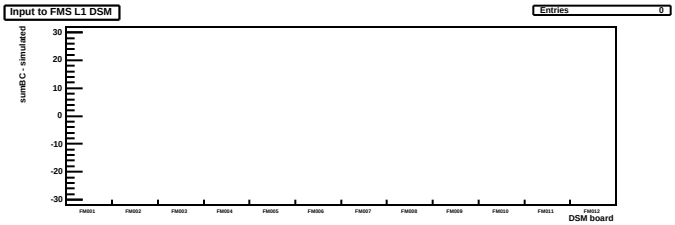

Input to FMS L1 DSM
Entries 1.937515e+07
sumD
DSM board

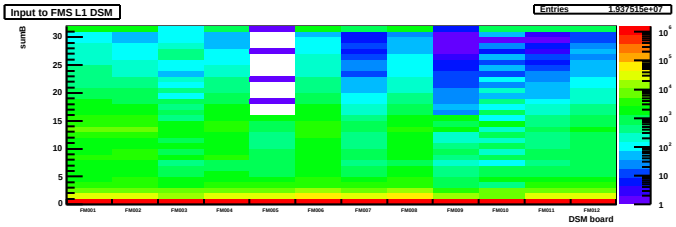

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

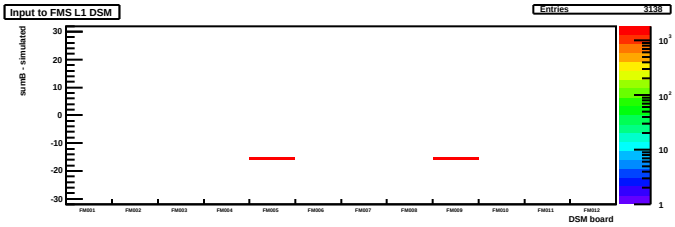

Input to FMS L1 DSM
Entries 1.937515e+07
sumC
DSM board

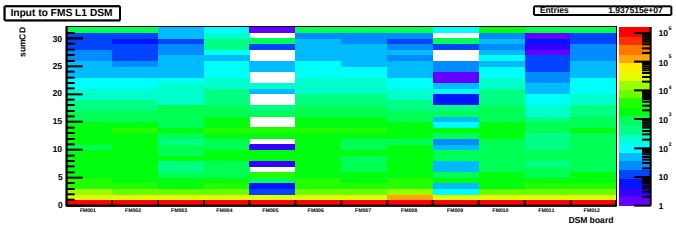

Input to FMS L1 DSM
Entries 31329
sumC - simulated
DSM board

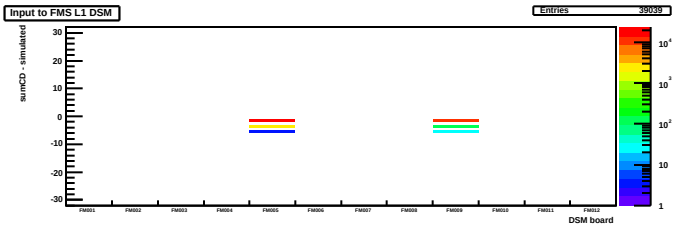

Input to FMS L1 DSM
Entries 1.937515e+07
sumBC
DSM board

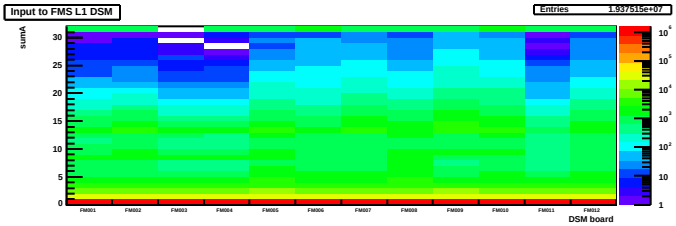

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

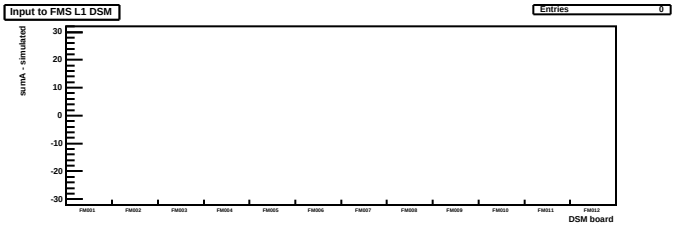

Input to FMS L1 DSM
Entries 1.937515e+07
sumB
DSM board

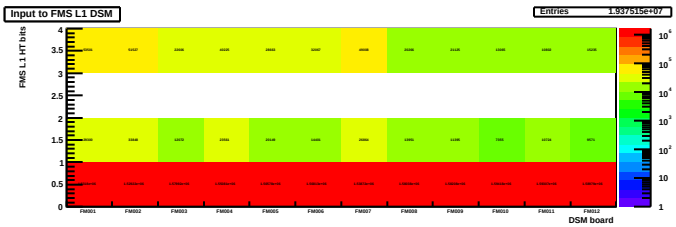

Input to FMS L1 DSM
Entries 3138
sumB - simulated
DSM board

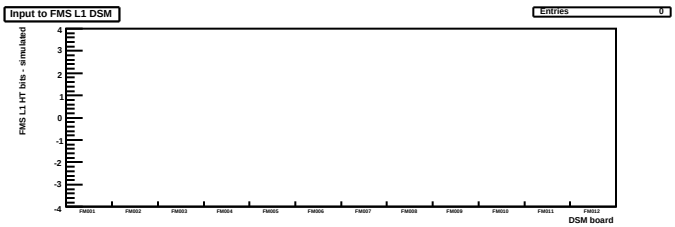

Input to FMS L1 DSM
Entries 1.937515e+07
sumCD
DSM board

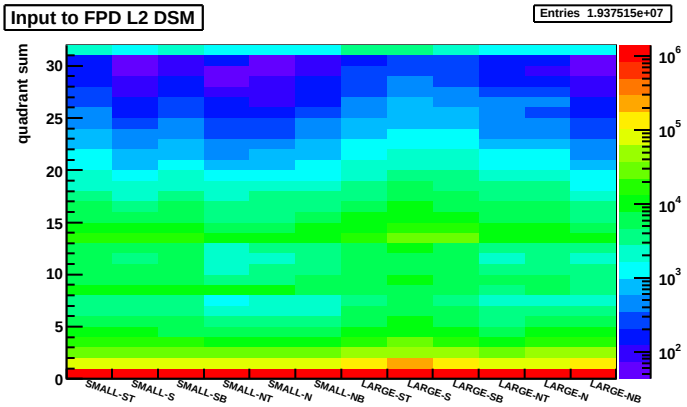

Input to FMS L1 DSM
Entries 39039
sumCD - simulated
DSM board

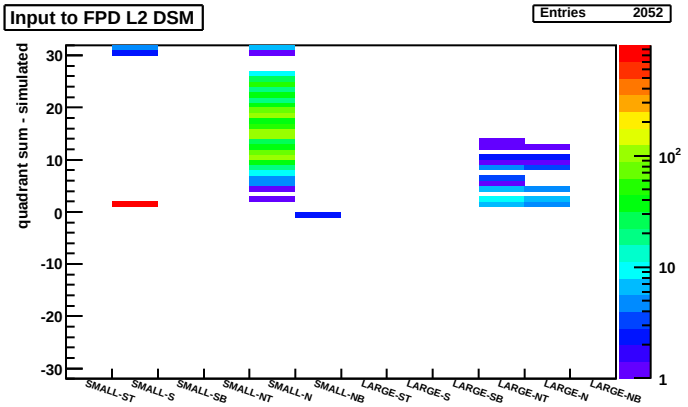

Input to FMS L1 DSM
Entries 1.937515e+07
sumA
DSM board

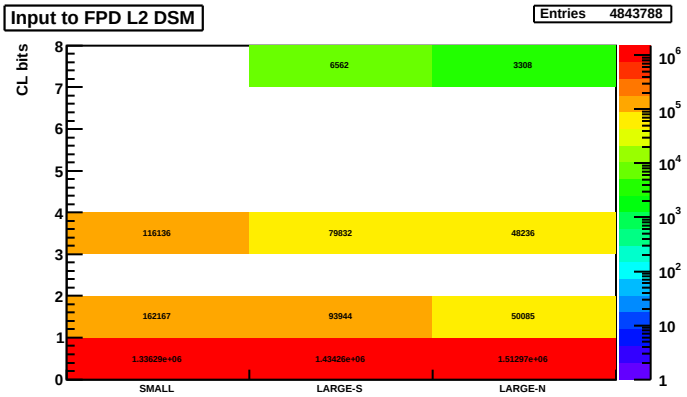

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

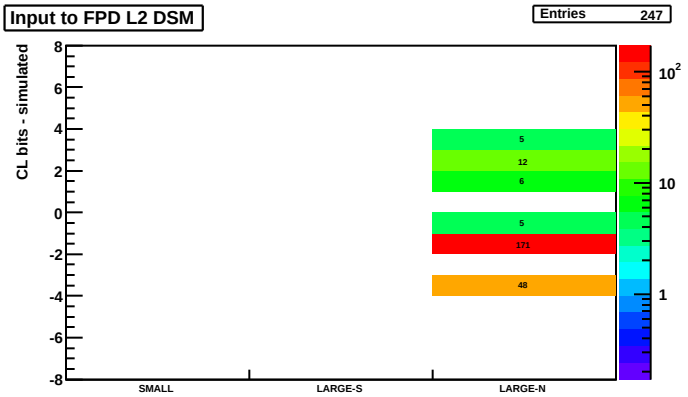

Input to FMS L1 DSM
Entries 1.937515e+07
FMS L1 HT bits
DSM board

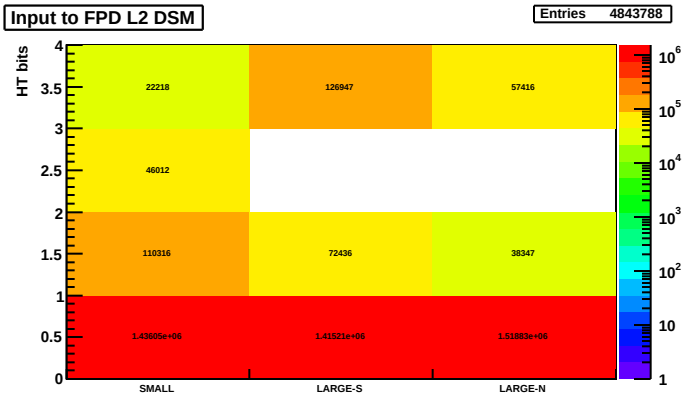

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

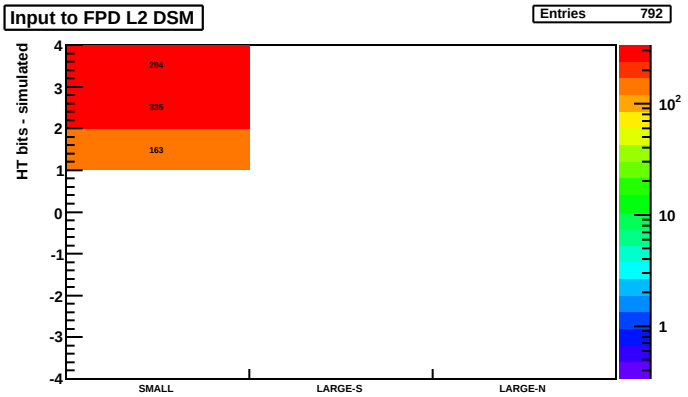

Input to FPD L2 DSM
Entries 1.937515e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

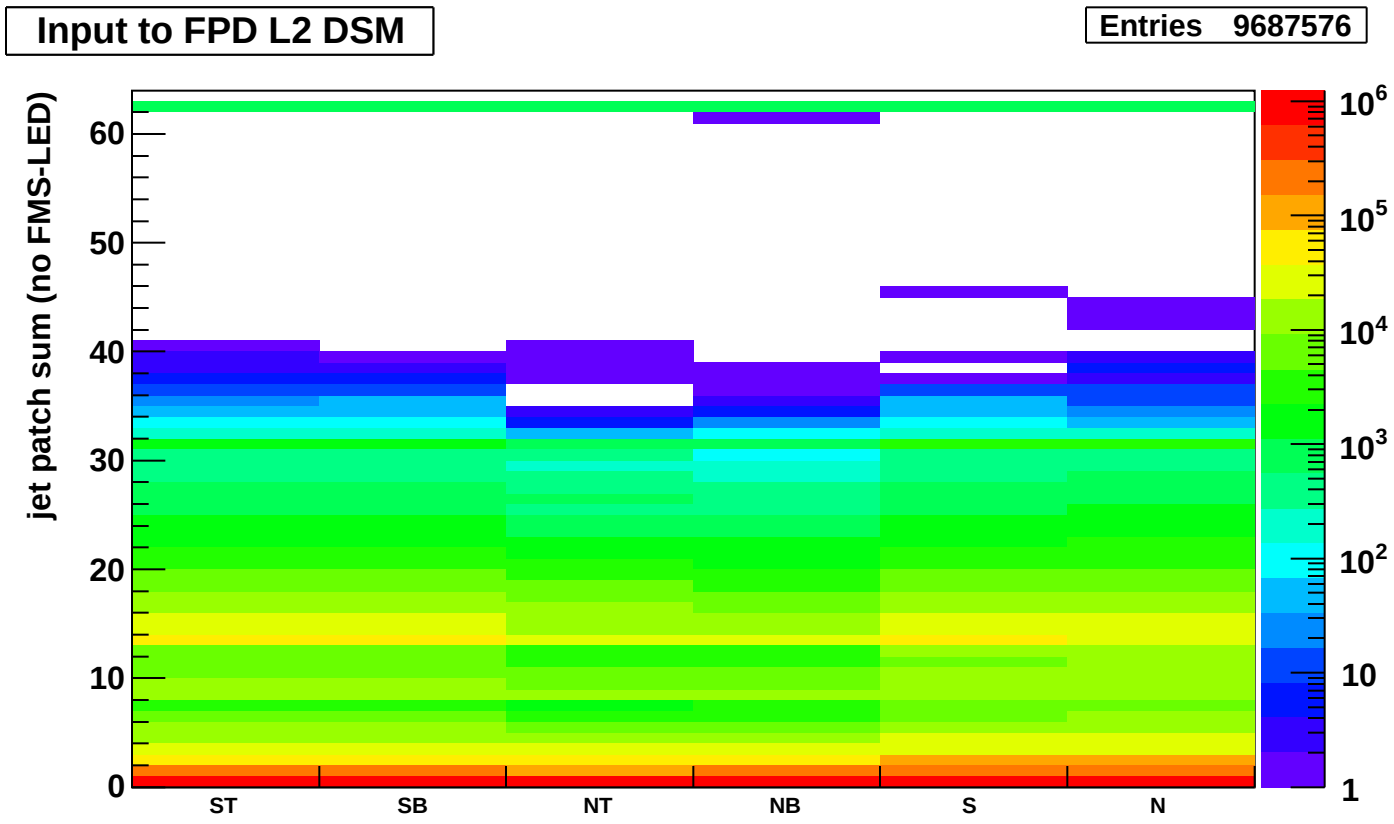

Input to FPD L2 DSM
Entries 2052
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

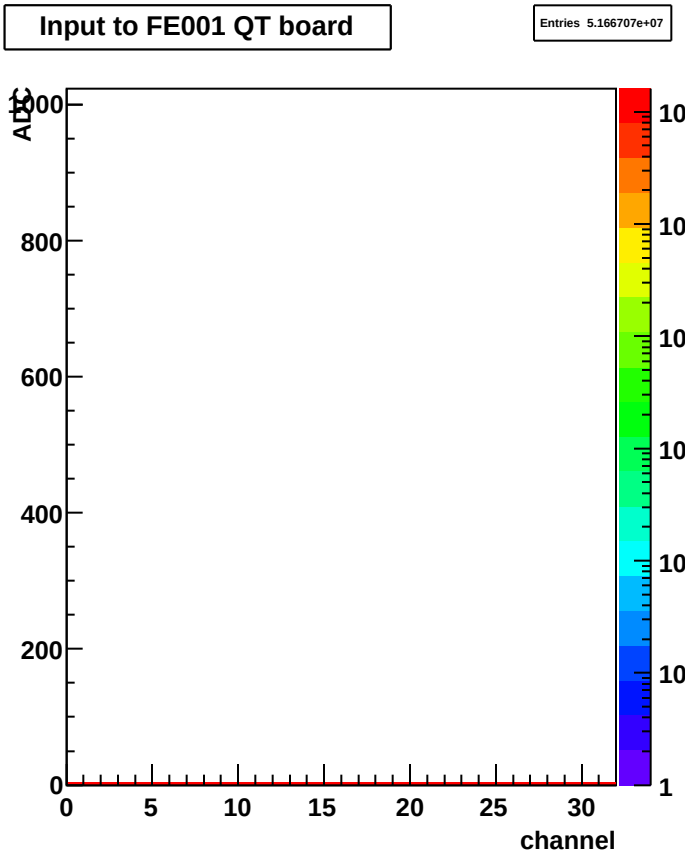

Input to FPD L2 DSM
Entries 4843788
CL bits
6562
3308
116136
79832
48236
162167
93944
50085
1.33629e+06
1.43426e+06
1.51297e+06
SMALL
LARGE-S
LARGE-N

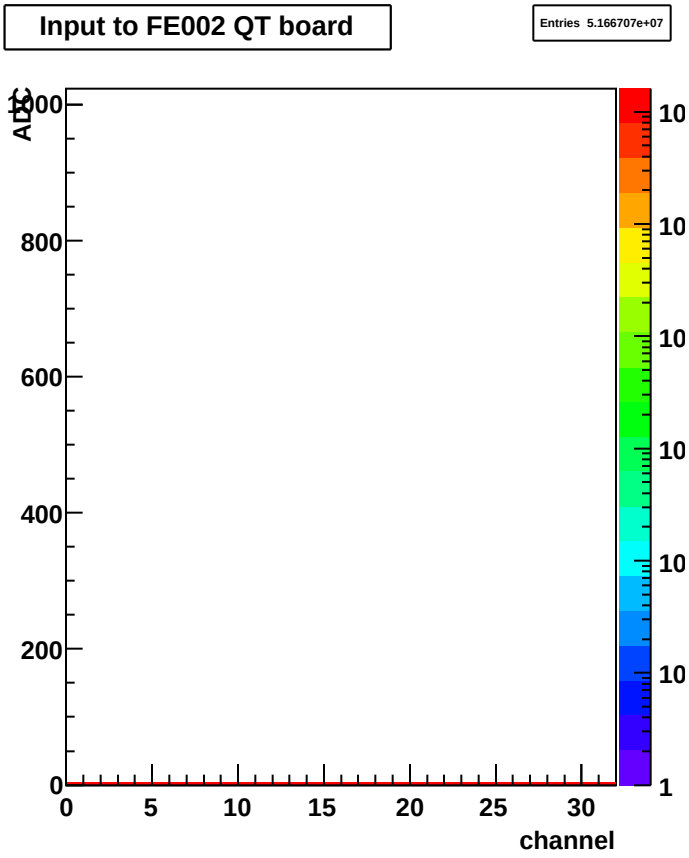

Input to FPD L2 DSM
Entries 247
CL bits - simulated
5
12
6
5
171
48
SMALL
LARGE-S
LARGE-N

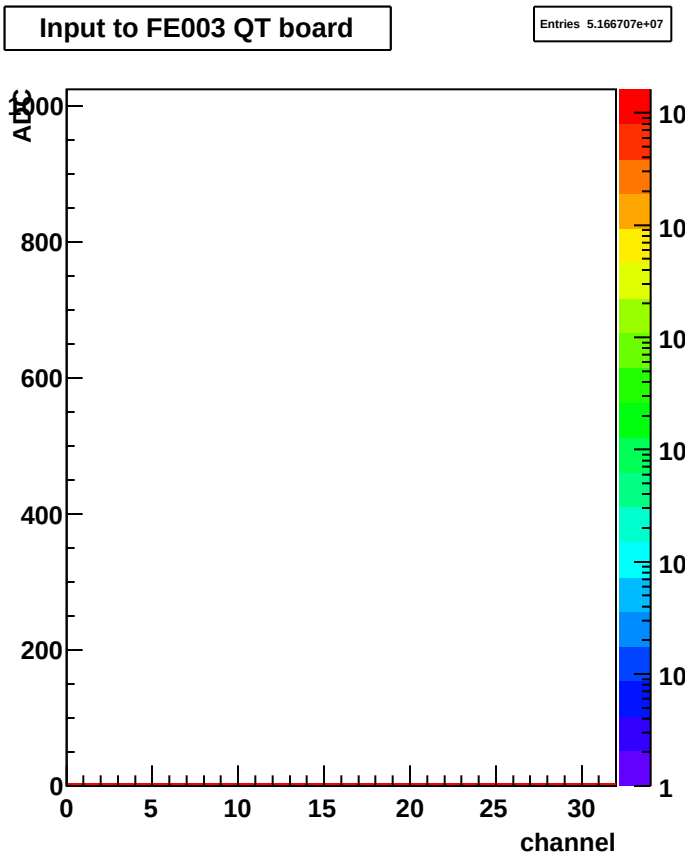

Input to FPD L2 DSM
Entries 4843788
HT bits
22218
126947
57416
46012
110316
72436
38347
1.43605e+06
1.41521e+06
1.51883e+06
SMALL
LARGE-S
LARGE-N

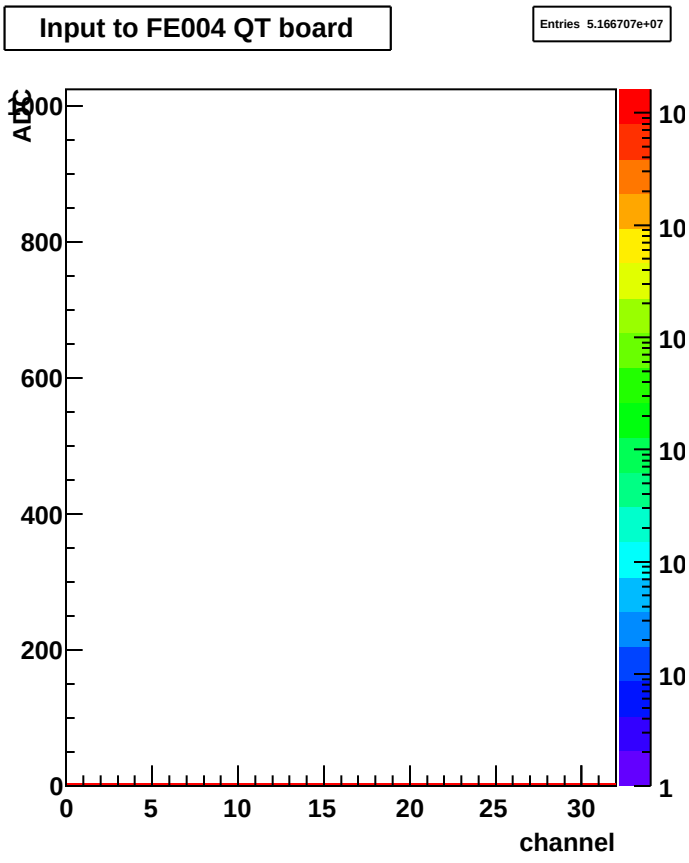

Input to FPD L2 DSM
Entries 792
HT bits - simulated
294
335
163
SMALL
LARGE-S
LARGE-N

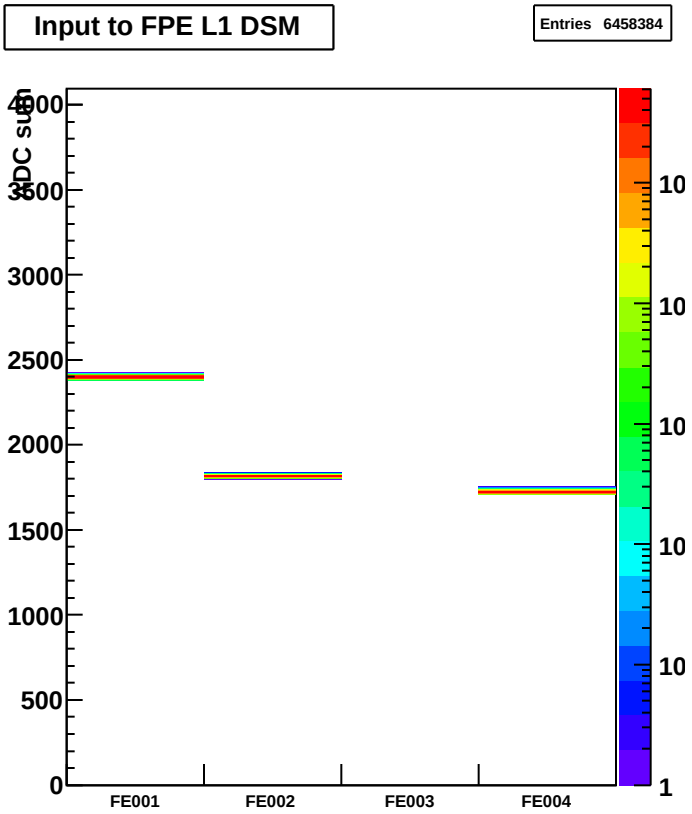

Input to FPD L2 DSM
Entries 9687576
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

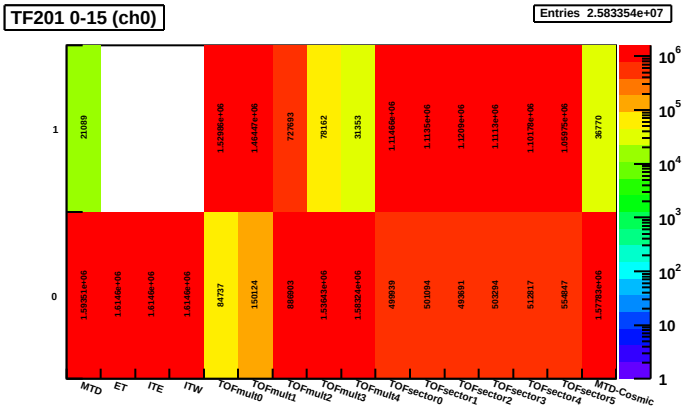

Input to FE001 QT board
Entries 5.166707e+07
ADC
channel

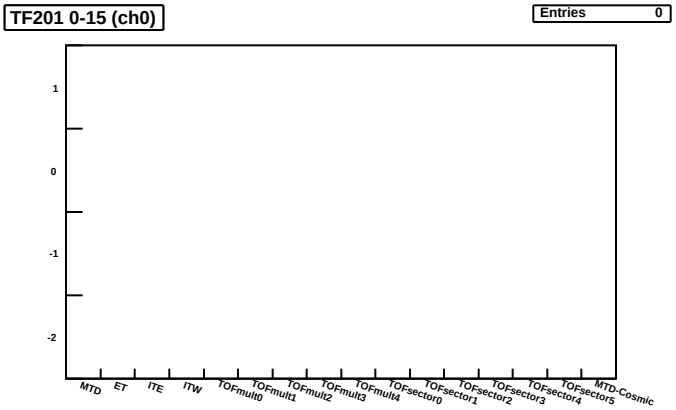

Input to FE002 QT board
Entries 5.166707e+07
ADC
channel

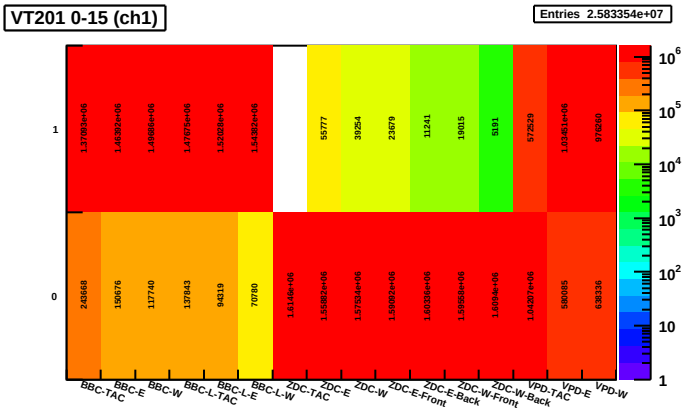

Input to FE003 QT board
Entries 5.166707e+07
ADC
channel


Input to FE004 QT board
Entries 5.166707e+07
ADC
channel


Input to FPE L1 DSM
Entries 6458384
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 2.583354e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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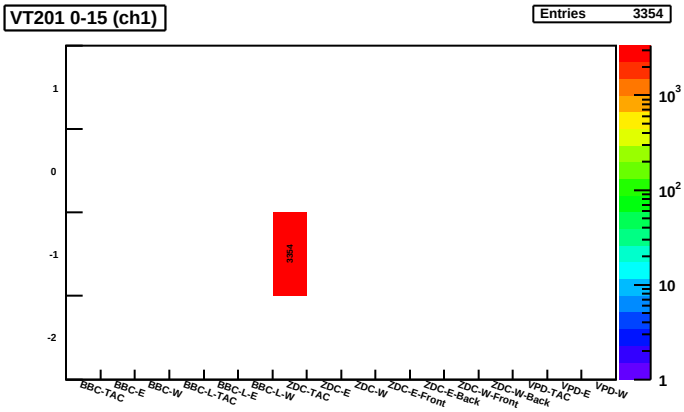
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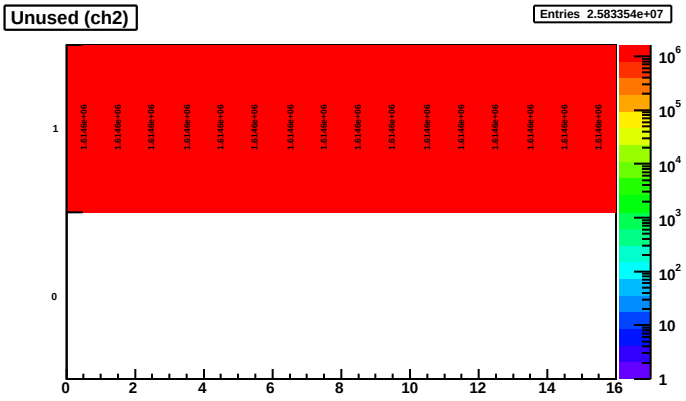
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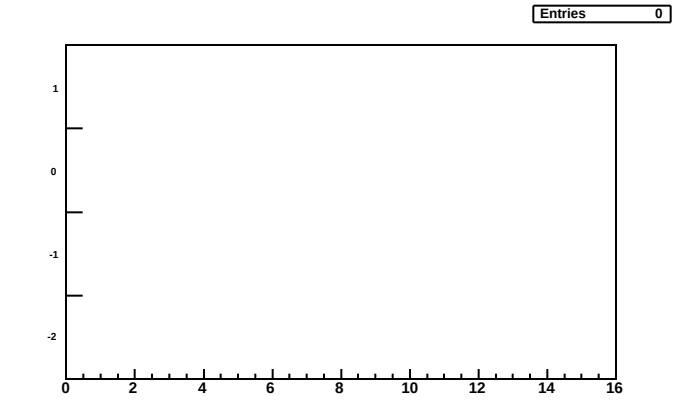
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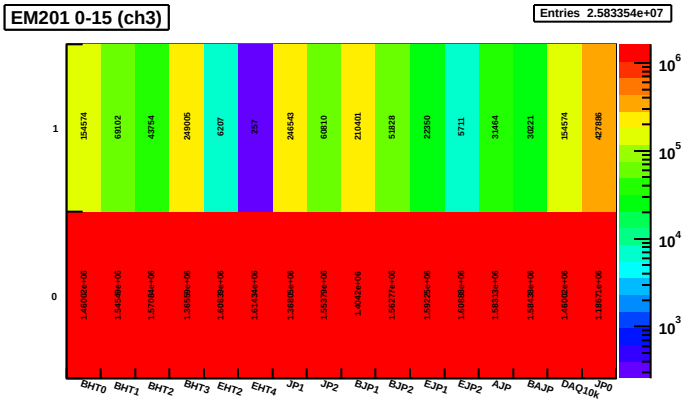
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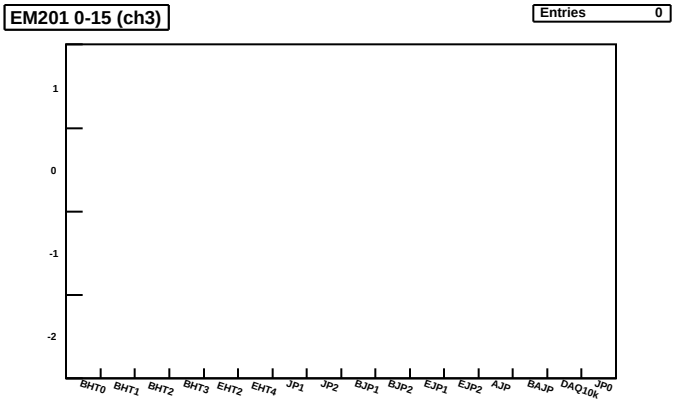
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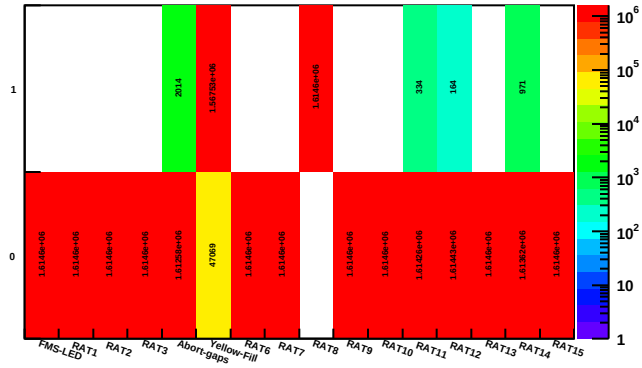
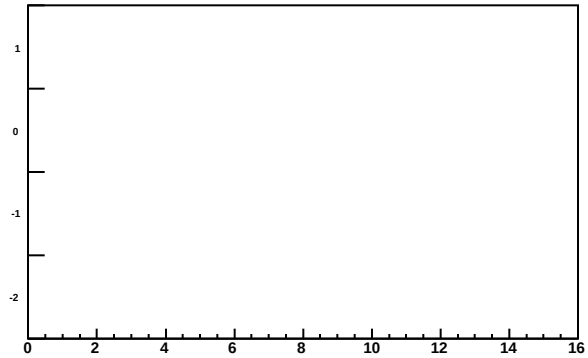
RAT board (ch4)





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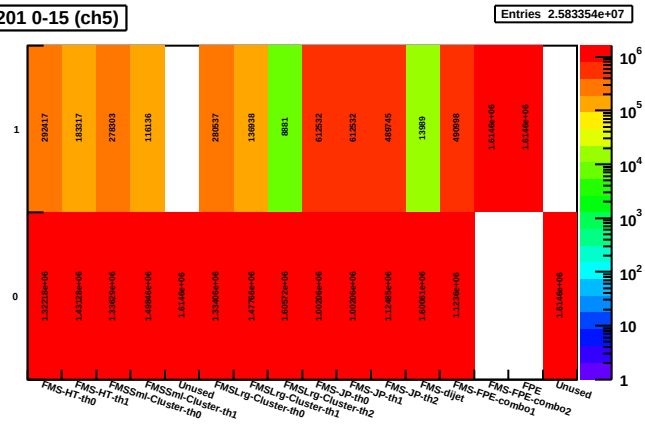

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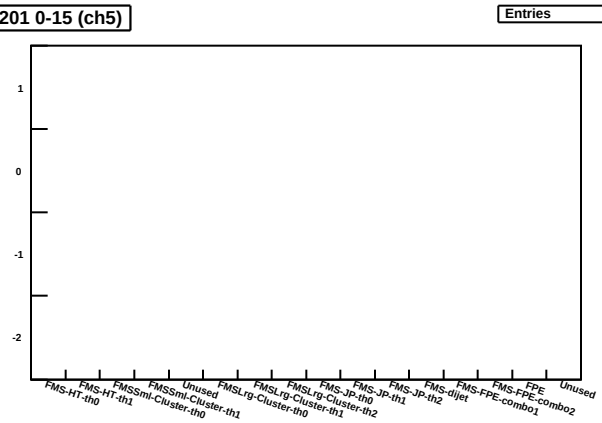
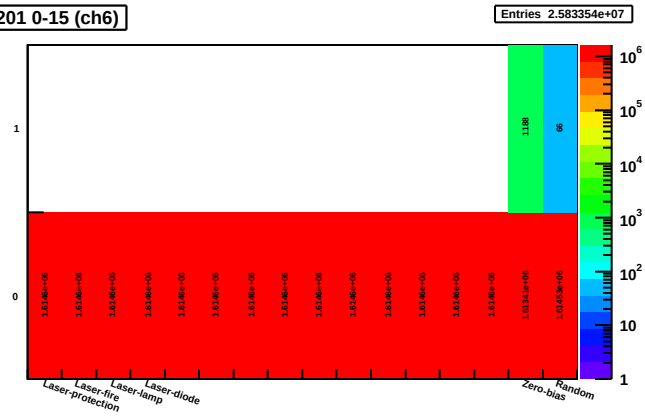
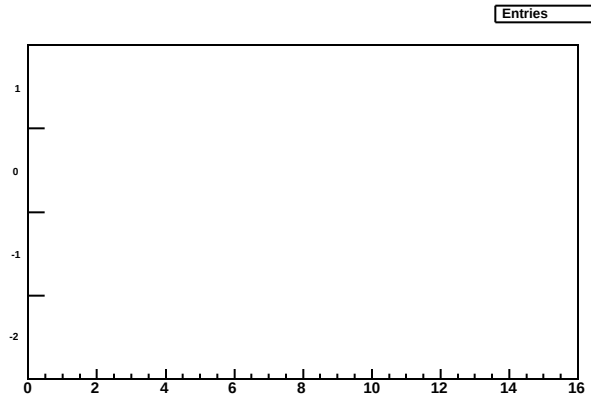
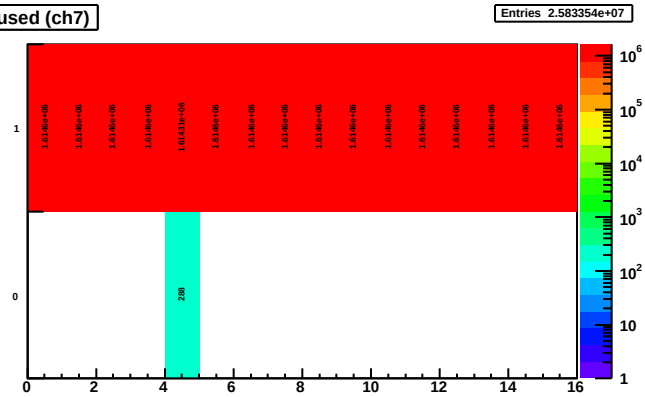
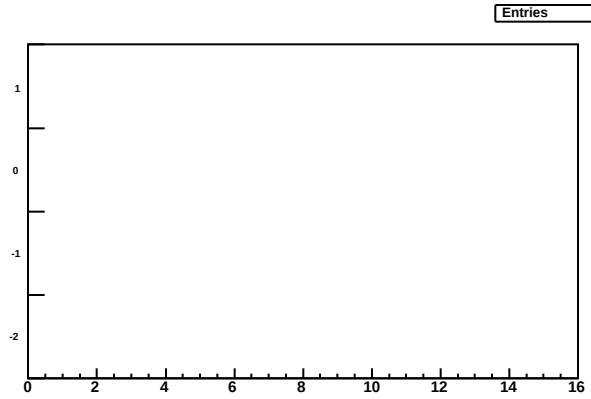


ST201 0-15 (ch6)





Unused (ch7)