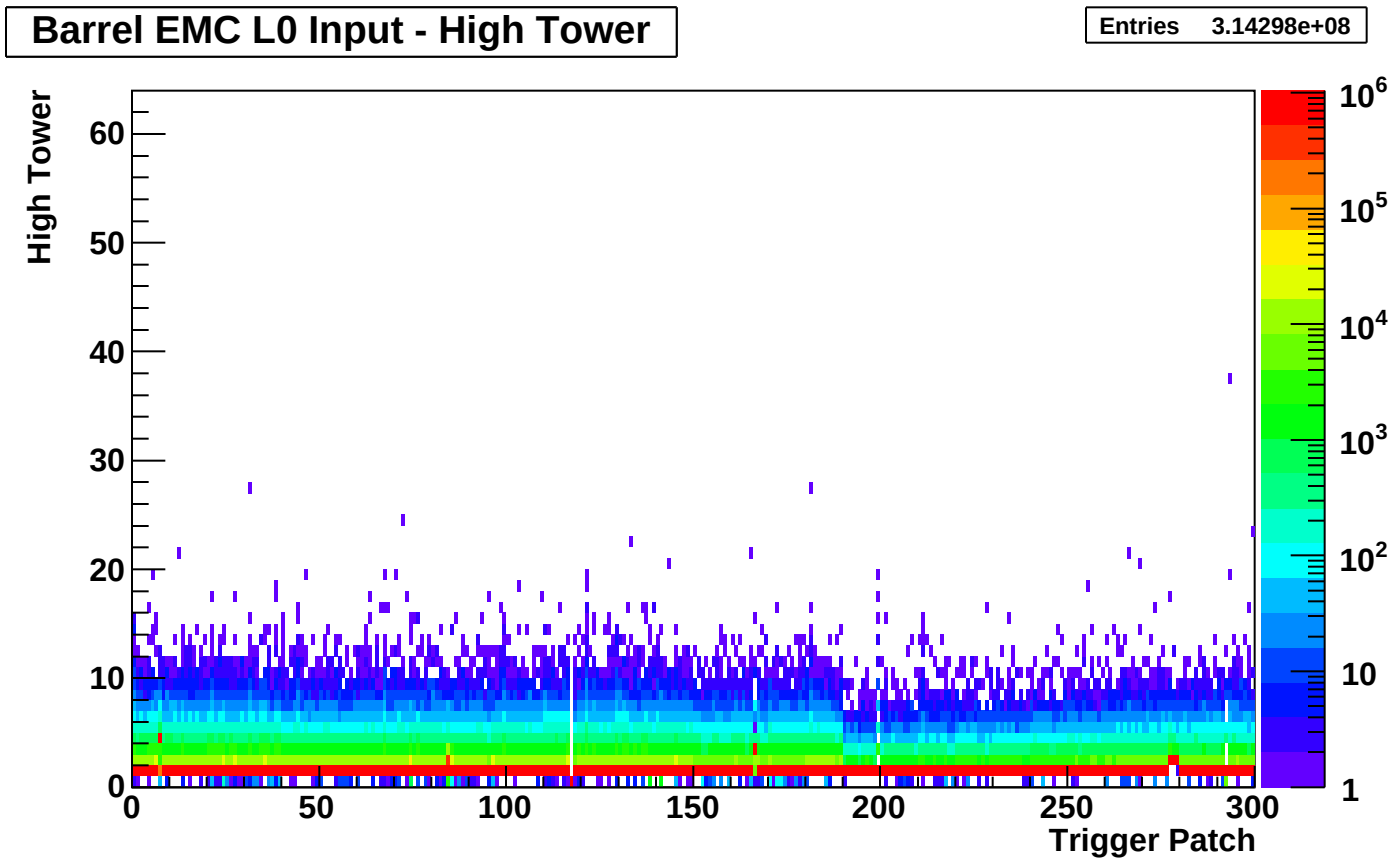
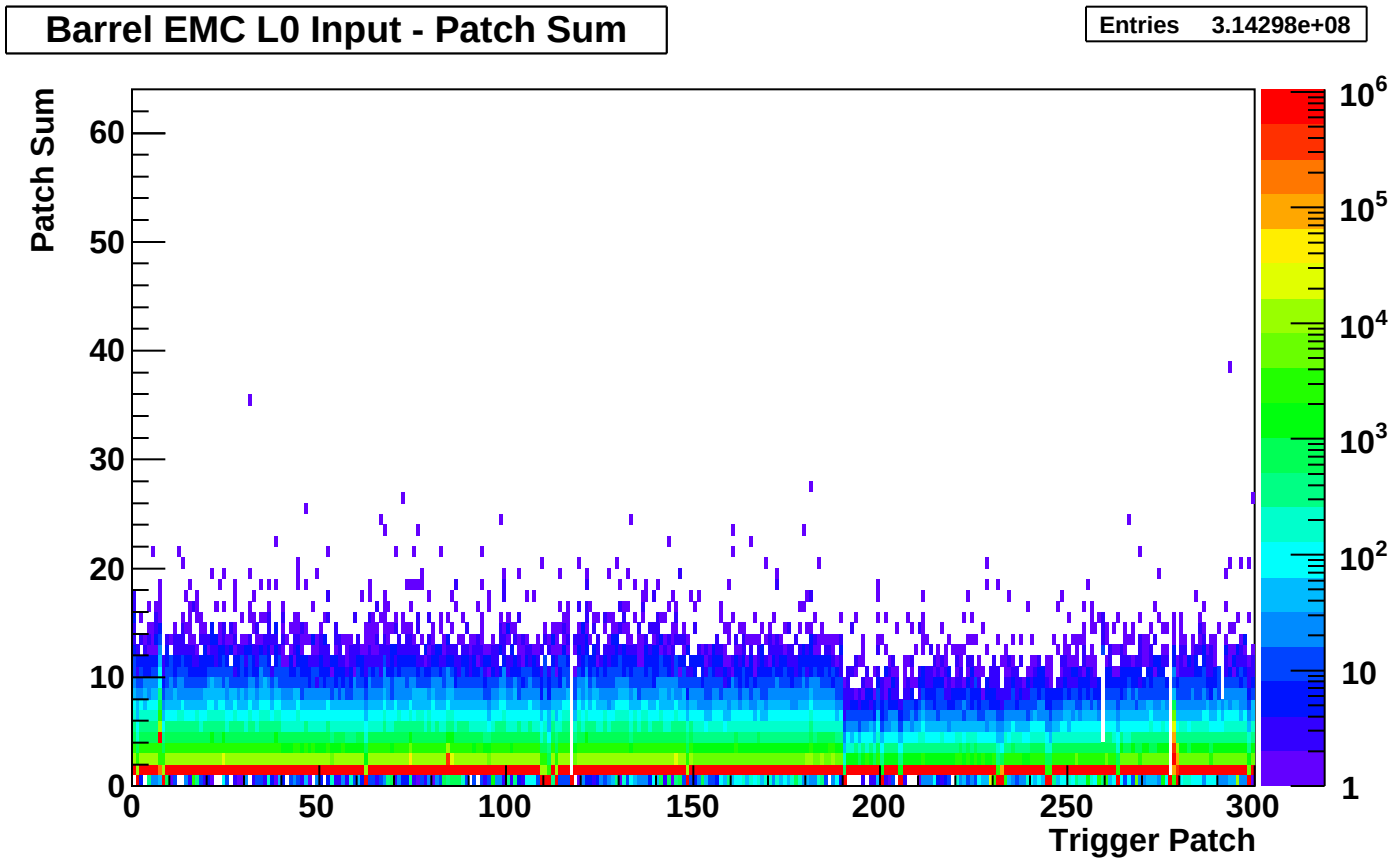
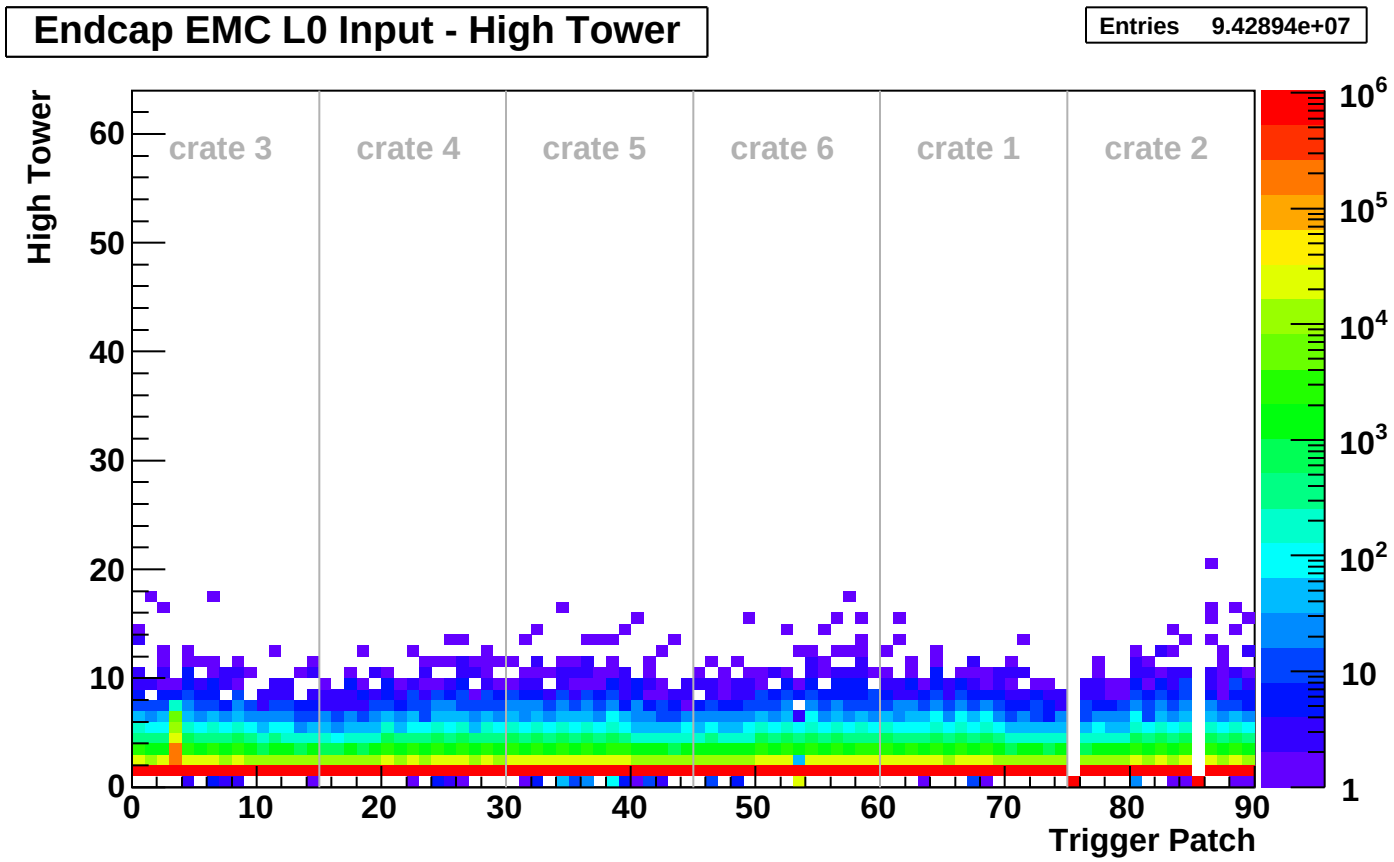
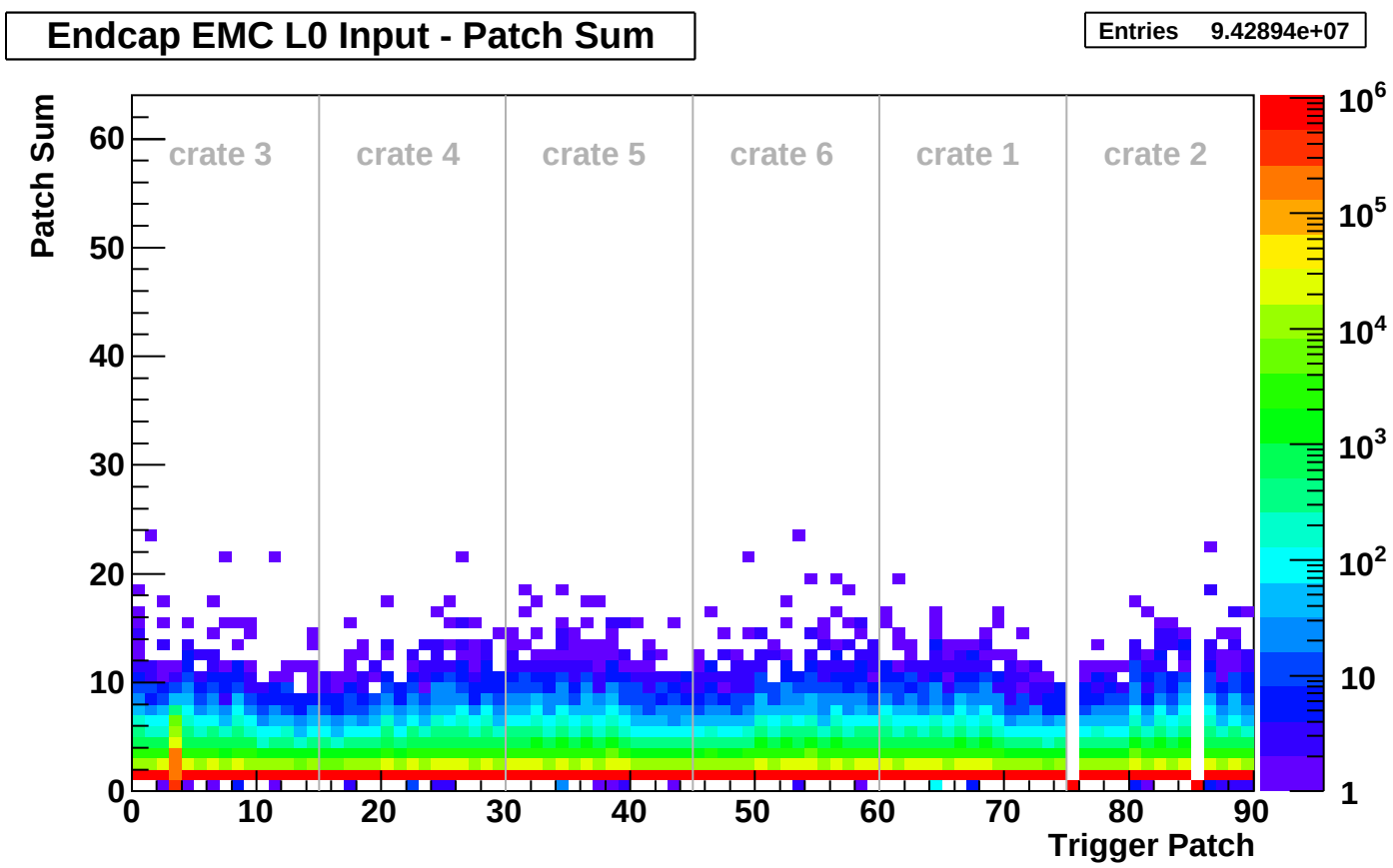
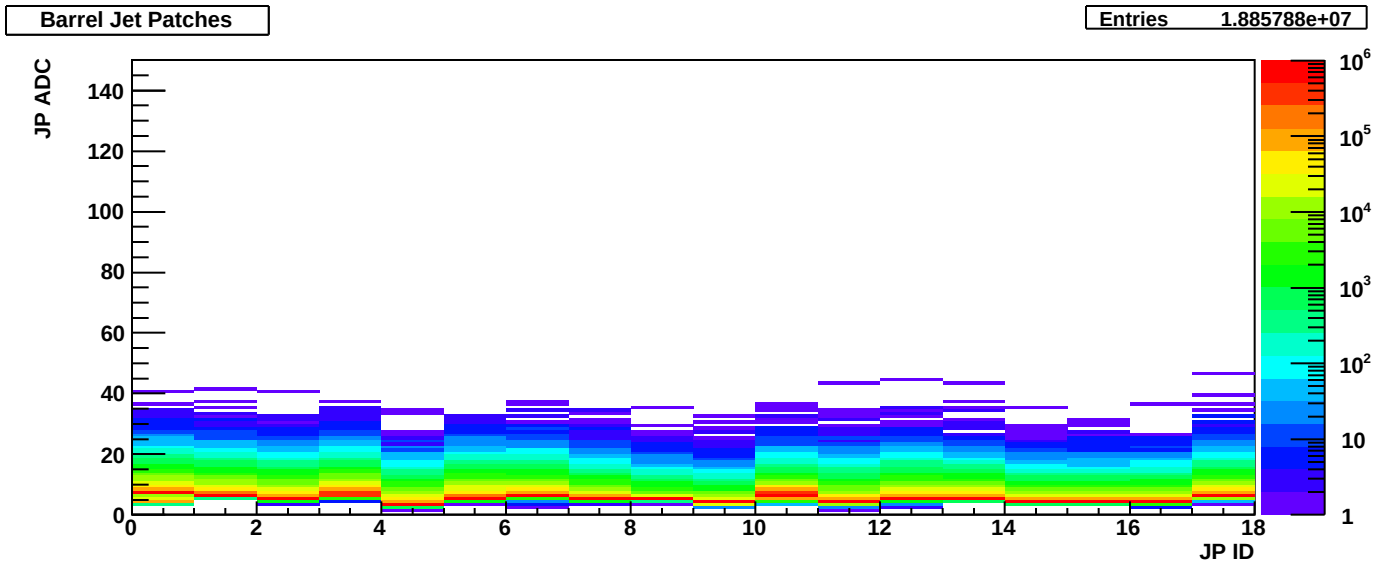
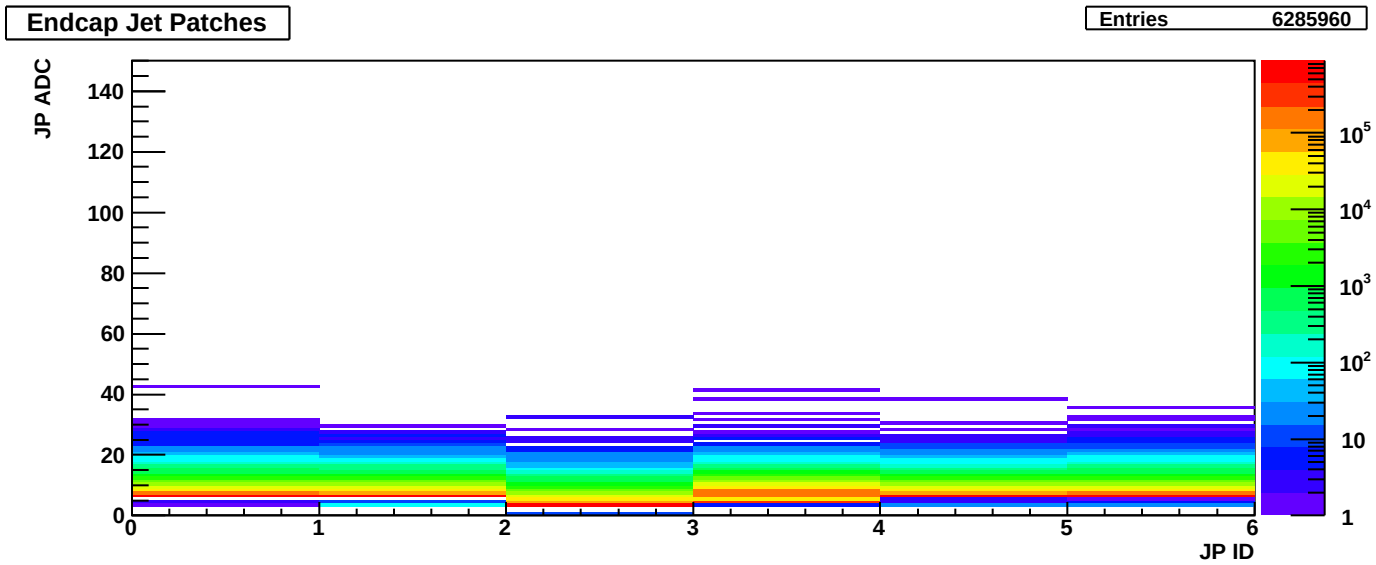

Barrel EMC L0 Input - High Tower
Entries 3.14298e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

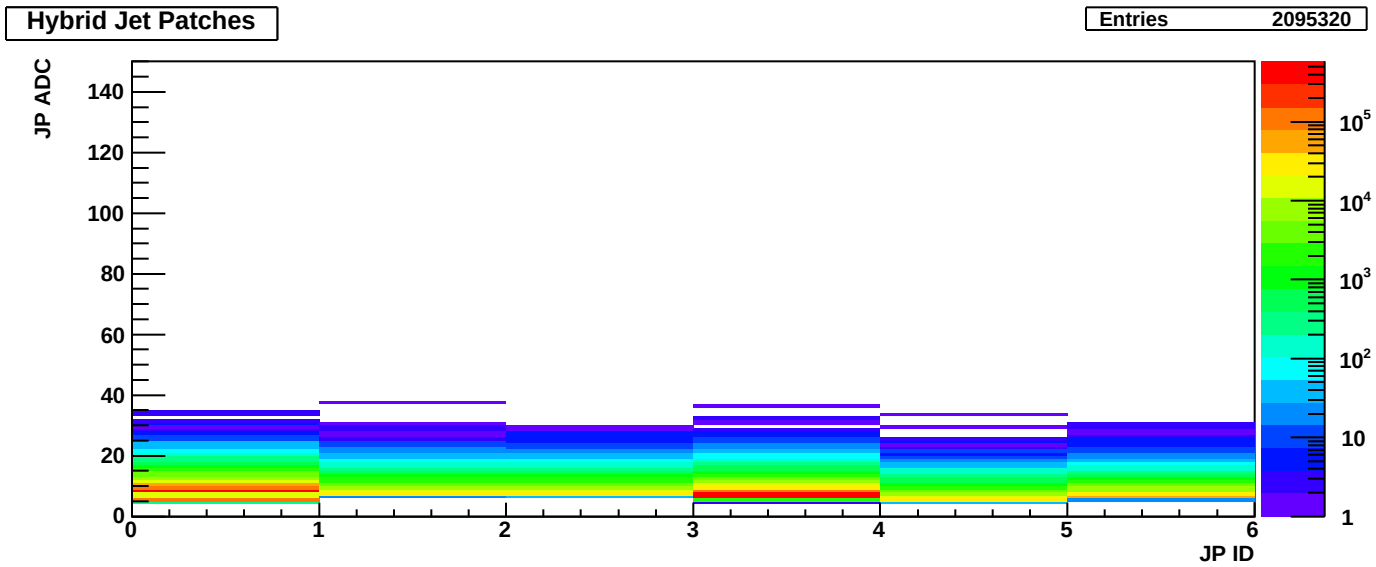

Barrel EMC L0 Input - Patch Sum
Entries 3.14298e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

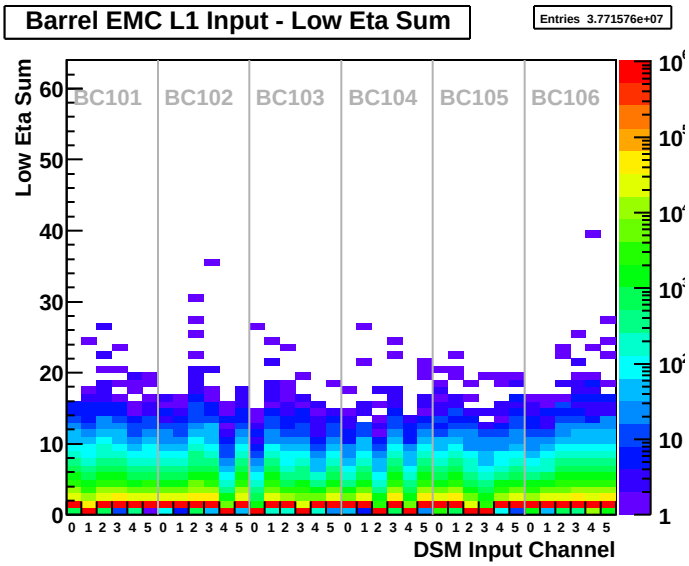

Endcap EMC L0 Input - High Tower
Entries 9.42894e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

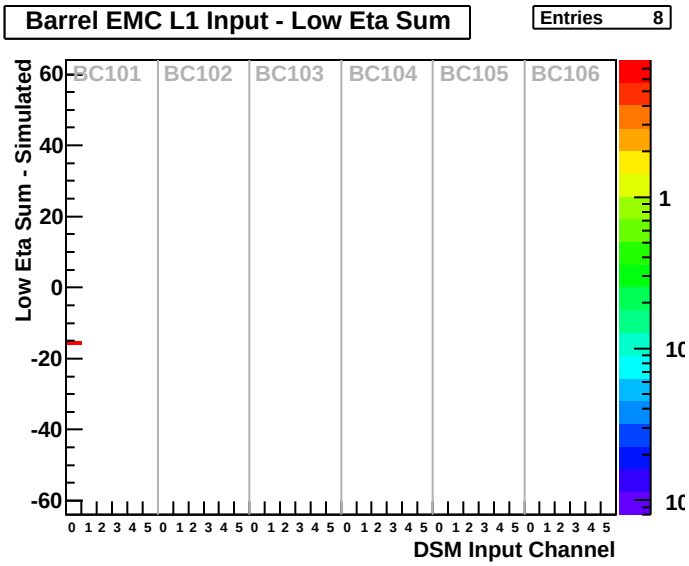

Endcap EMC L0 Input - Patch Sum
Entries 9.42894e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

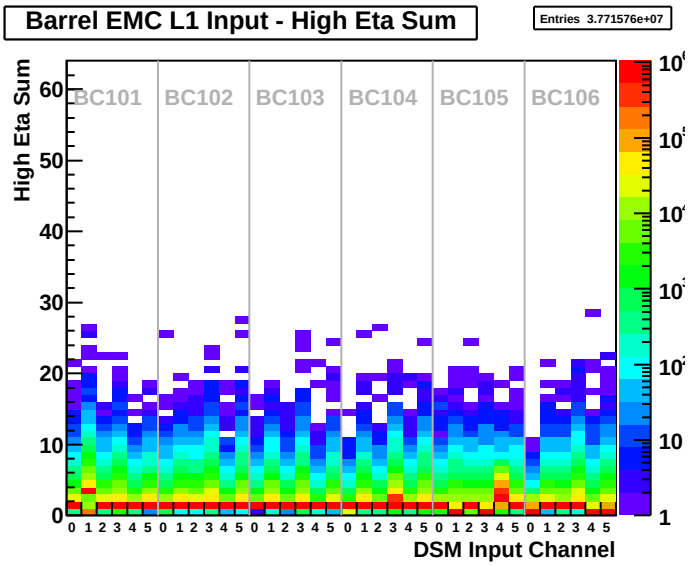

Barrel Jet Patches
Entries 1.885788e+07
JP ADC
JP ID

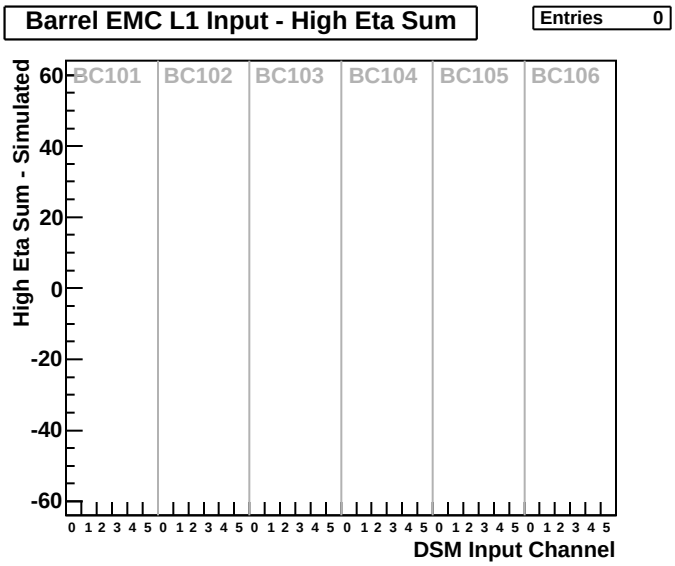

Endcap Jet Patches
Entries 6285960
JP ADC
JP ID

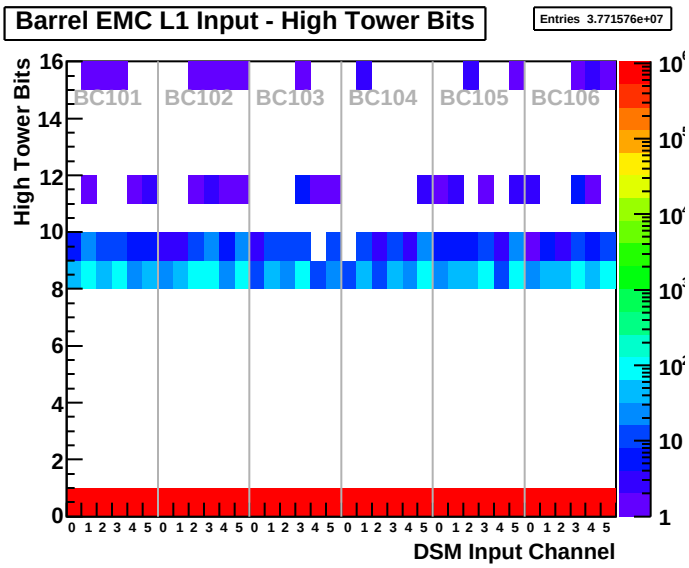

Hybrid Jet Patches
Entries 2095320
JP ADC
JP ID

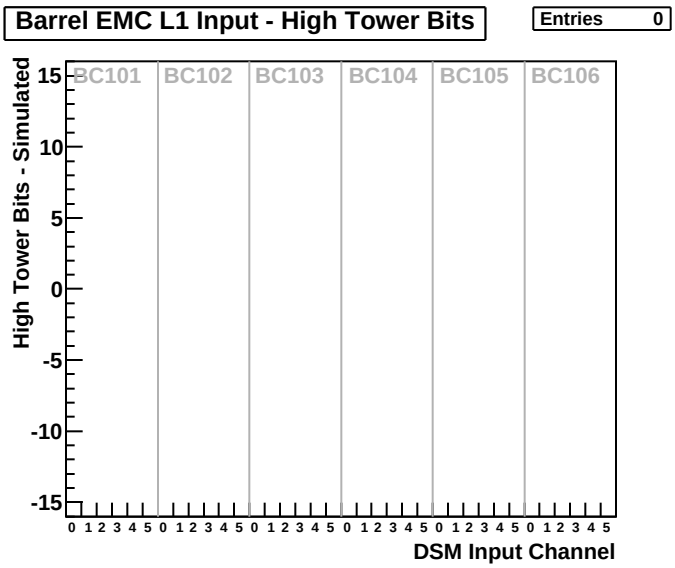

Barrel EMC L1 Input - Low Eta Sum
Entries 3.771576e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

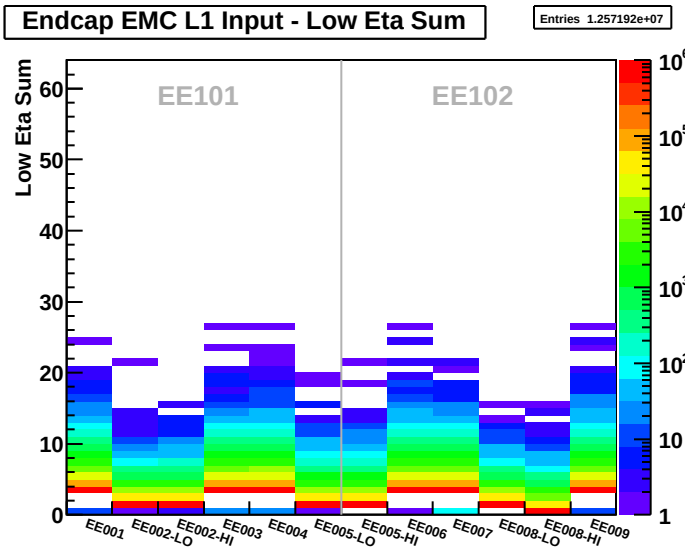

Barrel EMC L1 Input - Low Eta Sum
Entries 8
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

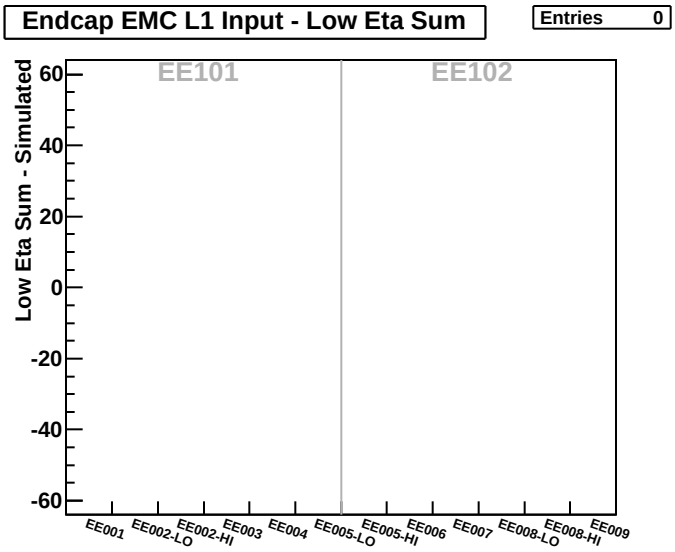

Barrel EMC L1 Input - High Eta Sum
Entries 3.771576e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

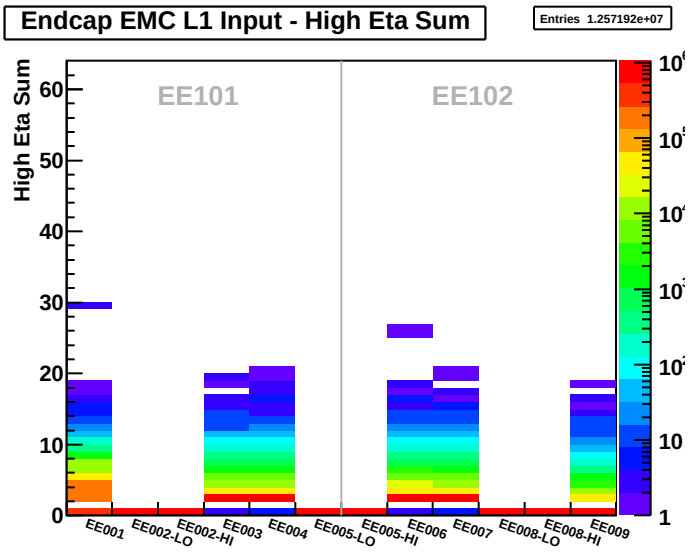

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

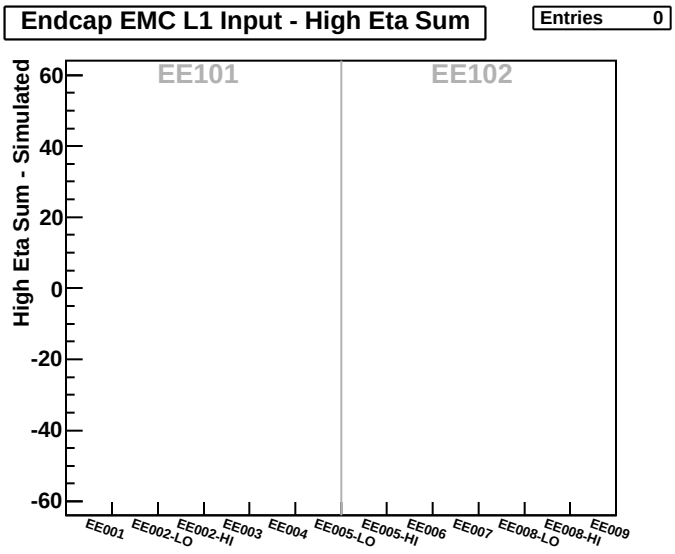

Barrel EMC L1 Input - High Tower Bits
Entries 3.771576e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

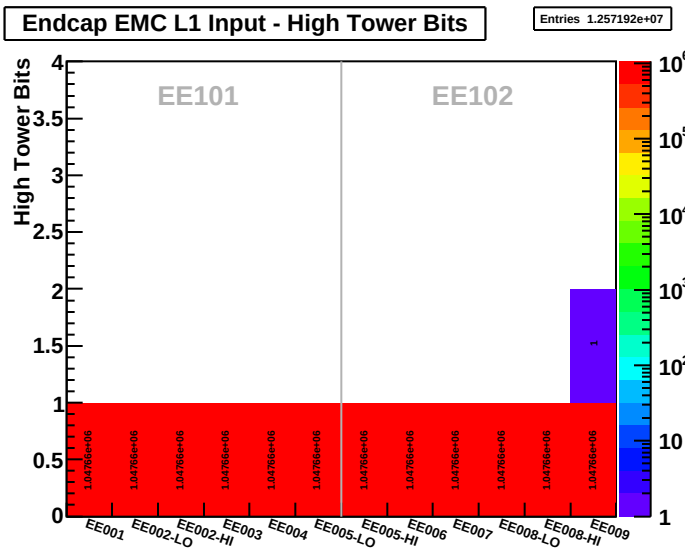

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

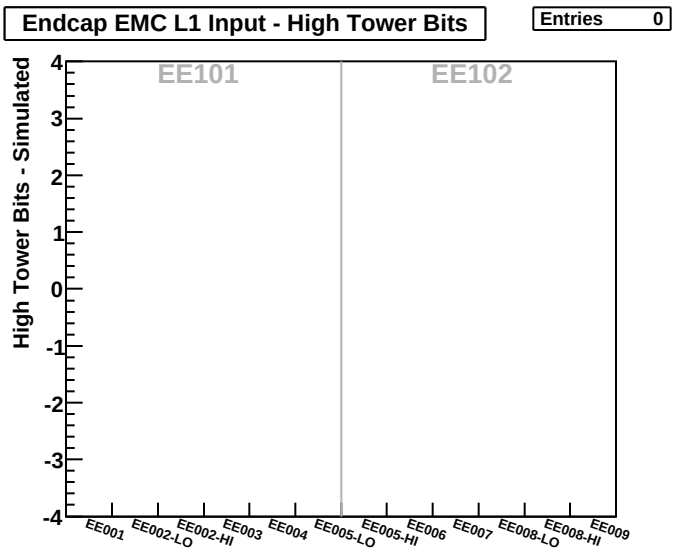

Endcap EMC L1 Input - Low Eta Sum
Entries 1.257192e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

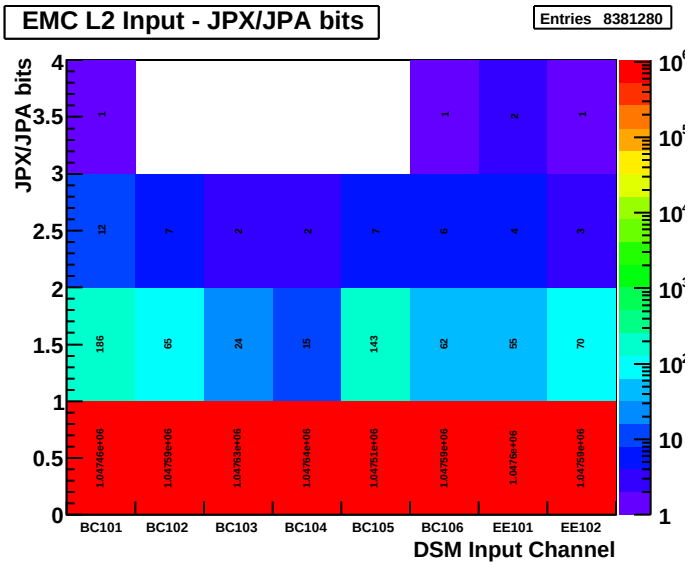

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

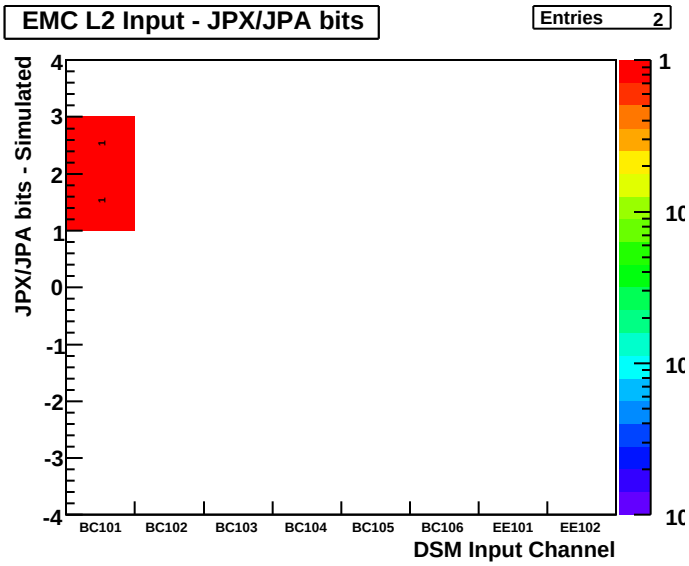

Endcap EMC L1 Input - High Eta Sum
Entries 1.257192e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

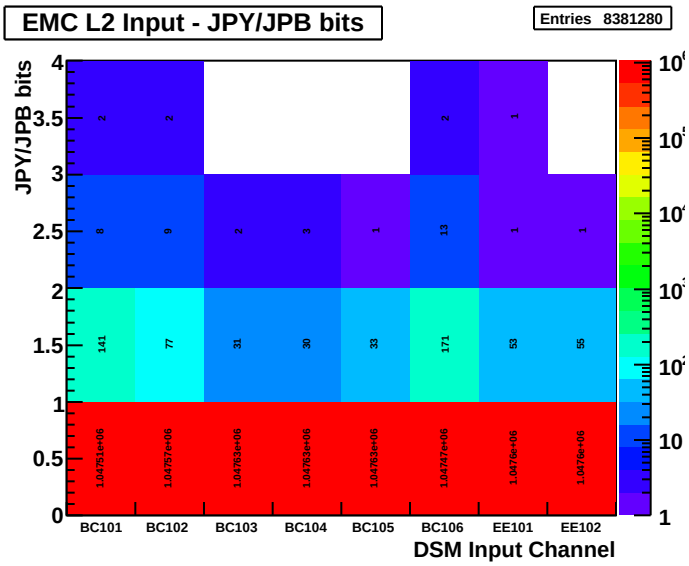

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

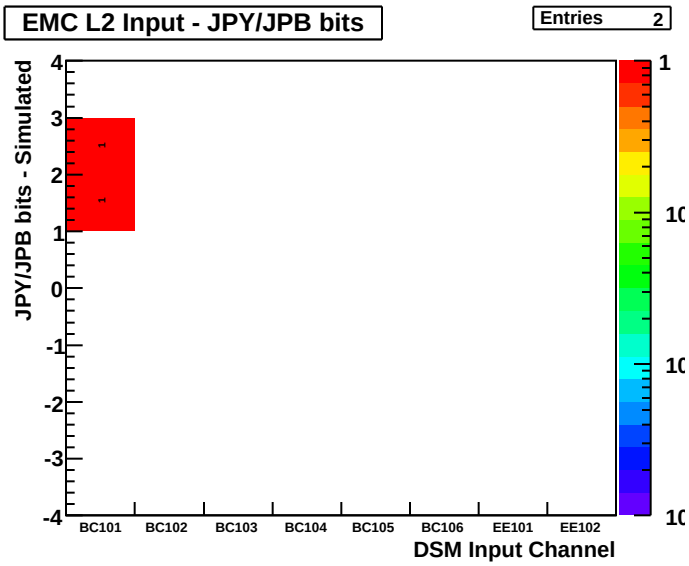

Endcap EMC L1 Input - High Tower Bits
Entries 1.257192e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

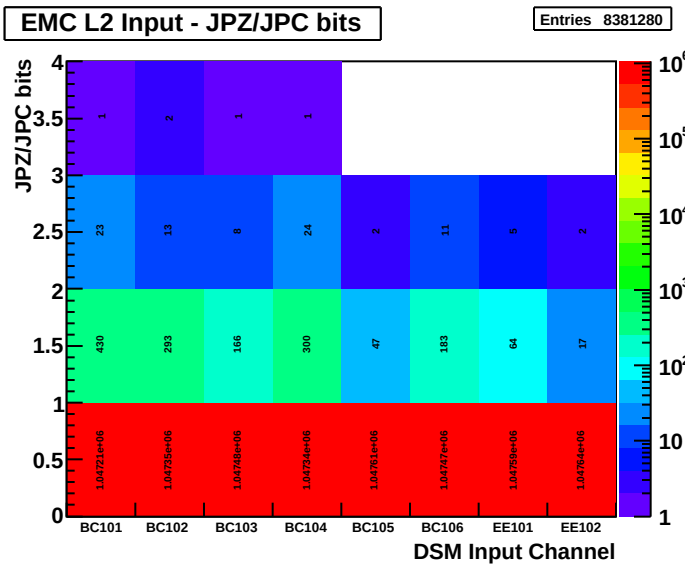

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

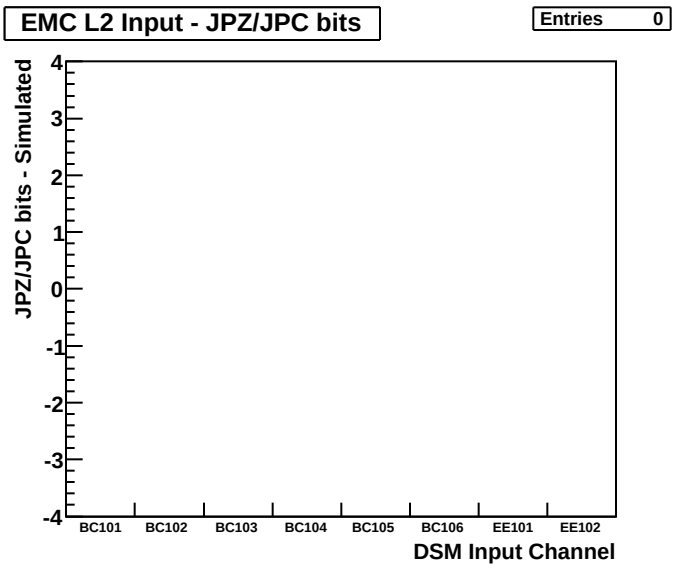

EMC L2 Input - JPX/JPA bits
Entries 8381280
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

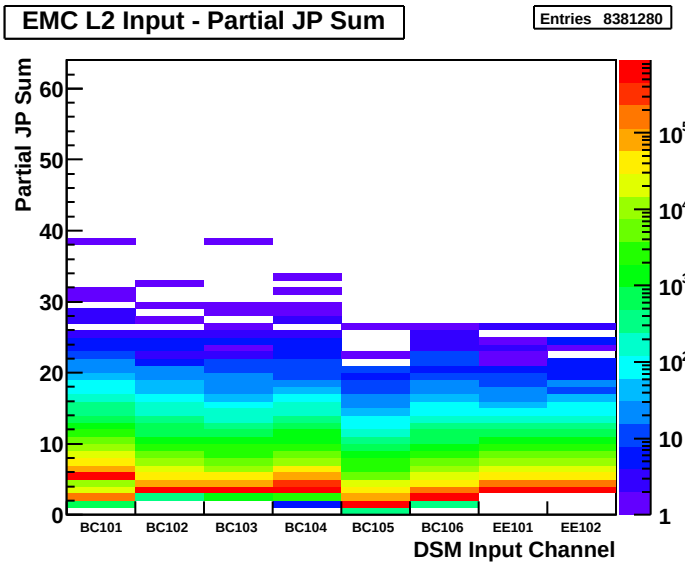

EMC L2 Input - JPX/JPA bits
Entries 2
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

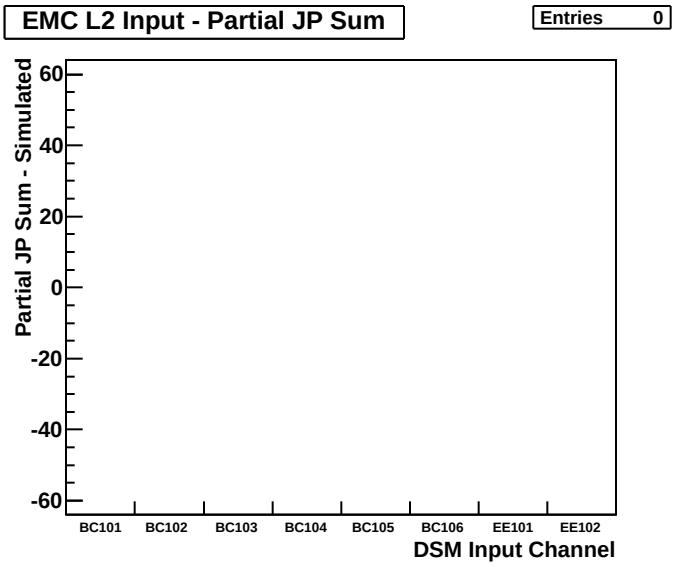

EMC L2 Input - JPY/JPB bits
Entries 8381280
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

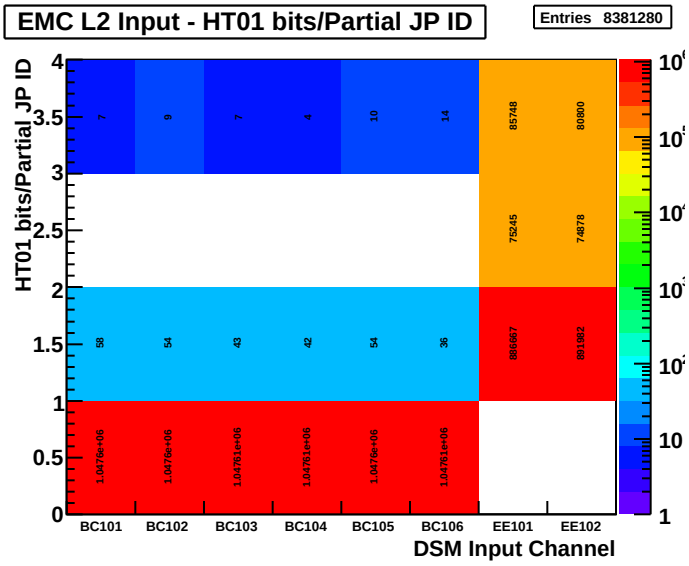

EMC L2 Input - JPY/JPB bits
Entries 2
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

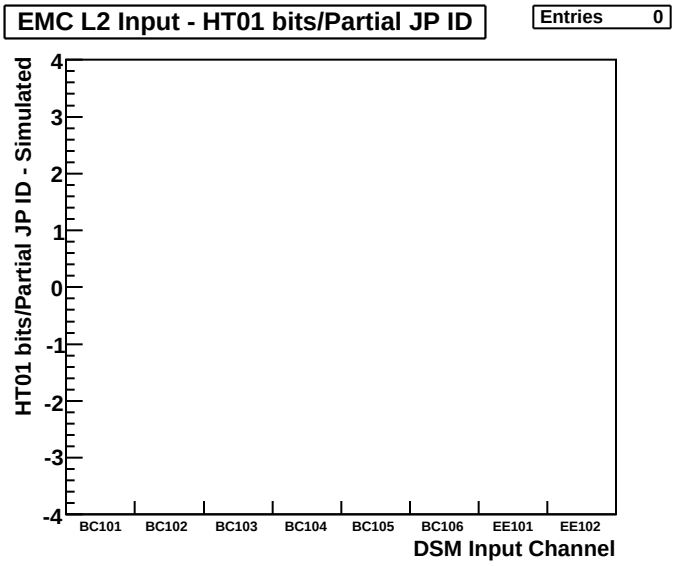

EMC L2 Input - JPZ/JPC bits
Entries 8381280
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

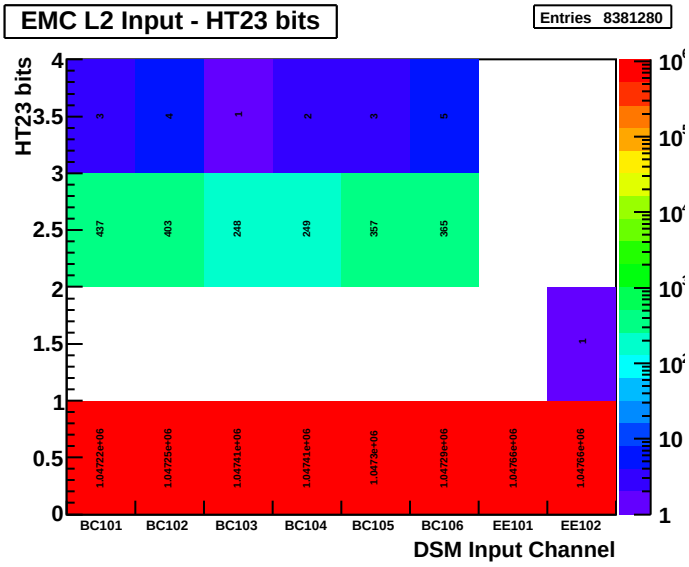

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

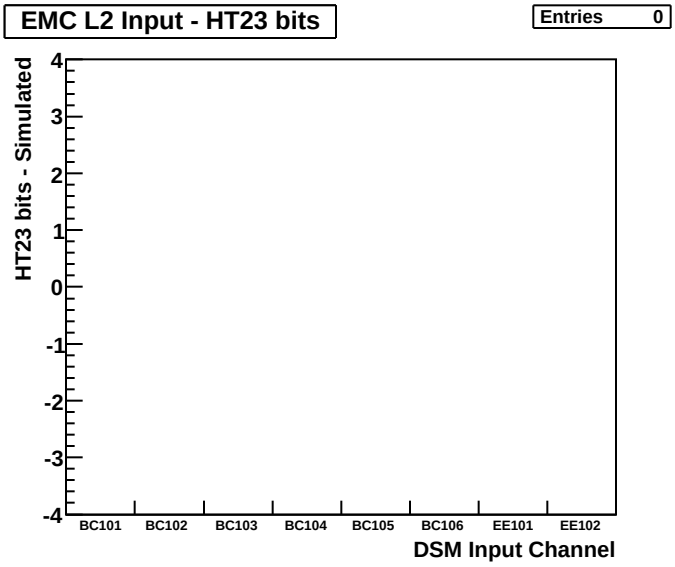

EMC L2 Input - Partial JP Sum
Entries 8381280
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

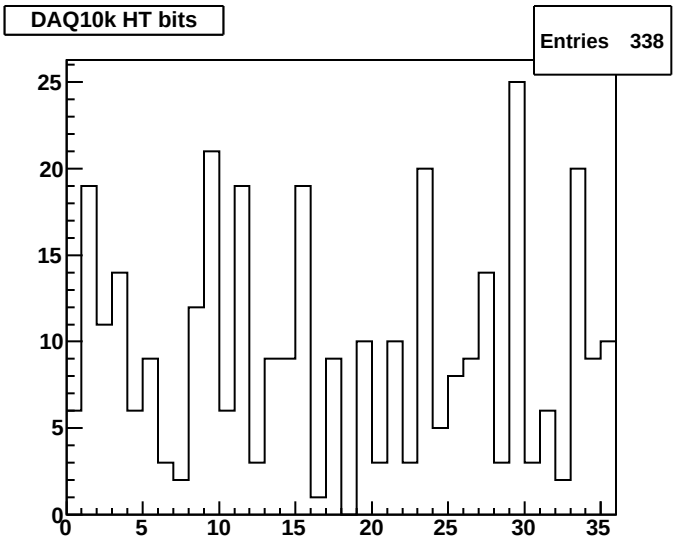

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

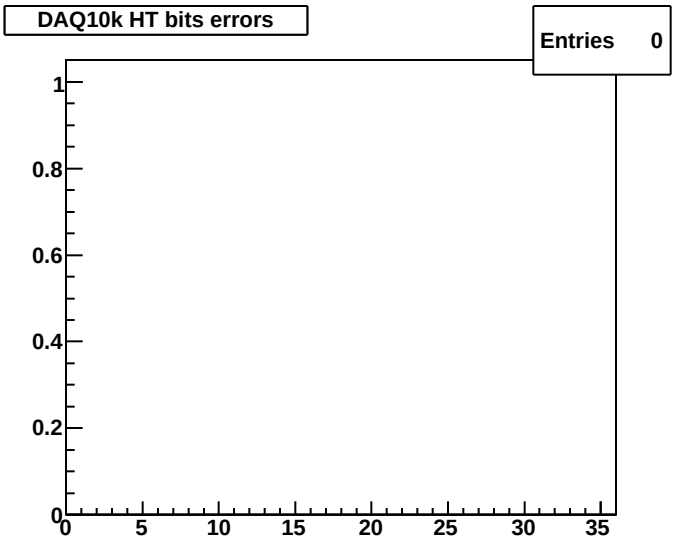

EMC L2 Input - HT01 bits/Partial JP ID
Entries 8381280
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

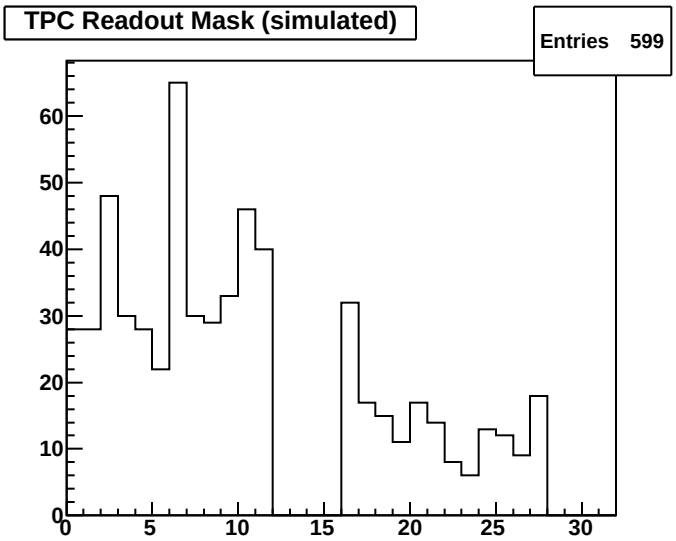

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

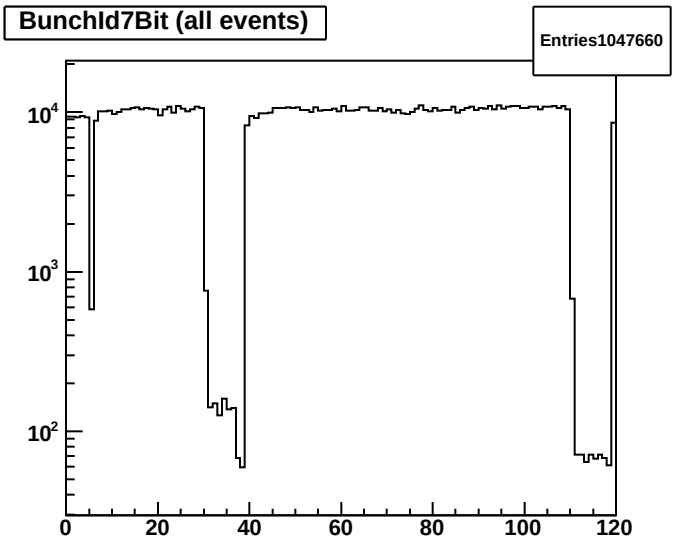

EMC L2 Input - HT23 bits
Entries 8381280
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

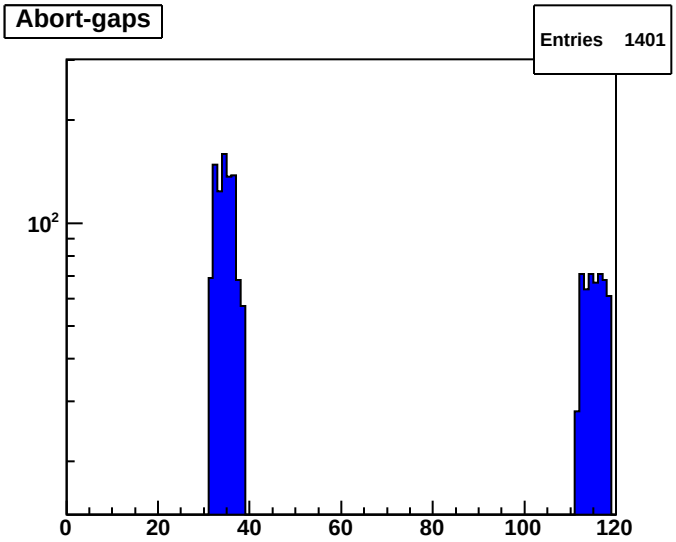

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

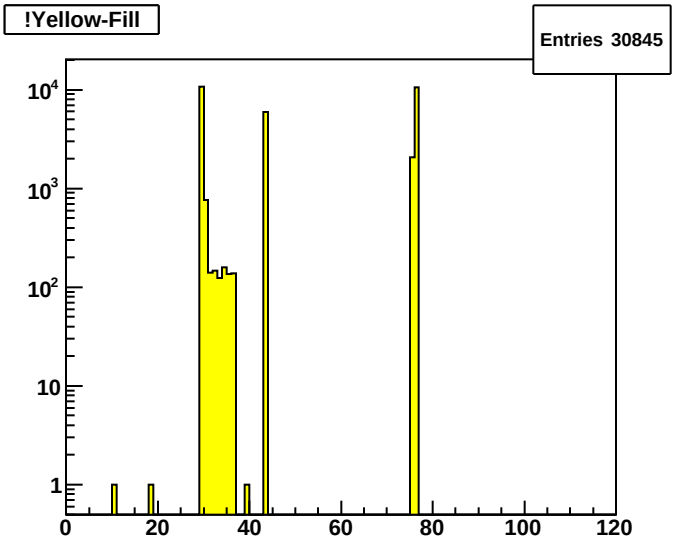

DAQ10k HT bits
Entries 338

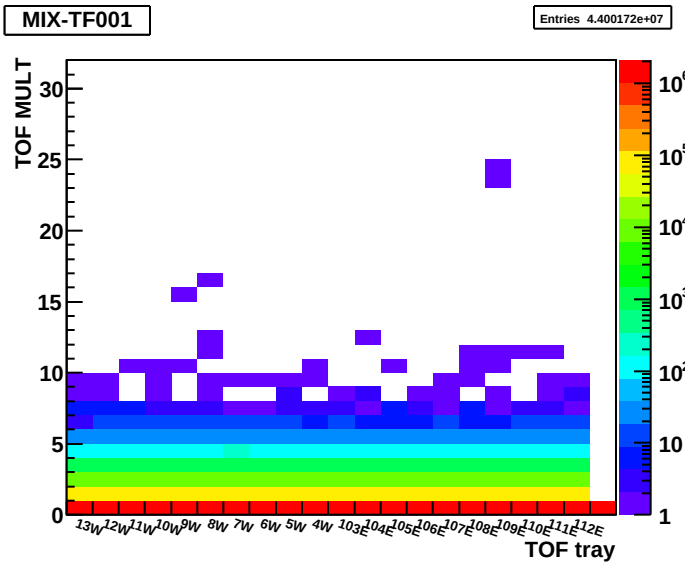

DAQ10k HT bits errors
Entries 0

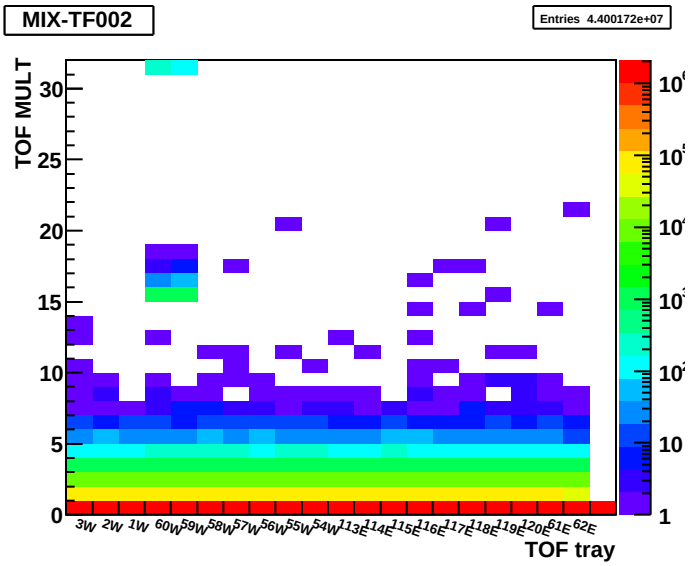

TPC Readout Mask (simulated)
Entries 599

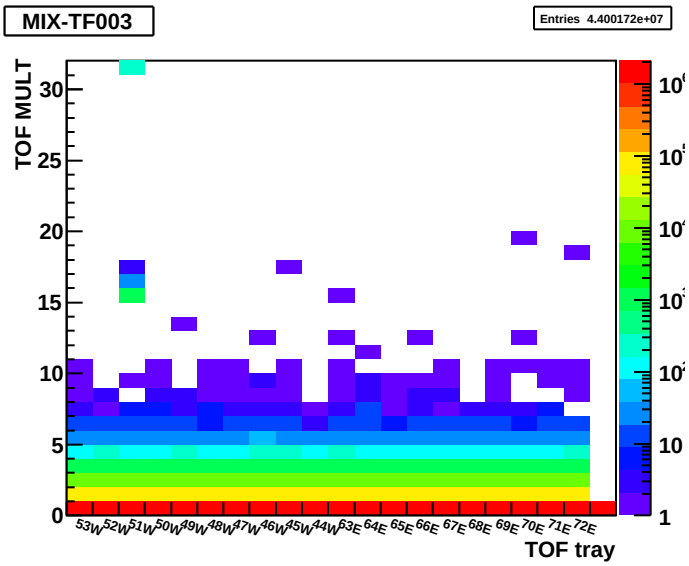

BunchId7Bit (all events)
Entries1047660

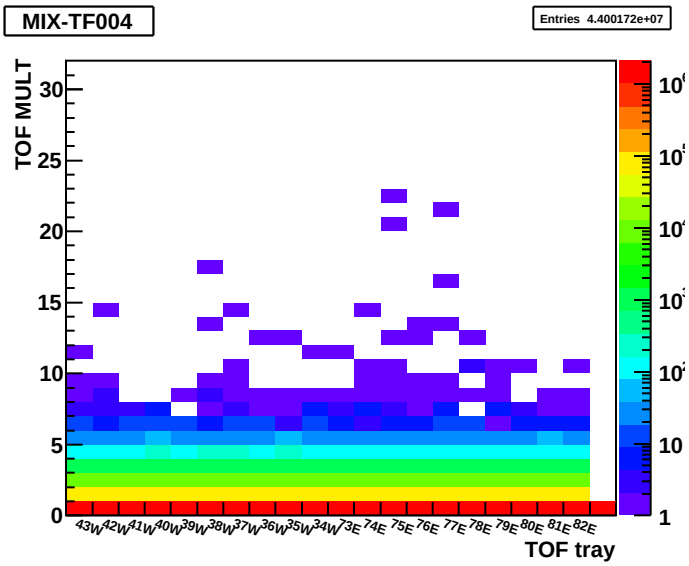

Abort-gaps
Entries 1401

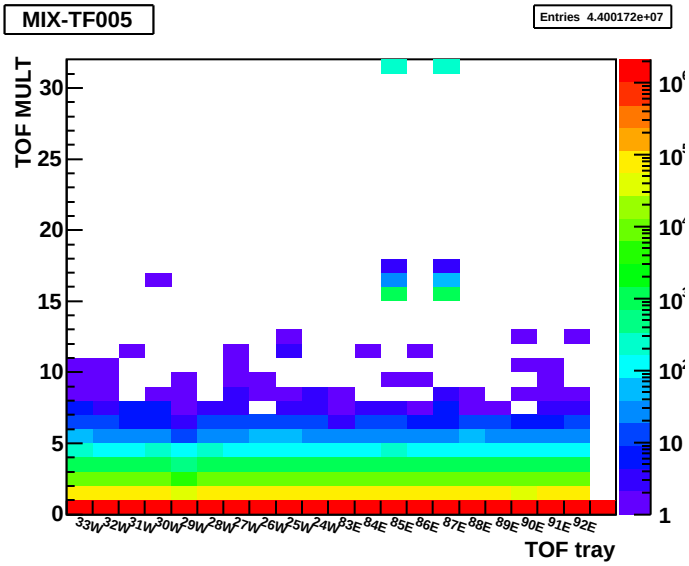

!Yellow-Fill
Entries 30845

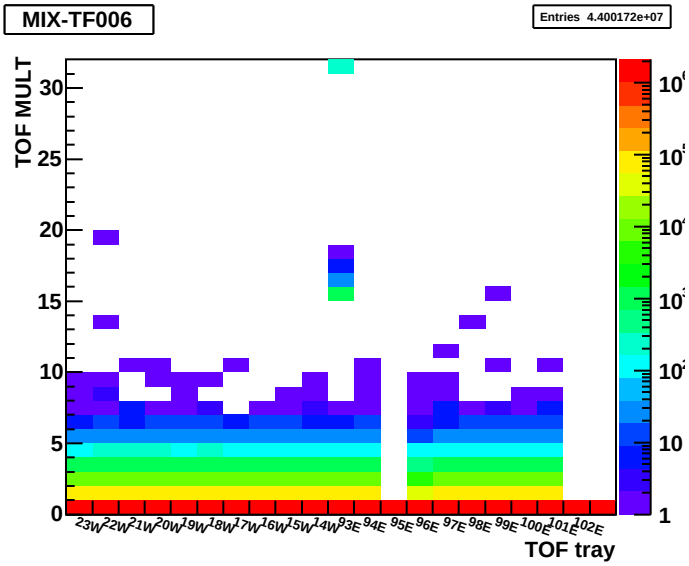

MIX-TF001
Entries 4.400172e+07
TOF MULT
TOF tray

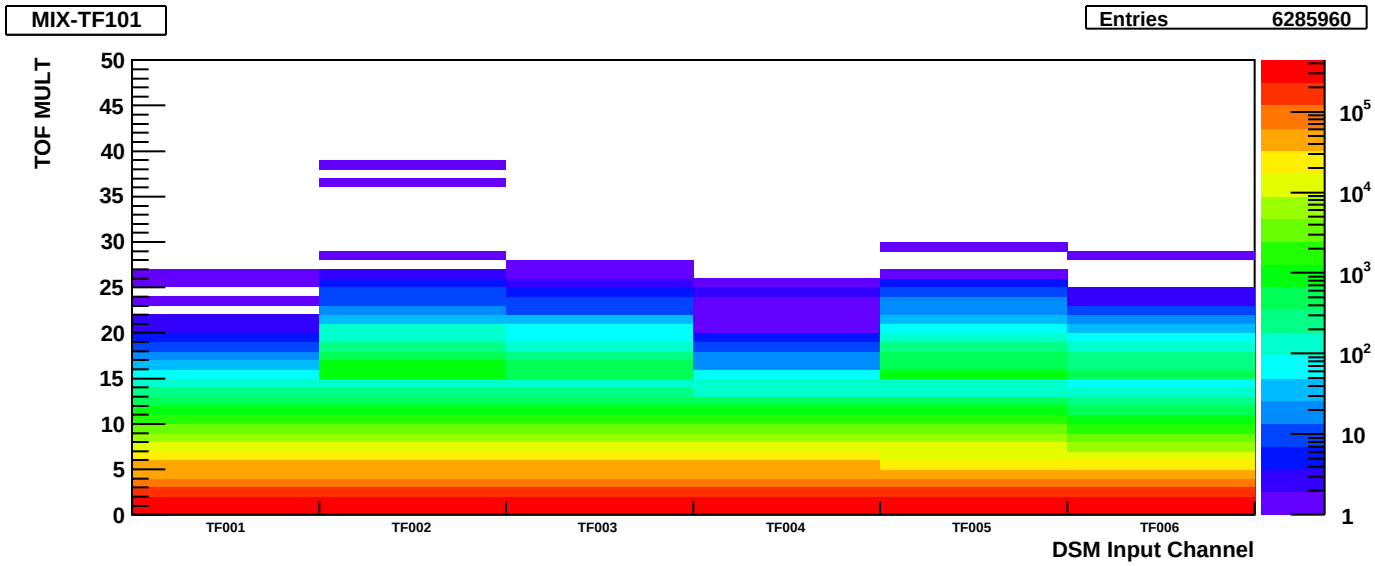

MIX-TF002
Entries 4.400172e+07
TOF MULT
TOF tray

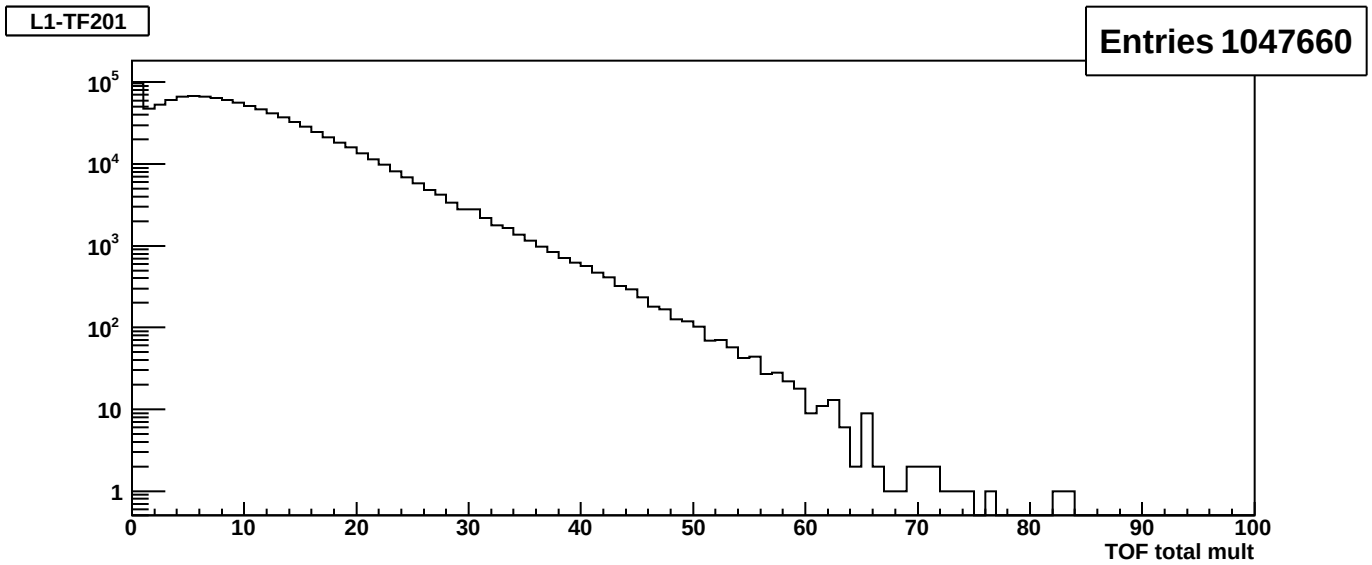

MIX-TF003
Entries 4.400172e+07
TOF MULT
TOF tray

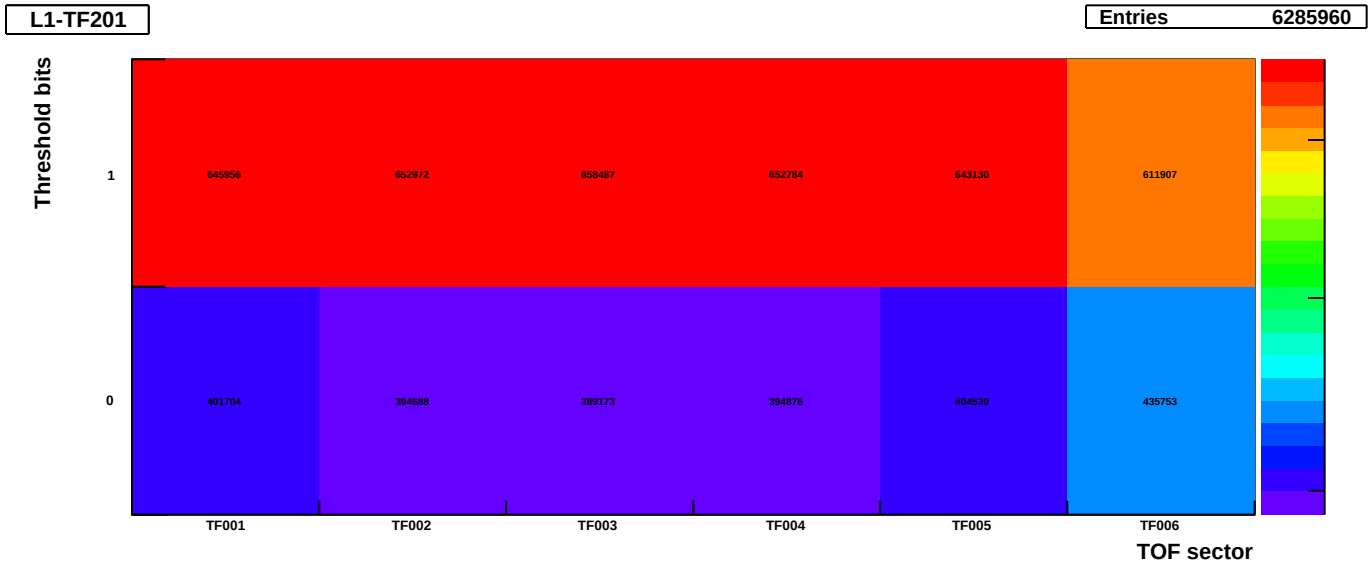

MIX-TF004
Entries 4.400172e+07
TOF MULT
TOF tray

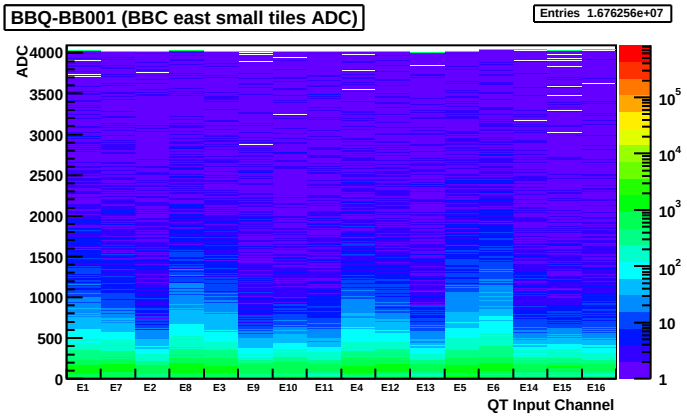

MIX-TF005
Entries 4.400172e+07
TOF MULT
TOF tray

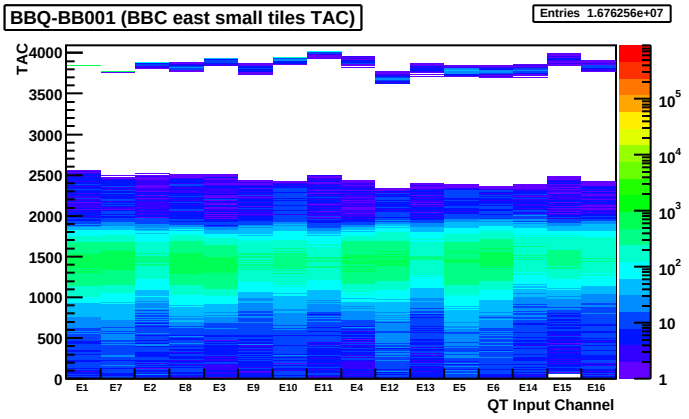

MIX-TF006
Entries 4.400172e+07
TOF MULT
TOF tray

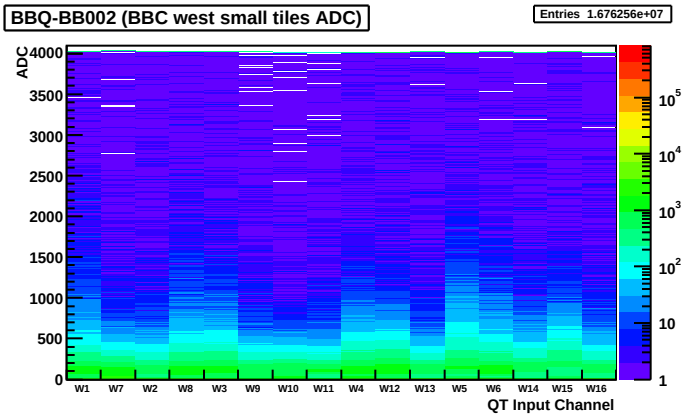

MIX-TF101
Entries 6285960
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

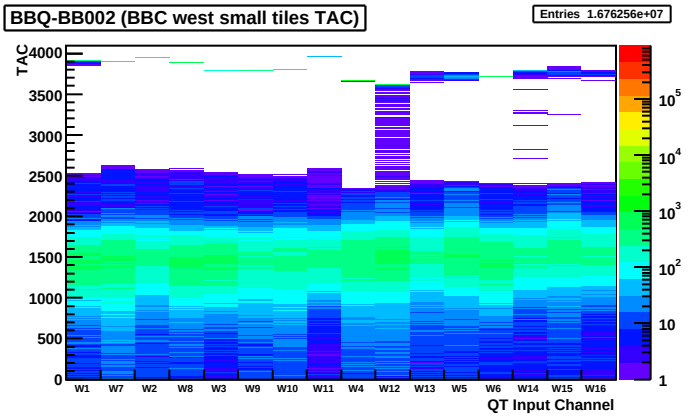

L1-TF201
Entries 1047660
TOF total mult

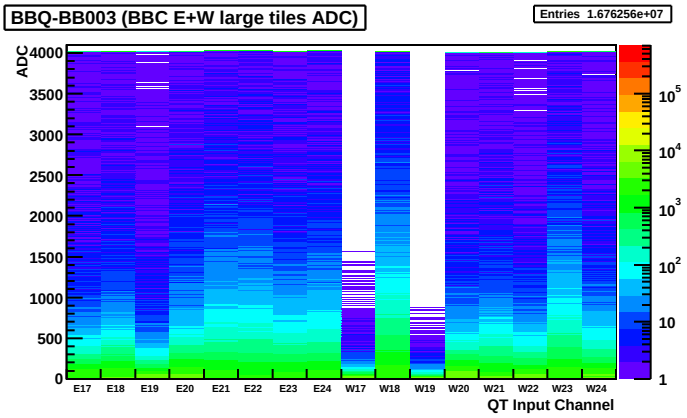

L1-TF201
Entries 6285960
Threshold bits
645956
652972
658487
652784
643130
611907
401704
394688
389173
394876
404530
435753
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

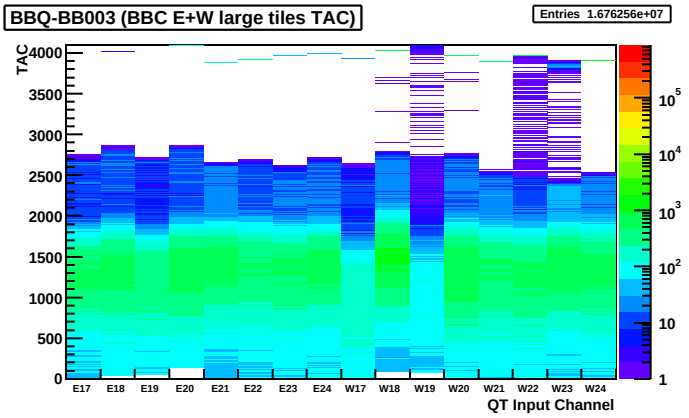

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.676256e+07
ADC
QT Input Channel

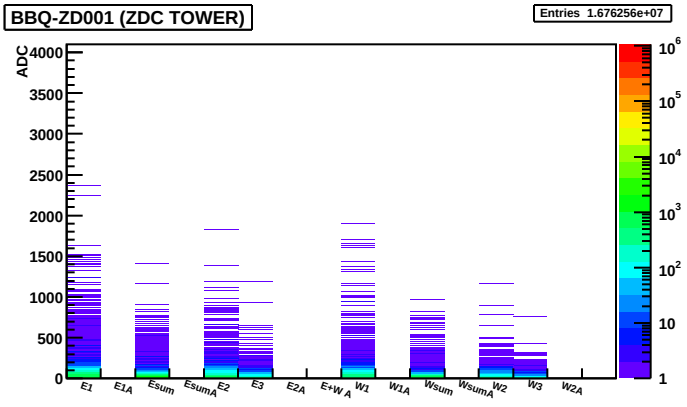

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.676256e+07
TAC
QT Input Channel

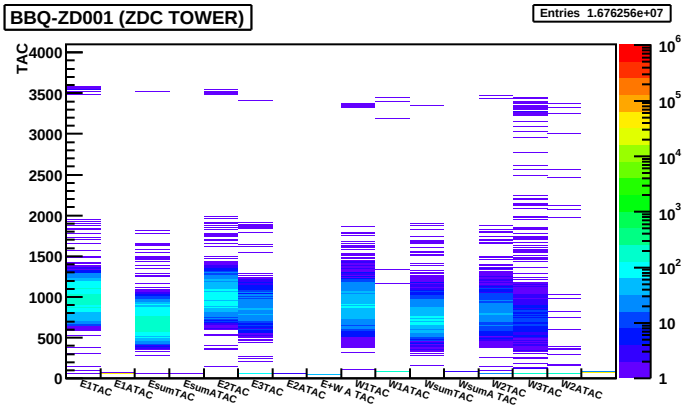

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.676256e+07
ADC
QT Input Channel

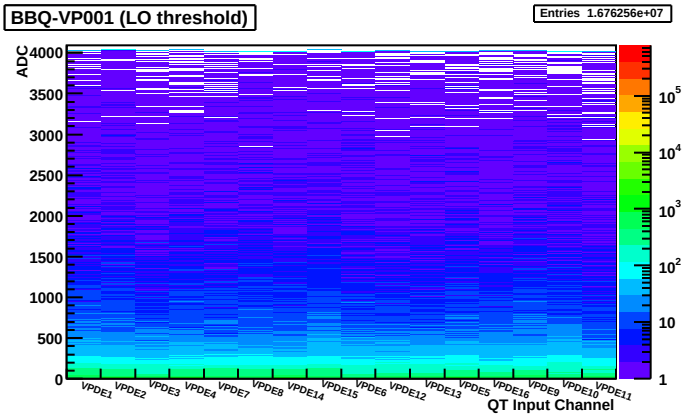

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.676256e+07
TAC
QT Input Channel

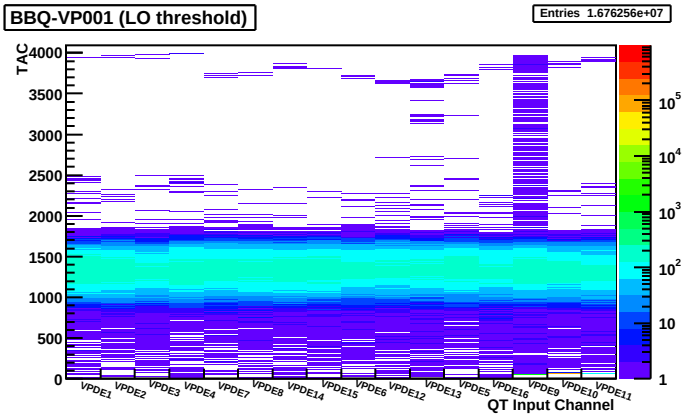

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.676256e+07
ADC
QT Input Channel

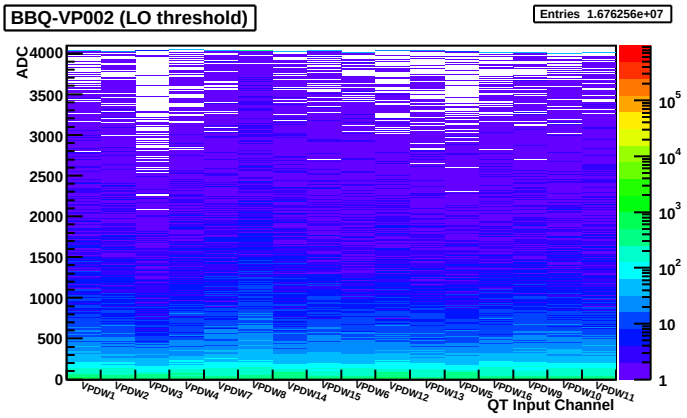

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.676256e+07
TAC
QT Input Channel

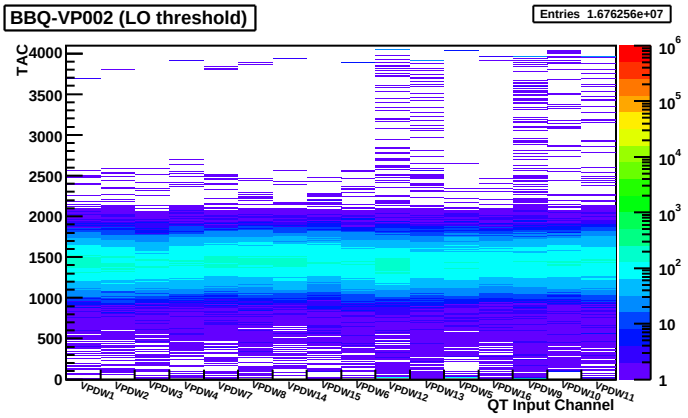

BBQ-ZD001 (ZDC TOWER)
Entries 1.676256e+07
ADC

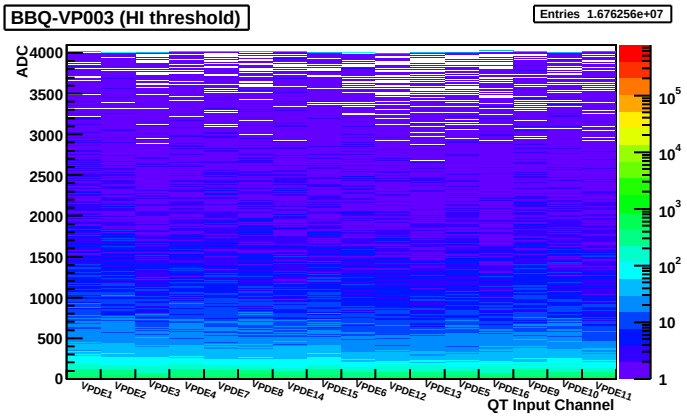

BBQ-ZD001 (ZDC TOWER)
Entries 1.676256e+07
TAC

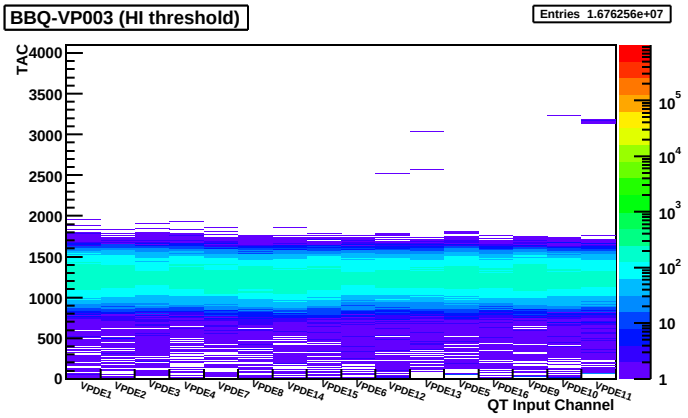

BBQ-VP001 (LO threshold)
Entries 1.676256e+07
ADC
QT Input Channel

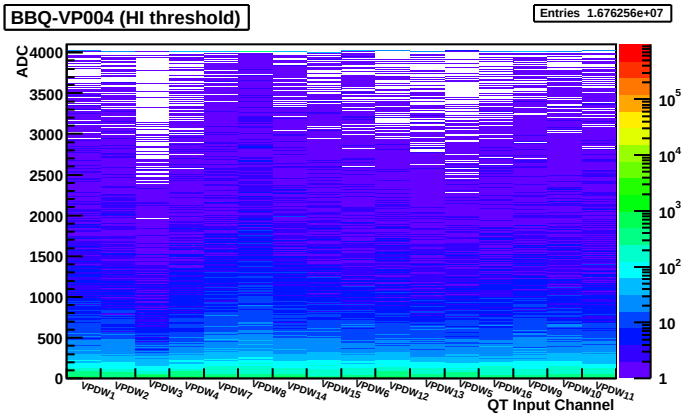

BBQ-VP001 (LO threshold)
Entries 1.676256e+07
TAC
QT Input Channel

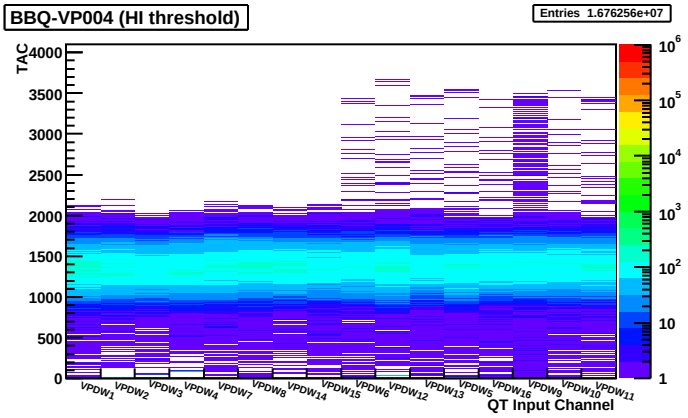

BBQ-VP002 (LO threshold)
Entries 1.676256e+07
ADC
QT Input Channel

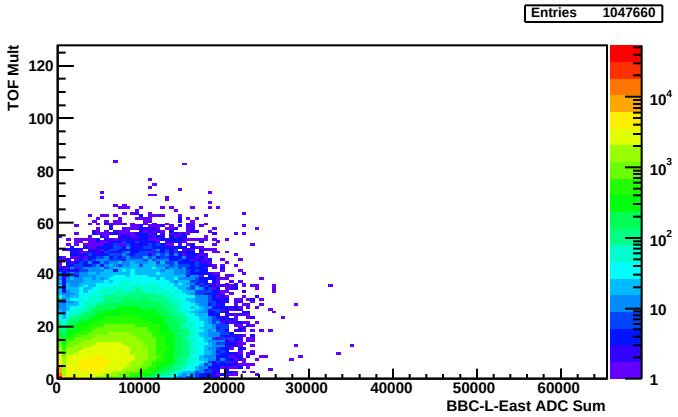

BBQ-VP002 (LO threshold)
Entries 1.676256e+07
TAC
QT Input Channel

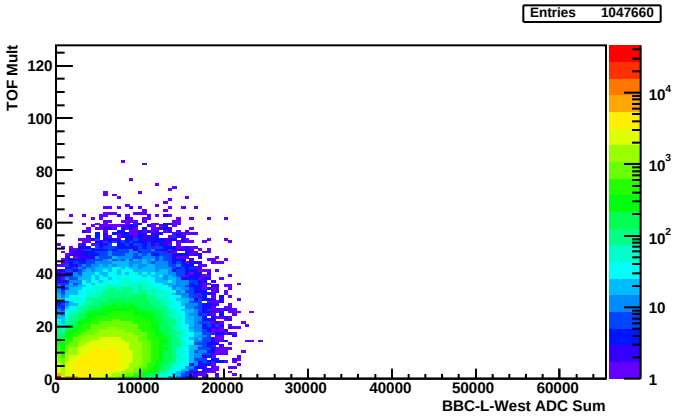

BBQ-VP003 (HI threshold)
Entries 1.676256e+07
ADC
QT Input Channel

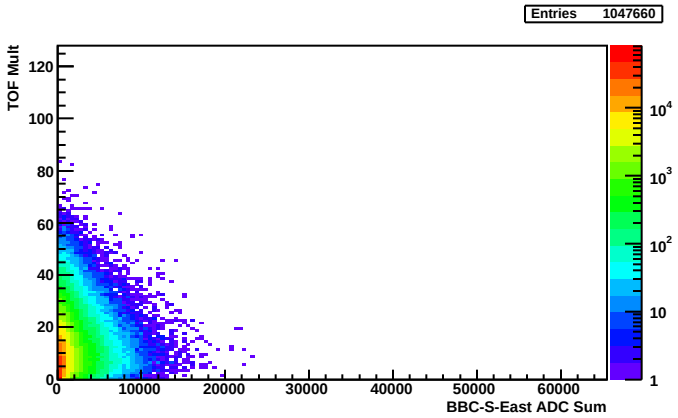

BBQ-VP003 (HI threshold)
Entries 1.676256e+07
TAC
QT Input Channel

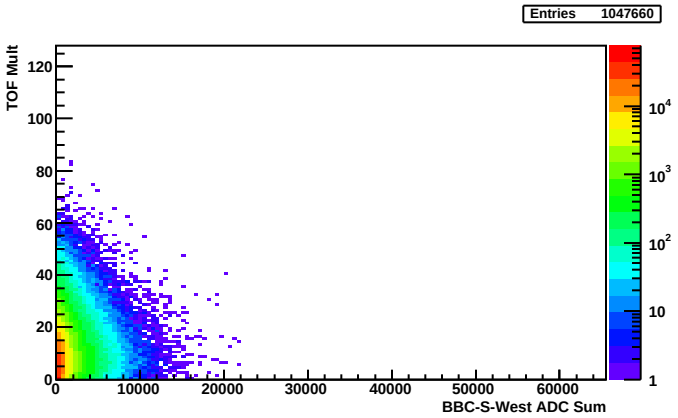

BBQ-VP004 (HI threshold)
Entries 1.676256e+07
ADC
QT Input Channel

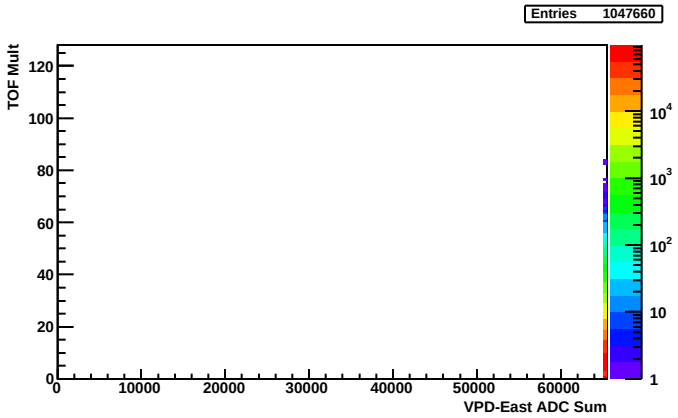

BBQ-VP004 (HI threshold)
Entries 1.676256e+07
TAC
QT Input Channel

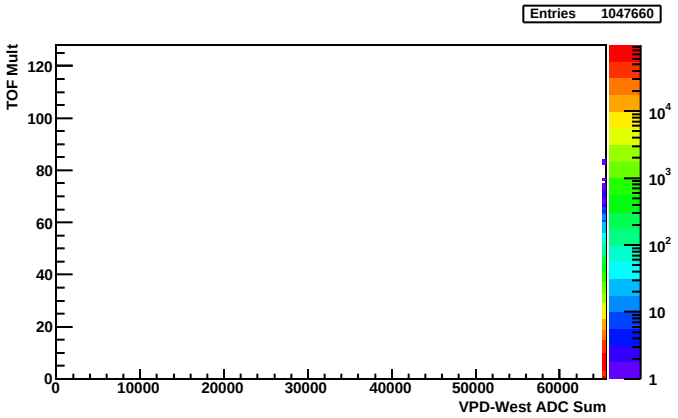

Entries 1047660
TOF Mult
BBC-L-East ADC Sum

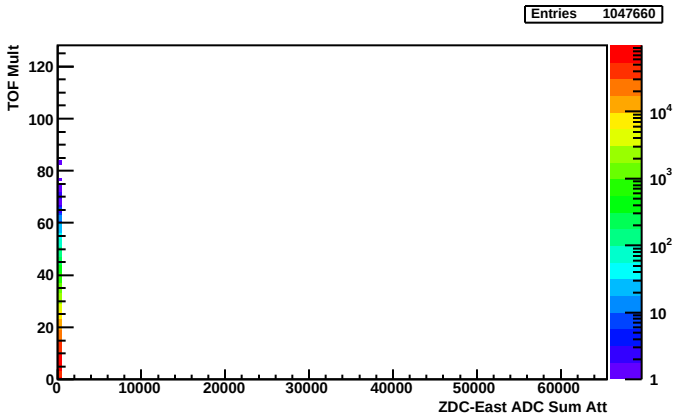

Entries 1047660
TOF Mult
BBC-L-West ADC Sum

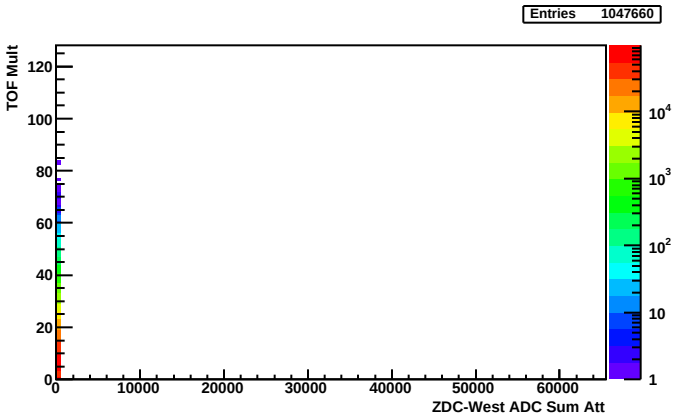

Entries 1047660
TOF Mult
BBC-S-East ADC Sum

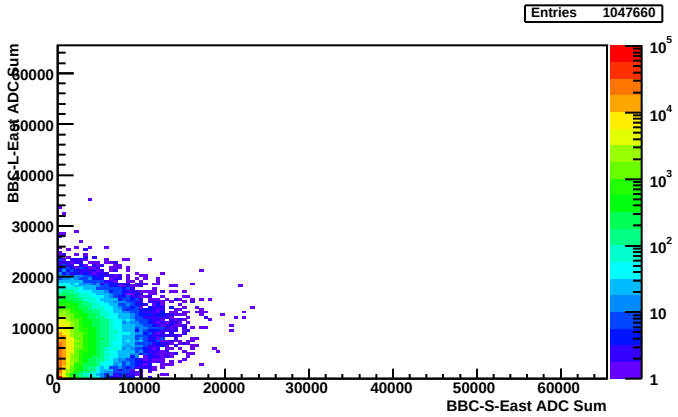

Entries 1047660
TOF Mult
BBC-S-West ADC Sum

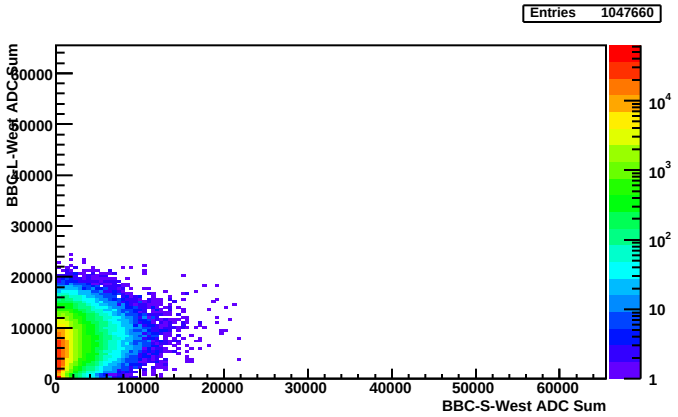

Entries 1047660
TOF Mult
VPD-East ADC Sum

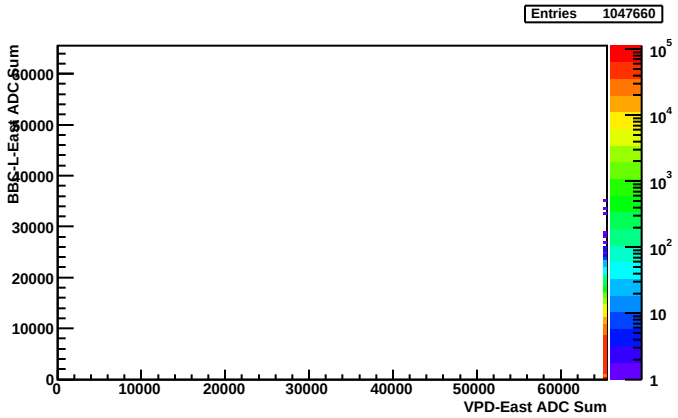

Entries 1047660
TOF Mult
VPD-West ADC Sum

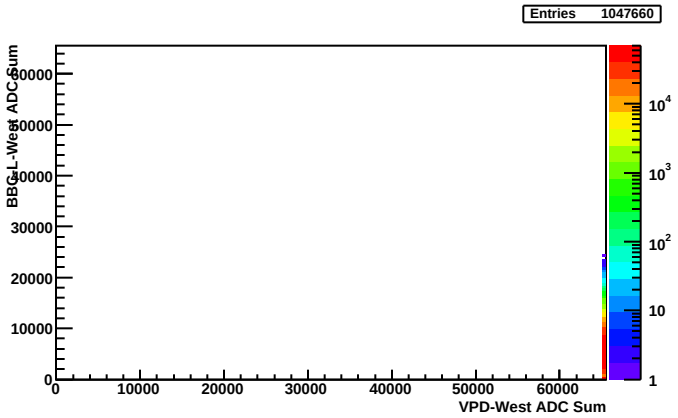

Entries 1047660
TOF Mult
ZDC-East ADC Sum Att

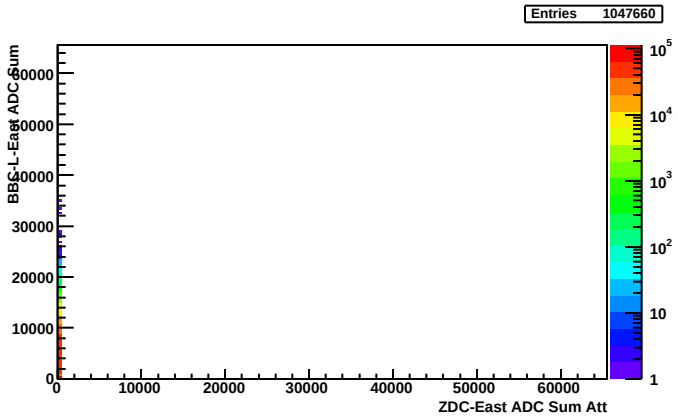

Entries 1047660
TOF Mult
ZDC-West ADC Sum Att


Entries 1047660
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1047660
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 1047660
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 1047660
BBC-L-West ADC Sum
VPD-West ADC Sum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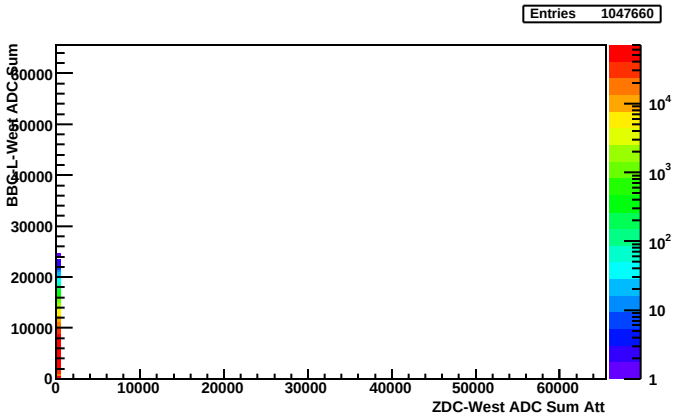
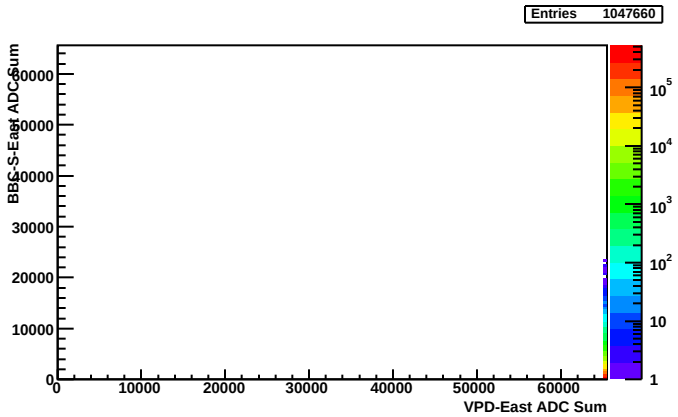
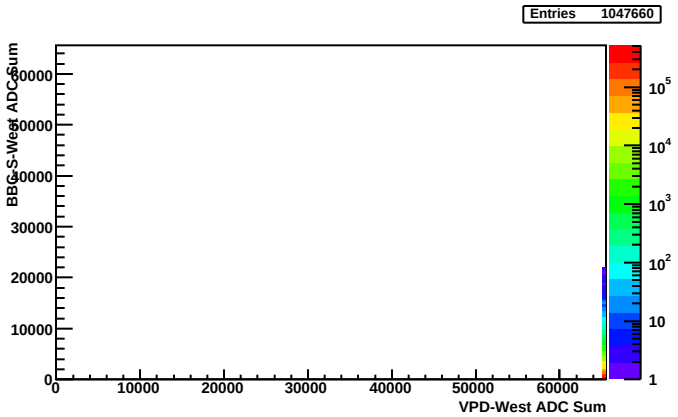
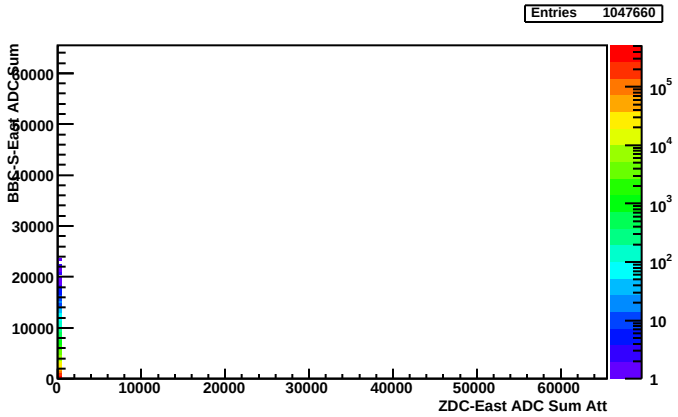
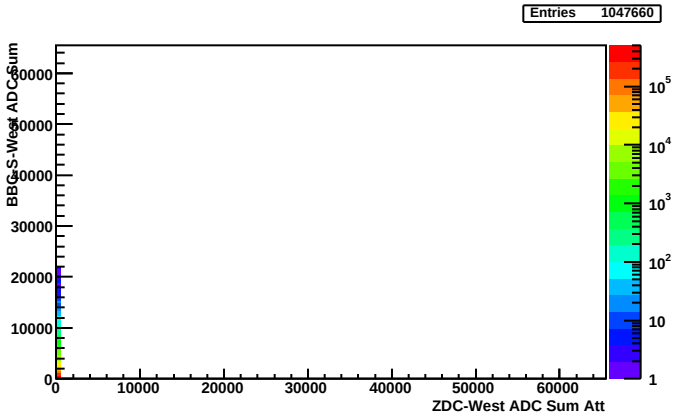
BBC-L-East ADC Sum
ZDC-East ADC Sum Att


Entries 1047660
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 1047660
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 1047660
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 1047660
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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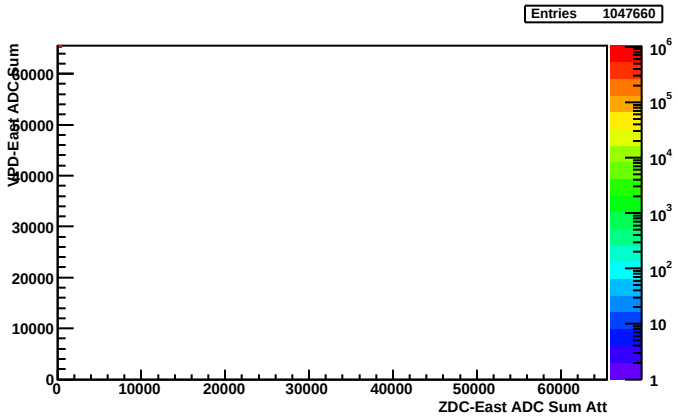
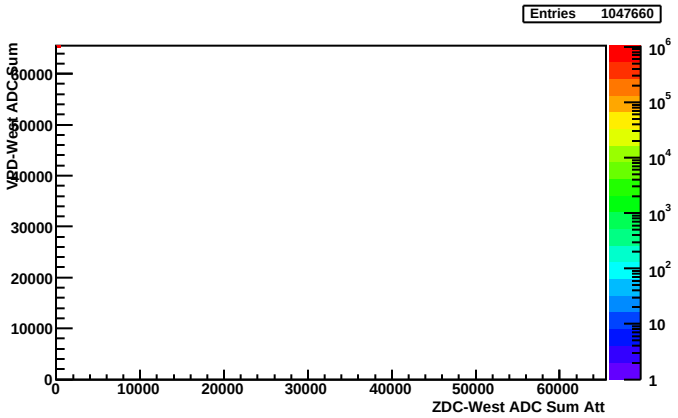
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1047660
VPD-East ADC Sum
ZDC-East ADC Sum Att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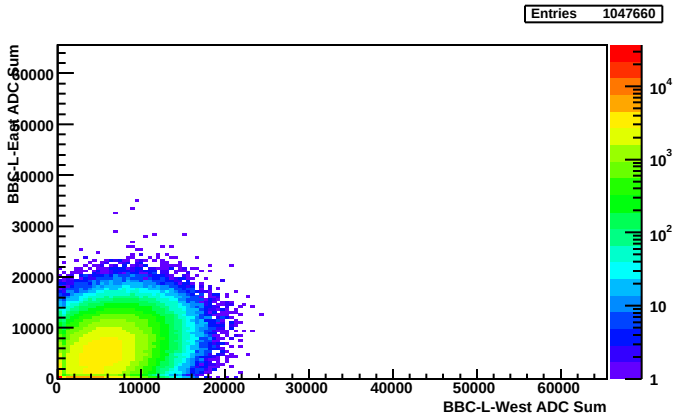
VPD-West ADC Sum
ZDC-West ADC Sum Att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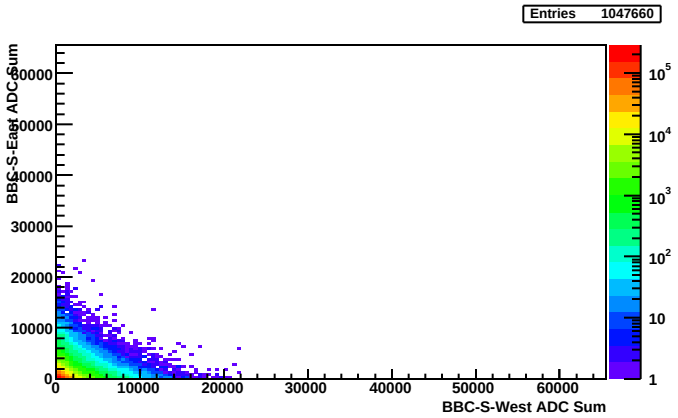
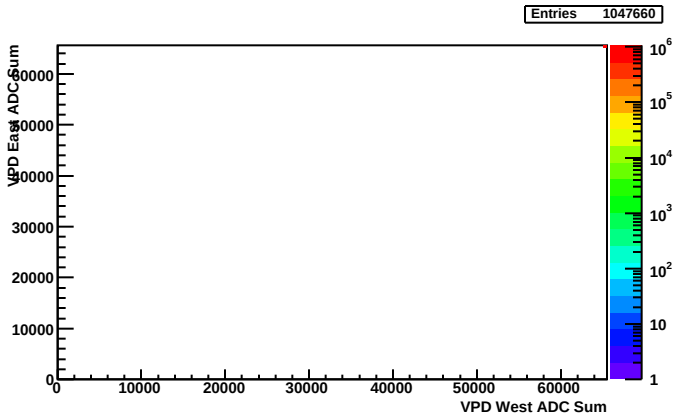
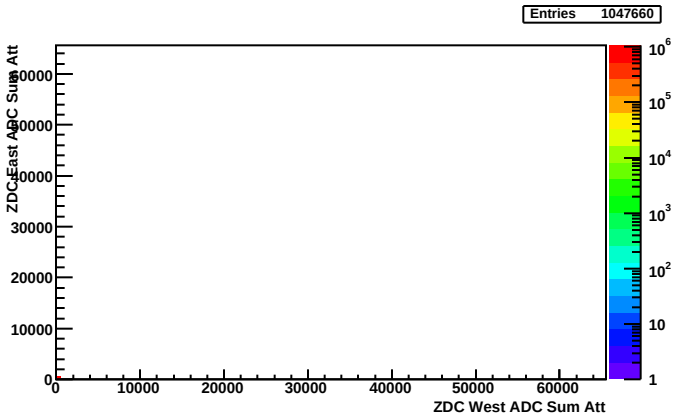
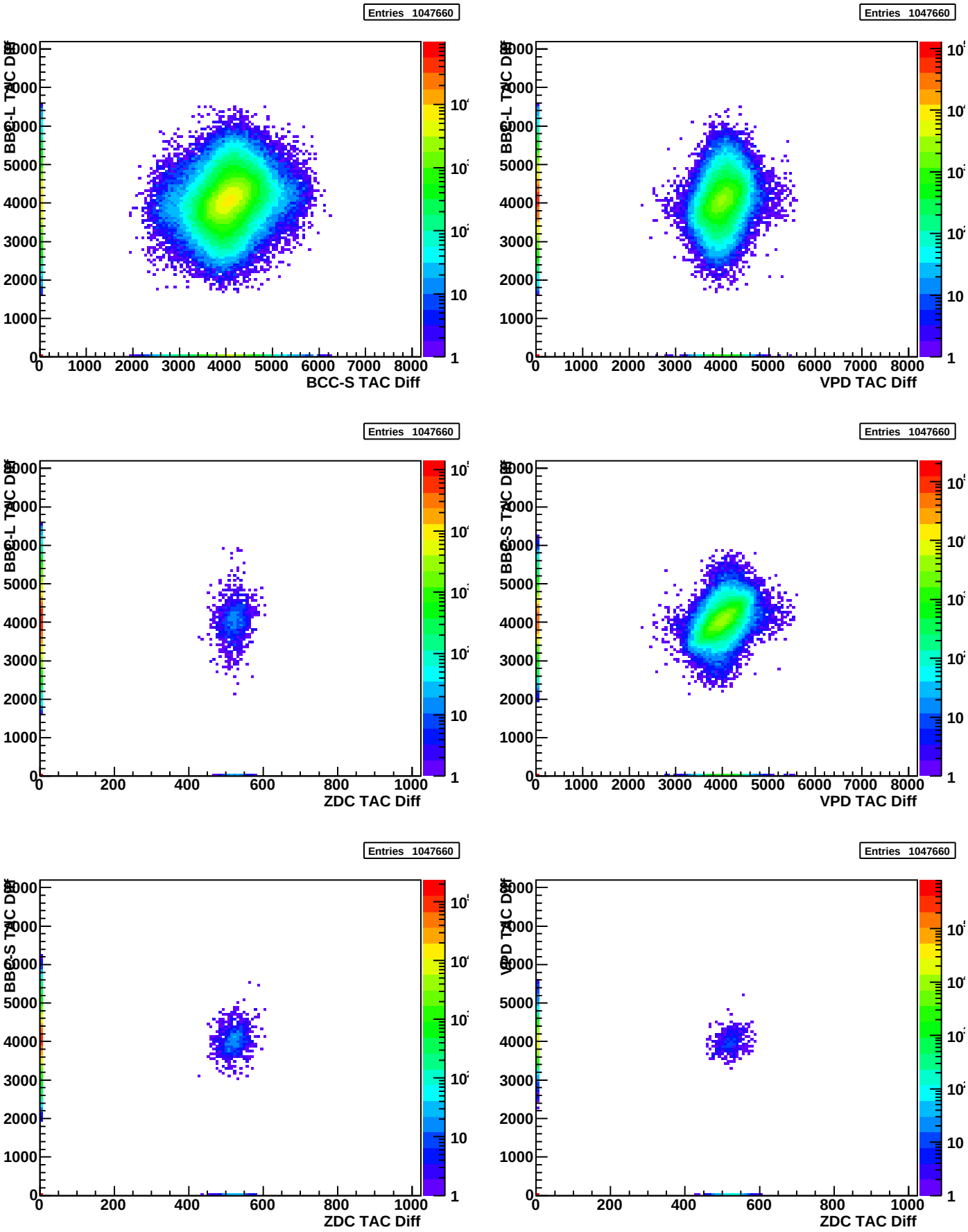
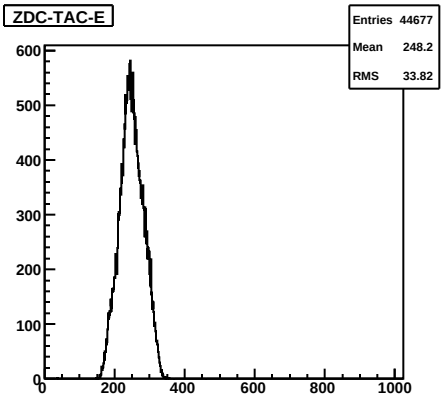
BBC-L-East ADC Sum
BBC-L-West ADC Sum

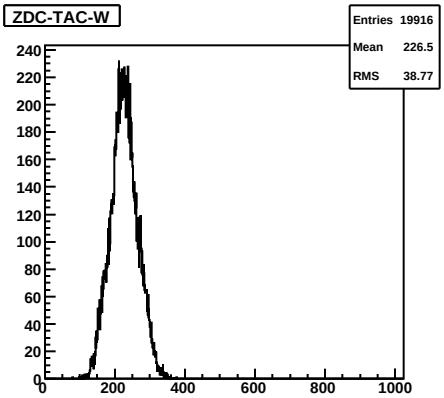

Entries 1047660
BBC-S-East ADC Sum
BBC-S-West ADC Sum

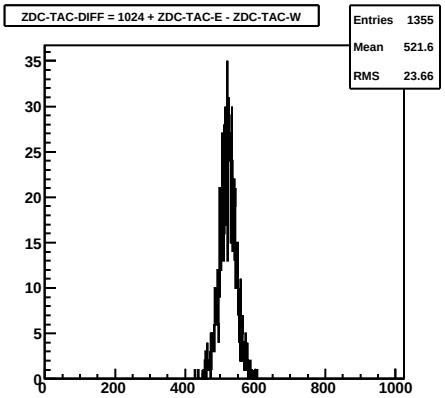

Entries 1047660
VPD East ADC Sum
VPD West ADC Sum

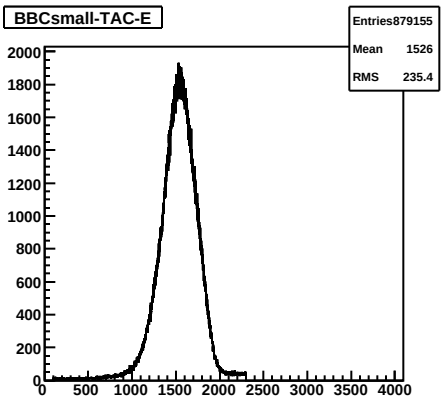

Entries 1047660
ZDC East ADC Sum Att
ZDC West ADC Sum Att

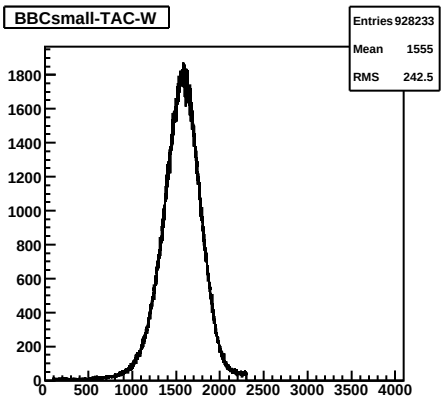

Entries 1047660
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1047660
BBC-L TAC Diff
VPD TAC Diff
Entries 1047660
BBC-L TAC Diff
ZDC TAC Diff
Entries 1047660
BBC-S TAC Diff
VPD TAC Diff
Entries 1047660
BBC-S TAC Diff
ZDC TAC Diff
Entries 1047660
VPD TAC Diff
ZDC TAC Diff

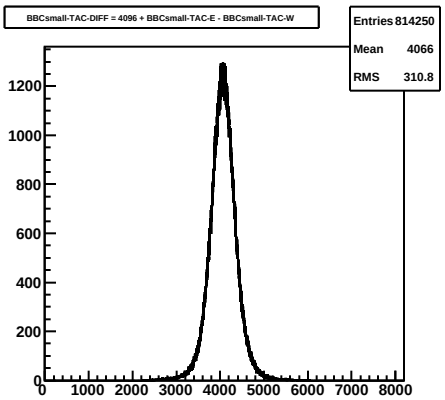

ZDC-TAC-E
Entries 44677
Mean 248.2
RMS 33.82

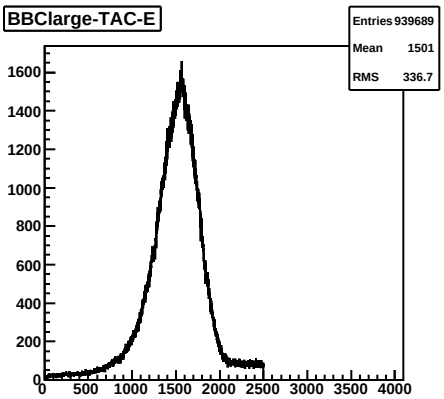

ZDC-TAC-W
Entries 19916
Mean 226.5
RMS 38.77

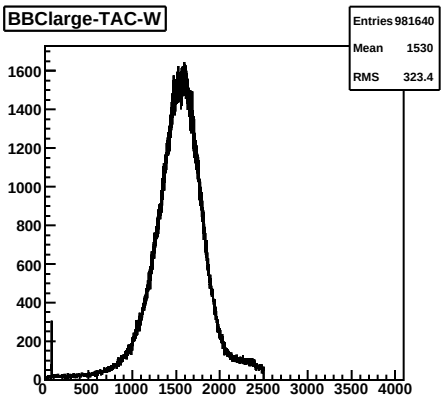

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1355
Mean 521.6
RMS 23.66

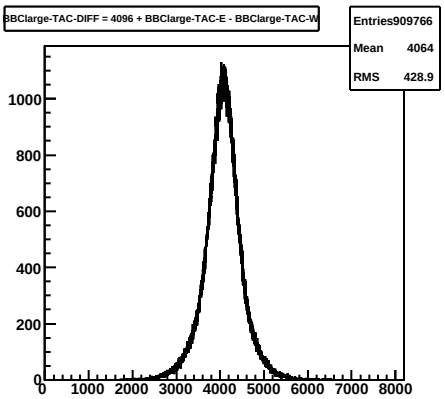

BBCsmall-TAC-E
Entries 879155
Mean 1526
RMS 235.4

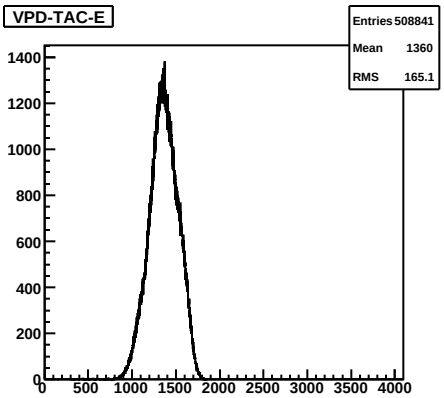

BBCsmall-TAC-W
Entries 928233
Mean 1555
RMS 242.5

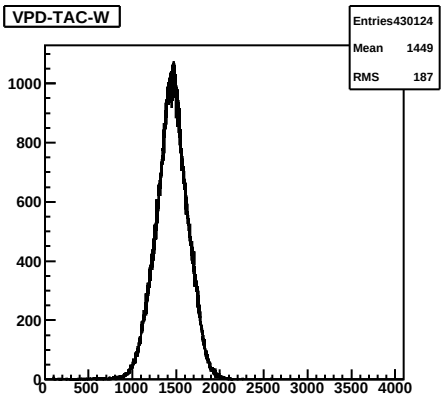

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 814250
Mean 4066
RMS 310.8

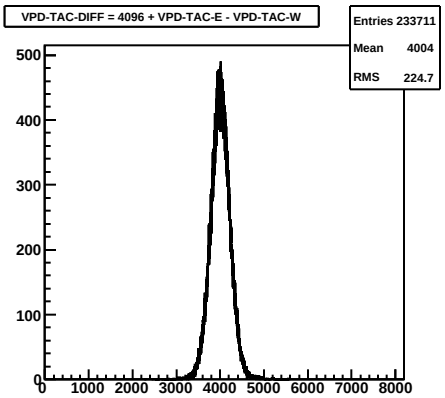

BBClarge-TAC-E
Entries 939689
Mean 1501
RMS 336.7

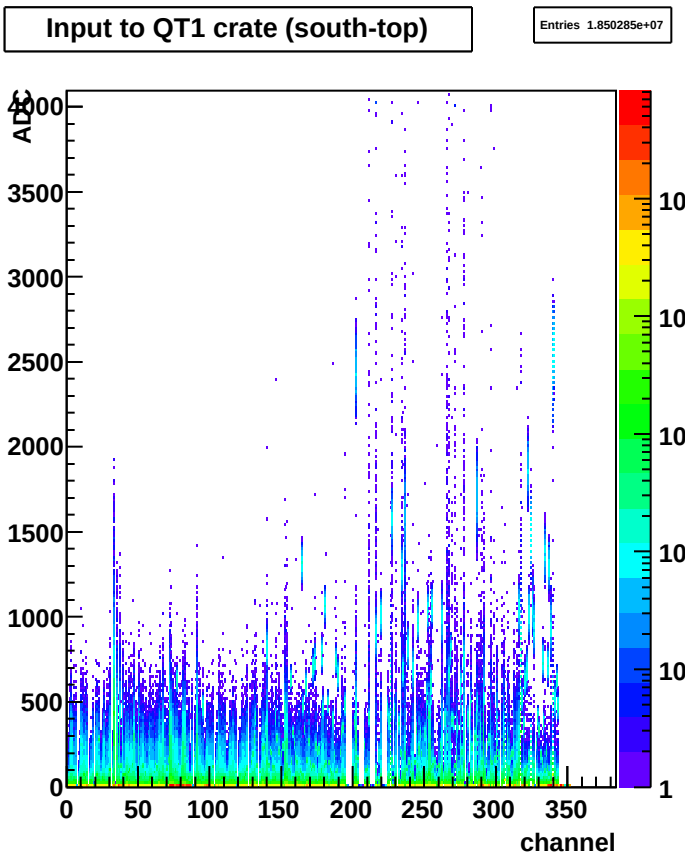

BBClarge-TAC-W
Entries 981640
Mean 1530
RMS 323.4

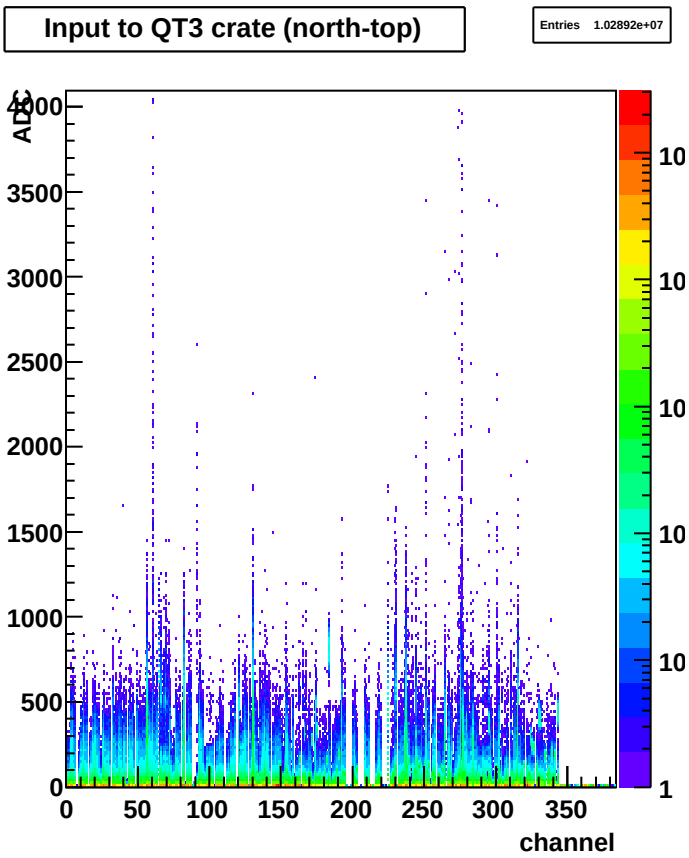

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 909766
Mean 4064
RMS 428.9

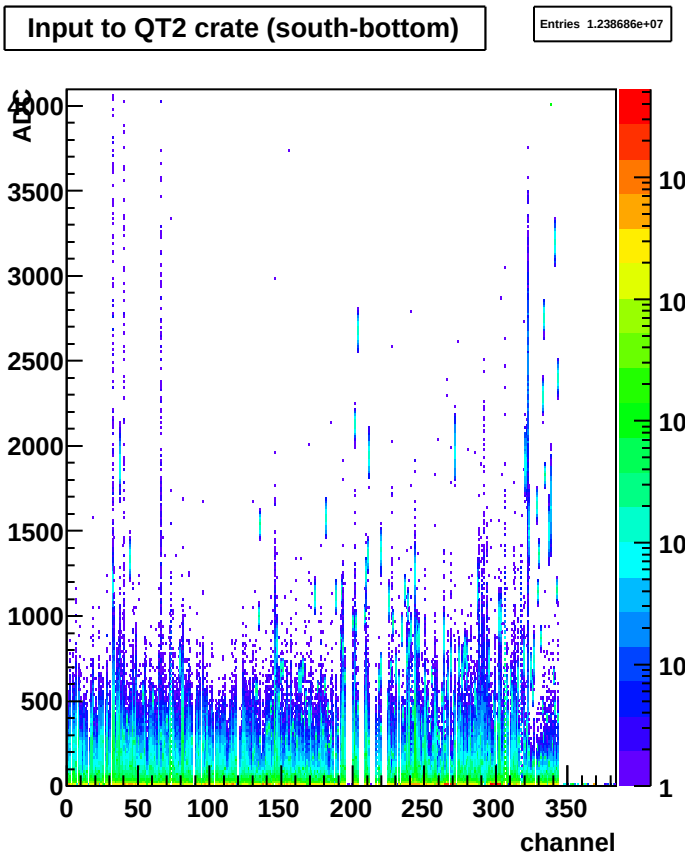

VPD-TAC-E
Entries 508841
Mean 1360
RMS 165.1

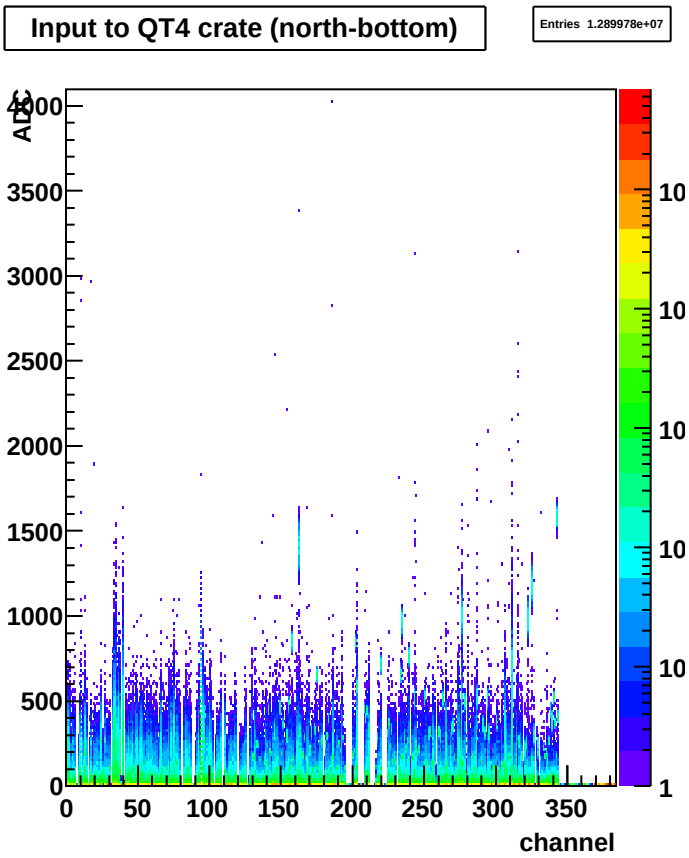

VPD-TAC-W
Entries 430124
Mean 1449
RMS 187

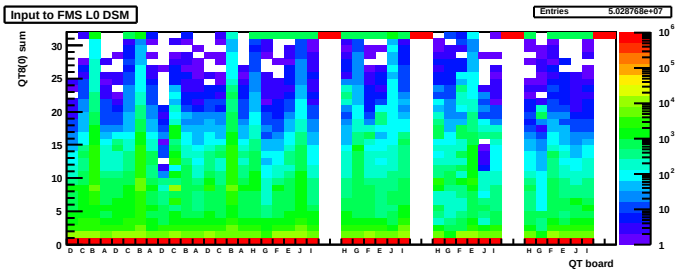

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 233711
Mean 4004
RMS 224.7

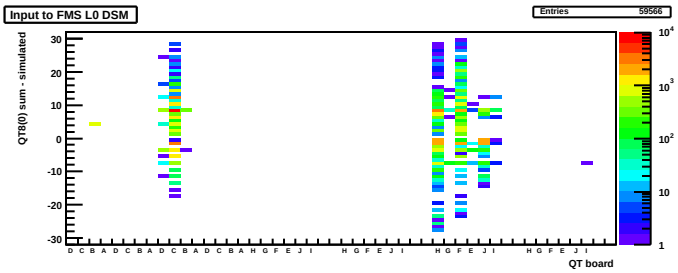

Input to QT1 crate (south-top)
Entries 1.850285e+07
ADC
channel

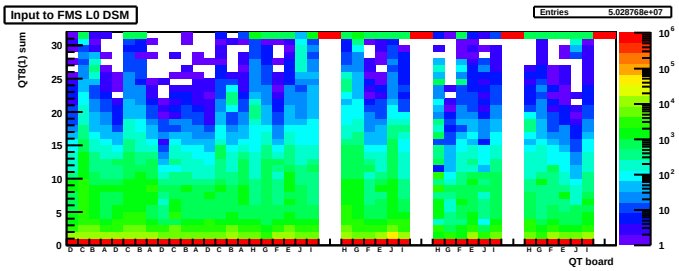

Input to QT3 crate (north-top)
Entries 1.02892e+07
ADC
channel

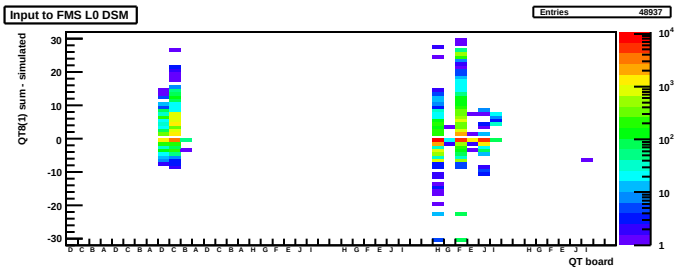

Input to QT2 crate (south-bottom)
Entries 1.238686e+07
ADC
channel

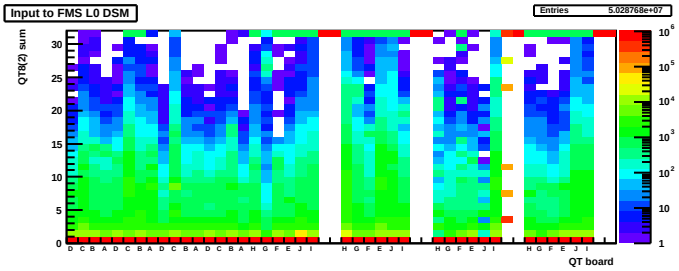

Input to QT4 crate (north-bottom)
Entries 1.289978e+07
ADC
channel

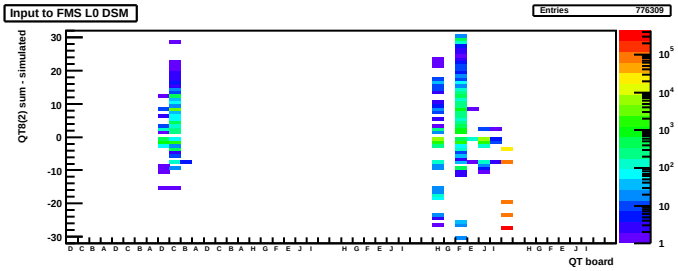

Input to FMS L0 DSM
Entries 5.028768e+07
QT8(0) sum
QT board

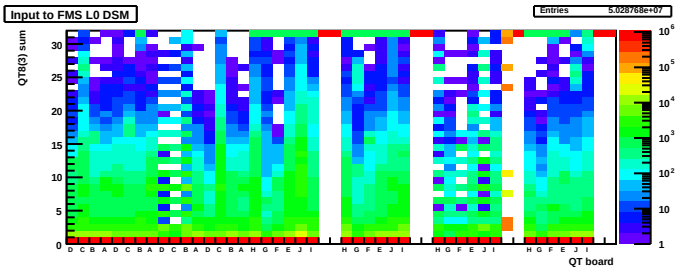

Input to FMS L0 DSM
Entries 59566
QT8(0) sum - simulated
QT board

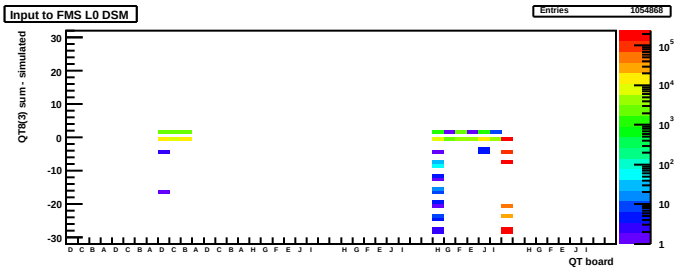

Input to FMS L0 DSM
Entries 5.028768e+07
QT8(1) sum
QT board

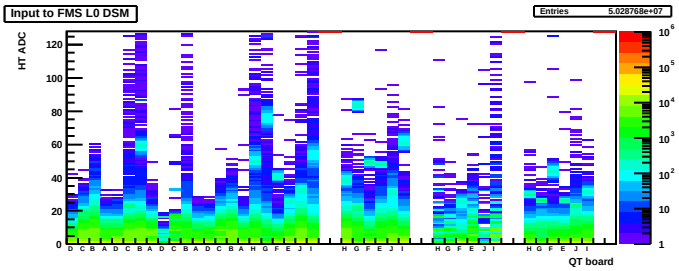

Input to FMS L0 DSM
Entries 48937
QT8(1) sum - simulated
QT board

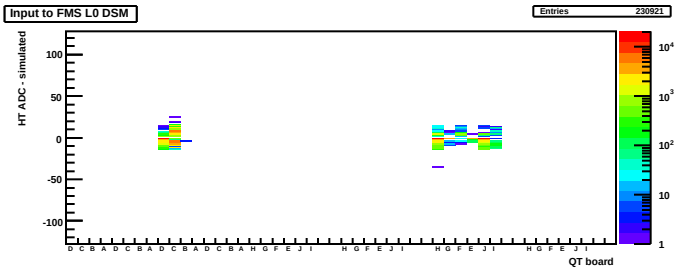

Input to FMS L0 DSM
Entries 5.028768e+07
QT8(2) sum
QT board

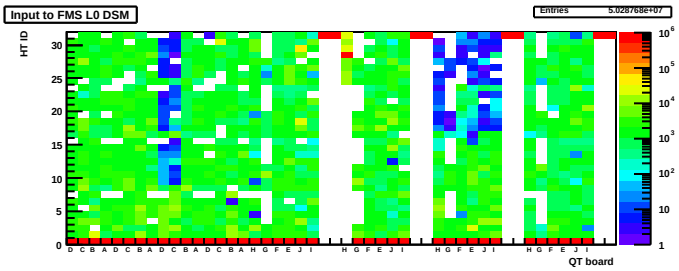

Input to FMS L0 DSM
Entries 776309
QT8(2) sum - simulated
QT board

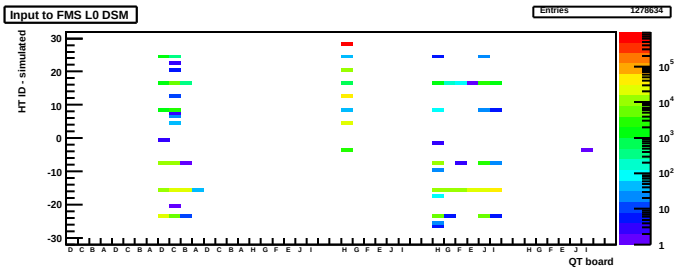

Input to FMS L0 DSM
Entries 5.028768e+07
QT8(3) sum
QT board

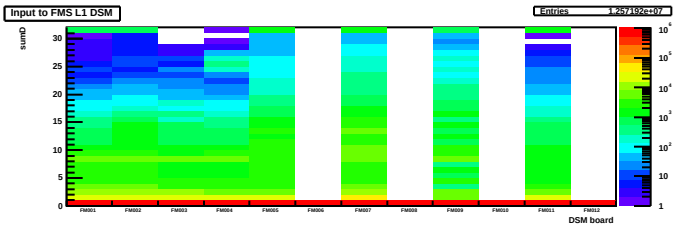

Input to FMS L0 DSM
Entries 1054868
QT8(3) sum - simulated
QT board

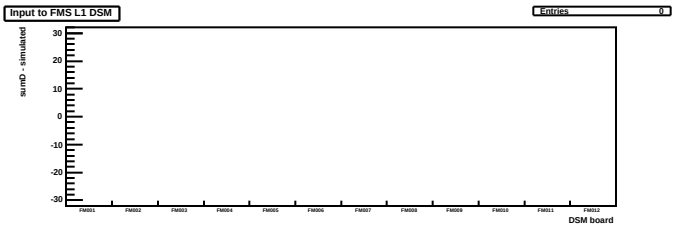

Input to FMS L0 DSM
Entries 5.028768e+07
HT ADC
QT board

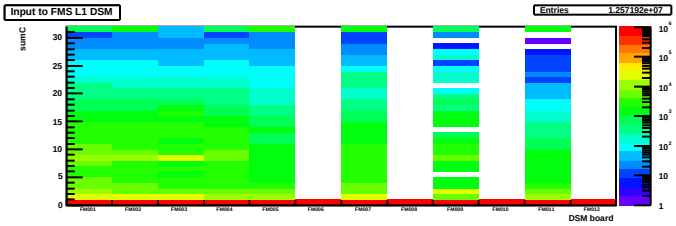

Input to FMS L0 DSM
Entries 230921
HT ADC - simulated
QT board

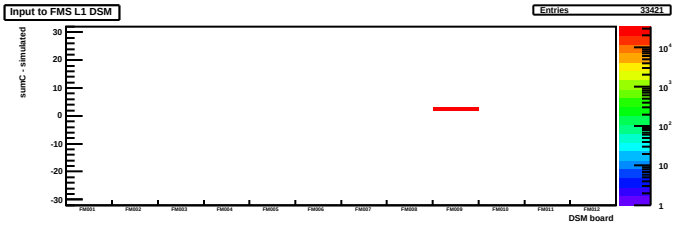

Input to FMS L0 DSM
Entries 5.028768e+07
HT ID
QT board

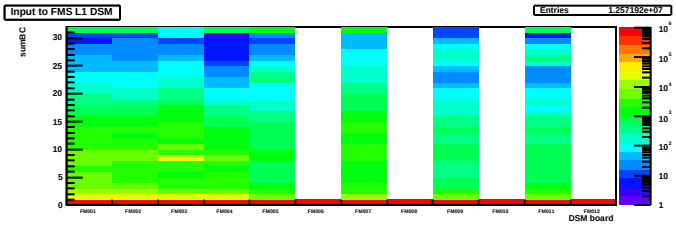

Input to FMS L0 DSM
Entries 1278634
HT ID - simulated
QT board

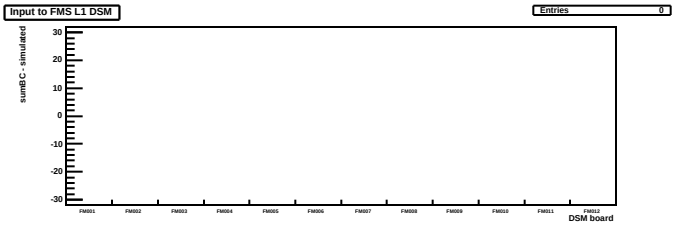

Input to FMS L1 DSM
Entries 1.257192e+07
sumD
DSM board

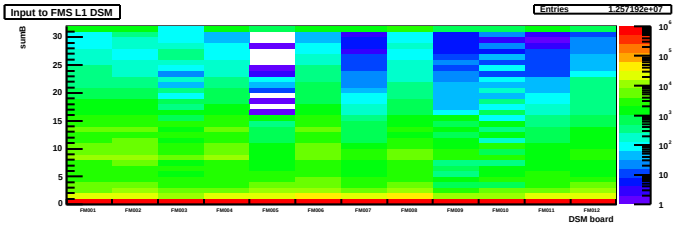

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

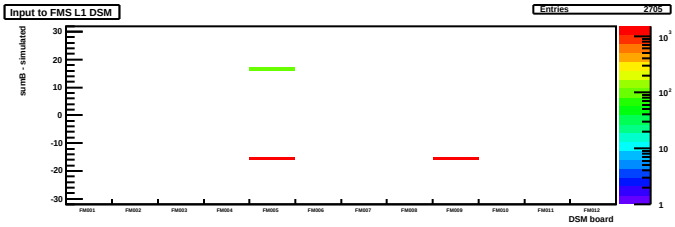

Input to FMS L1 DSM
Entries 1.257192e+07
sumC
DSM board

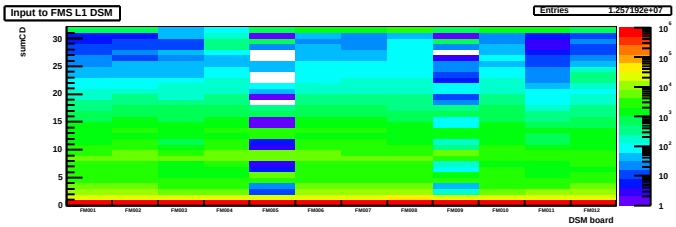

Input to FMS L1 DSM
Entries 33421
sumC - simulated
DSM board

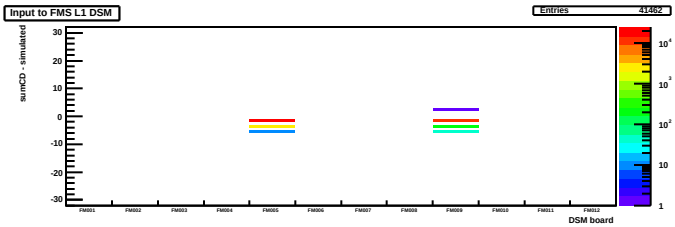

Input to FMS L1 DSM
Entries 1.257192e+07
sumBC
DSM board

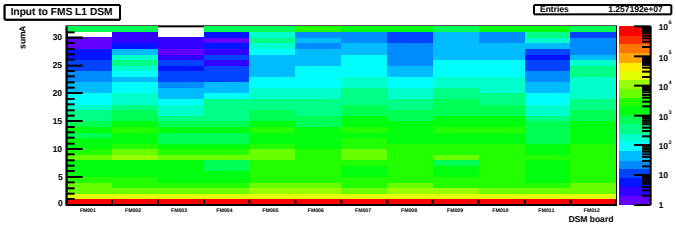

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

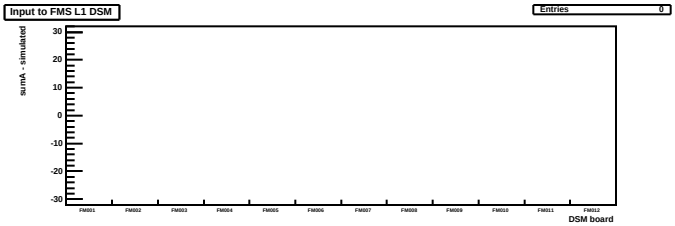

Input to FMS L1 DSM
Entries 1.257192e+07
sumB
DSM board

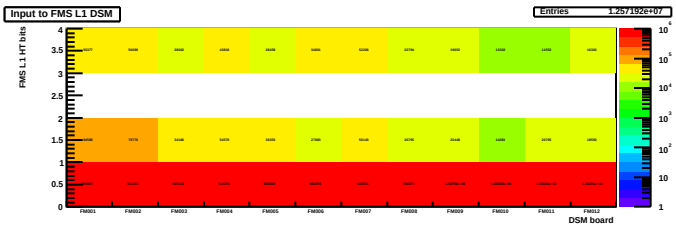

Input to FMS L1 DSM
Entries 2705
sumB - simulated
DSM board

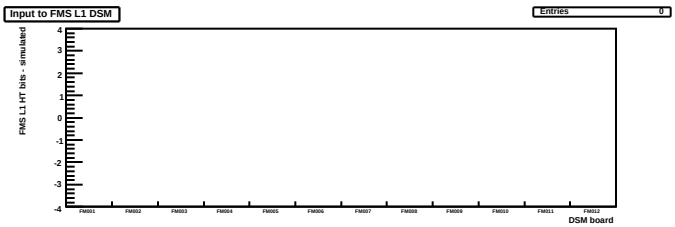

Input to FMS L1 DSM
Entries 1.257192e+07
sumCD
DSM board

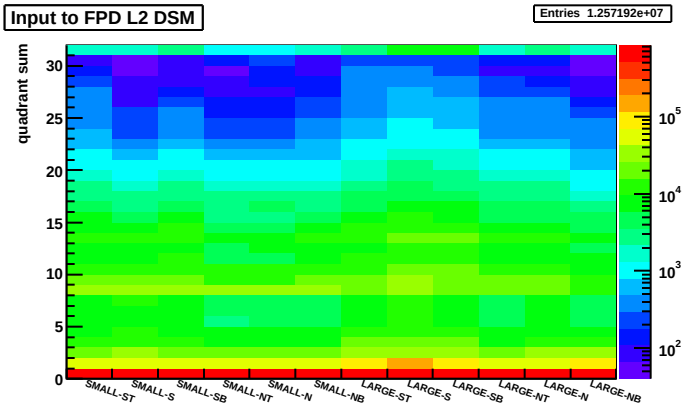

Input to FMS L1 DSM
Entries 41462
sumCD - simulated
DSM board

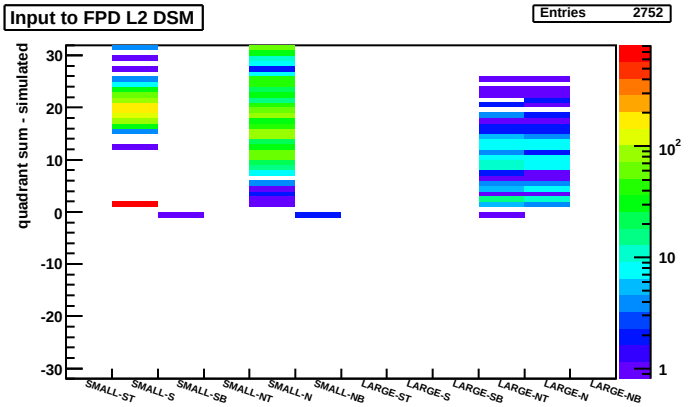

Input to FMS L1 DSM
Entries 1.257192e+07
sumA
DSM board

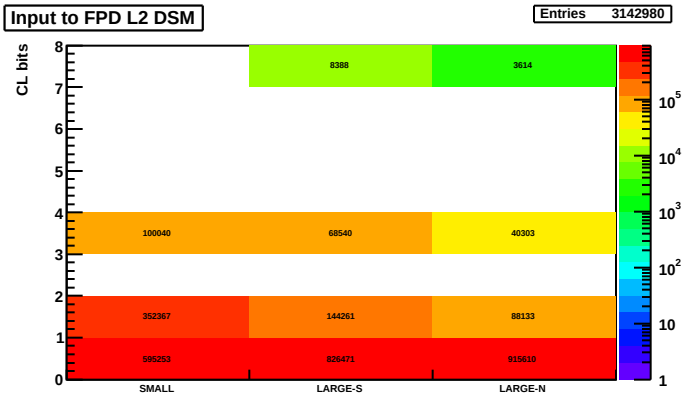

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

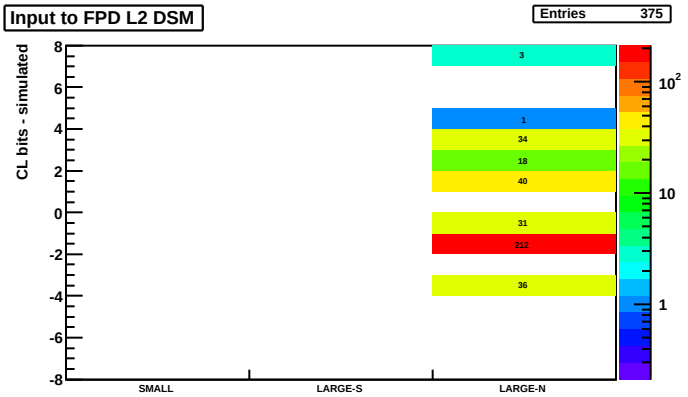

Input to FMS L1 DSM
Entries 1.257192e+07
FMS L1 HT bits
DSM board

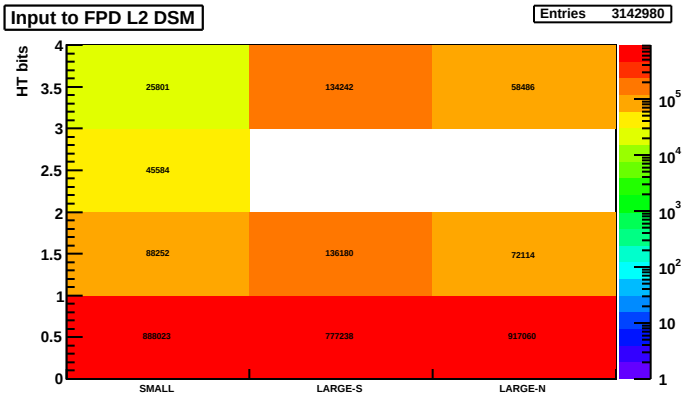

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

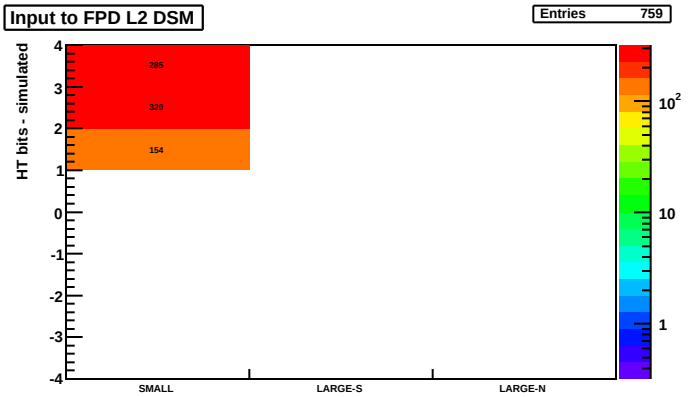

Input to FPD L2 DSM
Entries 1.257192e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

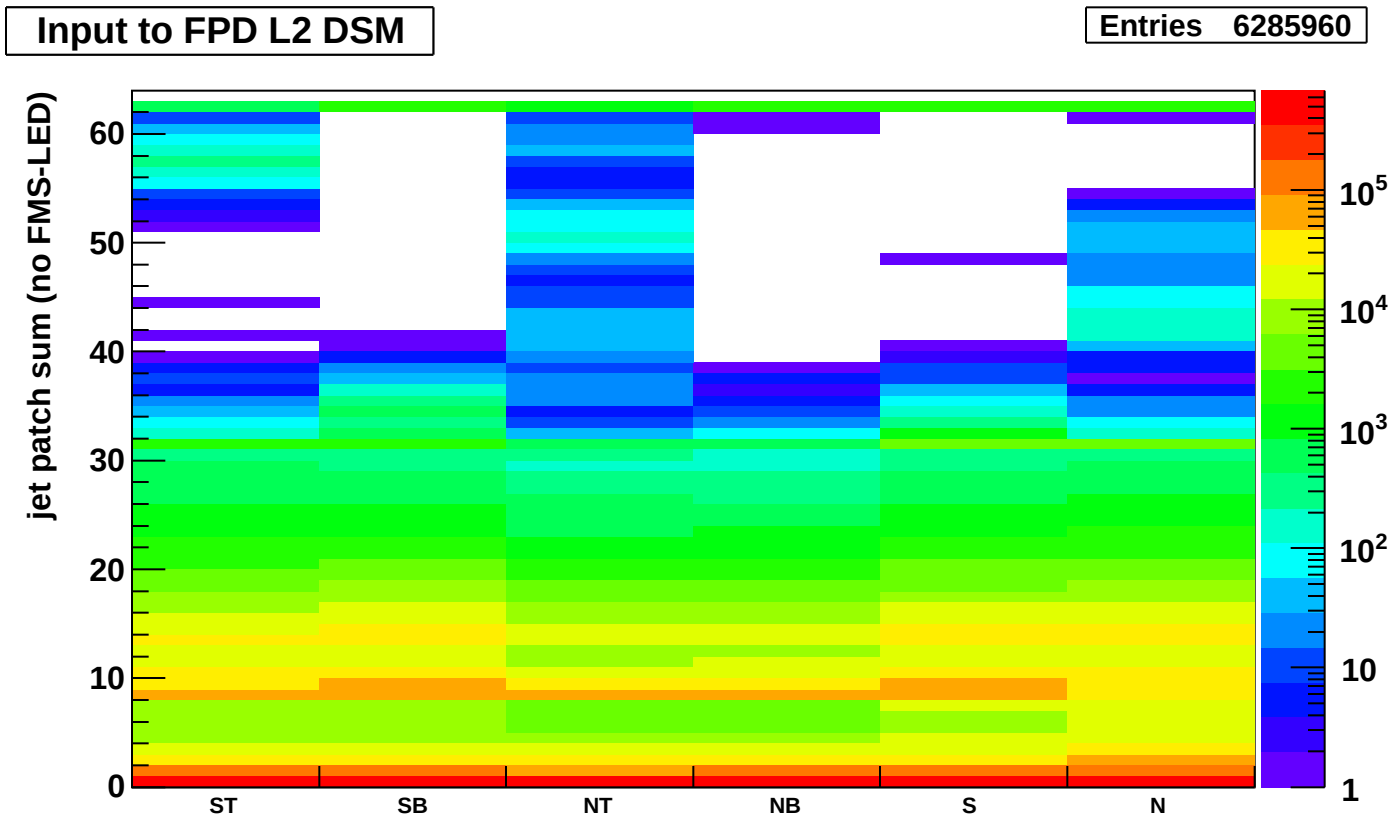

Input to FPD L2 DSM
Entries 2752
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

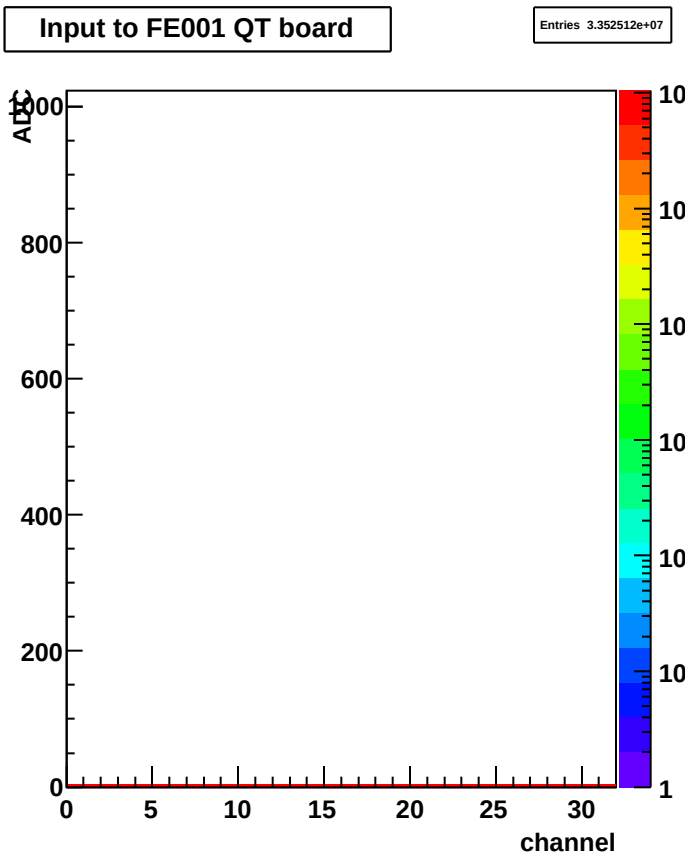

Input to FPD L2 DSM
Entries 3142980
CL bits
8388
3614
100040
68540
40303
352367
144261
88133
595253
826471
915610
SMALL
LARGE-S
LARGE-N

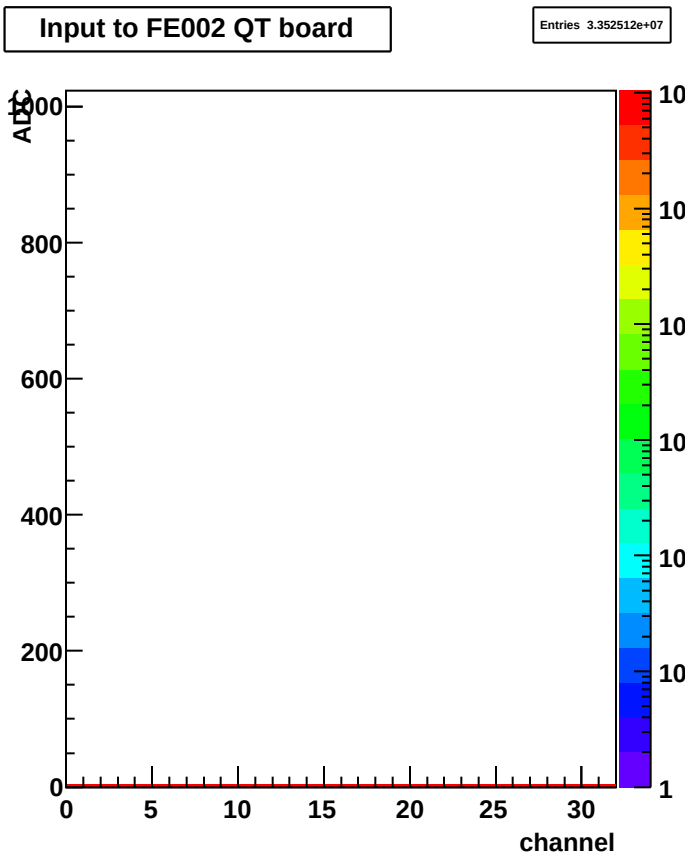

Input to FPD L2 DSM
Entries 375
CL bits - simulated
3
1
34
18
40
31
212
36
SMALL
LARGE-S
LARGE-N

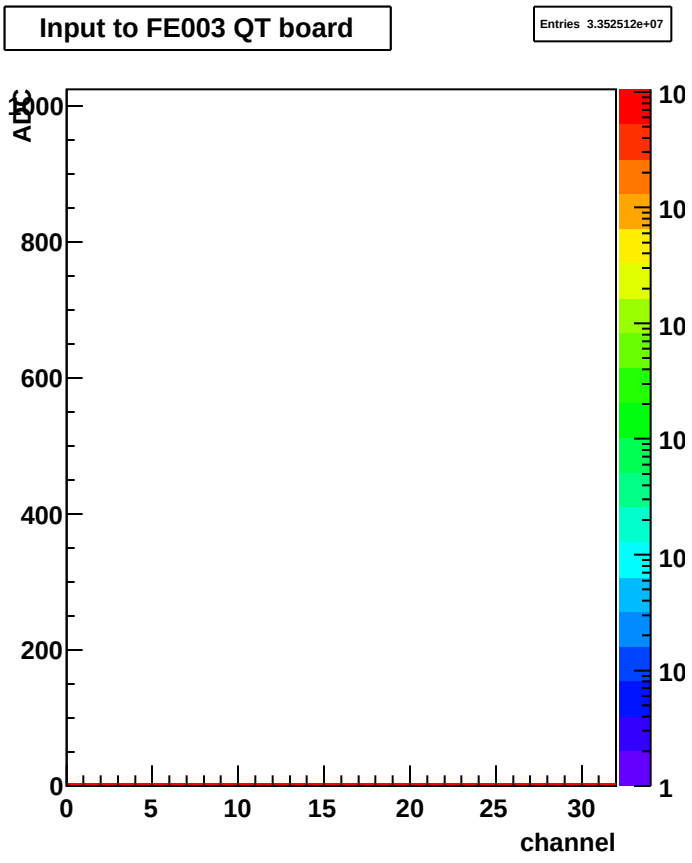

Input to FPD L2 DSM
Entries 3142980
HT bits
25801
134242
58486
45584
88252
136180
72114
888023
777238
917060
SMALL
LARGE-S
LARGE-N

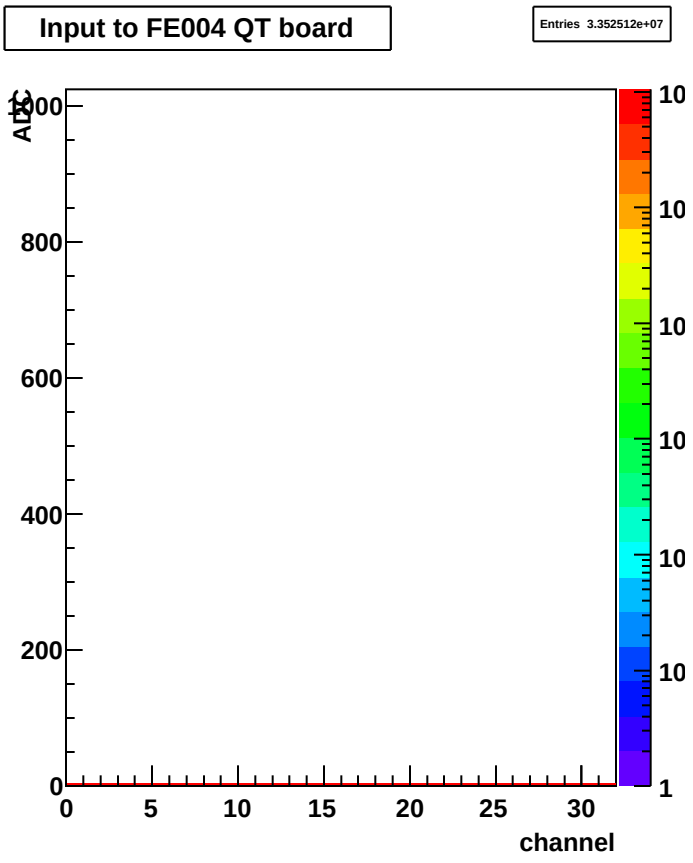

Input to FPD L2 DSM
Entries 759
HT bits - simulated
285
320
154
SMALL
LARGE-S
LARGE-N

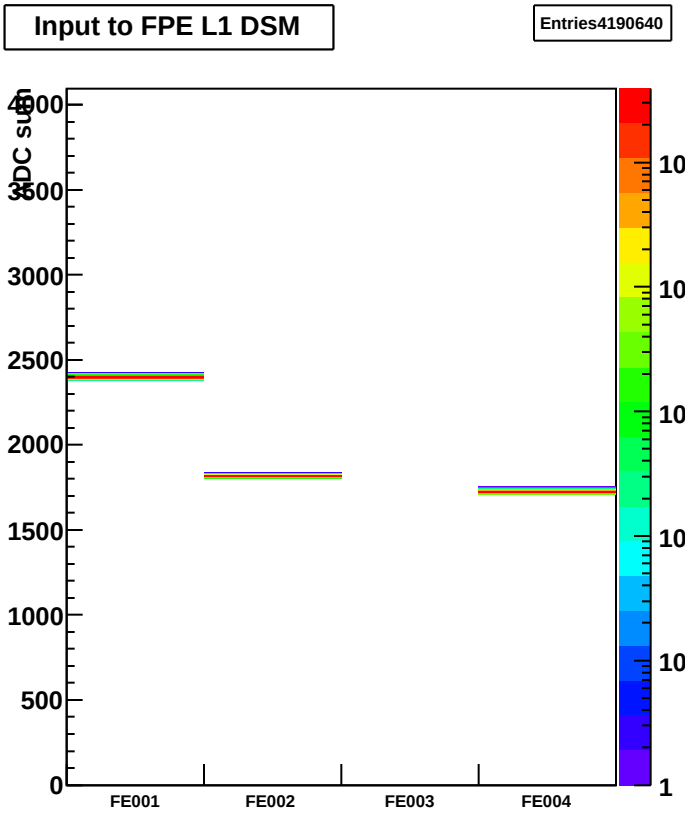

Input to FPD L2 DSM
Entries 6285960
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

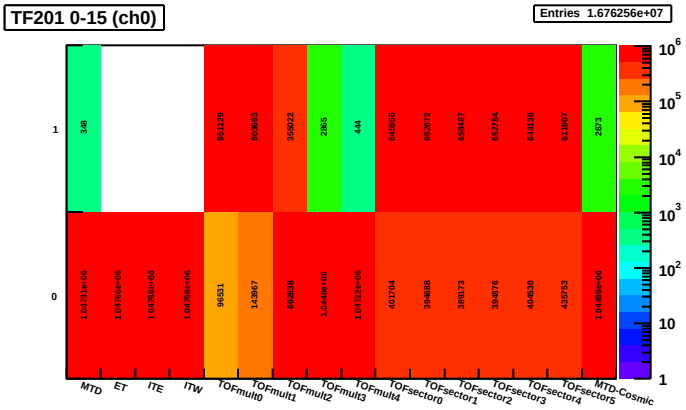

Input to FE001 QT board
Entries 3.352512e+07
ADC
channel

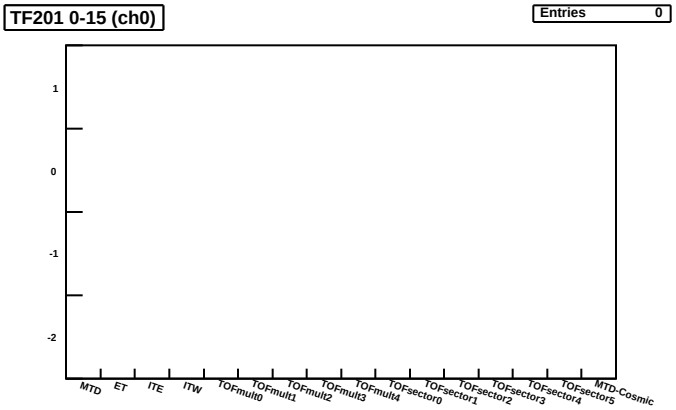

Input to FE002 QT board
Entries 3.352512e+07
ADC
channel

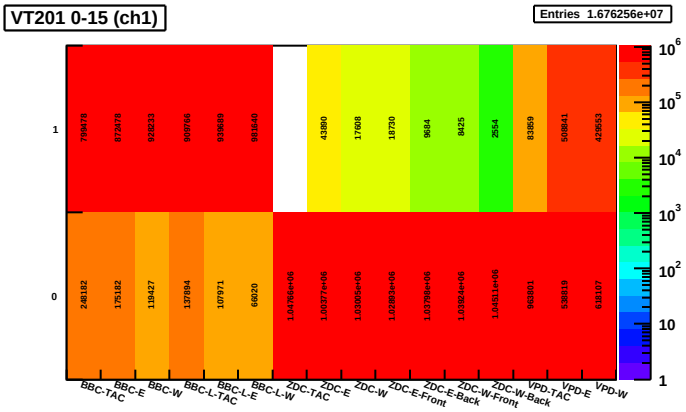

Input to FE003 QT board
Entries 3.352512e+07
ADC
channel

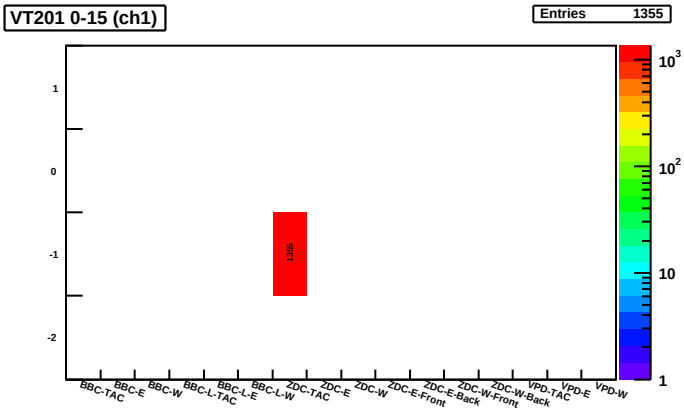

Input to FE004 QT board
Entries 3.352512e+07
ADC
channel

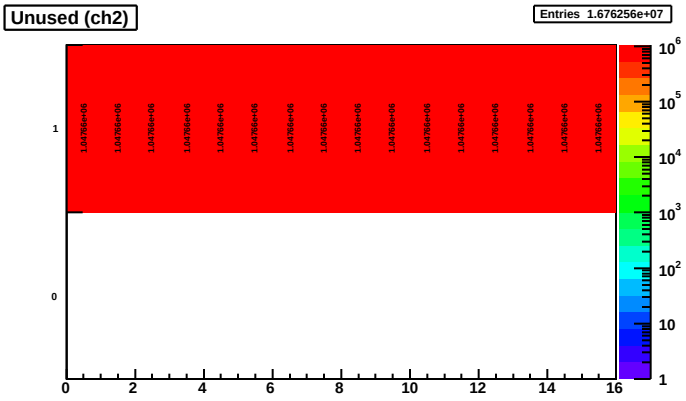

Input to FPE L1 DSM
Entries4190640
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

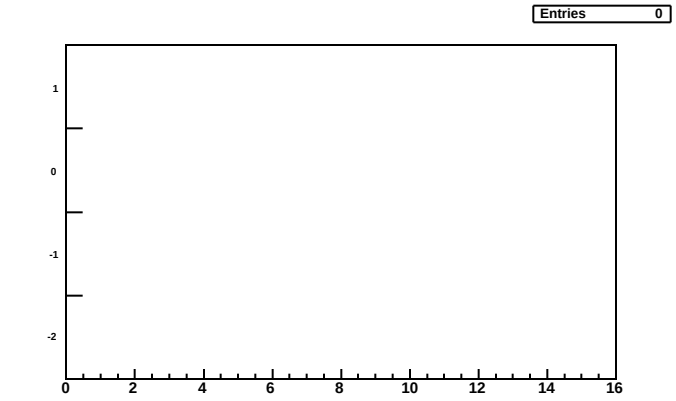

TF201 0-15 (ch0)
Entries 1.676256e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

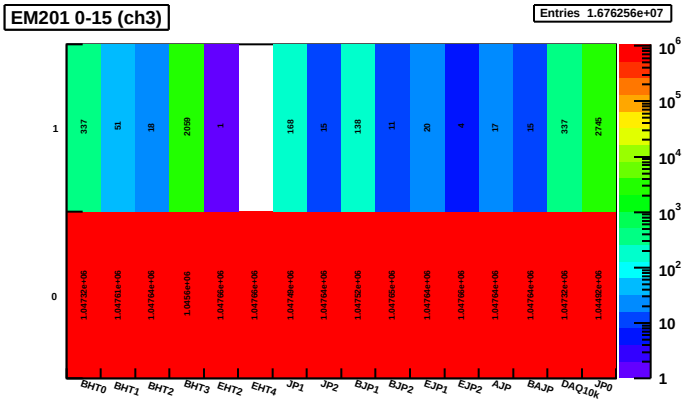

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

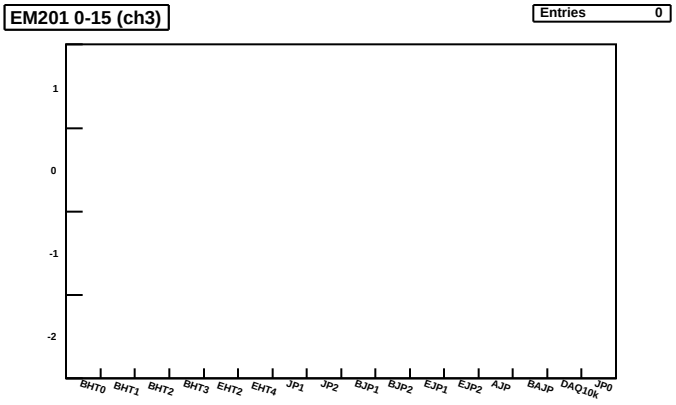

VT201 0-15 (ch1)
Entries 1.676256e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 1355
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

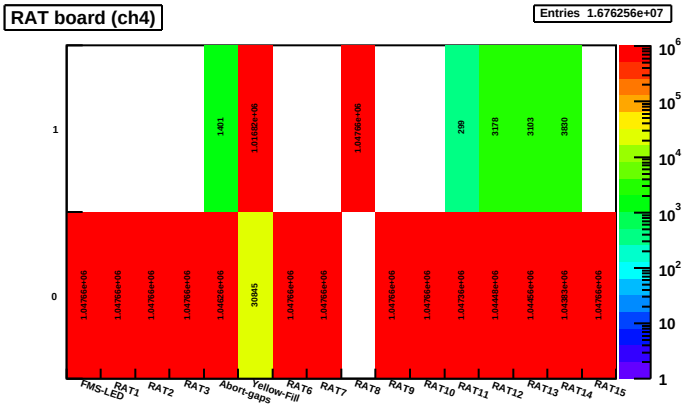

Unused (ch2)
Entries 1.676256e+07

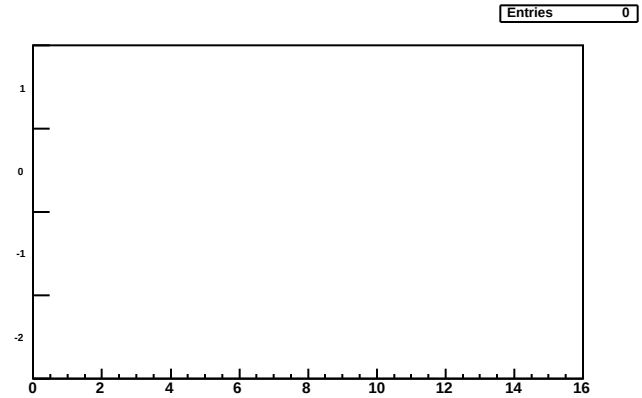

Entries 0


EM201 0-15 (ch3)
Entries 1.676256e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

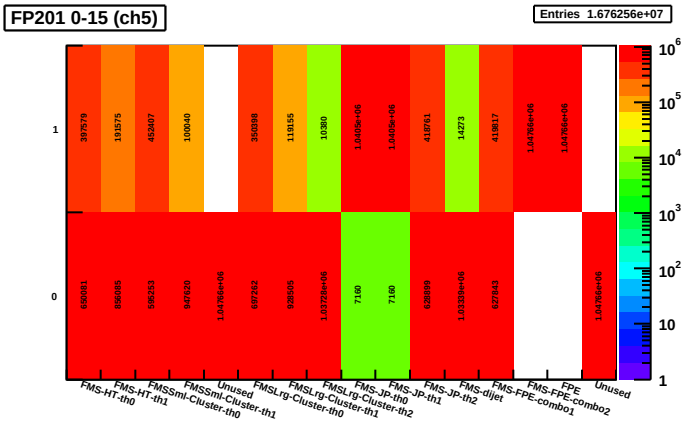

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

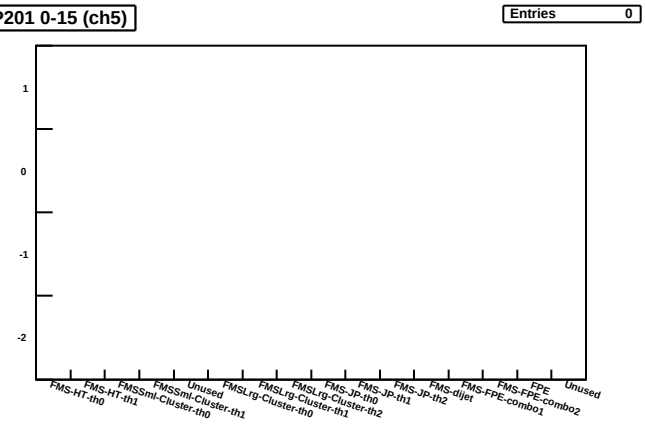




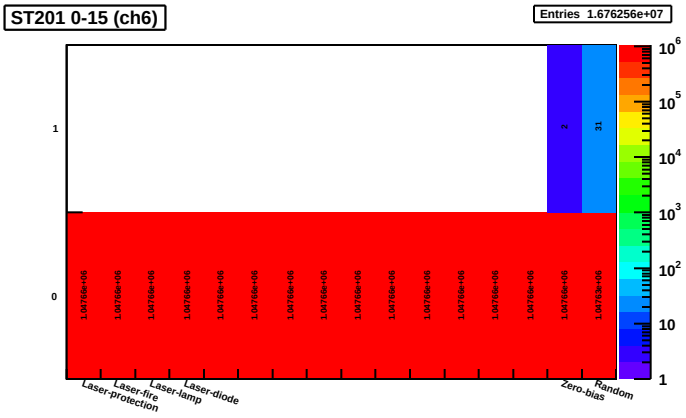
FP201 0-15 (ch5)

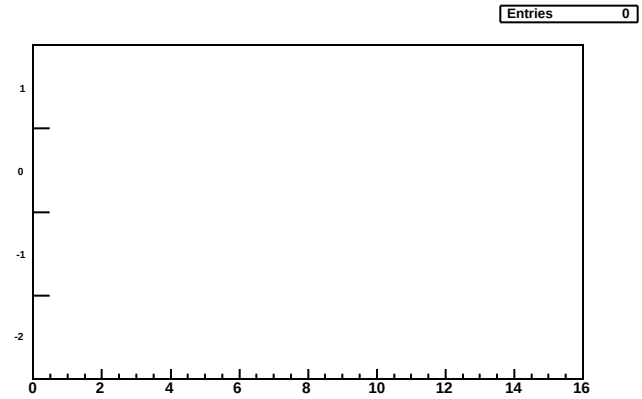
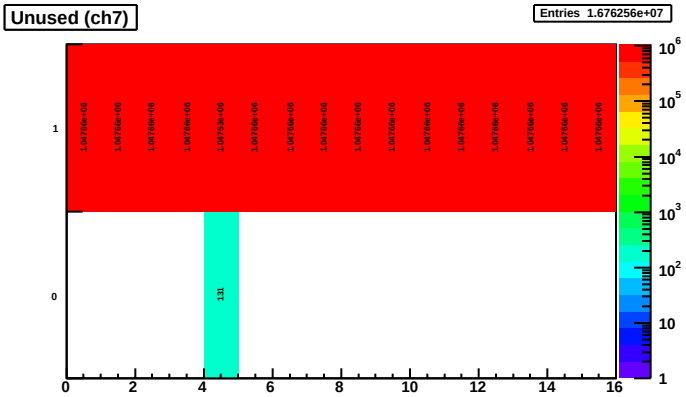
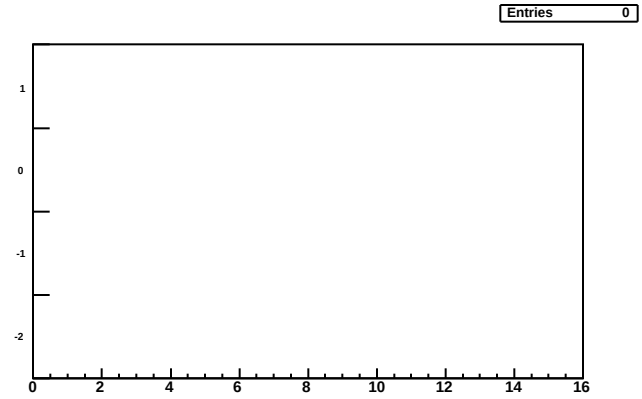


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)