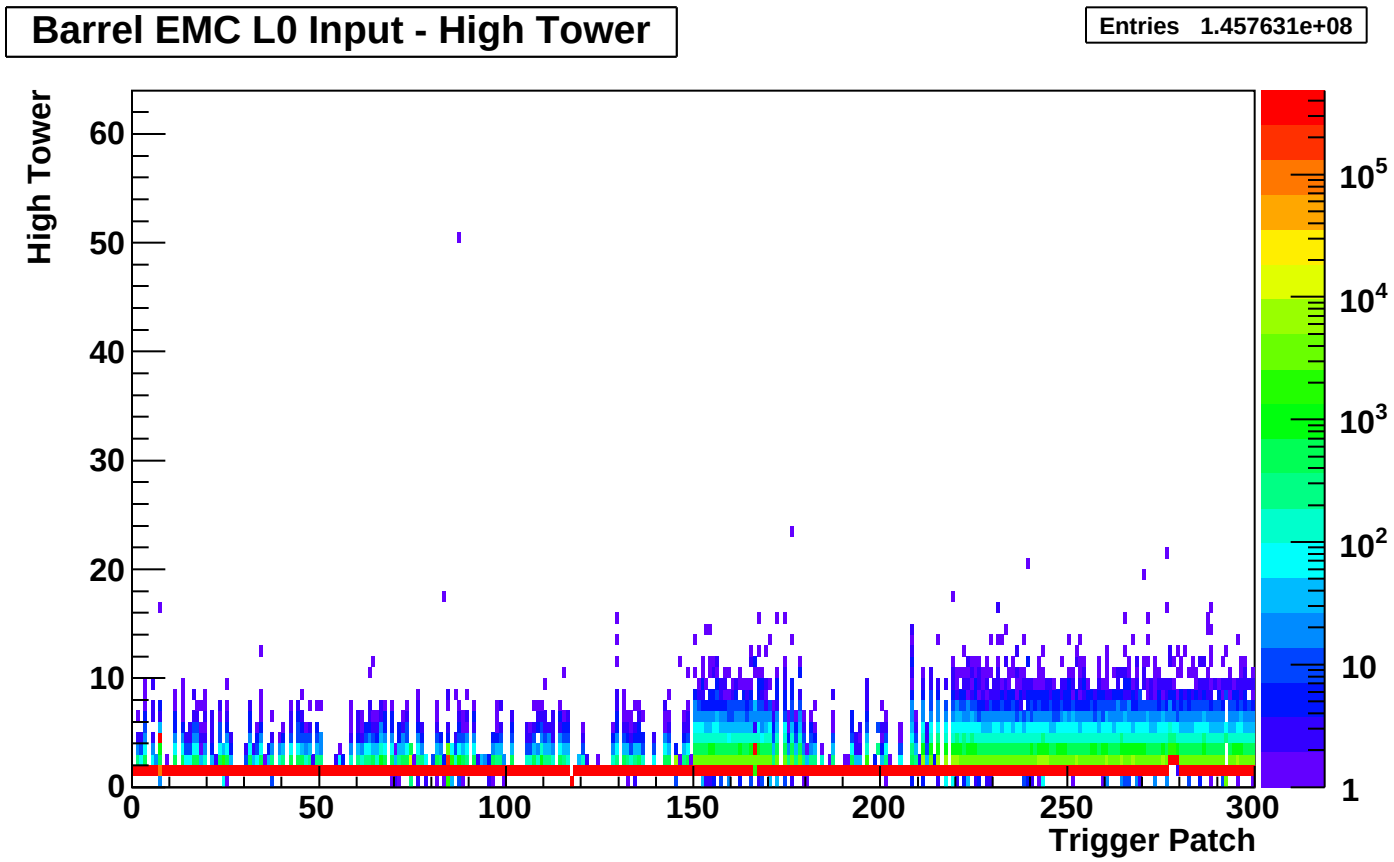
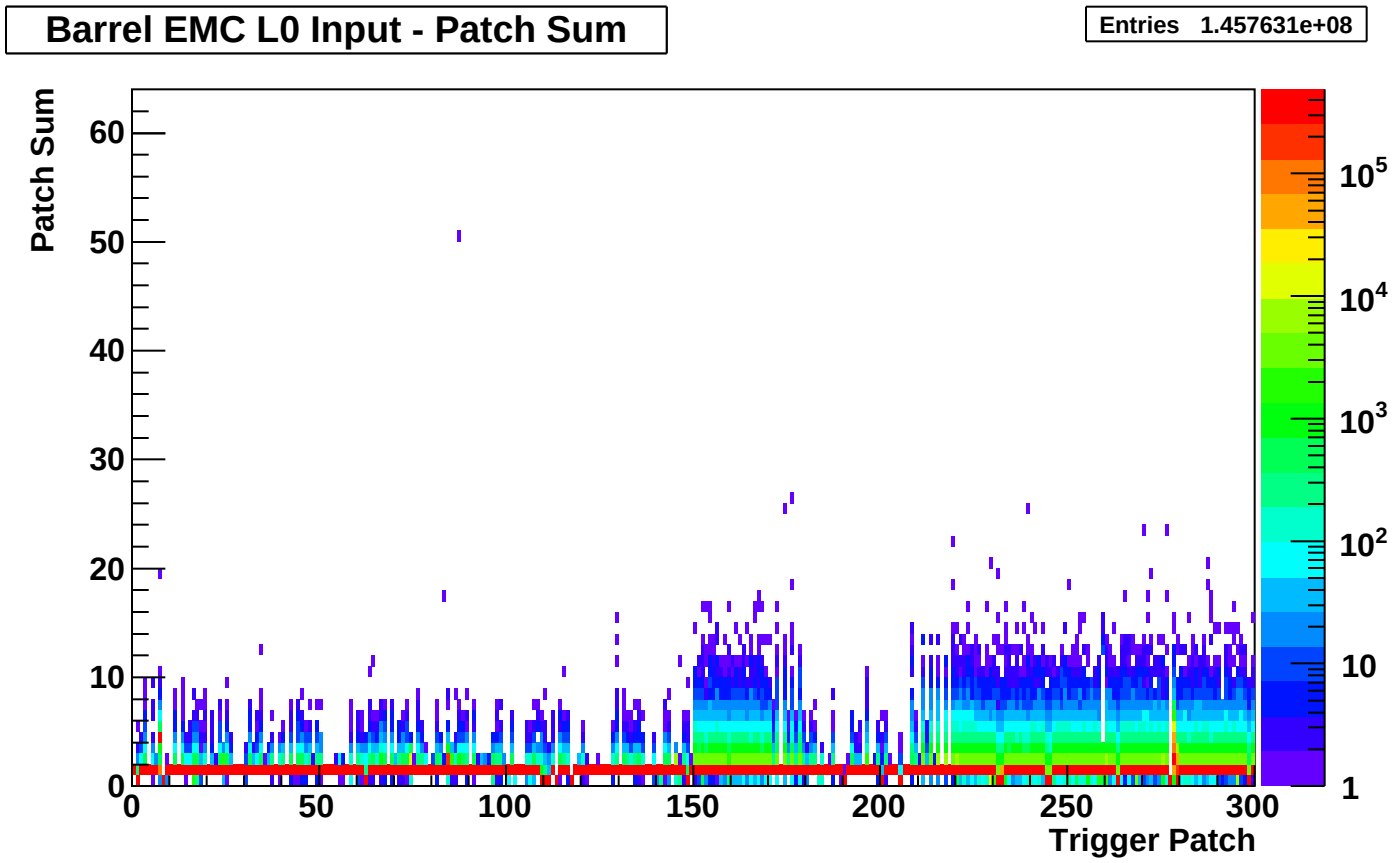
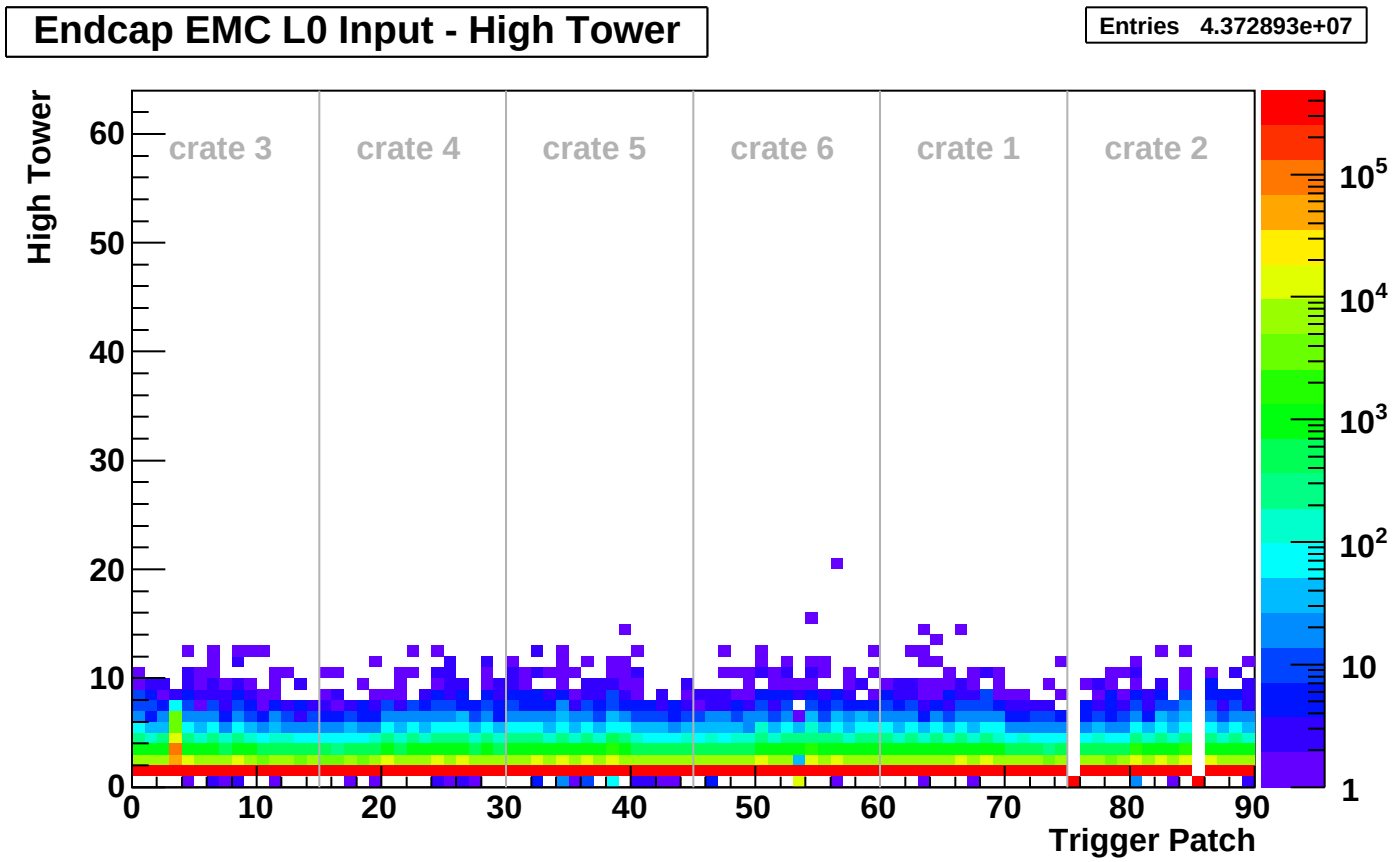
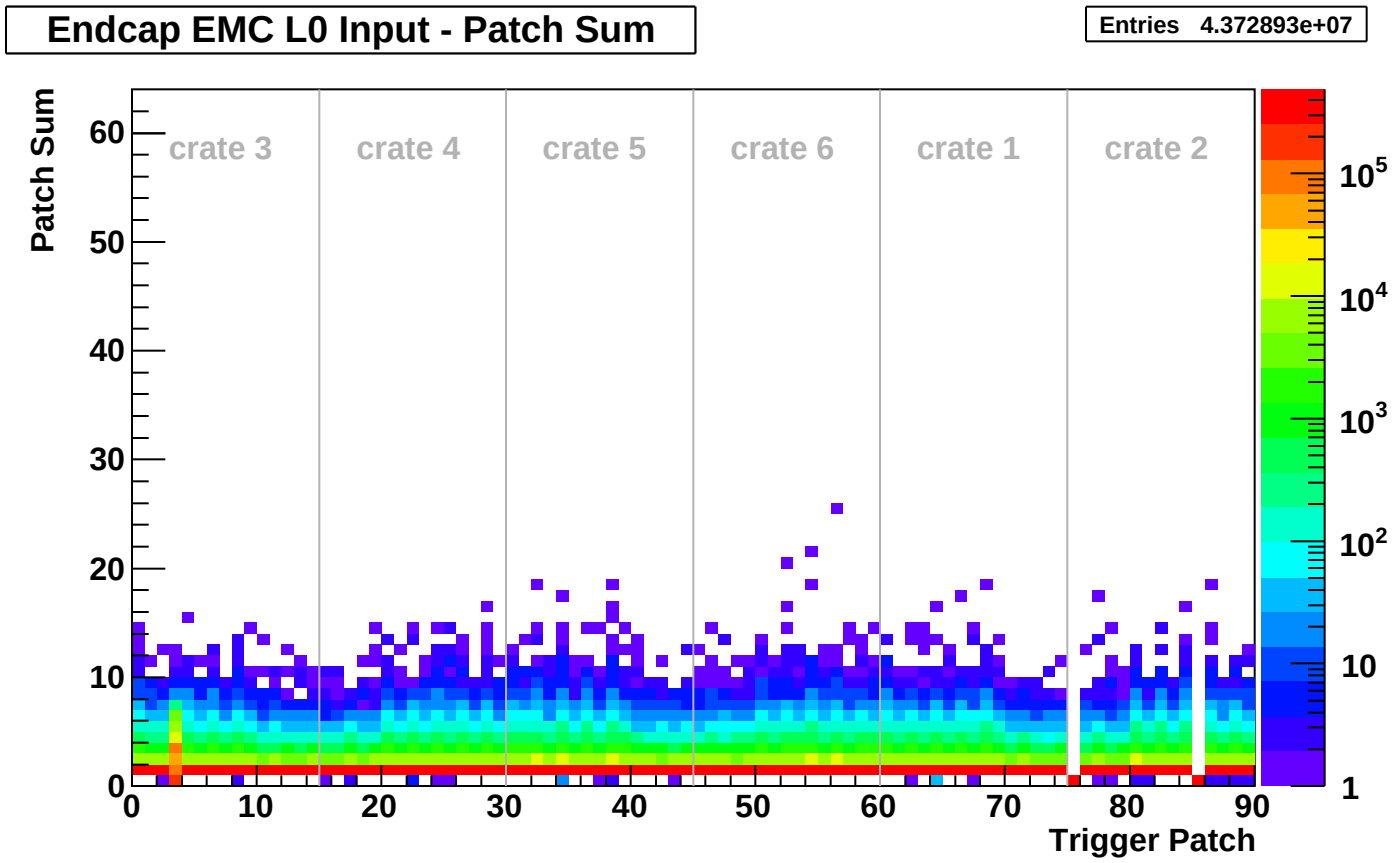
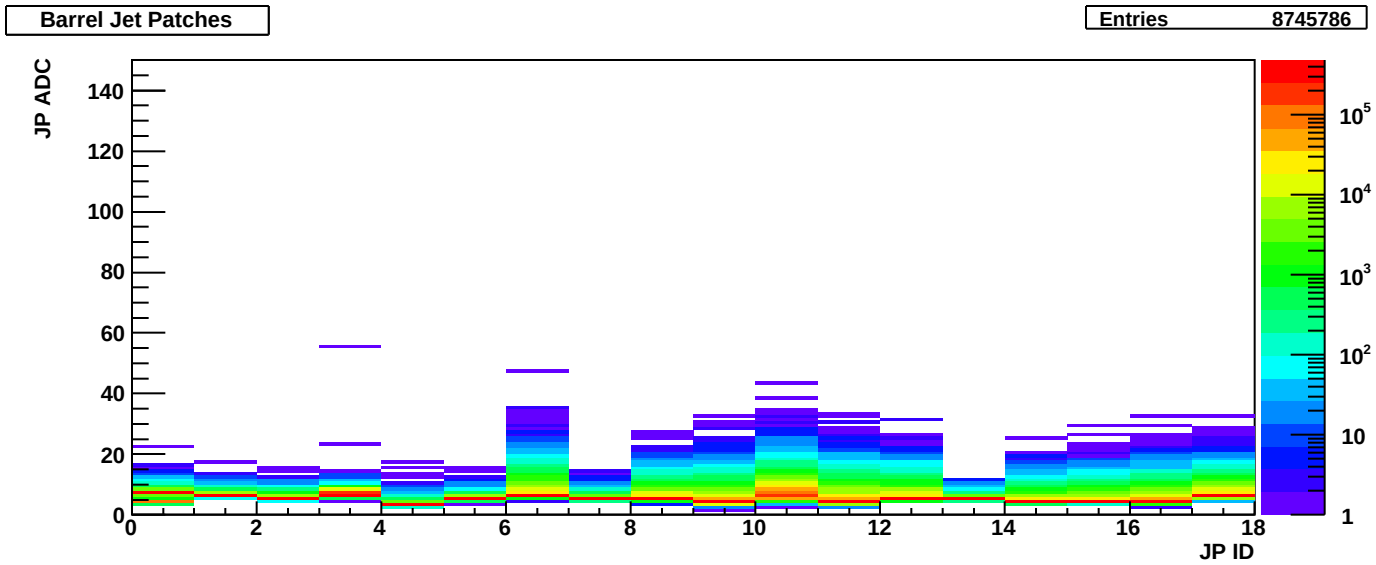
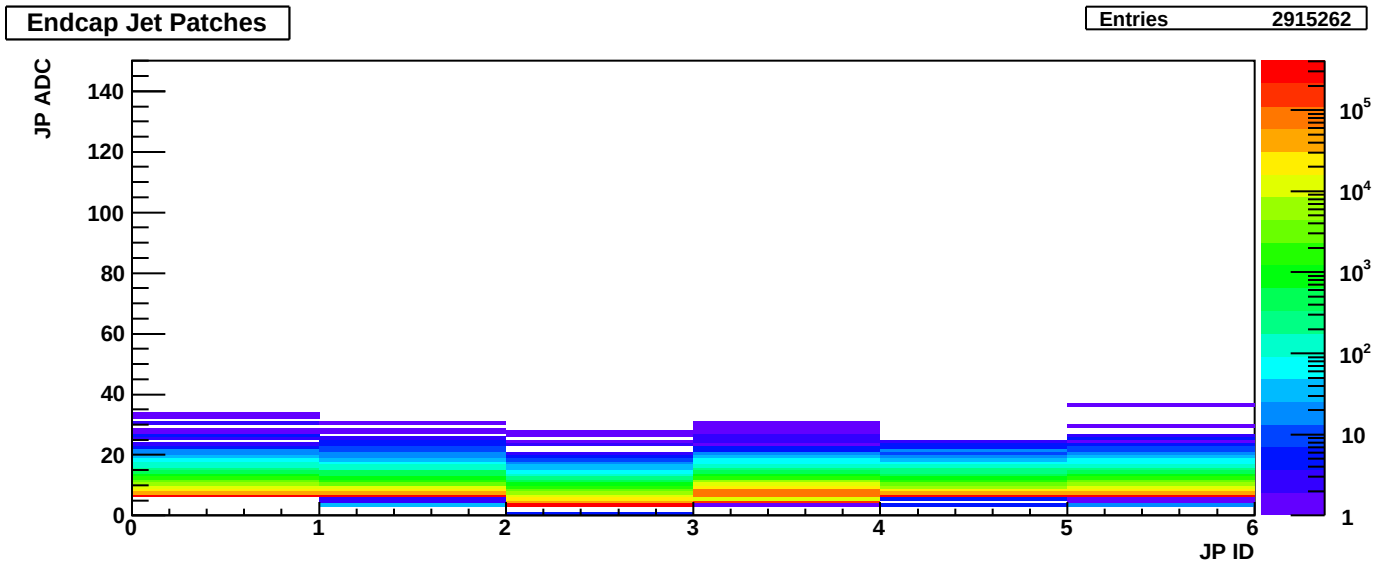

Barrel EMC L0 Input - High Tower
Entries 1.457631e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

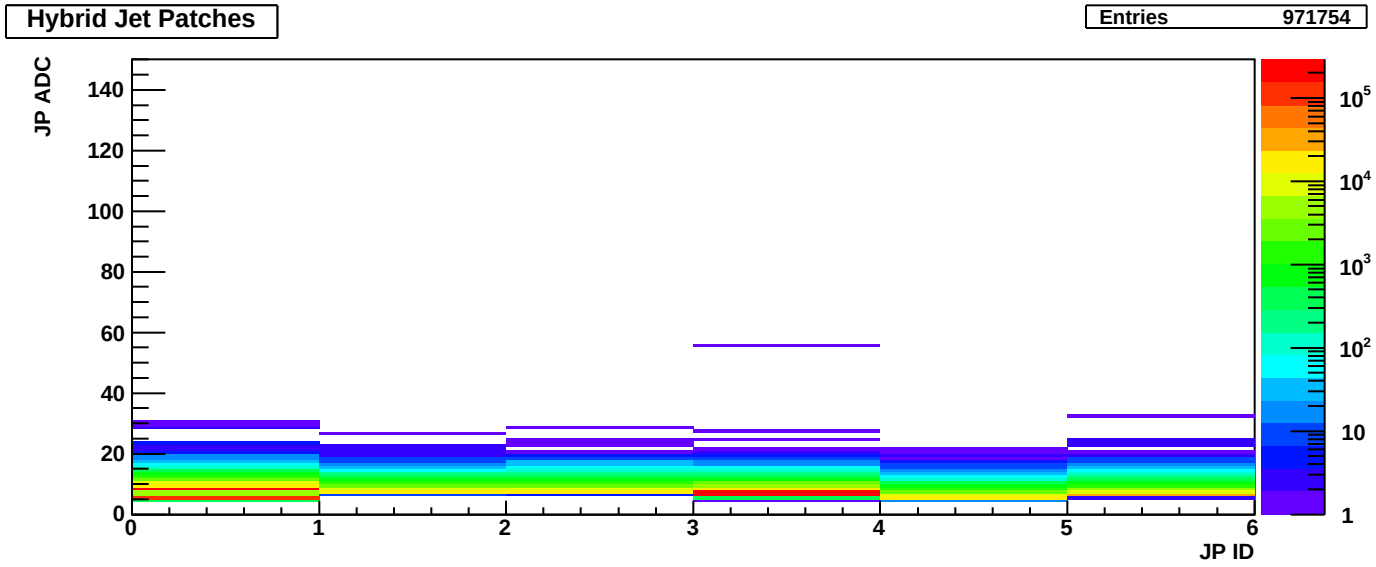

Barrel EMC L0 Input - Patch Sum
Entries 1.457631e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

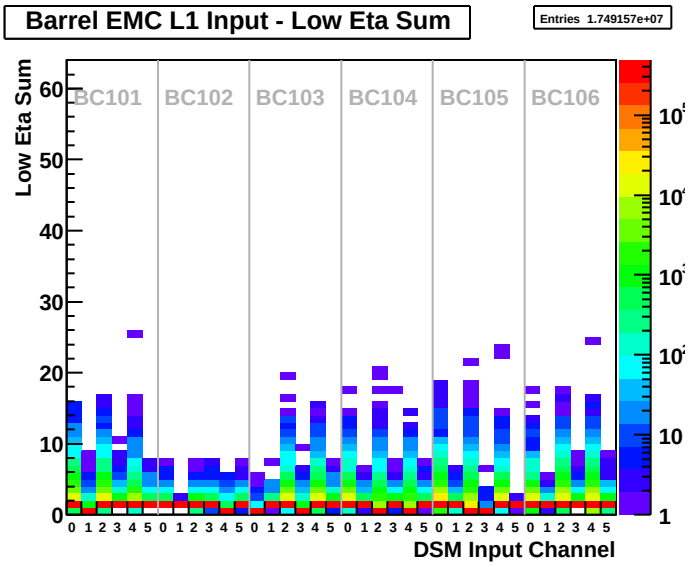

Endcap EMC L0 Input - High Tower
Entries 4.372893e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

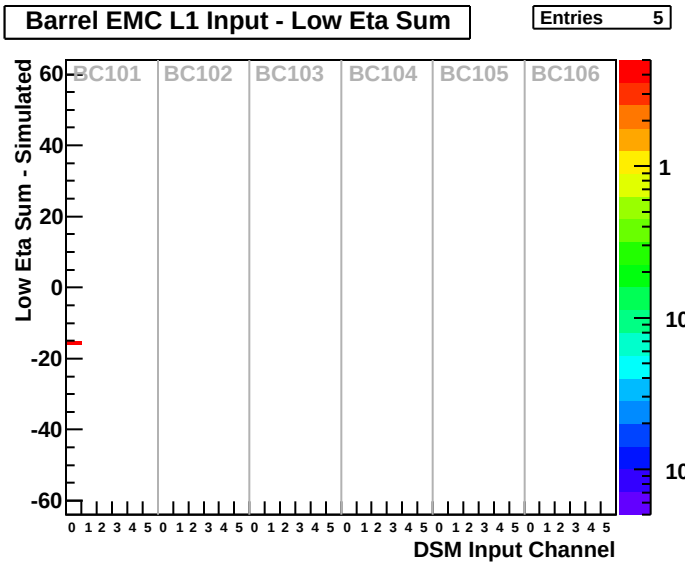

Endcap EMC L0 Input - Patch Sum
Entries 4.372893e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

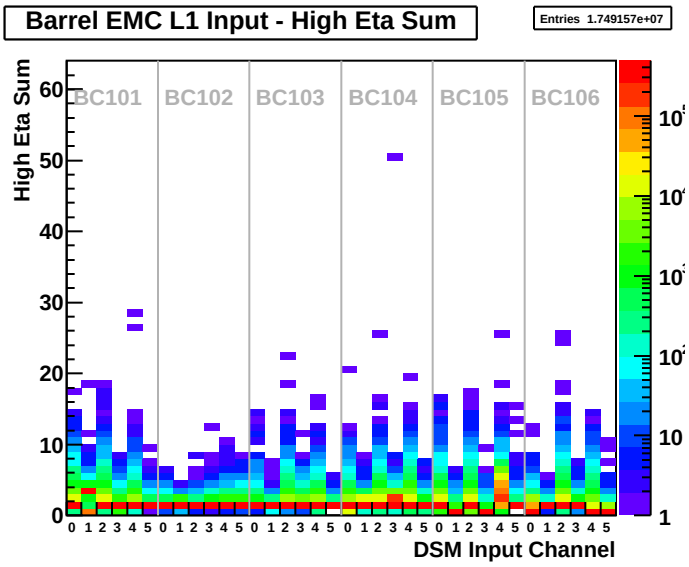

Barrel Jet Patches
Entries 8745786
JP ADC
JP ID

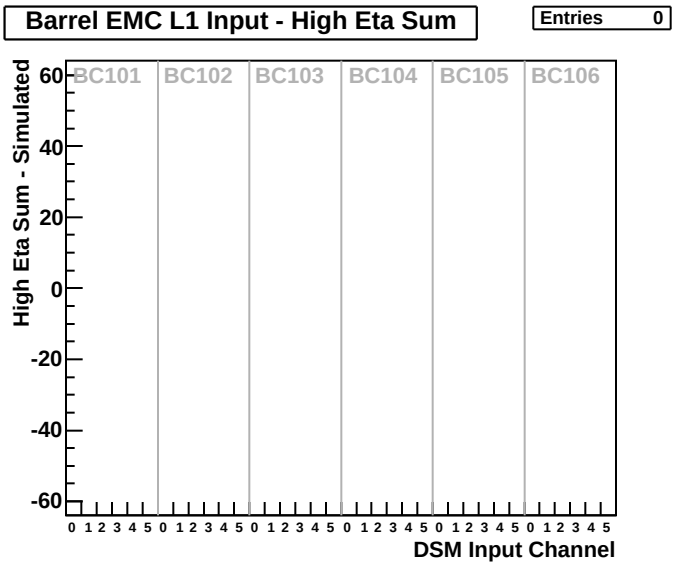

Endcap Jet Patches
Entries 2915262
JP ADC
JP ID

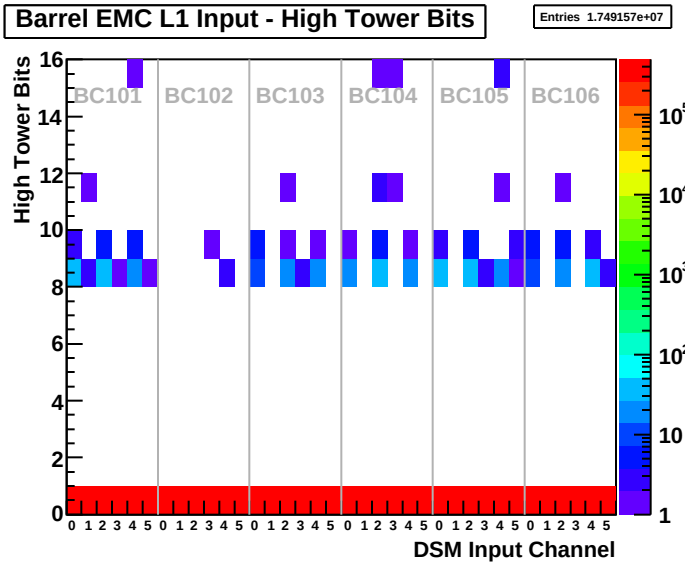

Hybrid Jet Patches
Entries 971754
JP ADC
JP ID

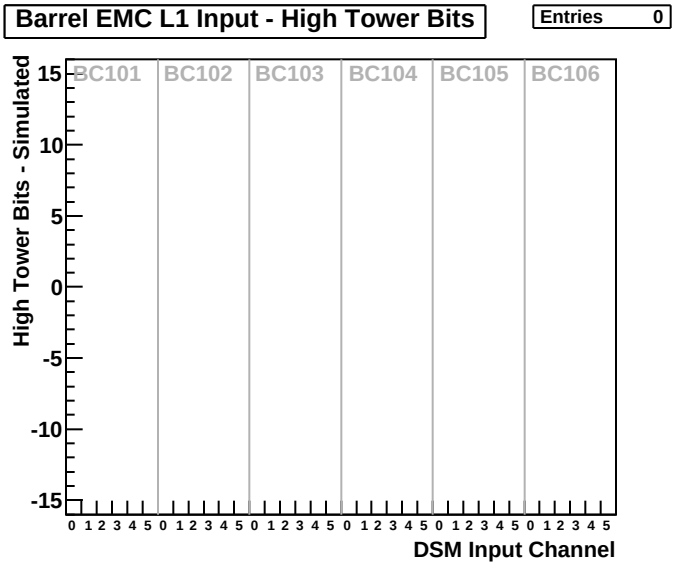

Barrel EMC L1 Input - Low Eta Sum
Entries 1.749157e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

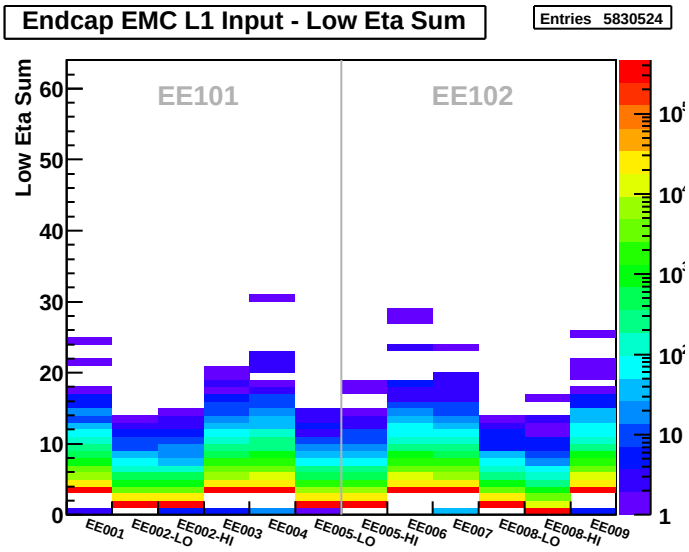

Barrel EMC L1 Input - Low Eta Sum
Entries 5
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

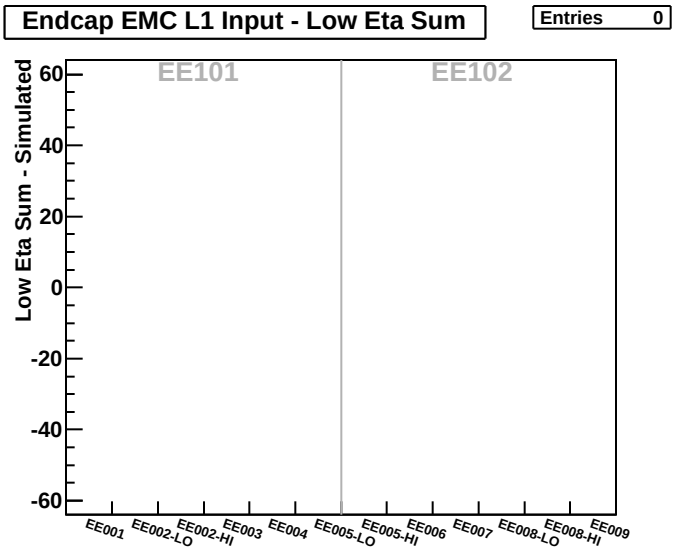

Barrel EMC L1 Input - High Eta Sum
Entries 1.749157e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

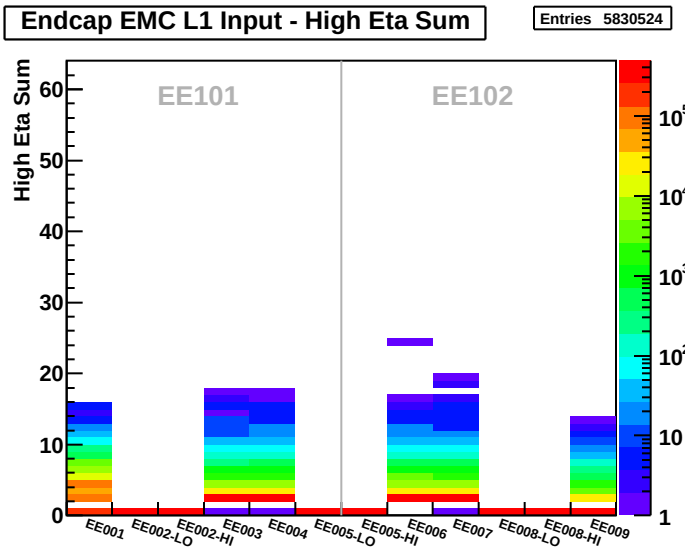

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

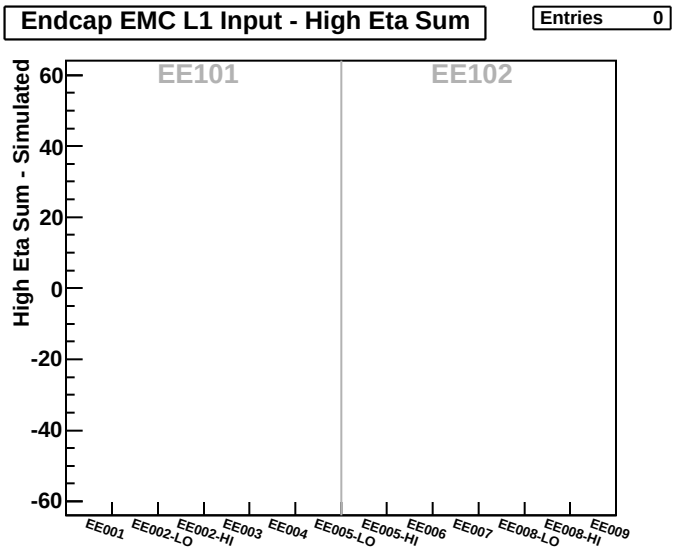

Barrel EMC L1 Input - High Tower Bits
Entries 1.749157e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

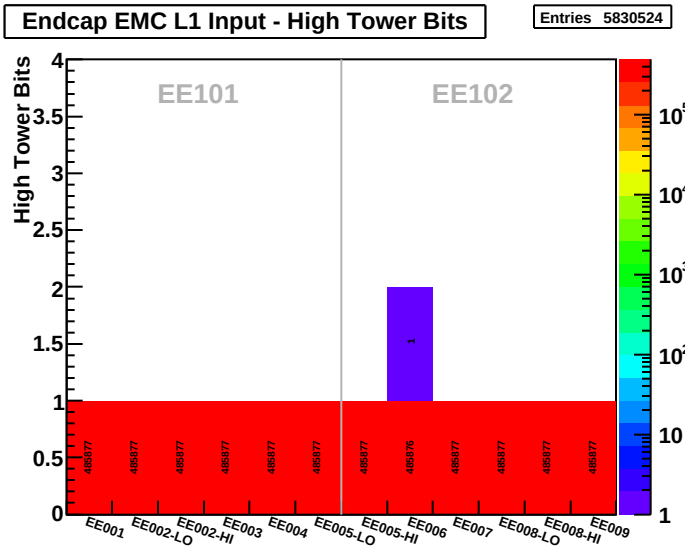

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

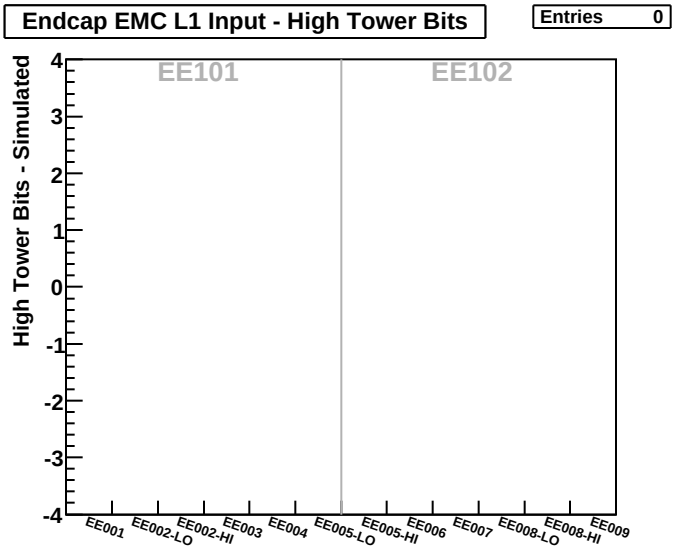

Endcap EMC L1 Input - Low Eta Sum
Entries 5830524
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

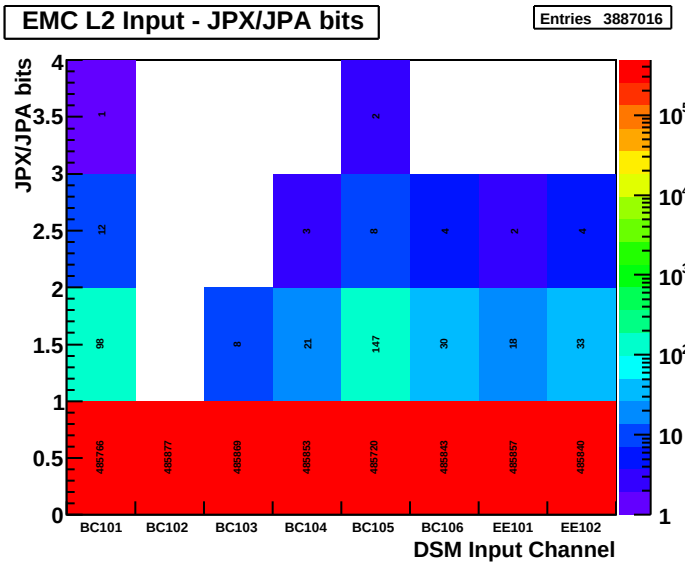

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

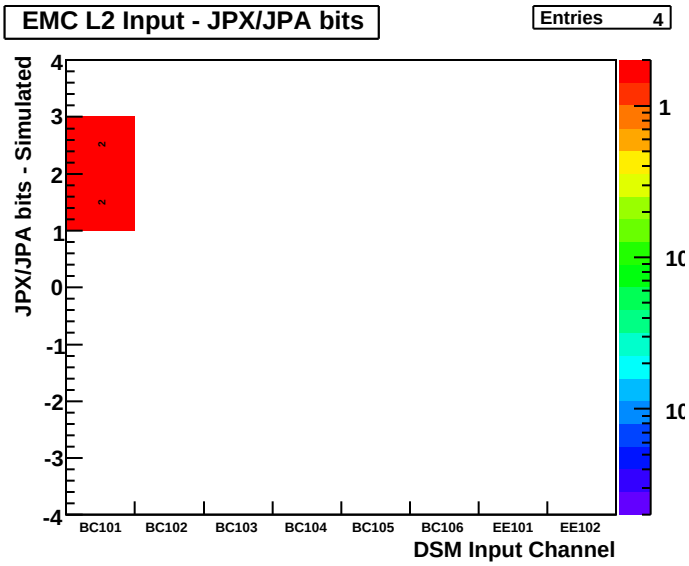

Endcap EMC L1 Input - High Eta Sum
Entries 5830524
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

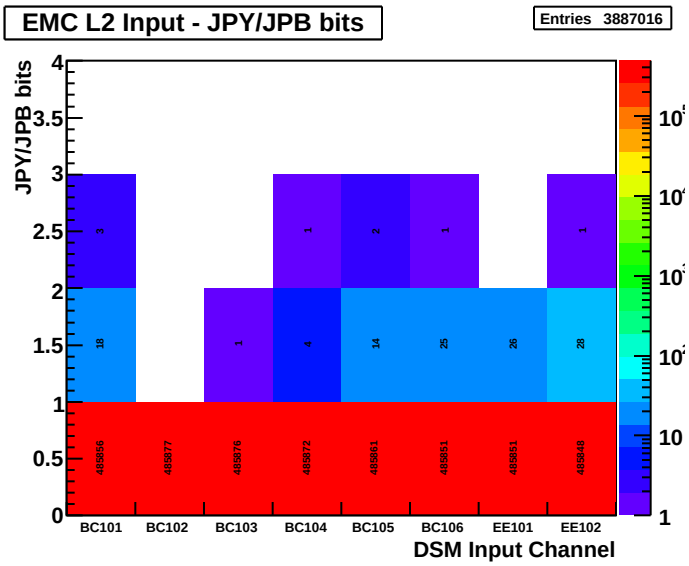

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

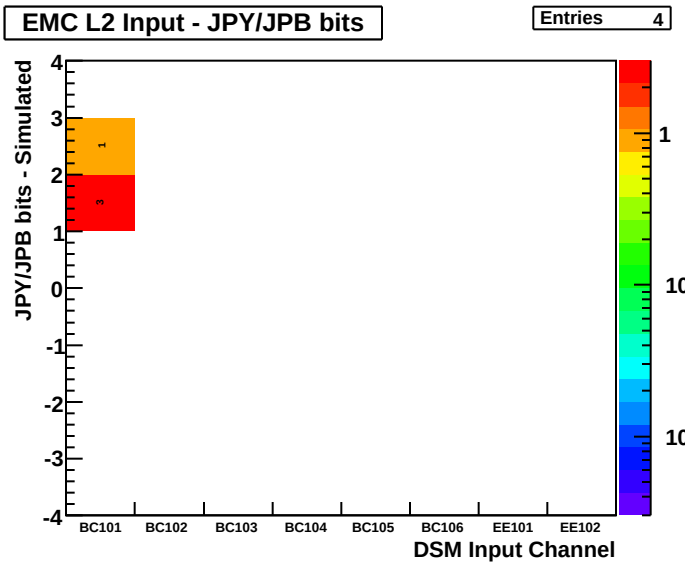

Endcap EMC L1 Input - High Tower Bits
Entries 5830524
High Tower Bits
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

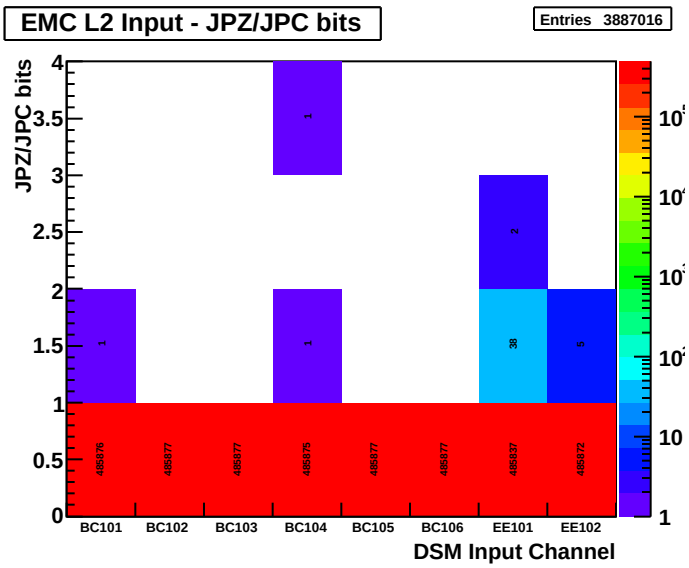

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

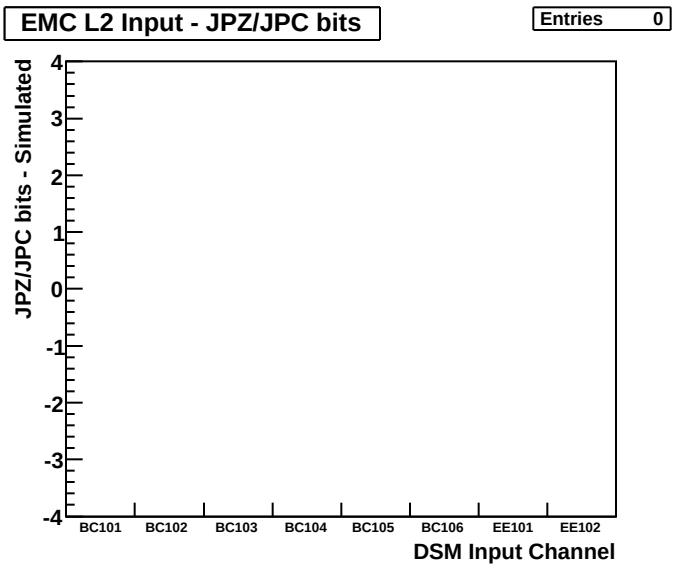

EMC L2 Input - JPX/JPA bits
Entries 3887016
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

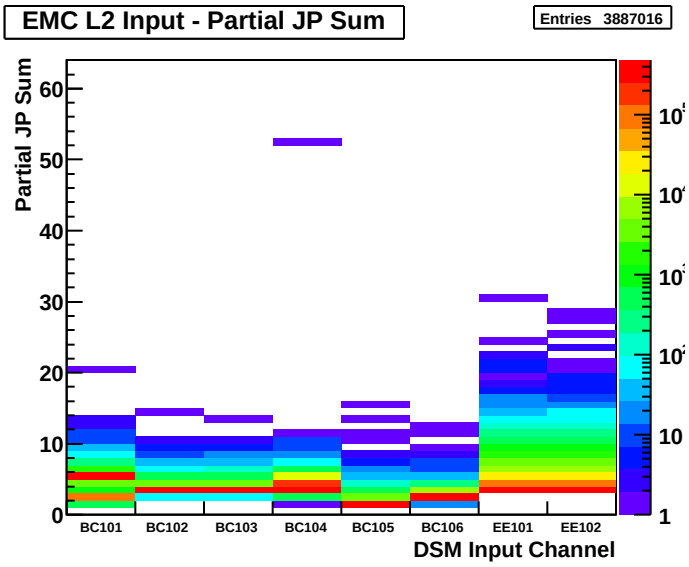

EMC L2 Input - JPX/JPA bits
Entries 4
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

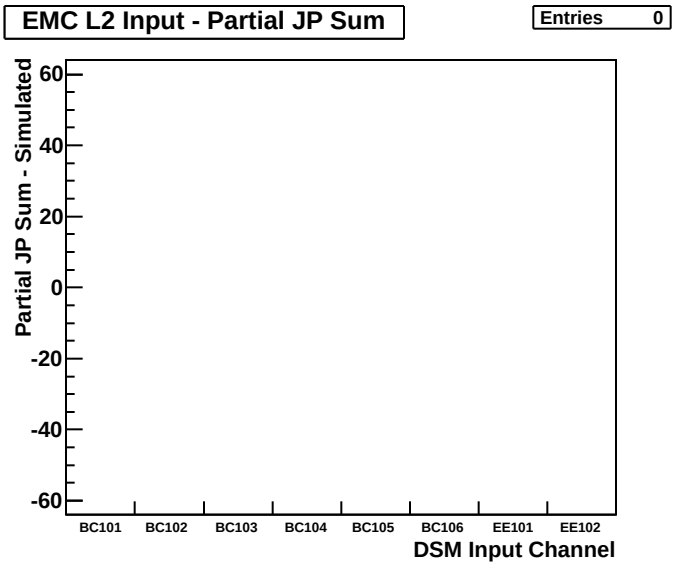

EMC L2 Input - JPY/JPB bits
Entries 3887016
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

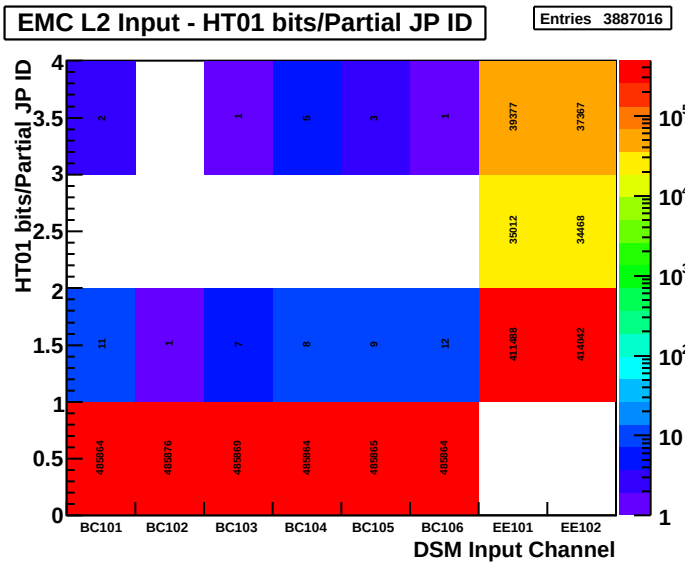

EMC L2 Input - JPY/JPB bits
Entries 4
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

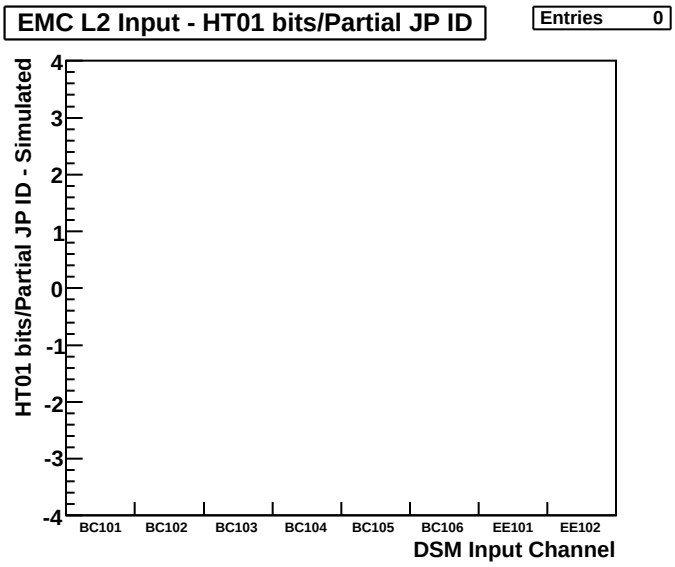

EMC L2 Input - JPZ/JPC bits
Entries 3887016
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

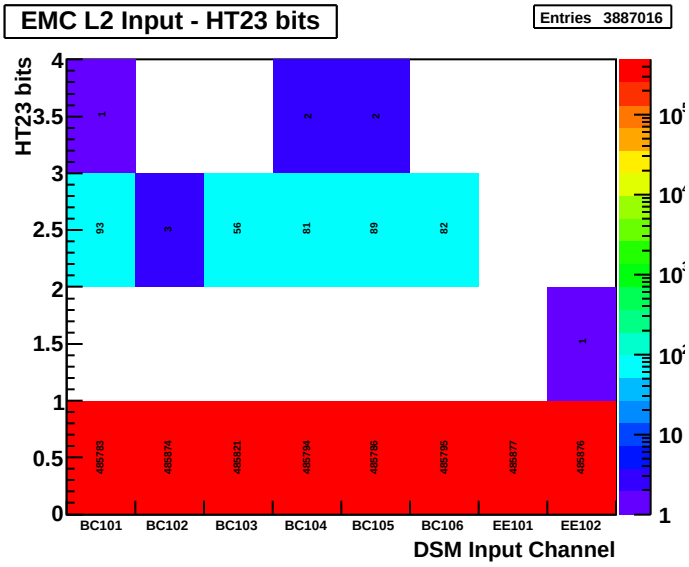

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

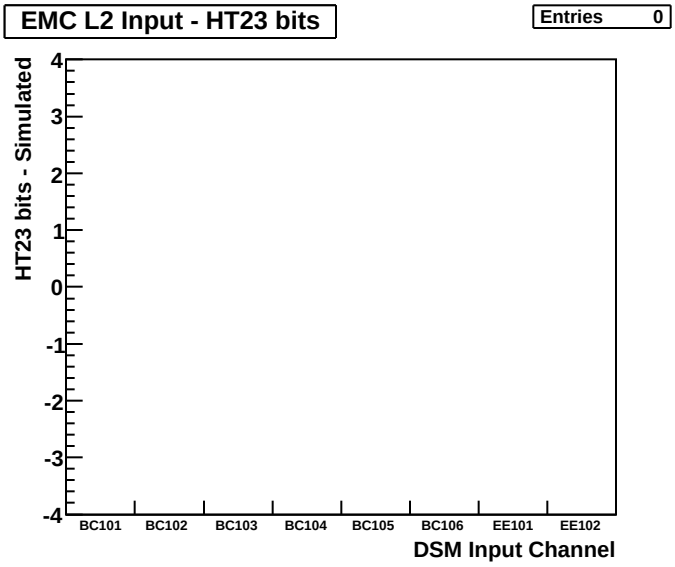

EMC L2 Input - Partial JP Sum
Entries 3887016
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

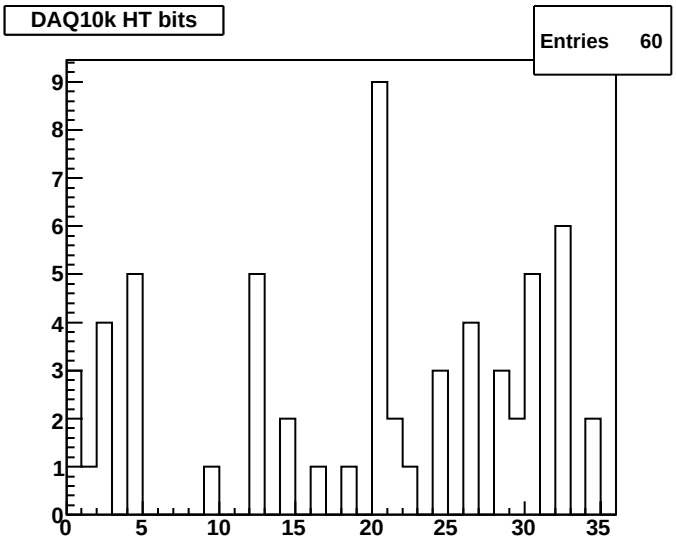

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

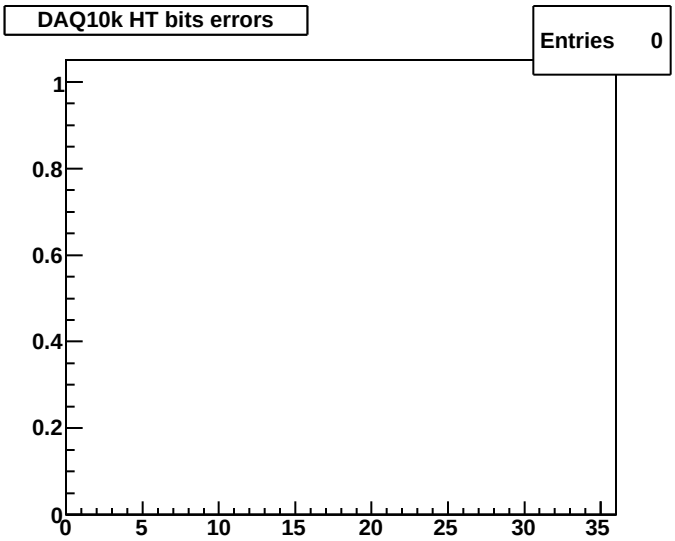

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3887016
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

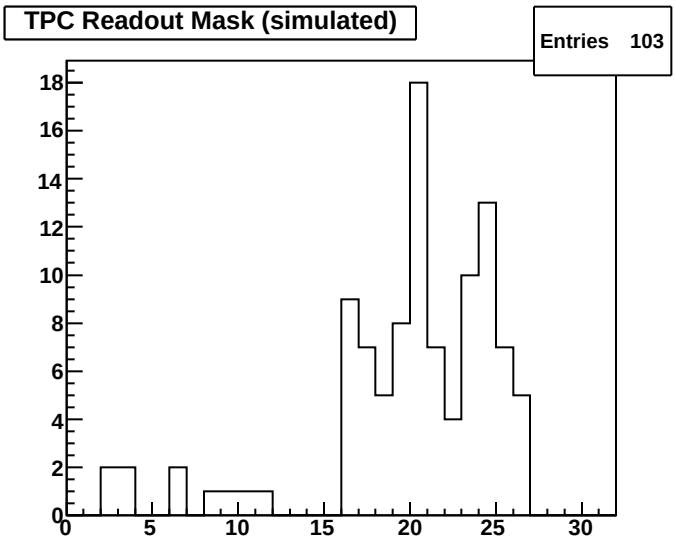

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

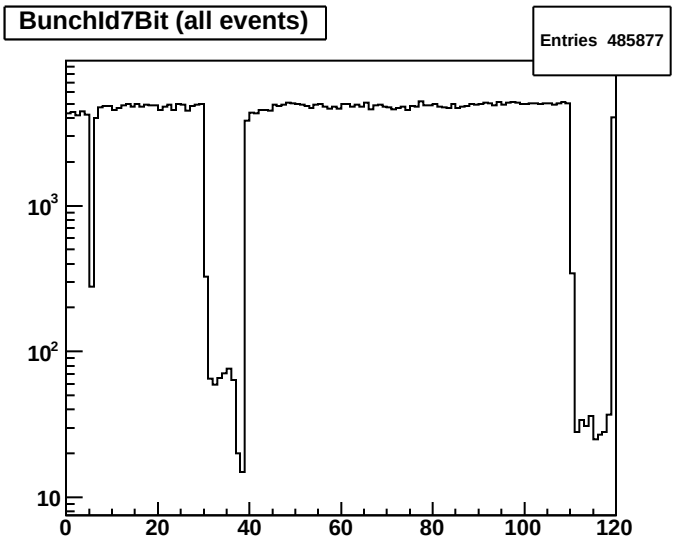

EMC L2 Input - HT23 bits
Entries 3887016
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

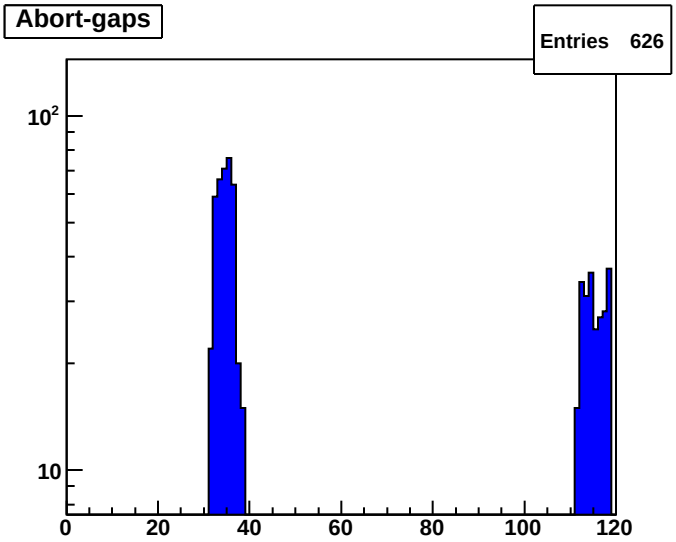

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

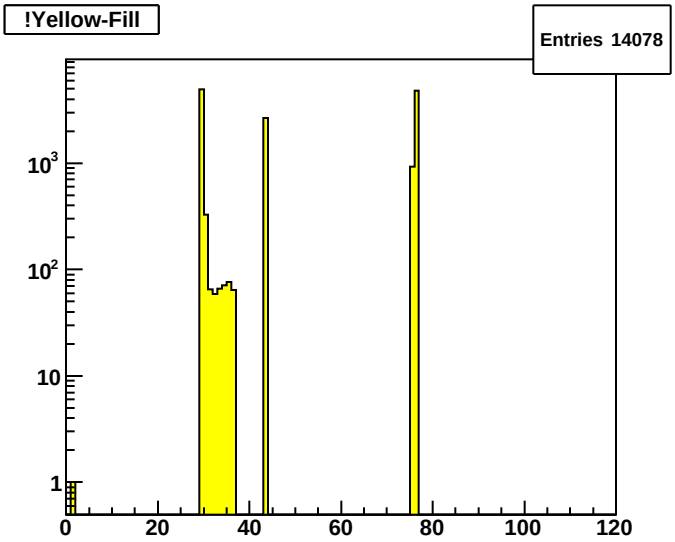

DAQ10k HT bits
Entries 60

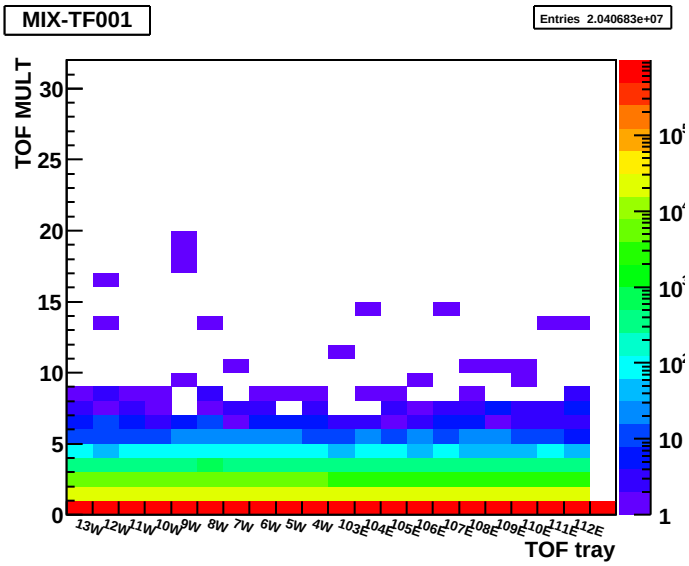

DAQ10k HT bits errors
Entries 0

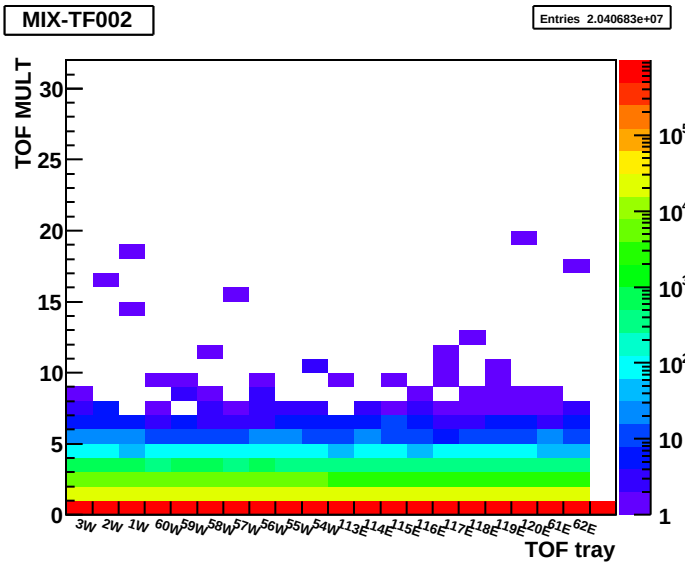

TPC Readout Mask (simulated)
Entries 103

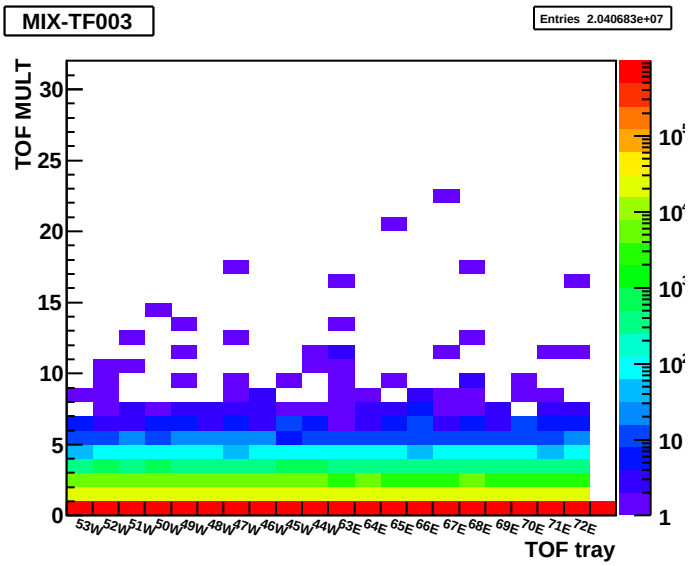

BunchId7Bit (all events)
Entries 485877

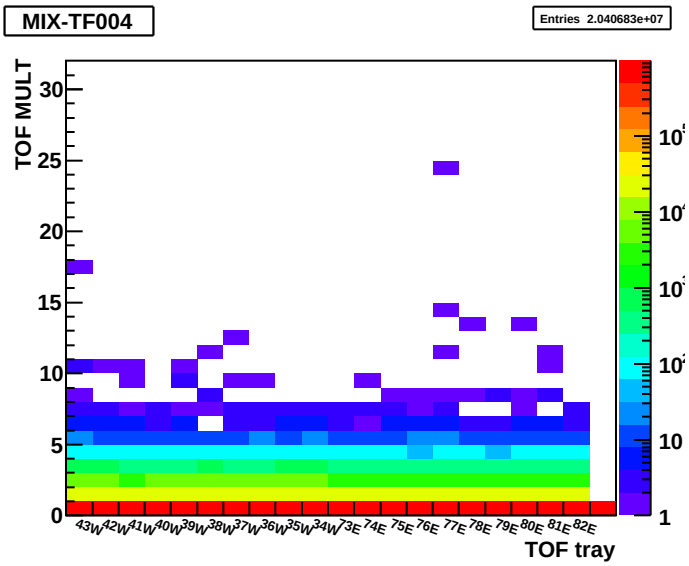

Abort-gaps
Entries 626

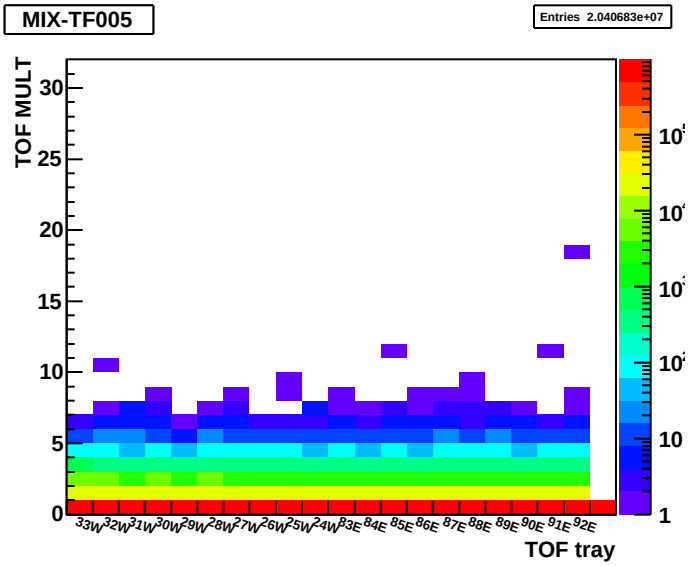

!Yellow-Fill
Entries 14078

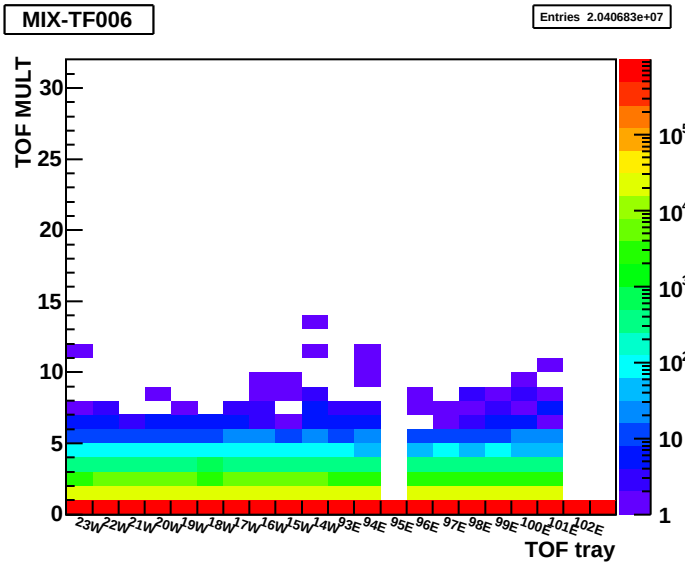

MIX-TF001
Entries 2.040683e+07
TOF MULT
TOF tray

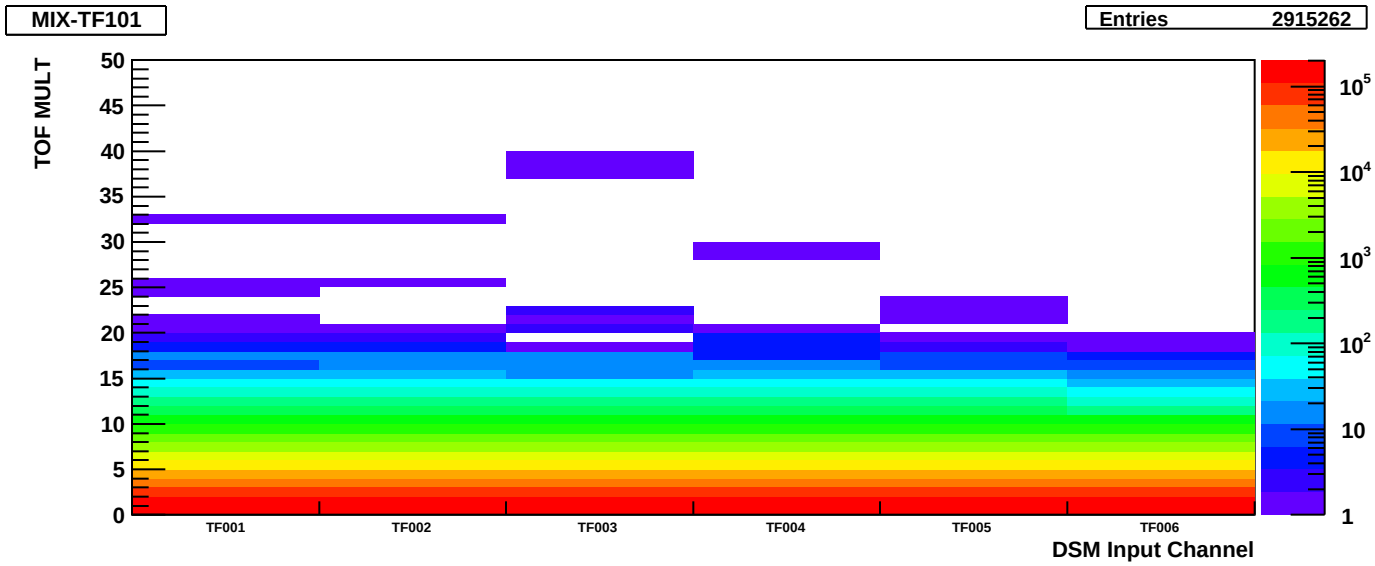

MIX-TF002
Entries 2.040683e+07
TOF MULT
TOF tray

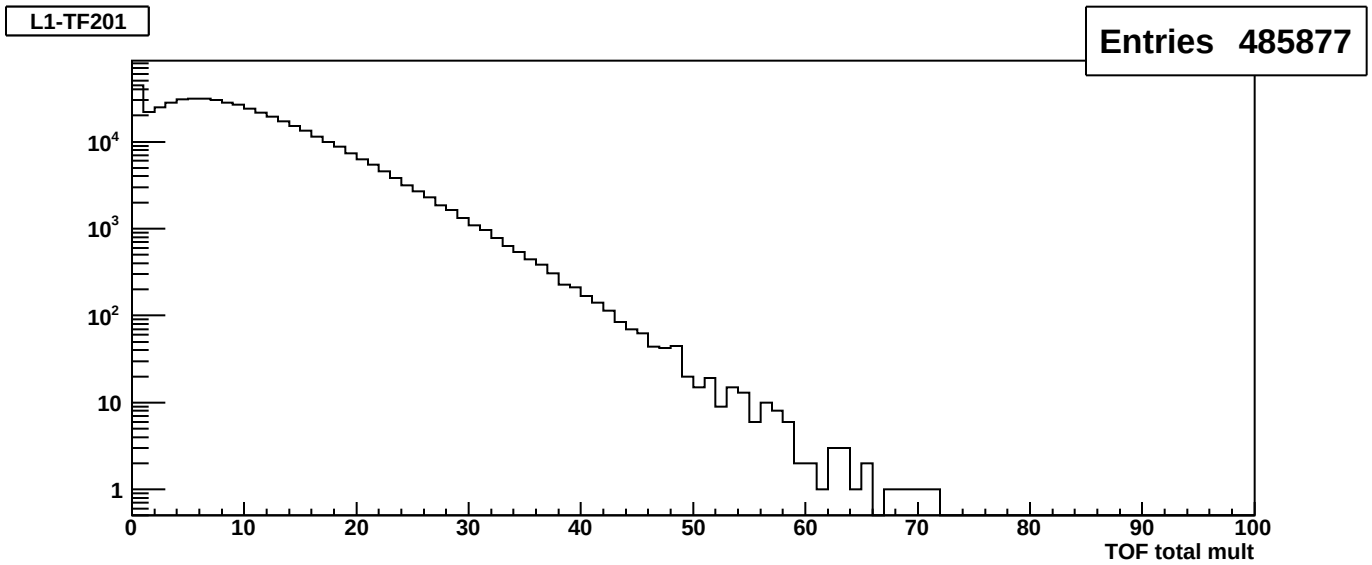

MIX-TF003
Entries 2.040683e+07
TOF MULT
TOF tray

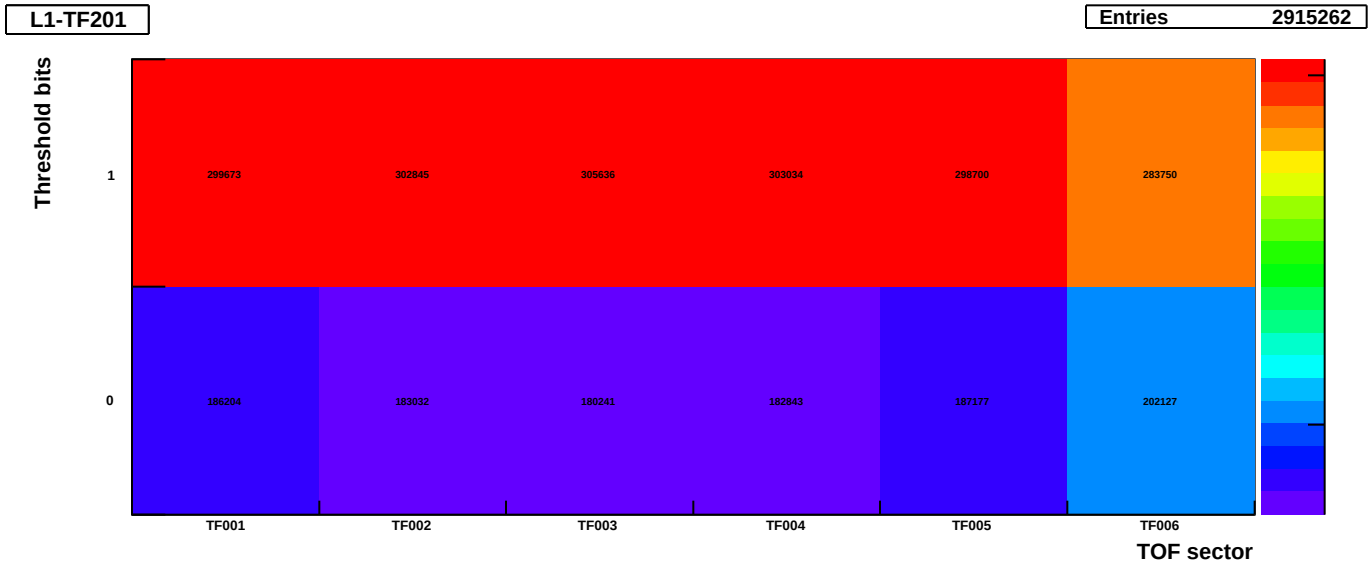

MIX-TF004
Entries 2.040683e+07
TOF MULT
TOF tray

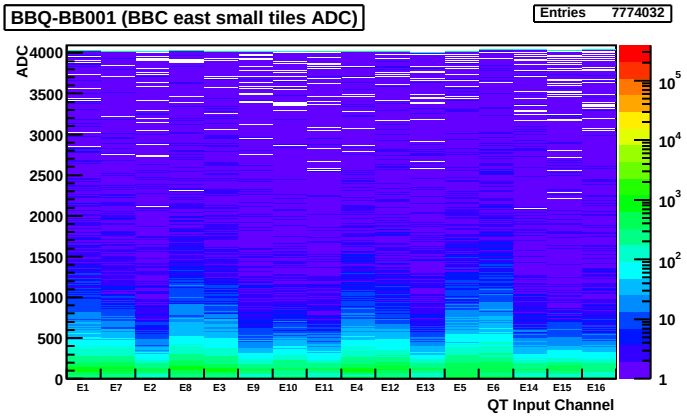

MIX-TF005
Entries 2.040683e+07
TOF MULT
TOF tray

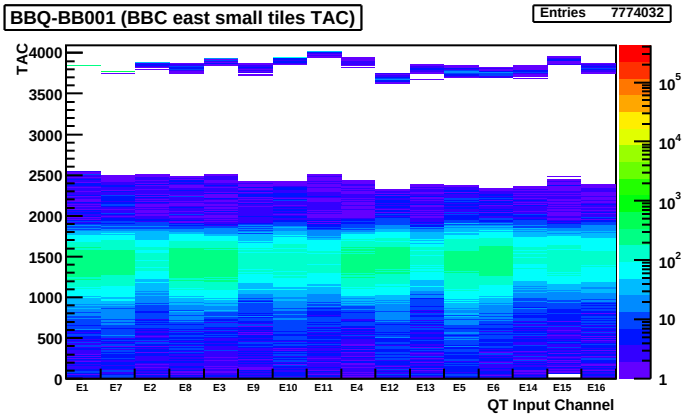

MIX-TF006
Entries 2.040683e+07
TOF MULT
TOF tray

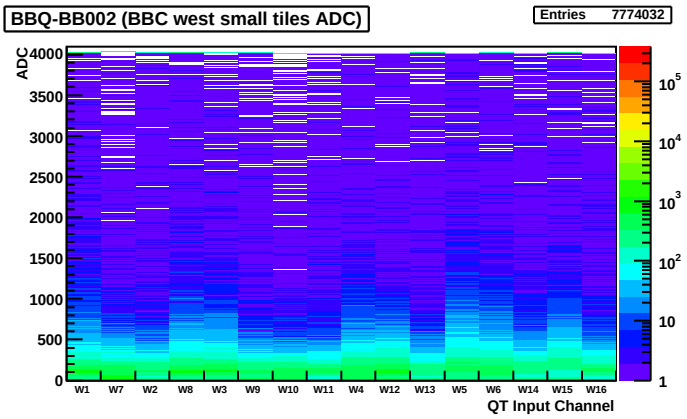

MIX-TF101
Entries 2915262
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

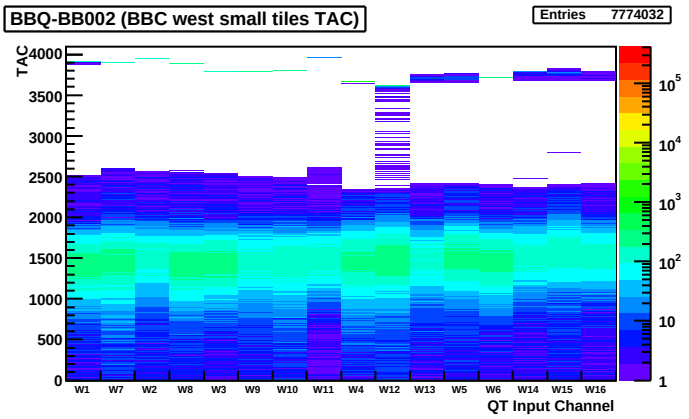

L1-TF201
Entries 485877
TOF total mult

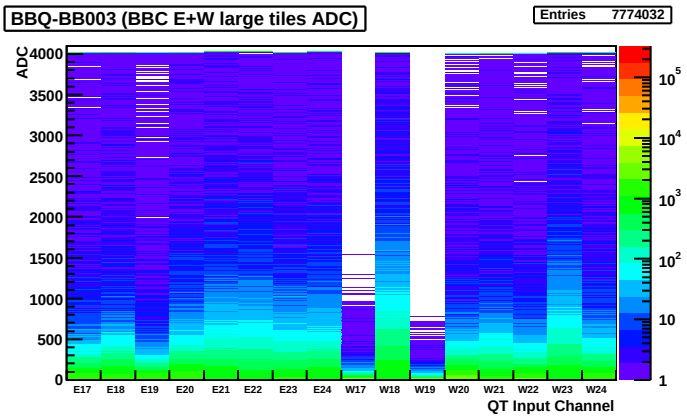

L1-TF201
Entries 2915262
Threshold bits
299673
302845
305636
303034
298700
283750
186204
183032
180241
182843
187177
202127
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

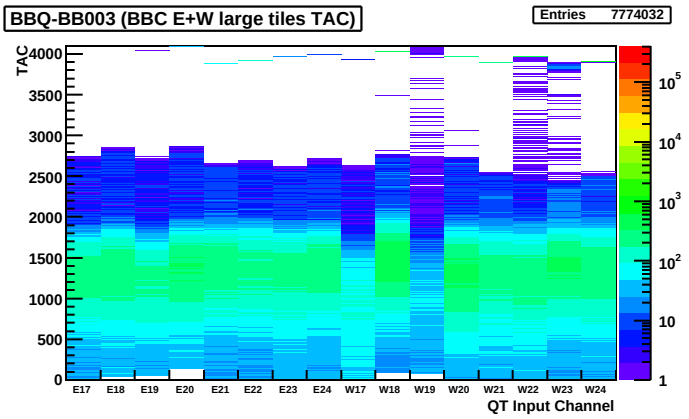

BBQ-BB001 (BBC east small tiles ADC)
Entries 7774032
ADC
QT Input Channel

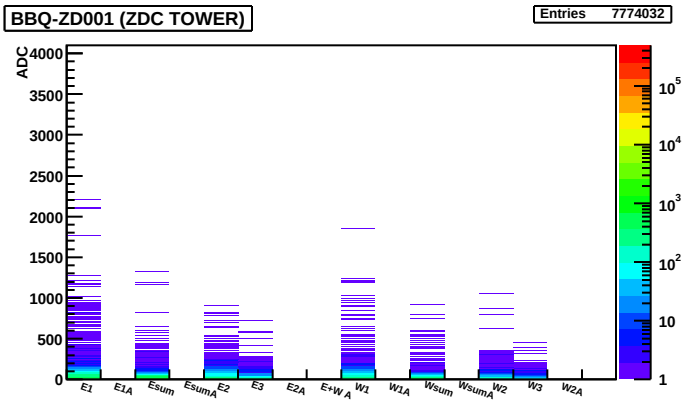

BBQ-BB001 (BBC east small tiles TAC)
Entries 7774032
TAC
QT Input Channel

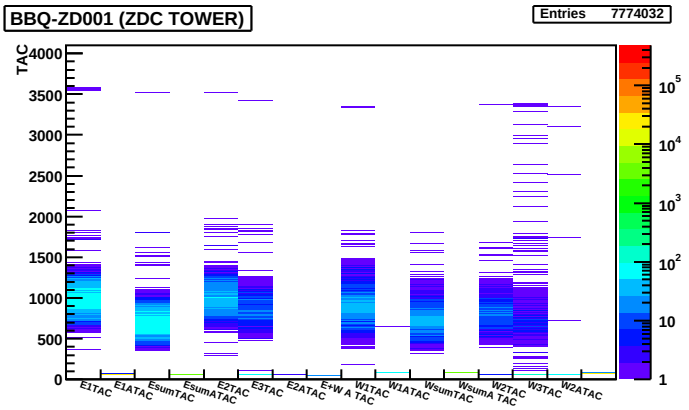

BBQ-BB002 (BBC west small tiles ADC)
Entries 7774032
ADC
QT Input Channel

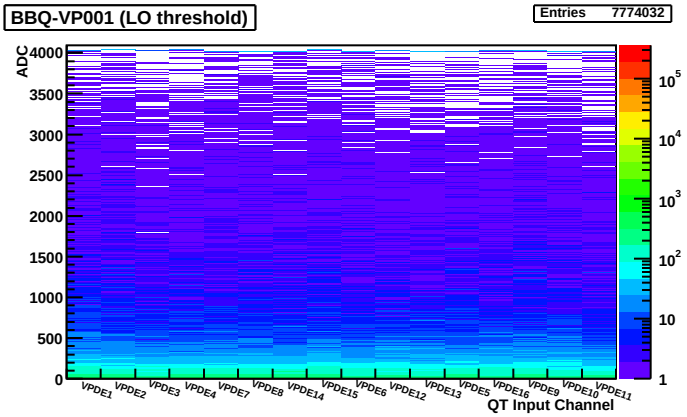

BBQ-BB002 (BBC west small tiles TAC)
Entries 7774032
TAC
QT Input Channel

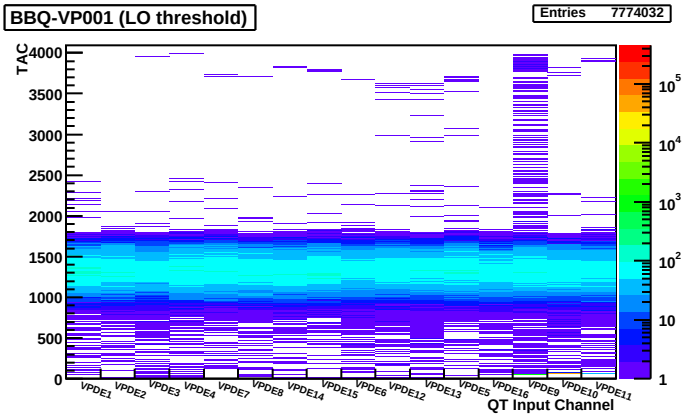

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7774032
ADC
QT Input Channel

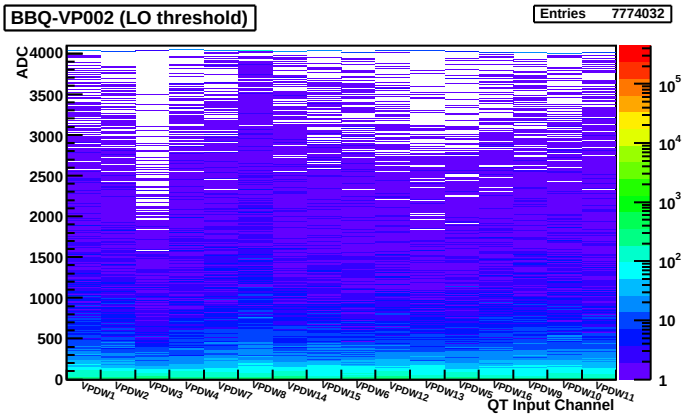

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7774032
TAC
QT Input Channel

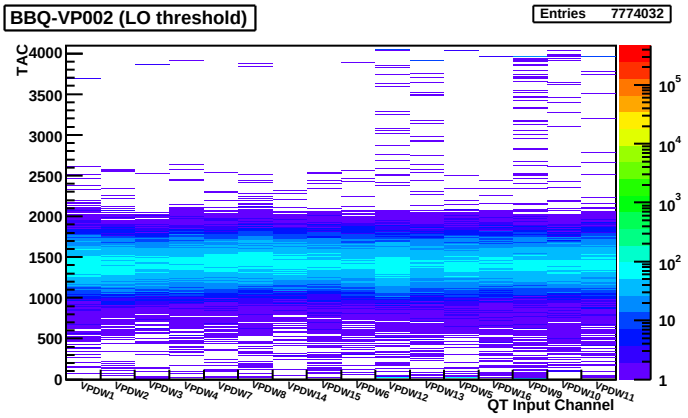

BBQ-ZD001 (ZDC TOWER)
Entries 7774032
ADC

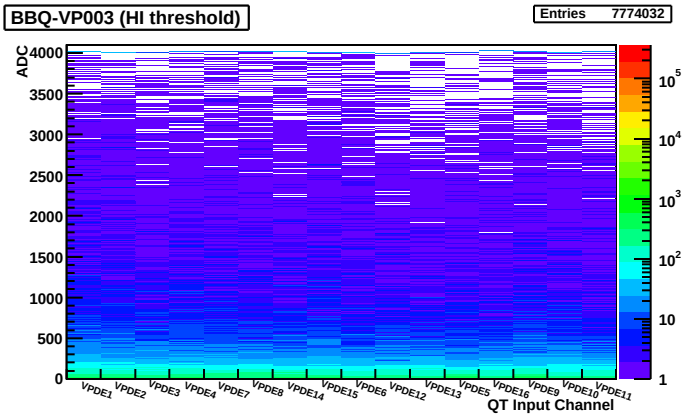

BBQ-ZD001 (ZDC TOWER)
Entries 7774032
TAC

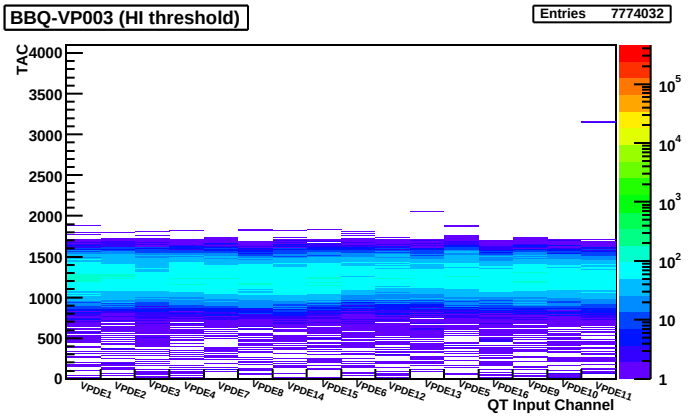

BBQ-VP001 (LO threshold)
Entries 7774032
ADC
QT Input Channel

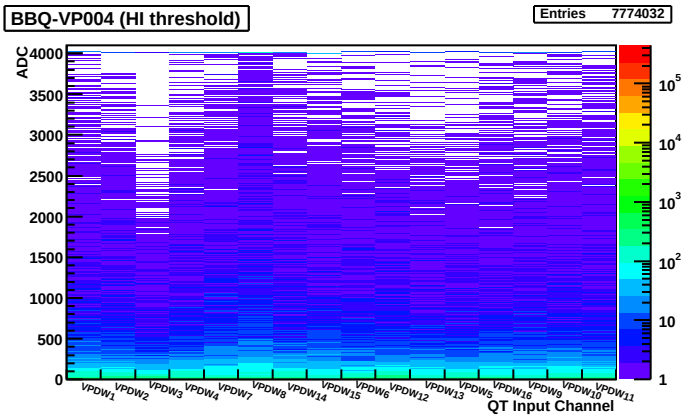

BBQ-VP001 (LO threshold)
Entries 7774032
TAC
QT Input Channel

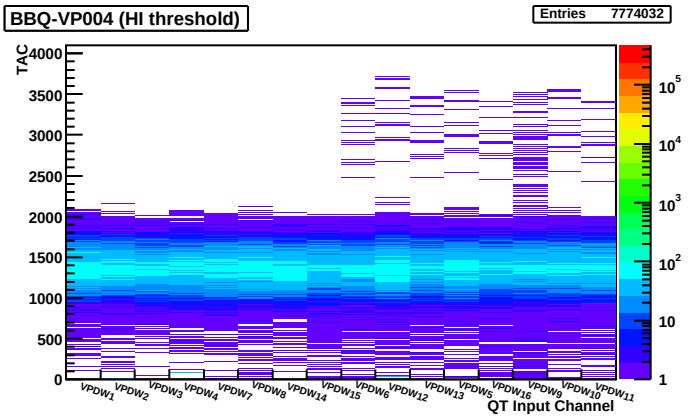

BBQ-VP002 (LO threshold)
Entries 7774032
ADC
QT Input Channel

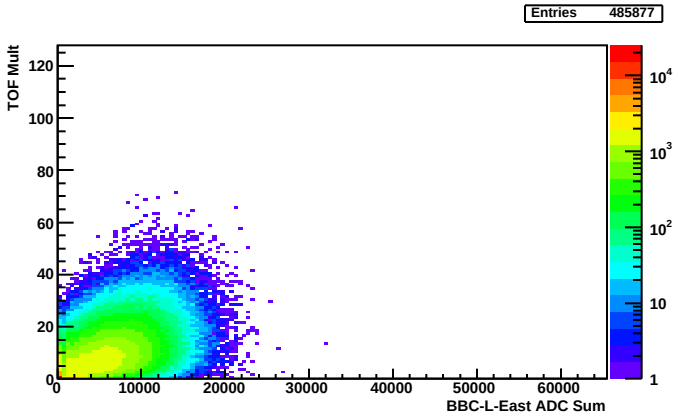

BBQ-VP002 (LO threshold)
Entries 7774032
TAC
QT Input Channel

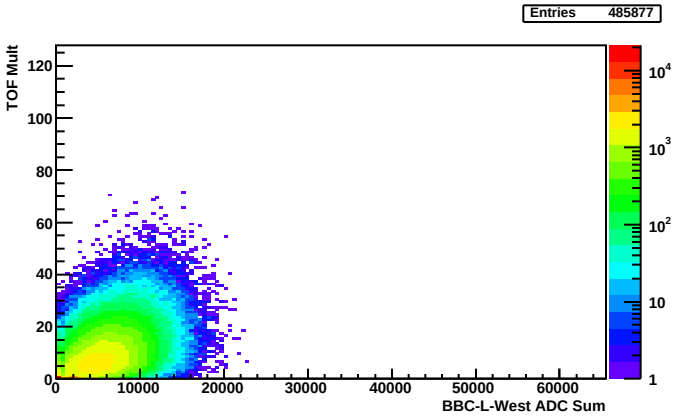

BBQ-VP003 (HI threshold)
Entries 7774032
ADC
QT Input Channel

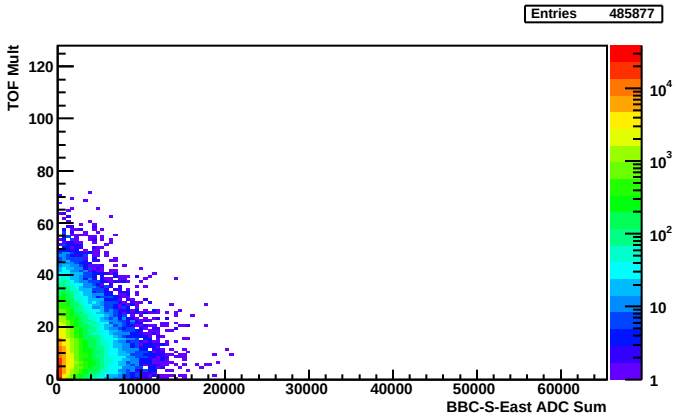

BBQ-VP003 (HI threshold)
Entries 7774032
TAC
QT Input Channel

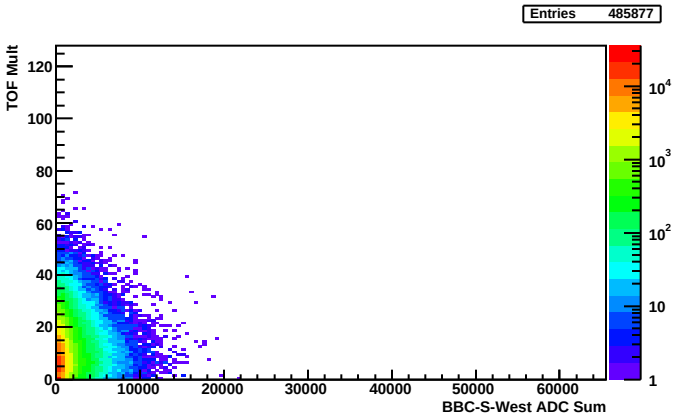

BBQ-VP004 (HI threshold)
Entries 7774032
ADC
QT Input Channel

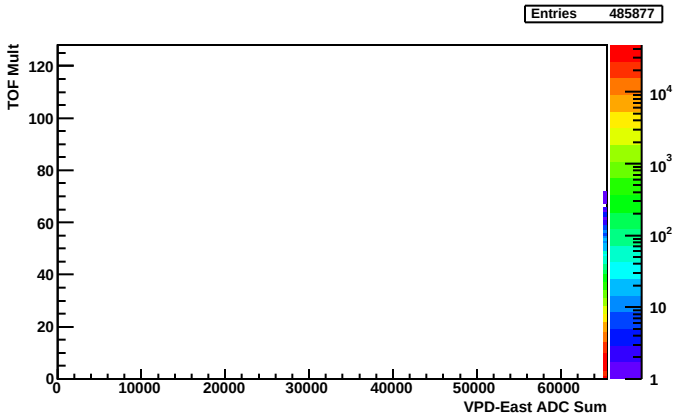

BBQ-VP004 (HI threshold)
Entries 7774032
TAC
QT Input Channel

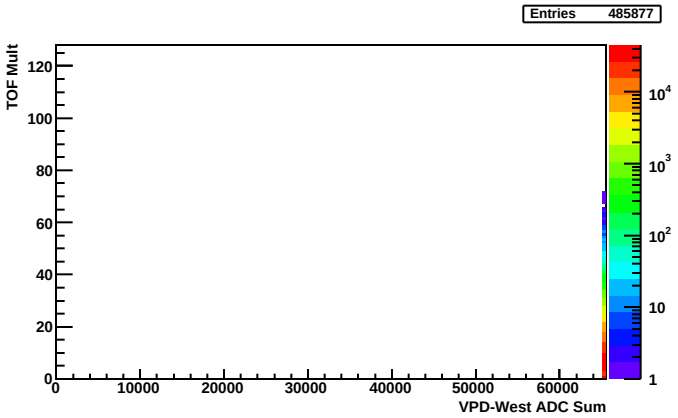

Entries 485877
TOF Mult
BBC-L-East ADC Sum

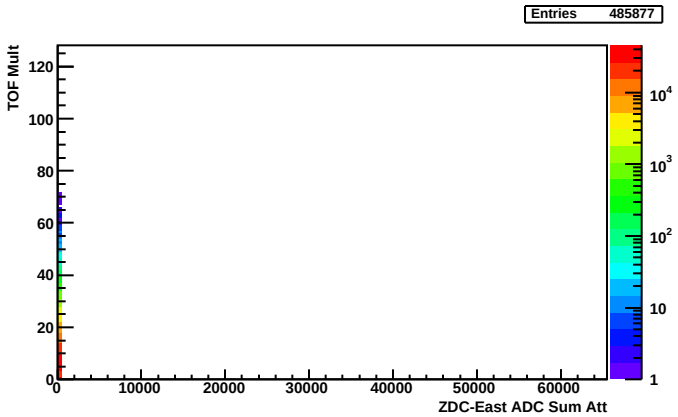

Entries 485877
TOF Mult
BBC-L-West ADC Sum

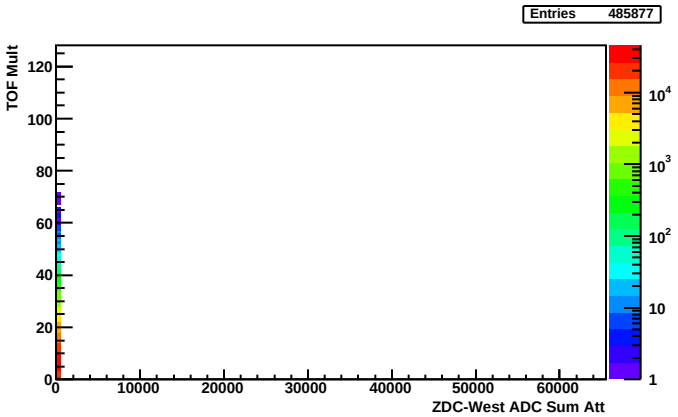

Entries 485877
TOF Mult
BBC-S-East ADC Sum

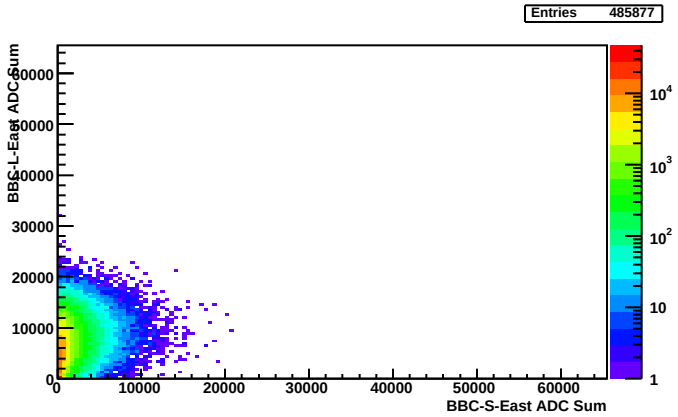

Entries 485877
TOF Mult
BBC-S-West ADC Sum

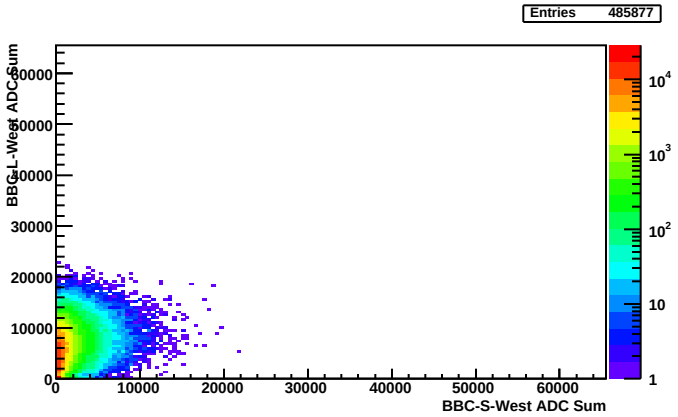

Entries 485877
TOF Mult
VPD-East ADC Sum

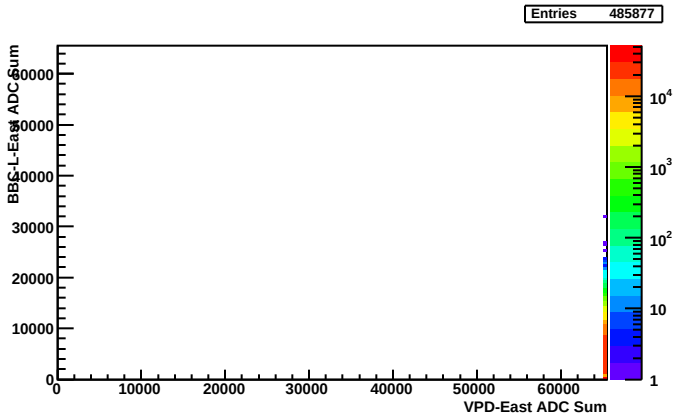

Entries 485877
TOF Mult
VPD-West ADC Sum

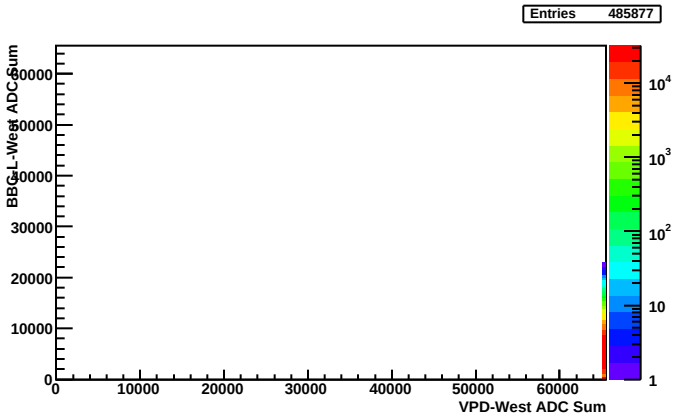

Entries 485877
TOF Mult
ZDC-East ADC Sum Att

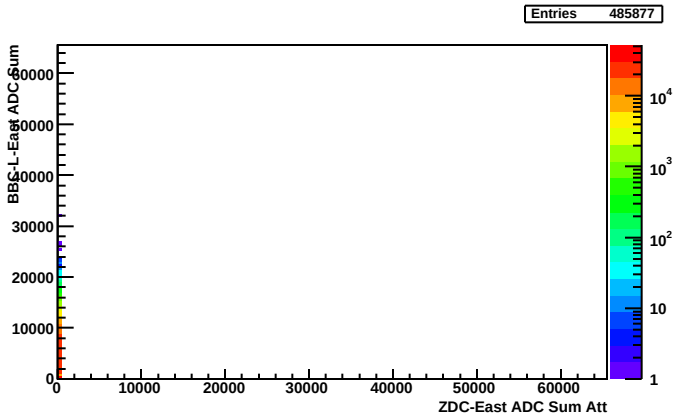

Entries 485877
TOF Mult
ZDC-West ADC Sum Att

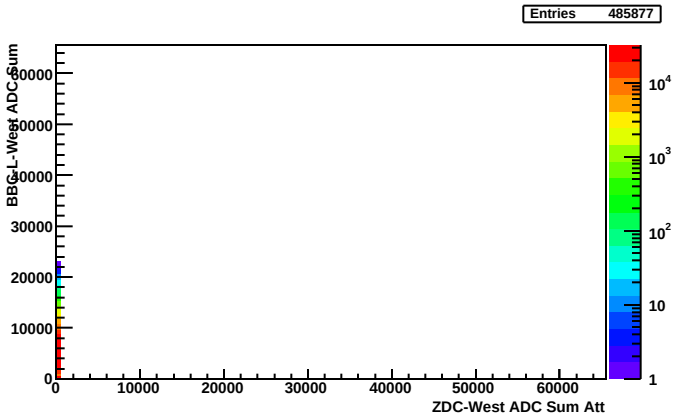

Entries 485877
BBC-L-East ADC Sum
BBC-S-East ADC Sum

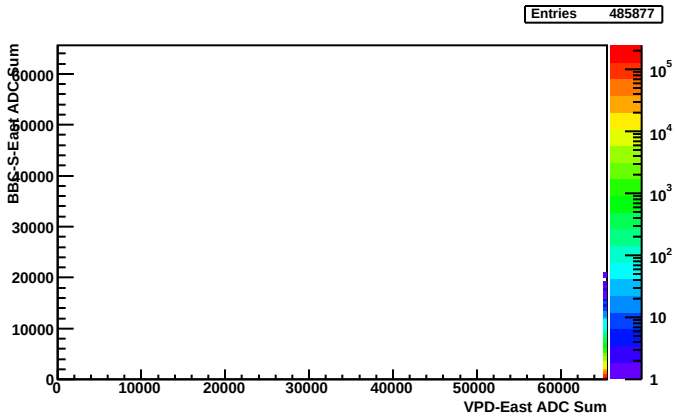

Entries 485877
BBC-L-West ADC Sum
BBC-S-West ADC Sum

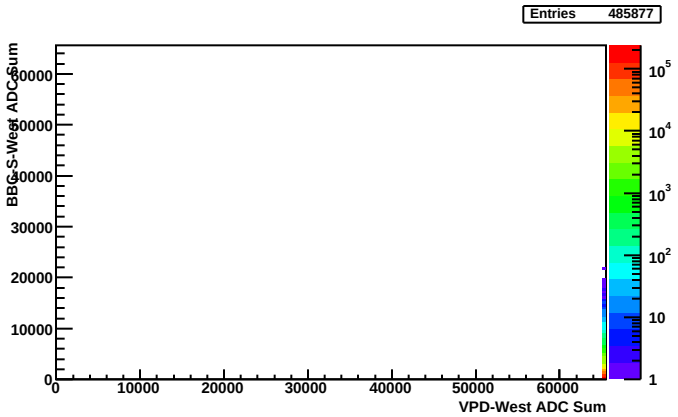

Entries 485877
BBC-L-East ADC Sum
VPD-East ADC Sum

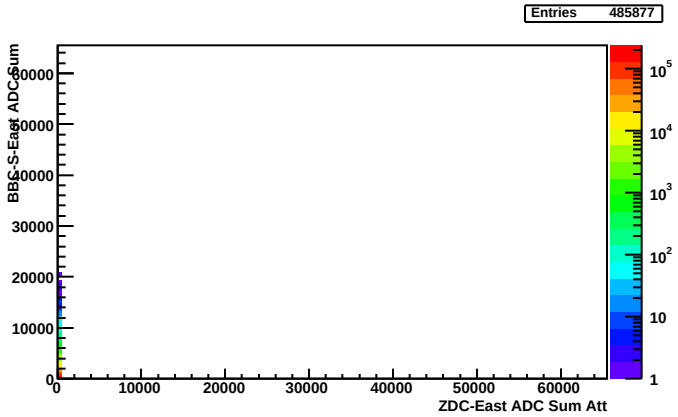

Entries 485877
BBC-L-West ADC Sum
VPD-West ADC Sum

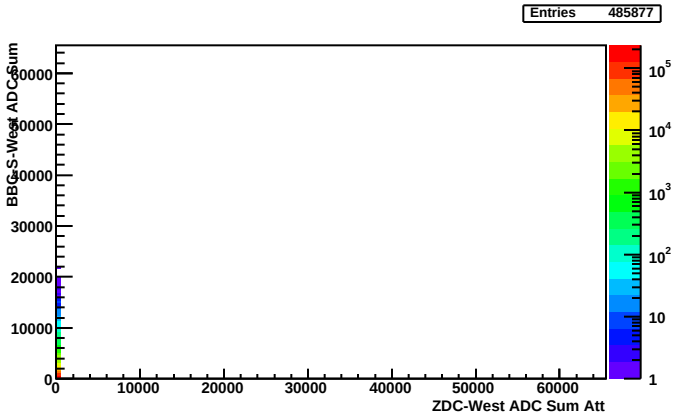

Entries 485877
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

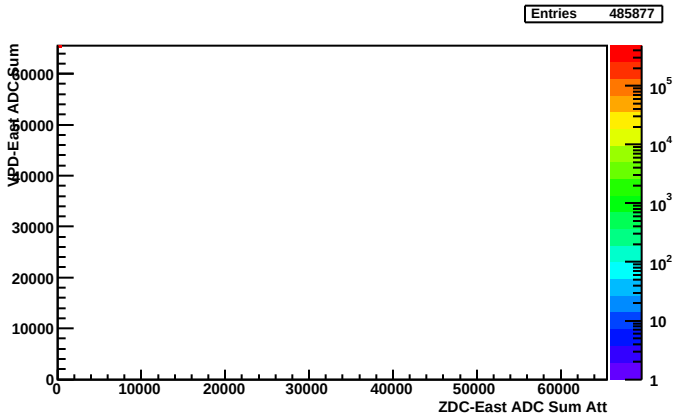

Entries 485877
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

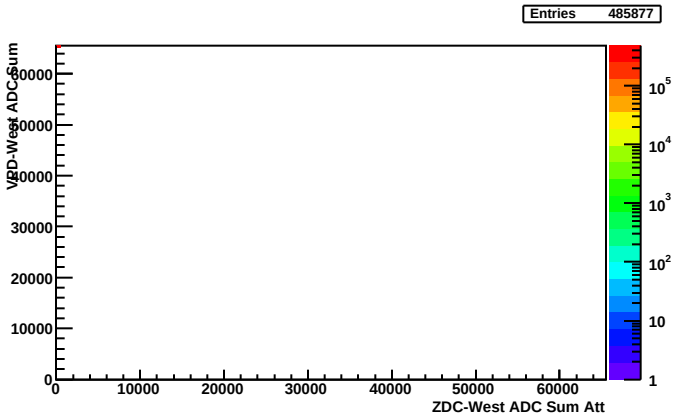

Entries 485877
BBC-S-East ADC Sum
VPD-East ADC Sum

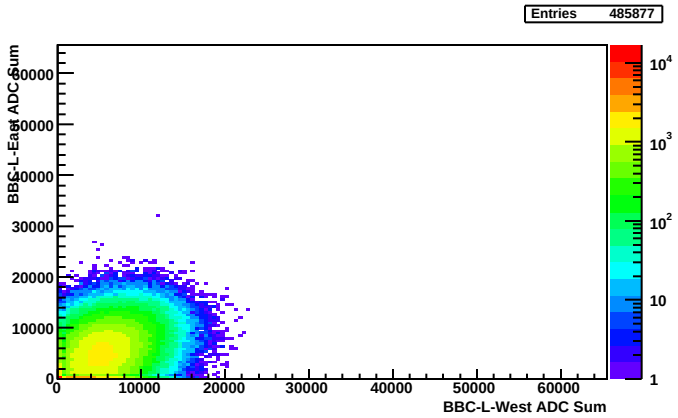

Entries 485877
BBC-S-West ADC Sum
VPD-West ADC Sum

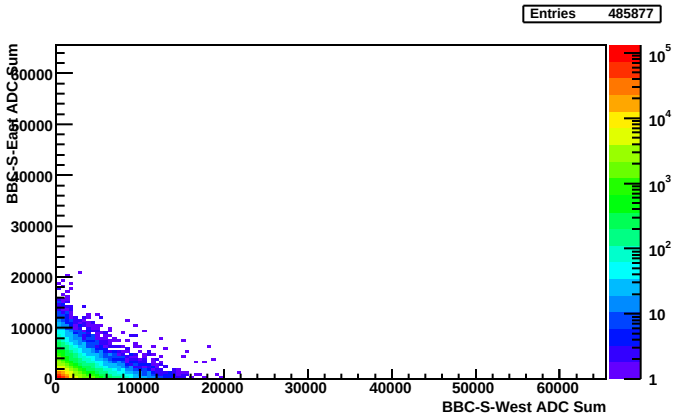

Entries 485877
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 485877
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 485877
VPD-East ADC Sum
ZDC-East ADC Sum Att


Entries 485877
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 485877
BBC-L-East ADC Sum
BBC-L-West ADC Sum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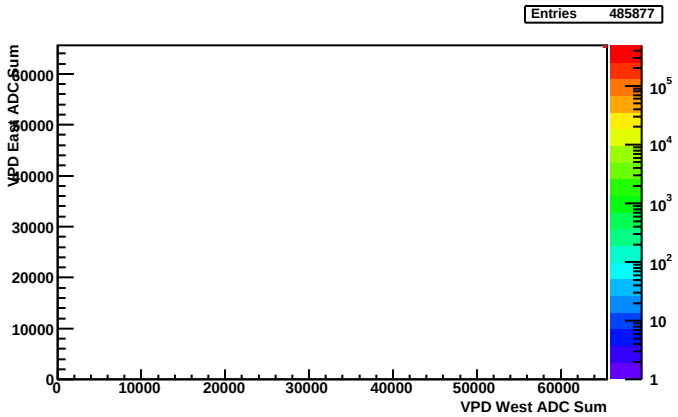
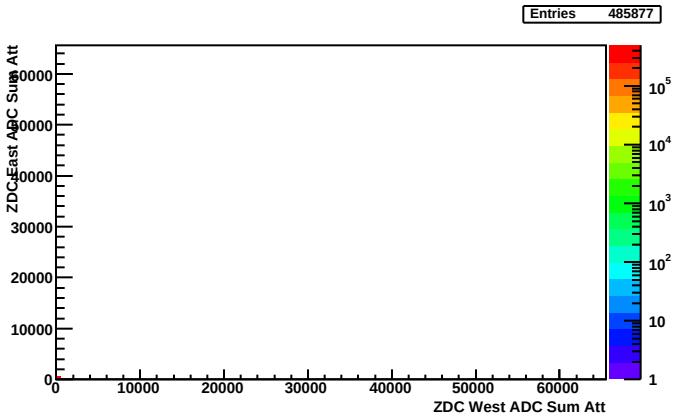
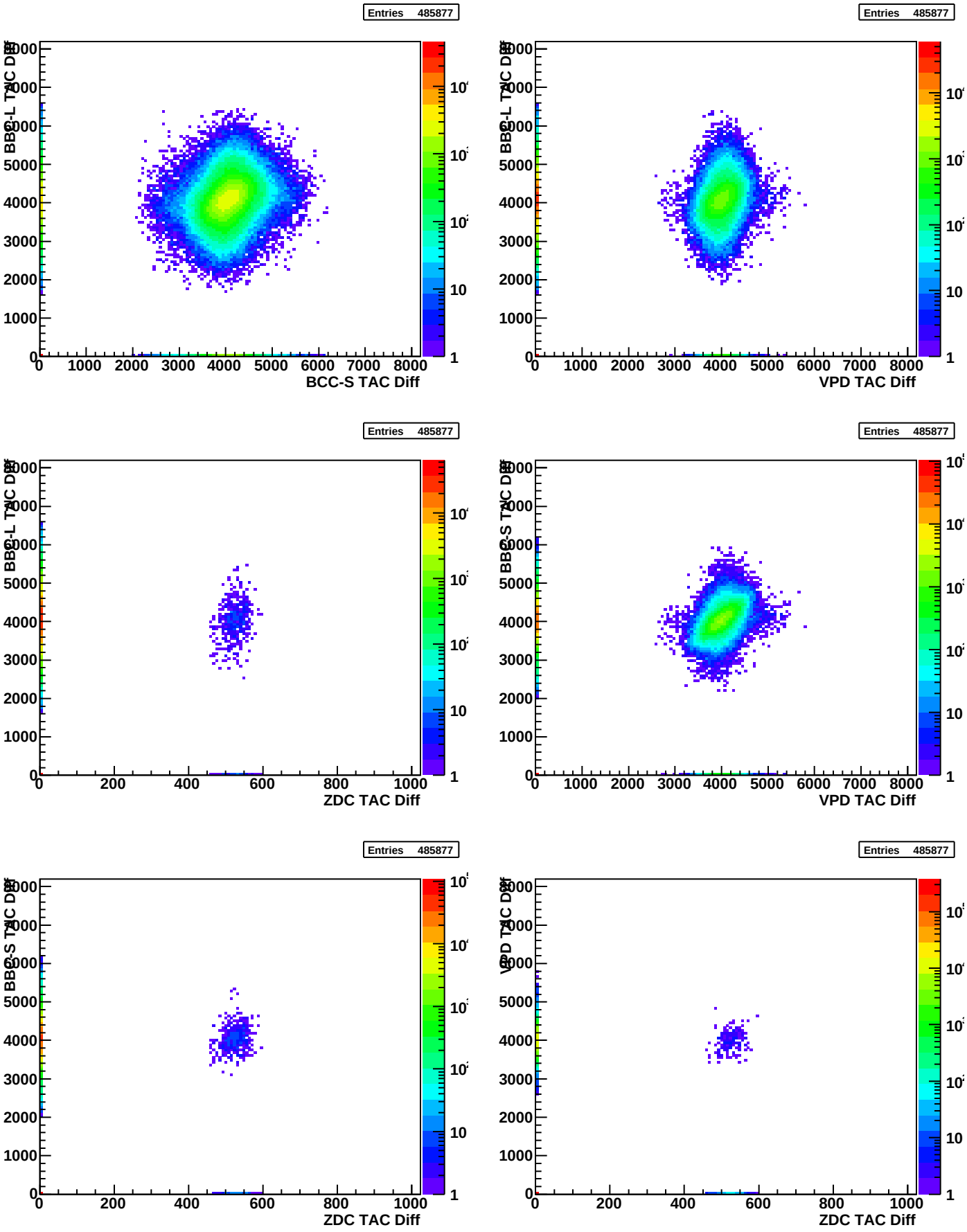
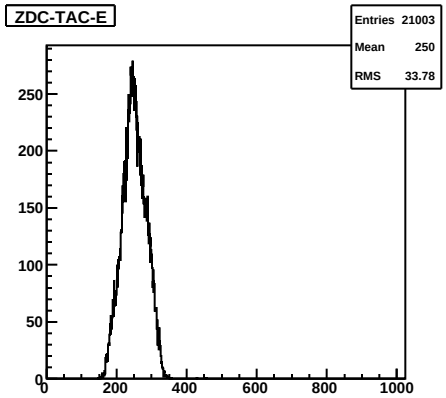
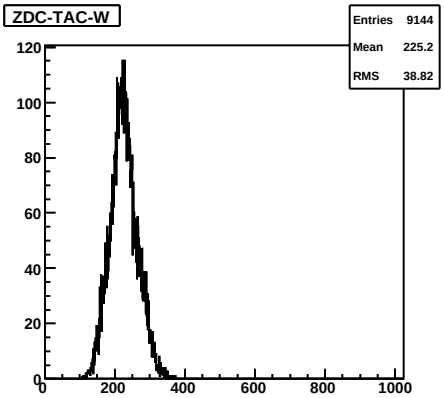
BBC-S-East ADC Sum
BBC-S-West ADC Sum

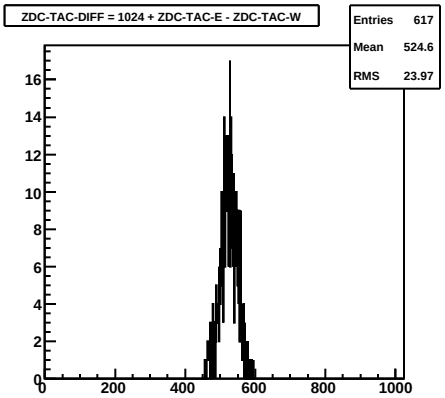

Entries 485877
VPD East ADC Sum
VPD West ADC Sum

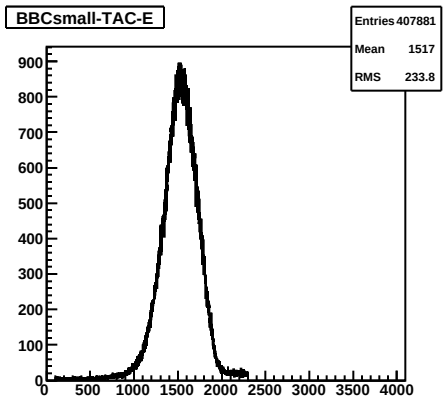

Entries 485877
ZDC East ADC Sum Att
ZDC West ADC Sum Att

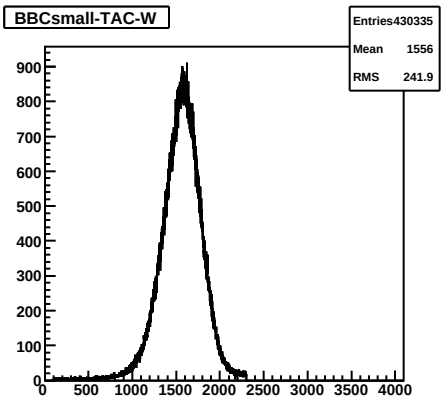

Entries 485877
BBC-L TAC Diff
BCC-S TAC Diff
Entries 485877
BBC-L TAC Diff
VPD TAC Diff
Entries 485877
BBC-L TAC Diff
ZDC TAC Diff
Entries 485877
BBC-S TAC Diff
VPD TAC Diff
Entries 485877
BBC-S TAC Diff
ZDC TAC Diff
Entries 485877
VPD TAC Diff
ZDC TAC Diff

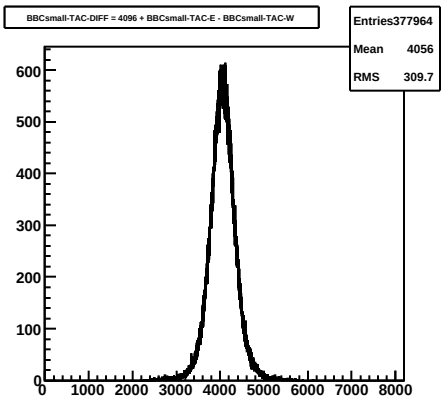

ZDC-TAC-E
Entries 21003
Mean 250
RMS 33.78

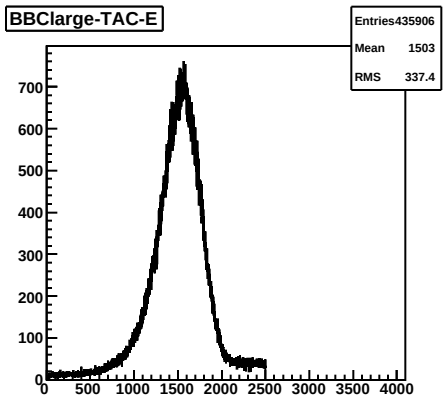

ZDC-TAC-W
Entries 9144
Mean 225.2
RMS 38.82

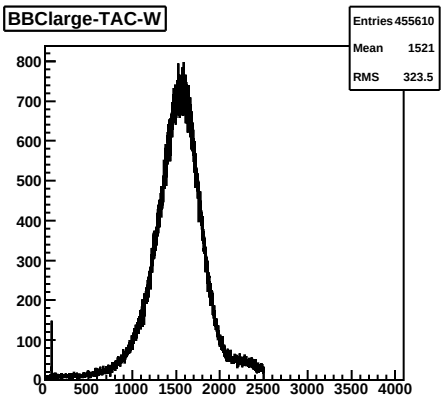

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 617
Mean 524.6
RMS 23.97

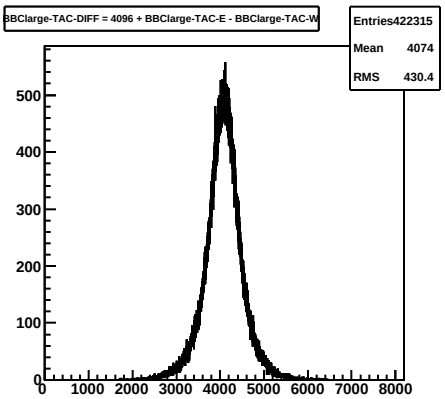

BBCsmall-TAC-E
Entries 407881
Mean 1517
RMS 233.8

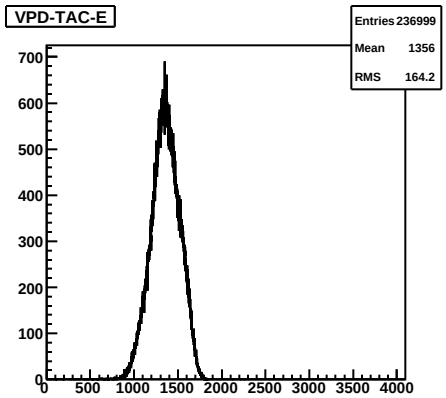

BBCsmall-TAC-W
Entries 430335
Mean 1556
RMS 241.9

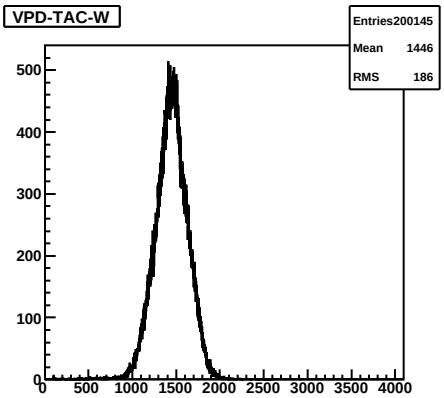

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 377964
Mean 4056
RMS 309.7

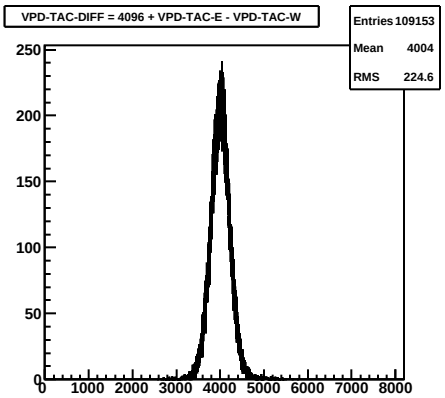

BBClarge-TAC-E
Entries 435906
Mean 1503
RMS 337.4

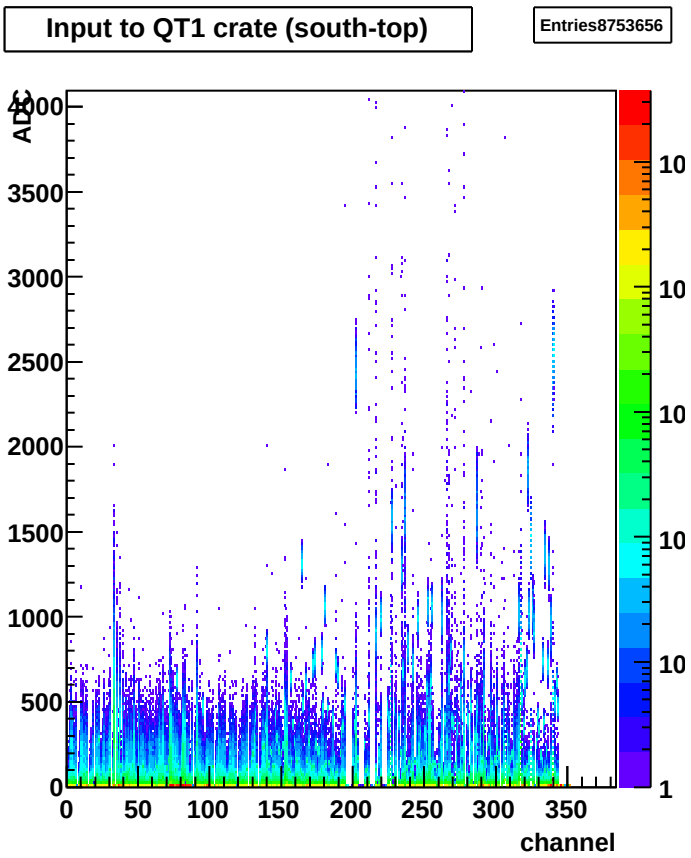

BBClarge-TAC-W
Entries 455610
Mean 1521
RMS 323.5

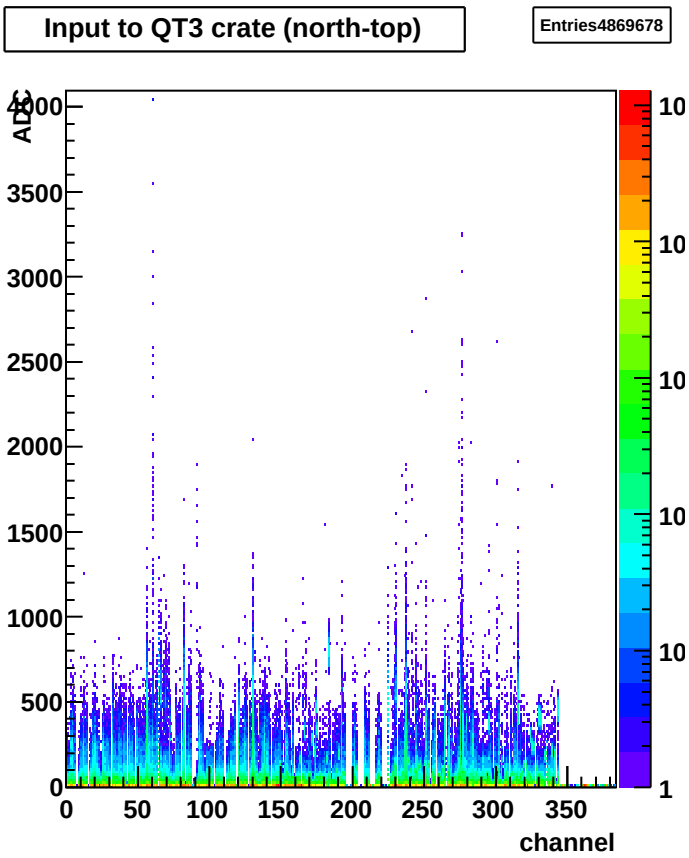

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 422315
Mean 4074
RMS 430.4

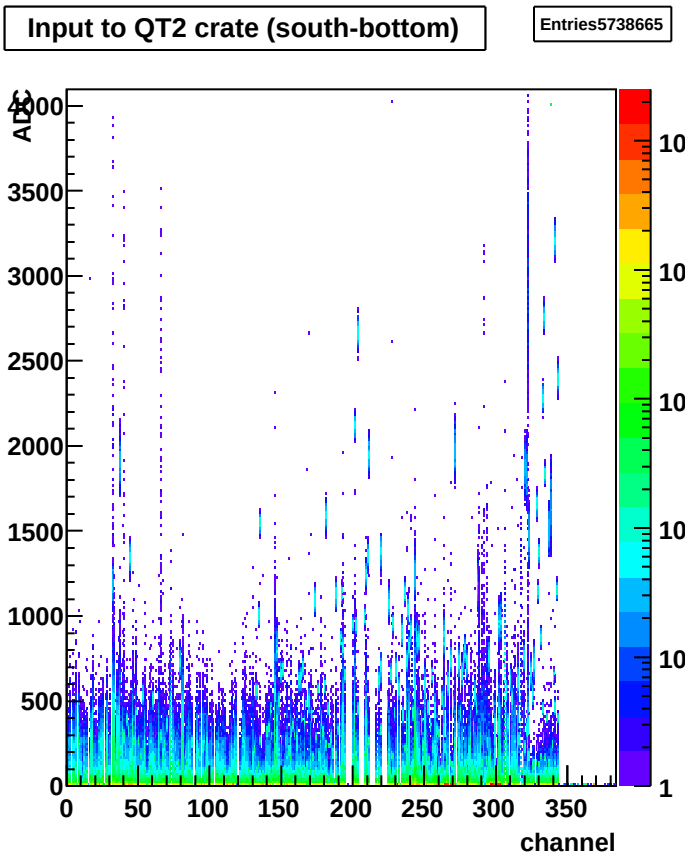

VPD-TAC-E
Entries 236999
Mean 1356
RMS 164.2

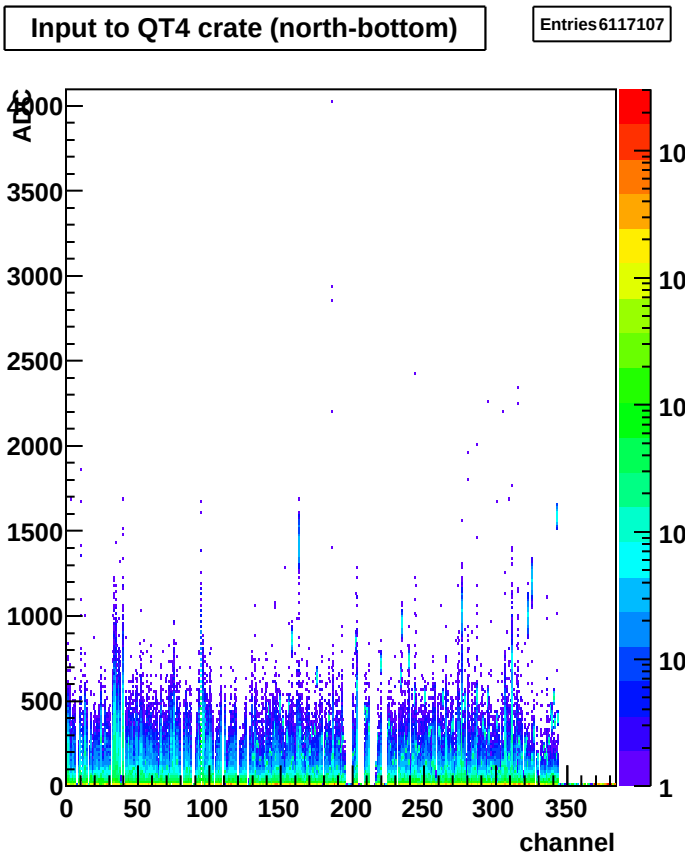

VPD-TAC-W
Entries 200145
Mean 1446
RMS 186

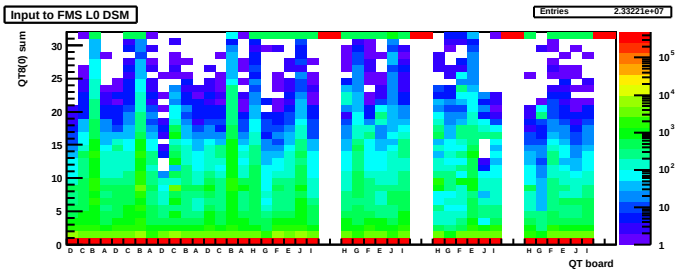

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 109153
Mean 4004
RMS 224.6

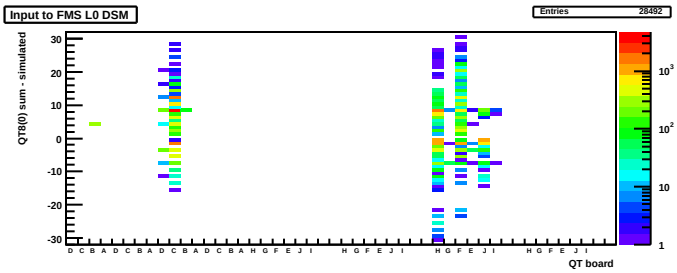

Input to QT1 crate (south-top)
Entries8753656
ADC
channel

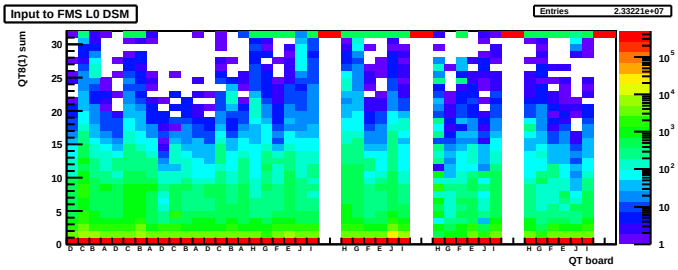

Input to QT3 crate (north-top)
Entries4869678
ADC
channel

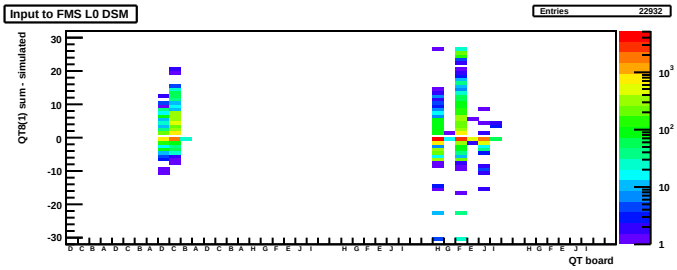

Input to QT2 crate (south-bottom)
Entries5738665
ADC
channel

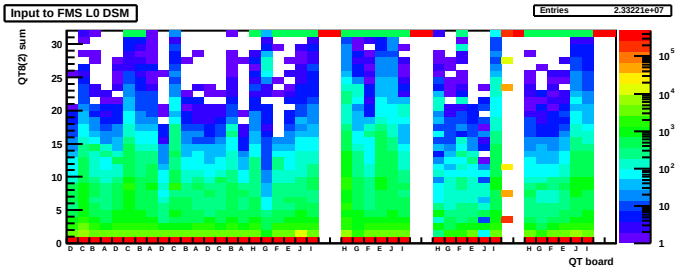

Input to QT4 crate (north-bottom)
Entries6117107
ADC
channel

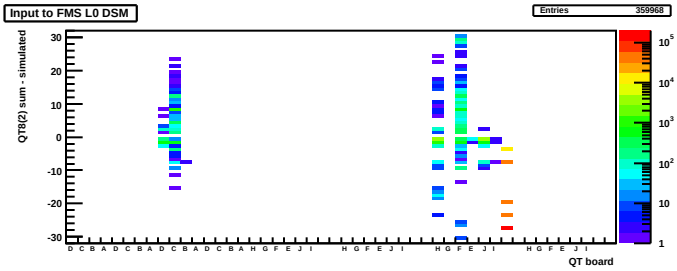

Input to FMS L0 DSM
Entries 2.33221e+07
QT8(0) sum
QT board

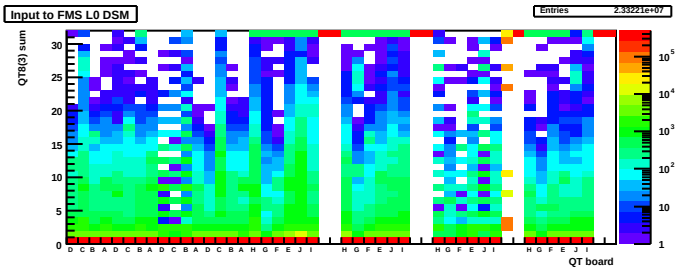

Input to FMS L0 DSM
Entries 28492
QT8(0) sum - simulated
QT board

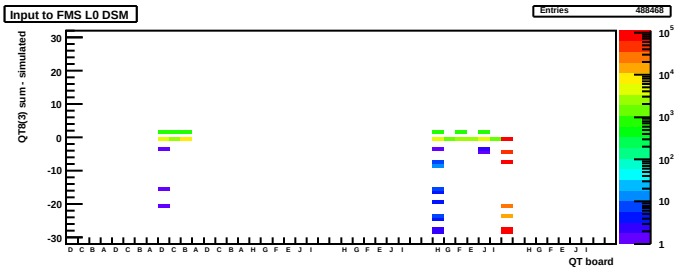

Input to FMS L0 DSM
Entries 2.33221e+07
QT8(1) sum
QT board

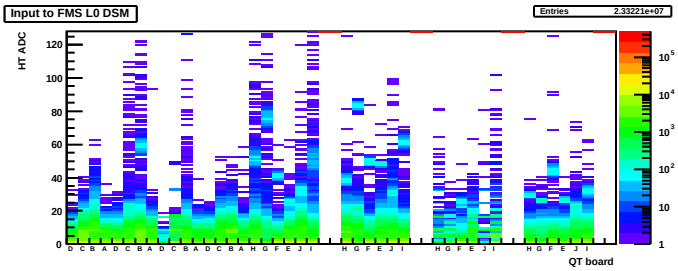

Input to FMS L0 DSM
Entries 22932
QT8(1) sum - simulated
QT board

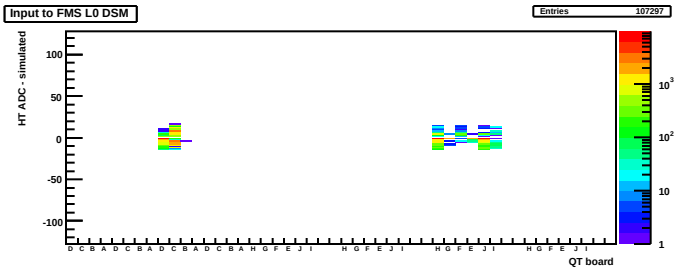

Input to FMS L0 DSM
Entries 2.33221e+07
QT8(2) sum
QT board

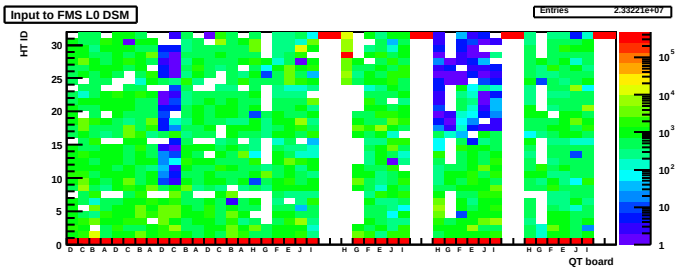

Input to FMS L0 DSM
Entries 359968
QT8(2) sum - simulated
QT board

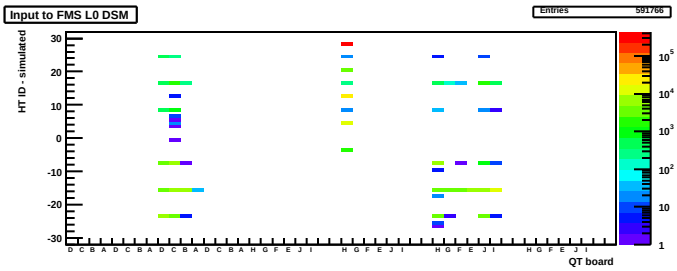

Input to FMS L0 DSM
Entries 2.33221e+07
QT8(3) sum
QT board

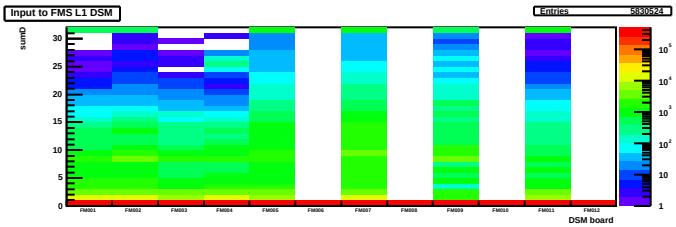

Input to FMS L0 DSM
Entries 488468
QT8(3) sum - simulated
QT board

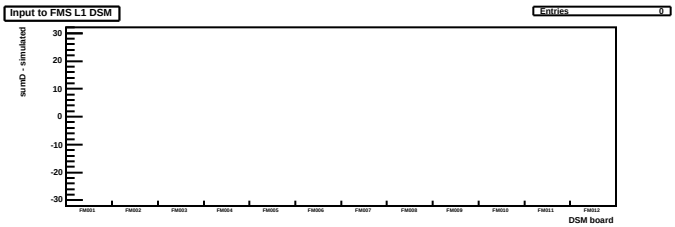

Input to FMS L0 DSM
Entries 2.33221e+07
HT ADC
QT board

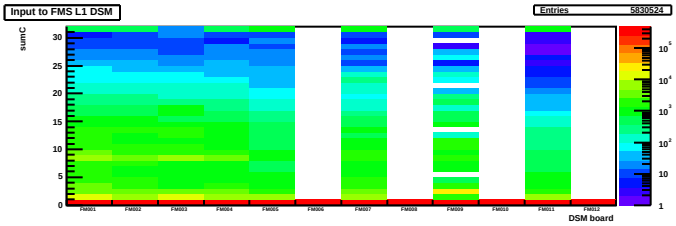

Input to FMS L0 DSM
Entries 107297
HT ADC - simulated
QT board

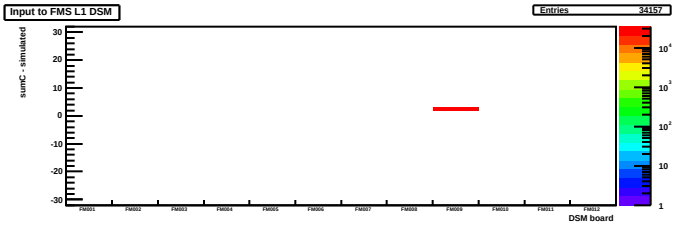

Input to FMS L0 DSM
Entries 2.33221e+07
HT ID
QT board

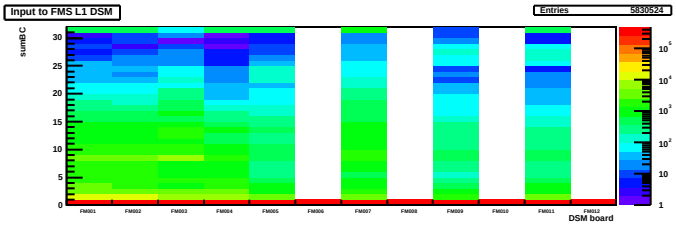

Input to FMS L0 DSM
Entries 591766
HT ID - simulated
QT board

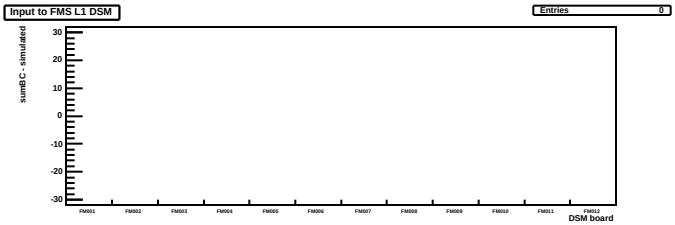

Input to FMS L1 DSM
Entries 5830524
sumD
DSM board

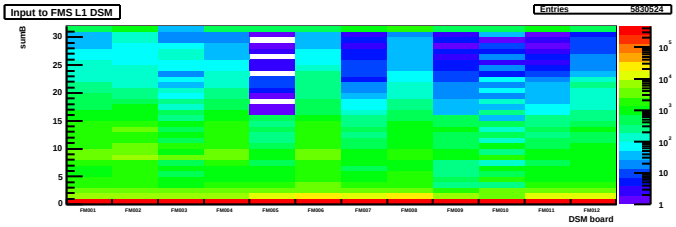

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

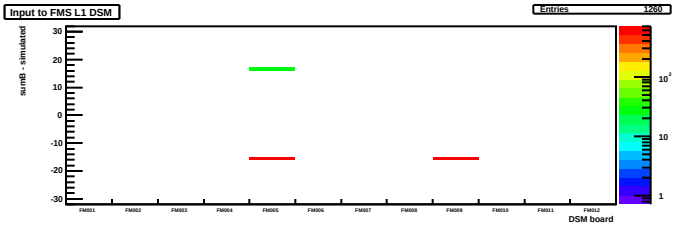

Input to FMS L1 DSM
Entries 5830524
sumC
DSM board

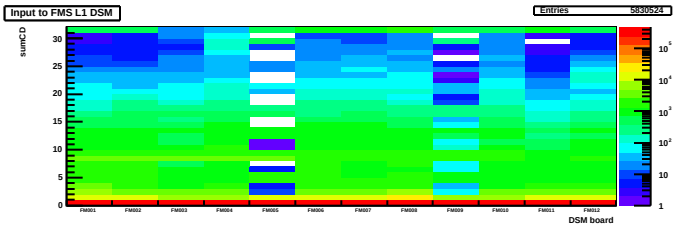

Input to FMS L1 DSM
Entries 34157
sumC - simulated
DSM board

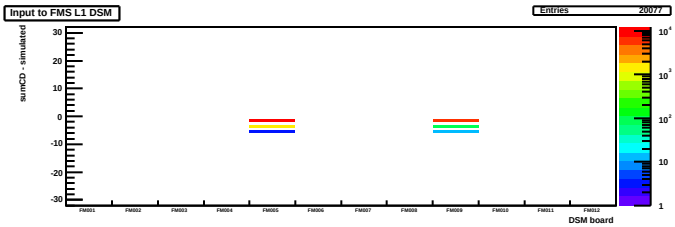

Input to FMS L1 DSM
Entries 5830524
sumBC
DSM board

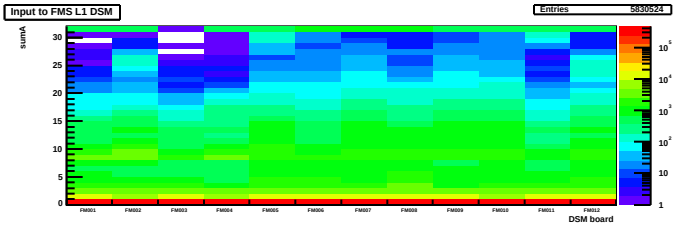

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

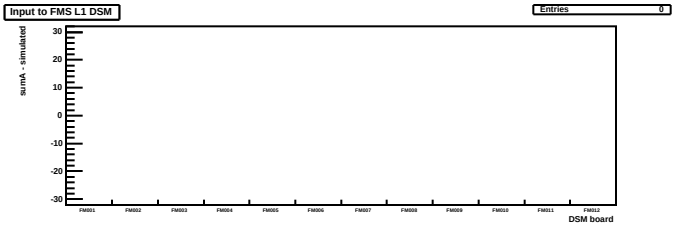

Input to FMS L1 DSM
Entries 5830524
sumB
DSM board

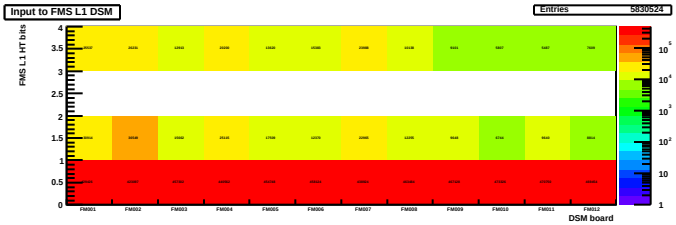

Input to FMS L1 DSM
Entries 1260
sumB - simulated
DSM board

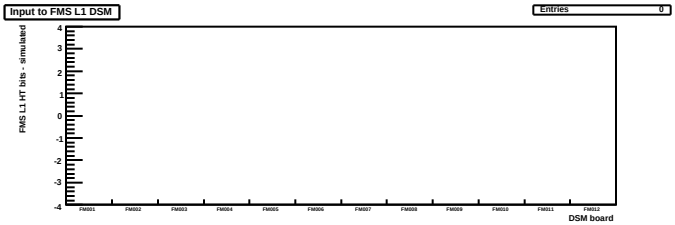

Input to FMS L1 DSM
Entries 5830524
sumCD
DSM board

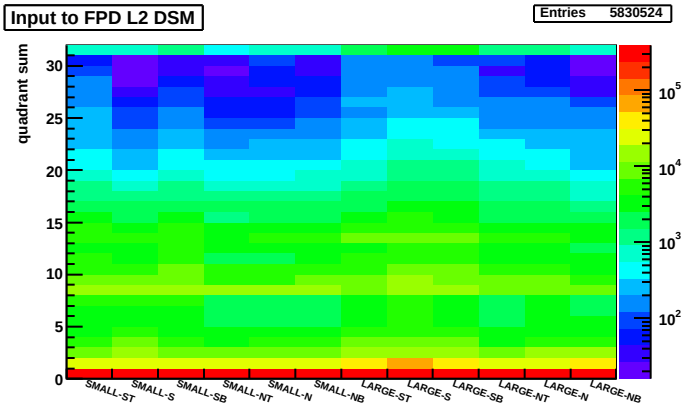

Input to FMS L1 DSM
Entries 20077
sumCD - simulated
DSM board

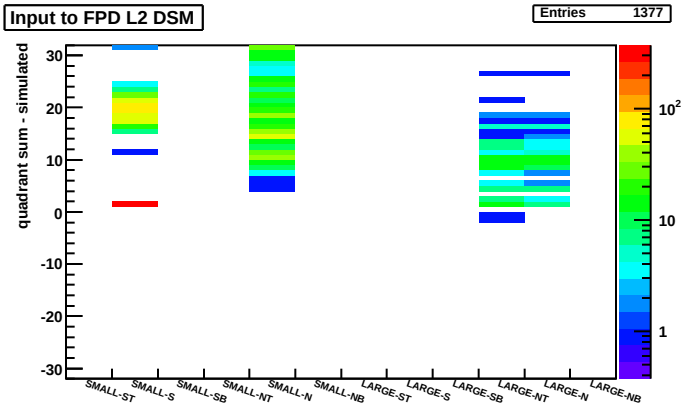

Input to FMS L1 DSM
Entries 5830524
sumA
DSM board

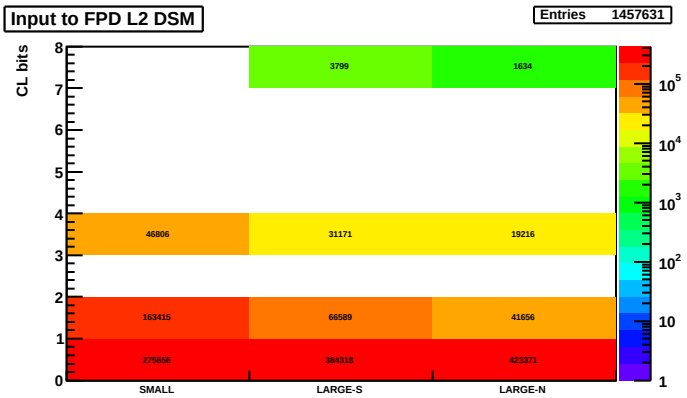

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

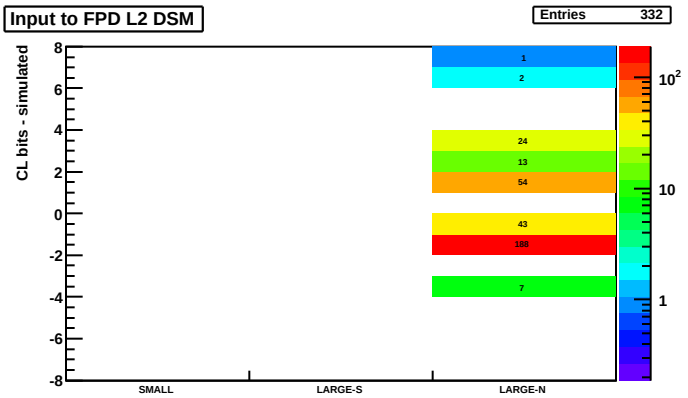

Input to FMS L1 DSM
Entries 5830524
FMS L1 HT bits
DSM board

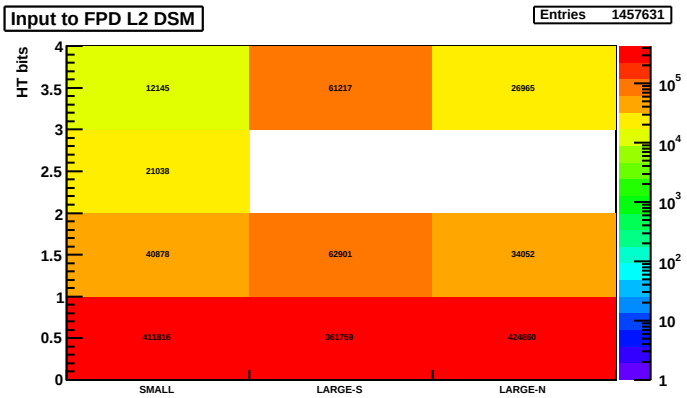

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

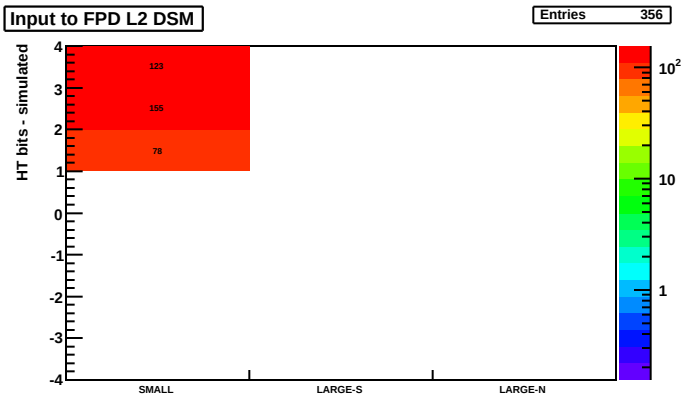

Input to FPD L2 DSM
Entries 5830524
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

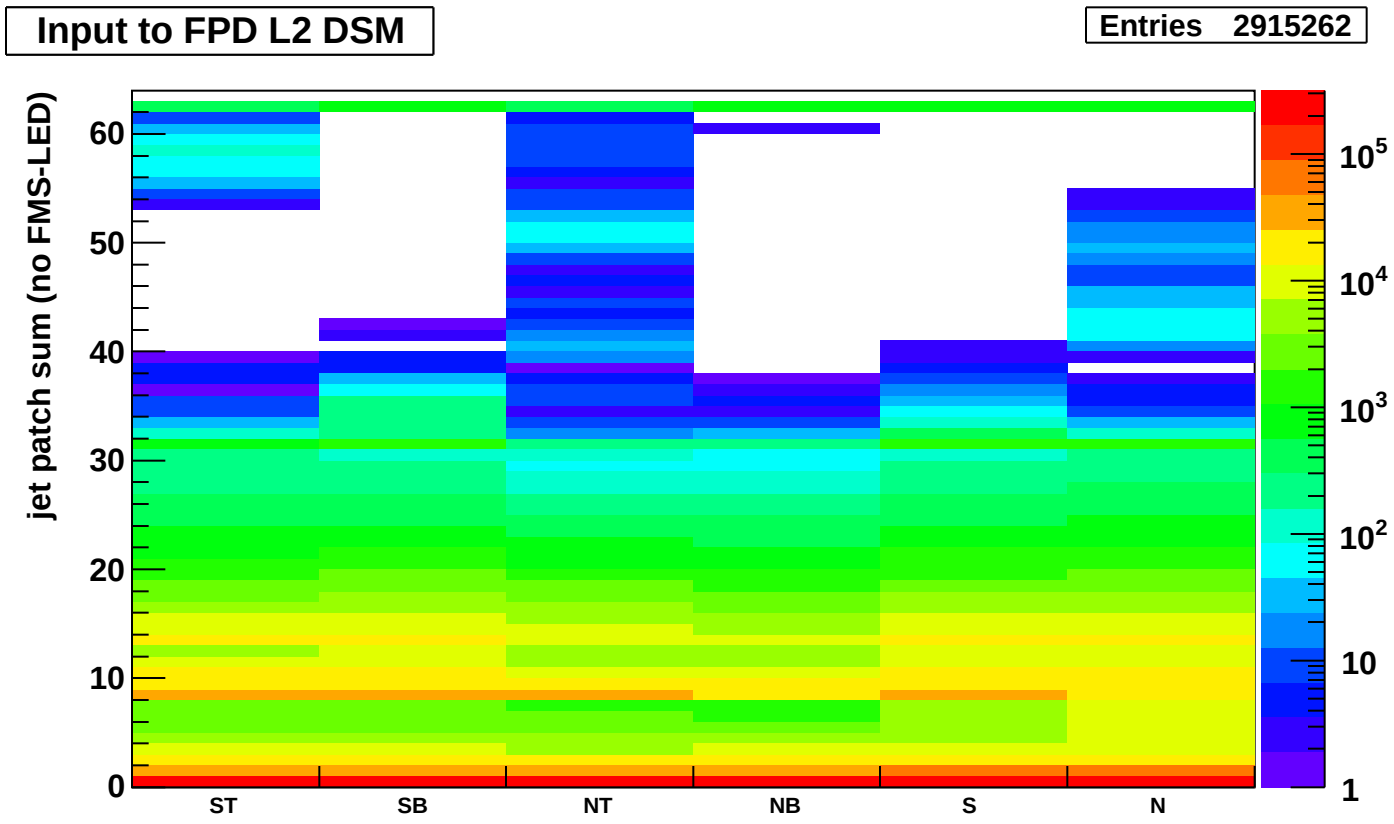

Input to FPD L2 DSM
Entries 1377
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

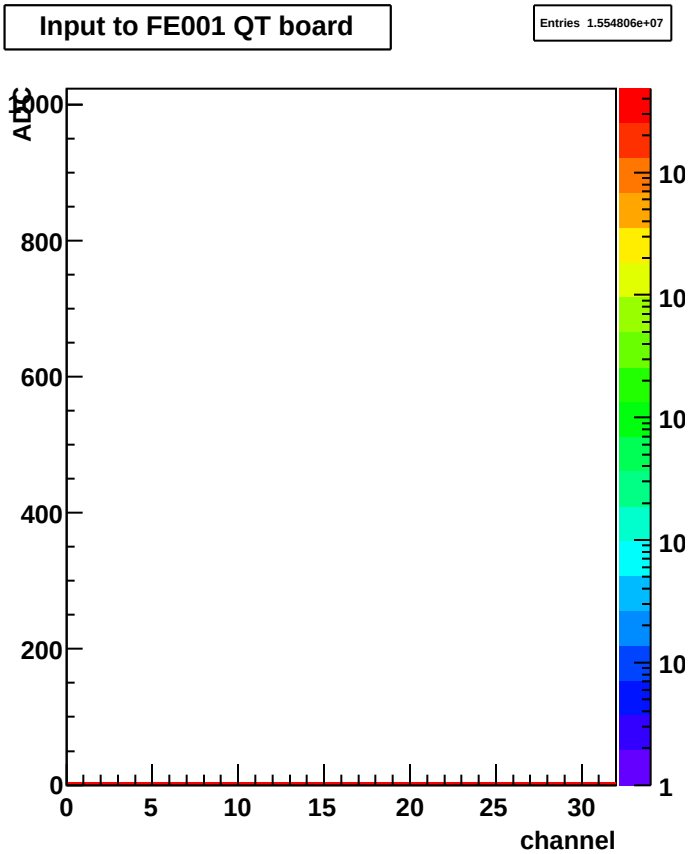

Input to FPD L2 DSM
Entries 1457631
CL bits
3799
1634
46806
31171
19216
163415
66589
41656
275656
384318
423371
SMALL
LARGE-S
LARGE-N

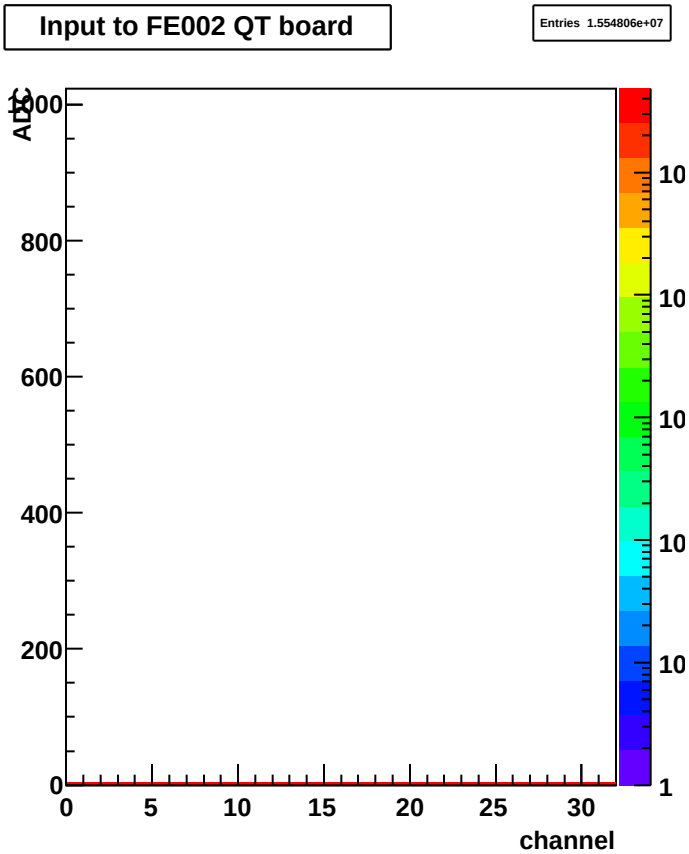

Input to FPD L2 DSM
Entries 332
CL bits - simulated
1
2
24
13
54
43
188
7
SMALL
LARGE-S
LARGE-N

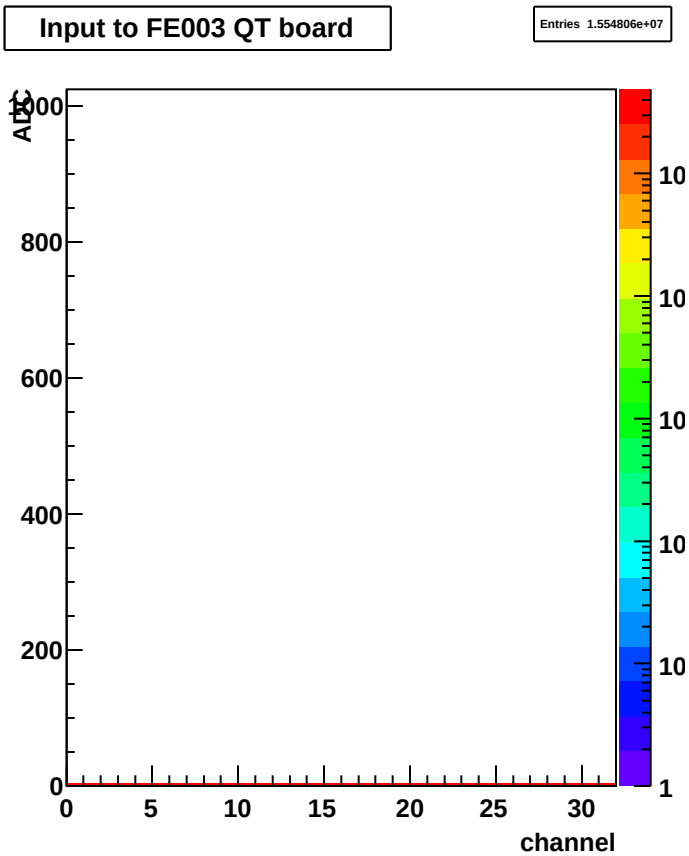

Input to FPD L2 DSM
Entries 1457631
HT bits
12145
61217
26965
21038
40878
62901
34052
411816
361759
424860
SMALL
LARGE-S
LARGE-N

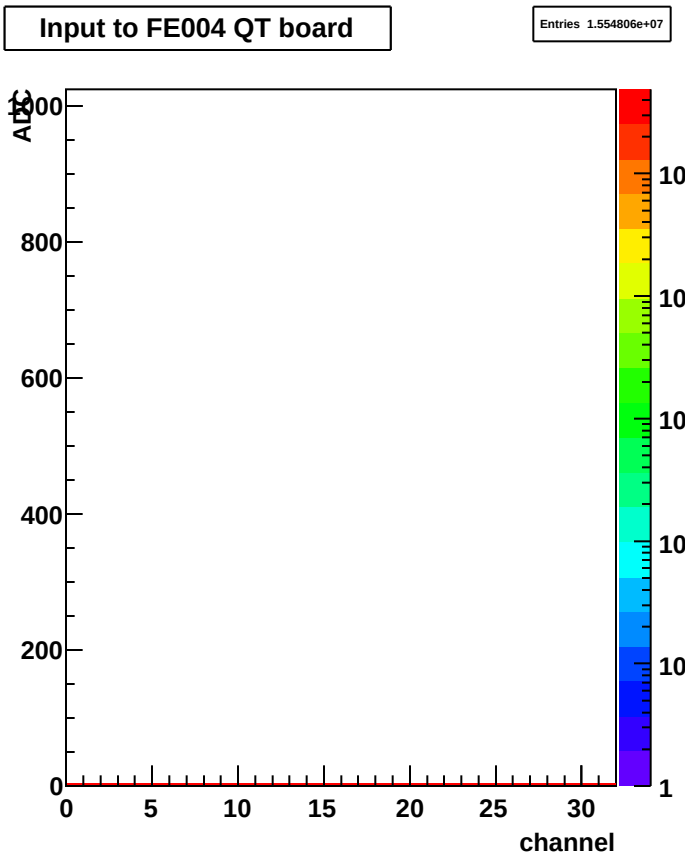

Input to FPD L2 DSM
Entries 356
HT bits - simulated
123
155
78
SMALL
LARGE-S
LARGE-N

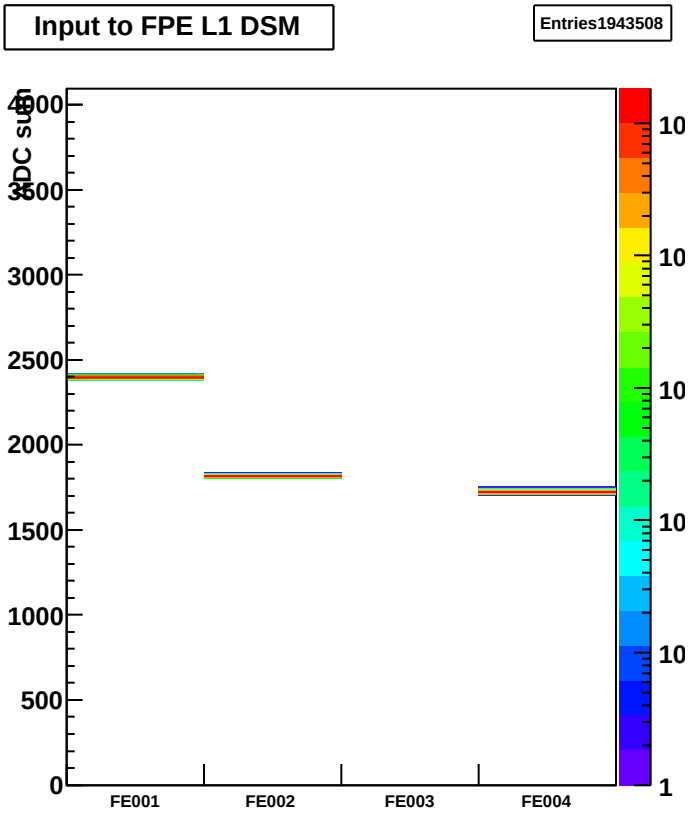

Input to FPD L2 DSM
Entries 2915262
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

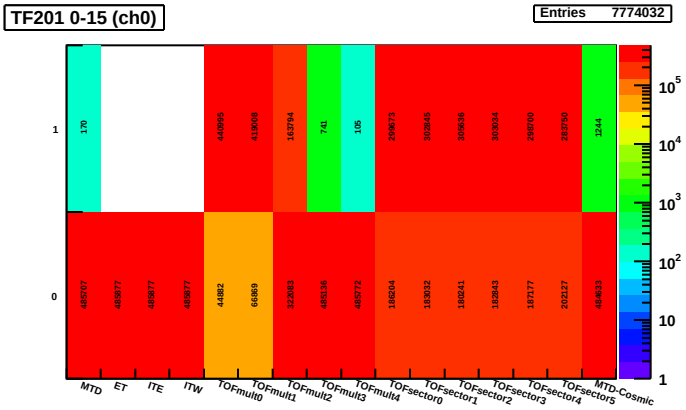

Input to FE001 QT board
Entries 1.554806e+07
ADC
channel

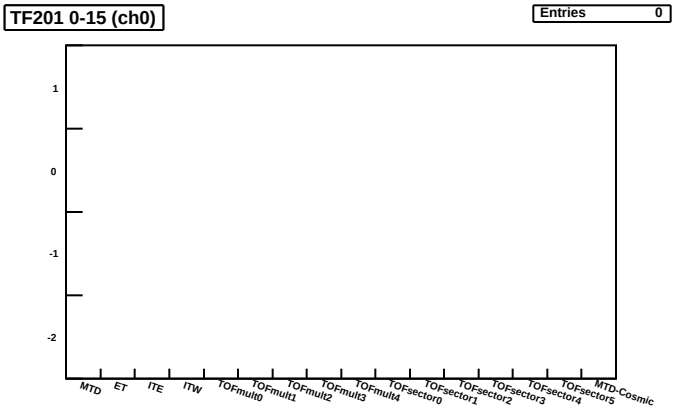

Input to FE002 QT board
Entries 1.554806e+07
ADC
channel

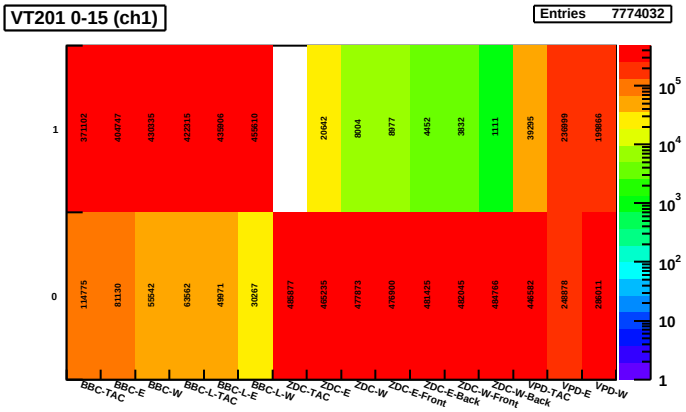

Input to FE003 QT board
Entries 1.554806e+07
ADC
channel

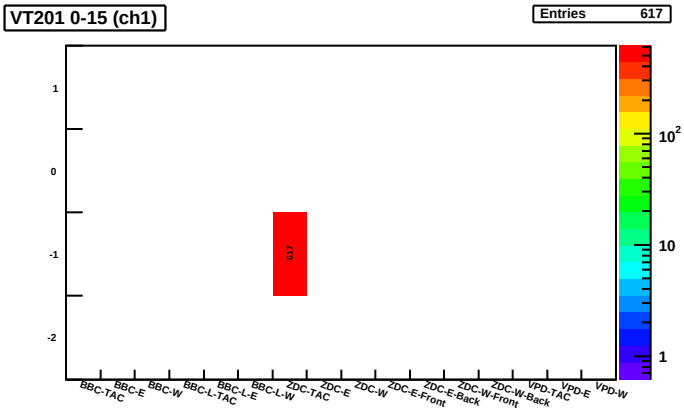

Input to FE004 QT board
Entries 1.554806e+07
ADC
channel


Input to FPE L1 DSM
Entries1943508
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 7774032
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 7774032
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
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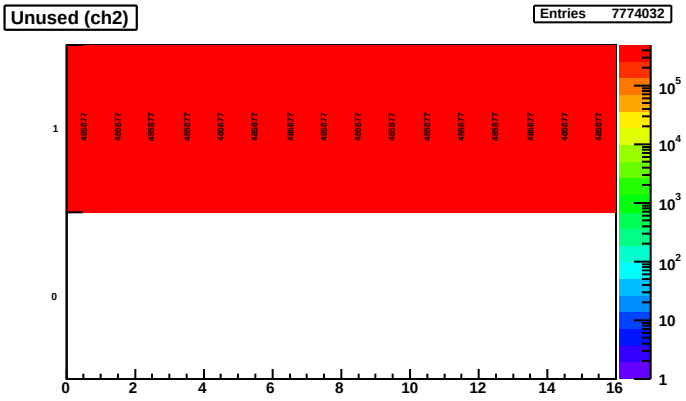
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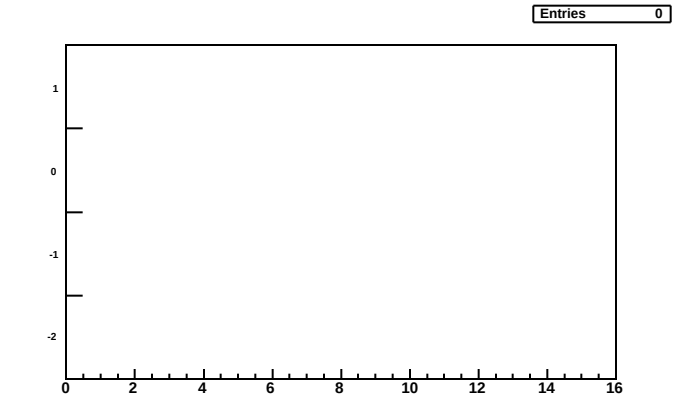
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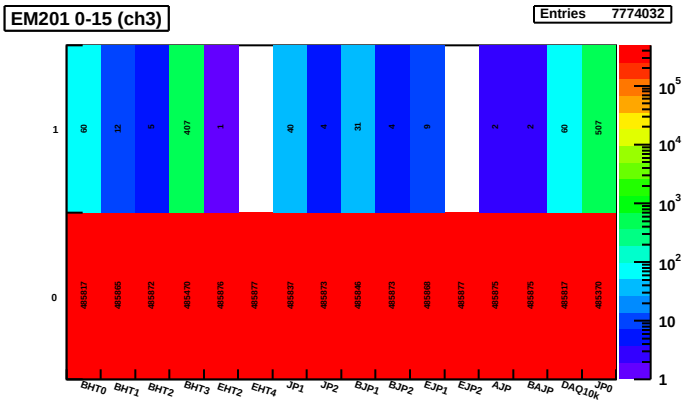
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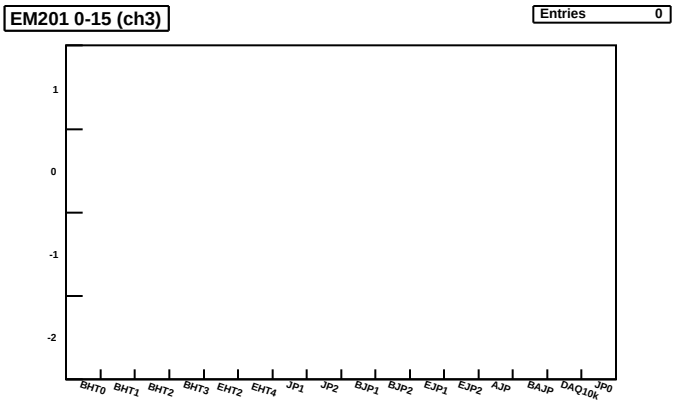
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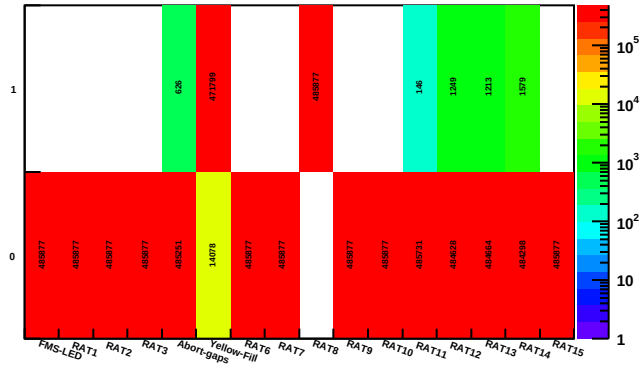
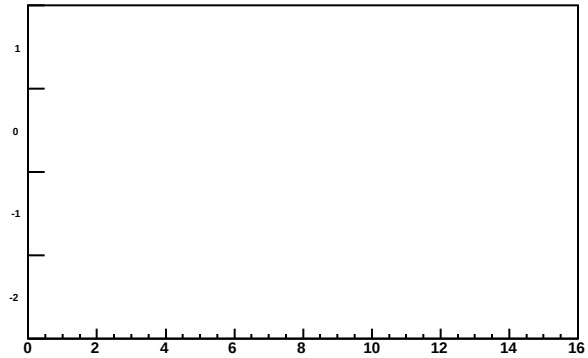
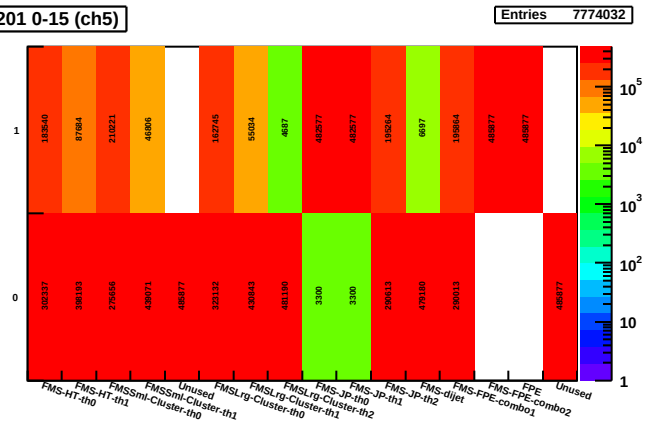
RAT board (ch4)

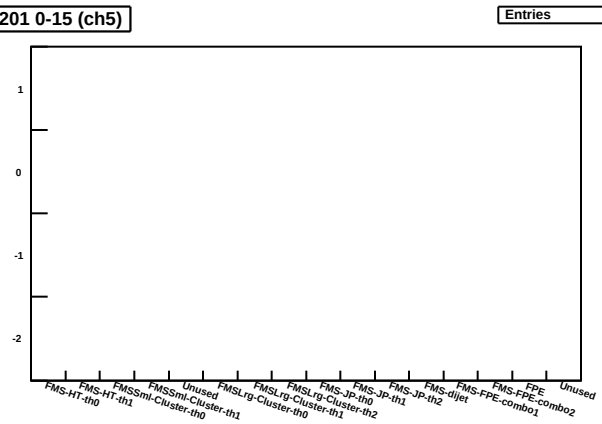




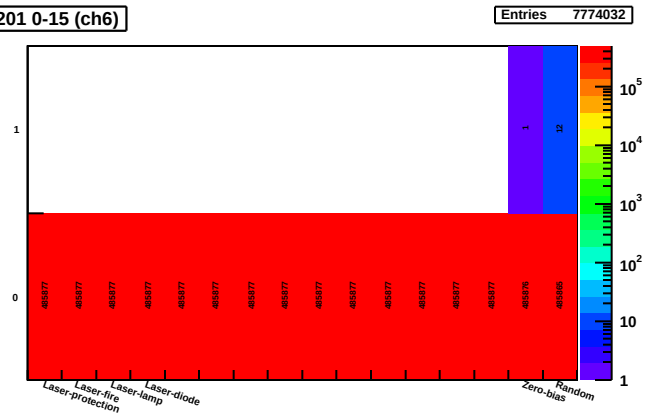
FP201 0-15 (ch5)

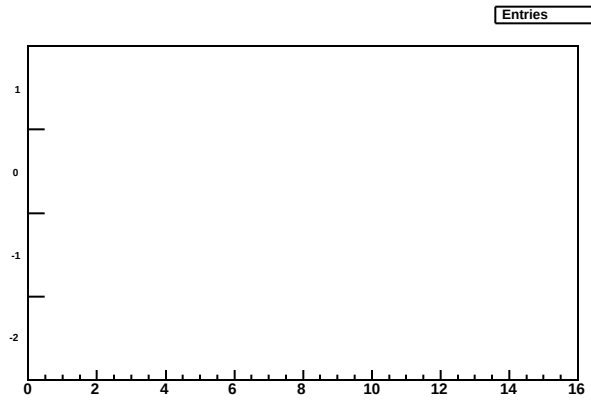
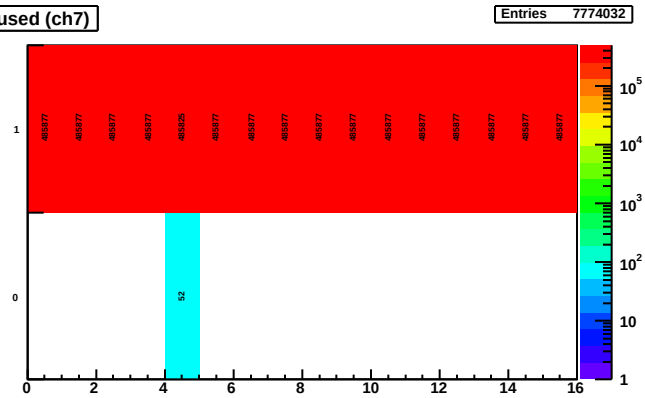
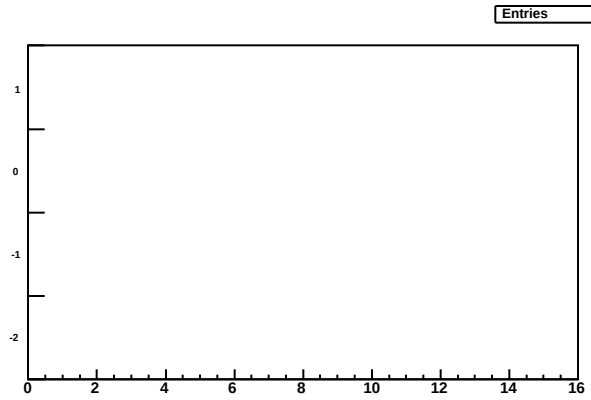


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)