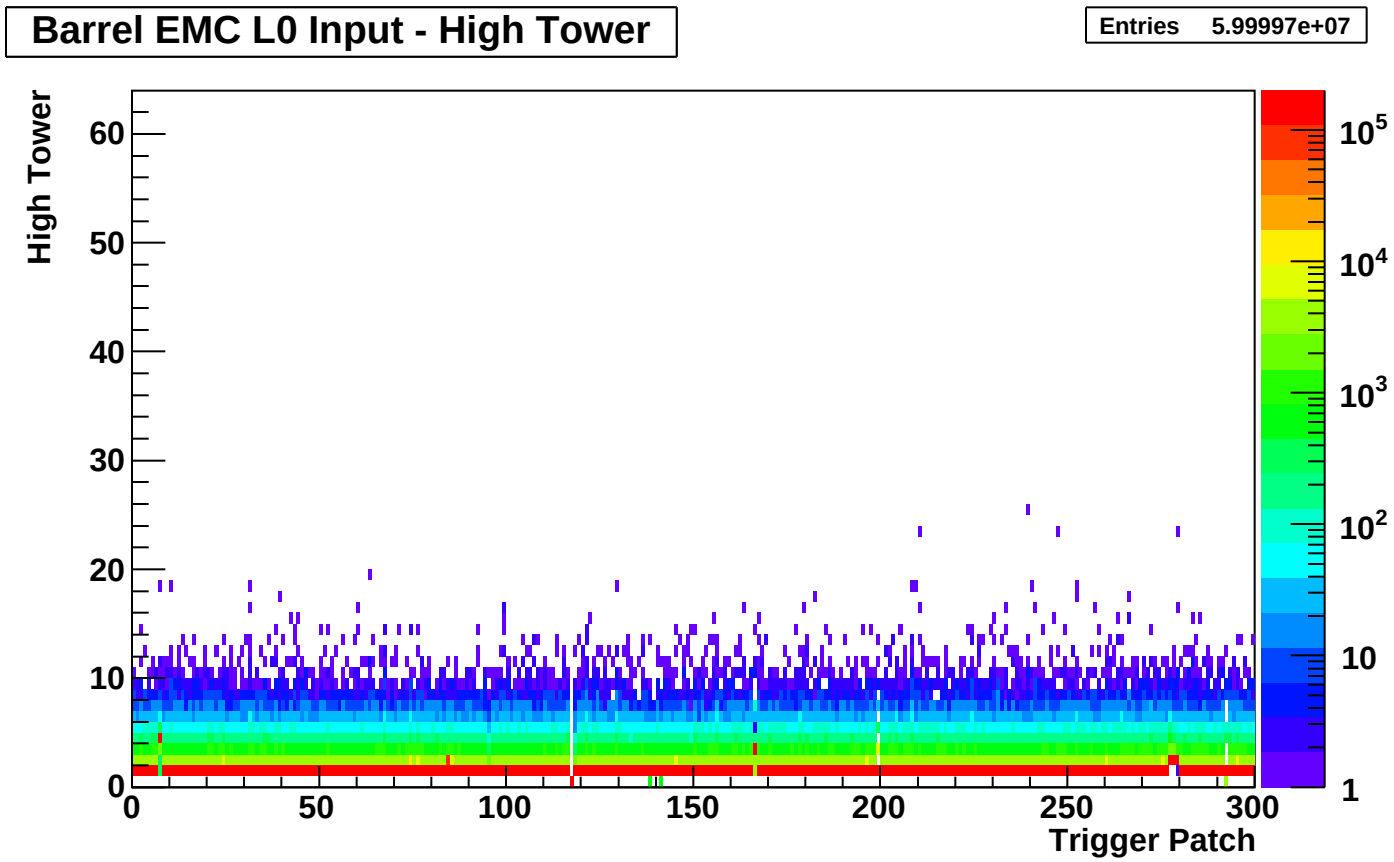
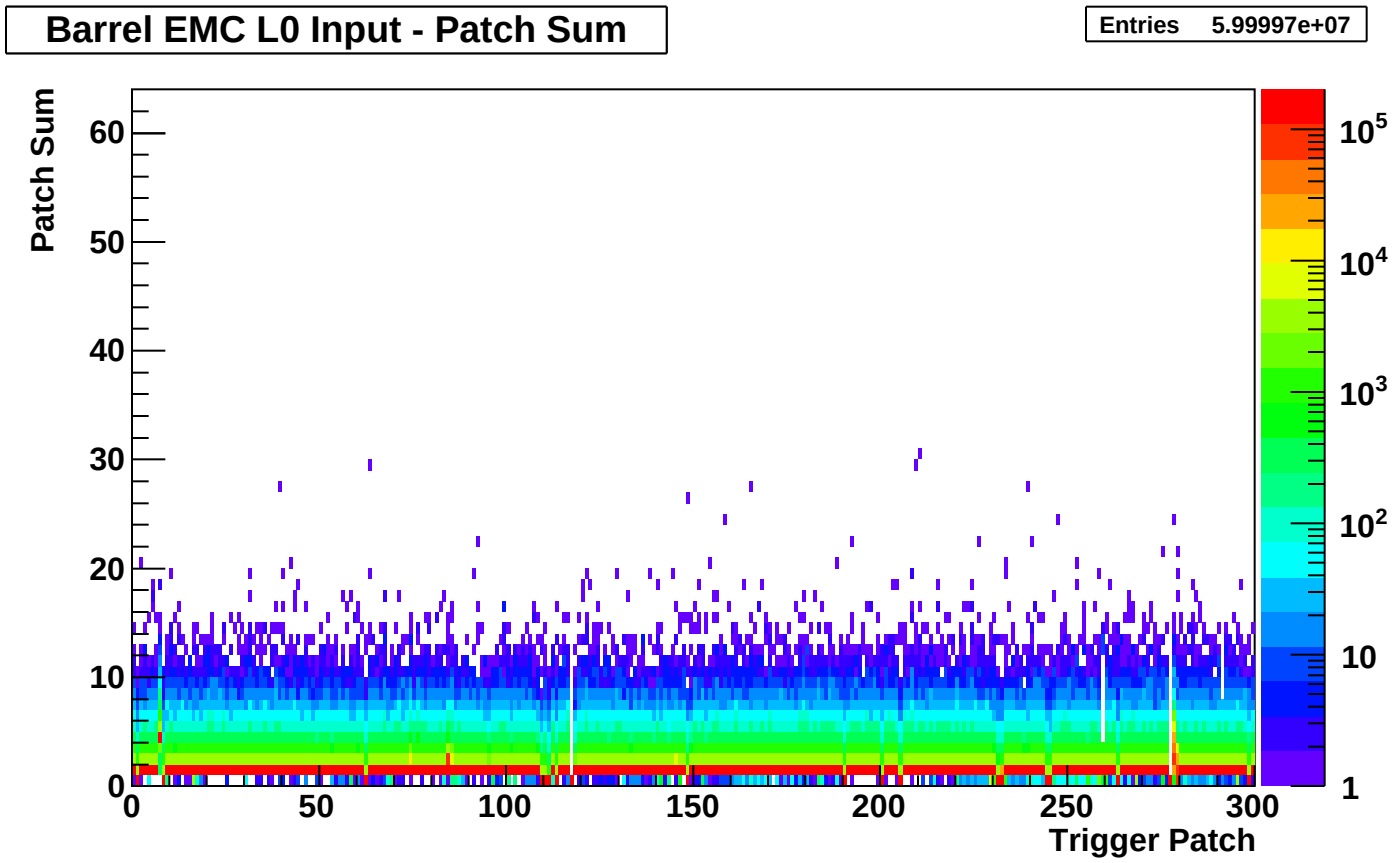
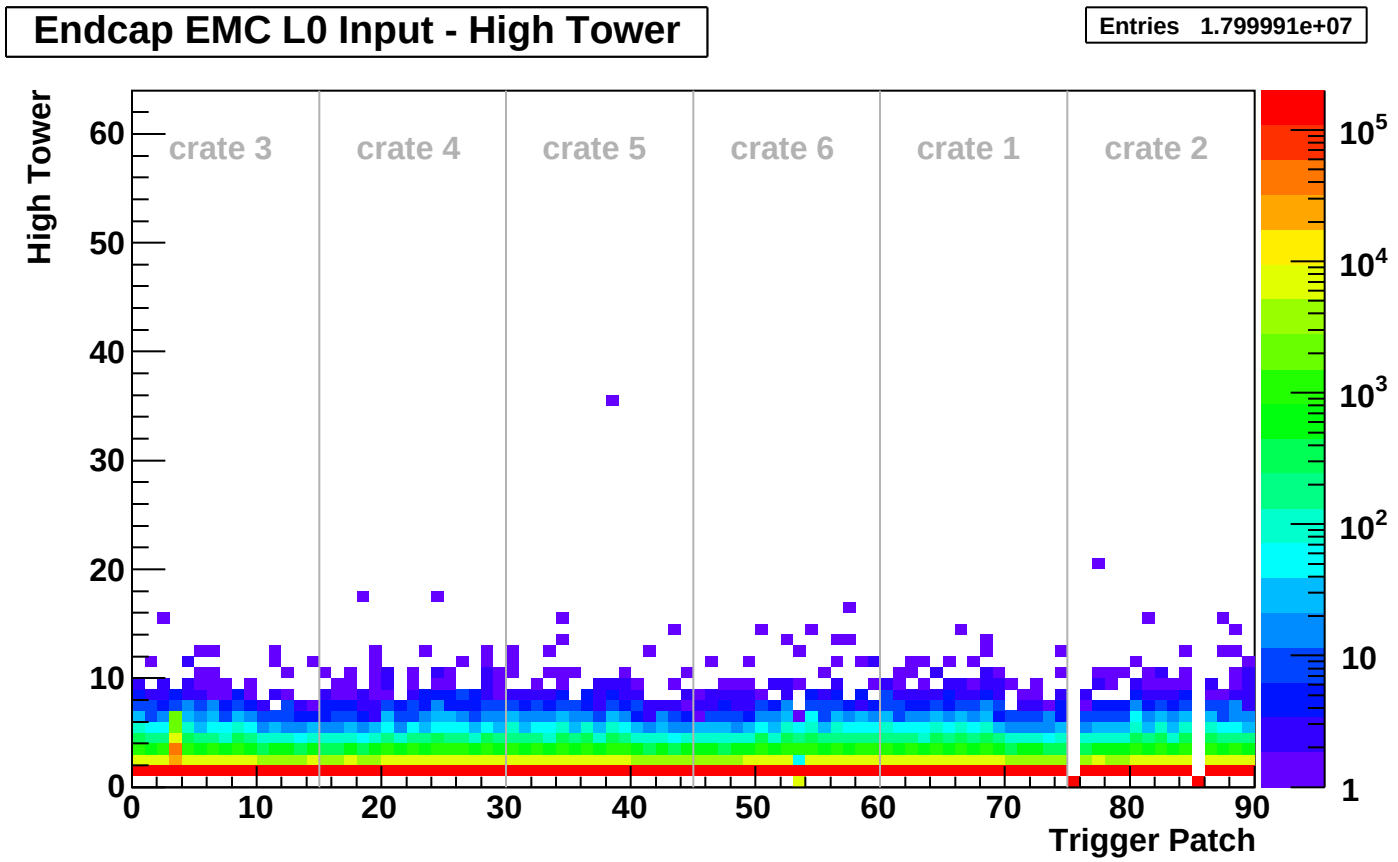
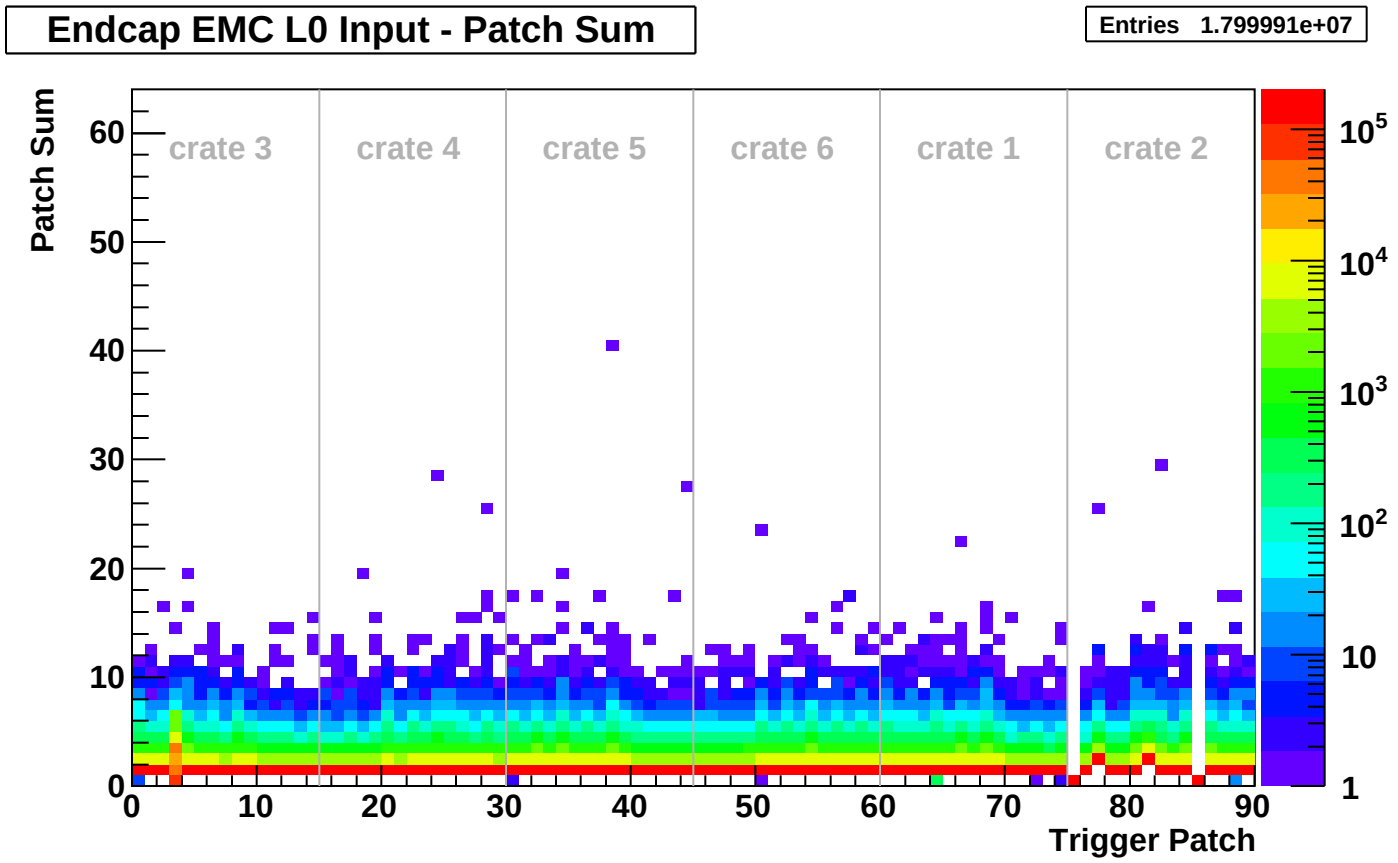
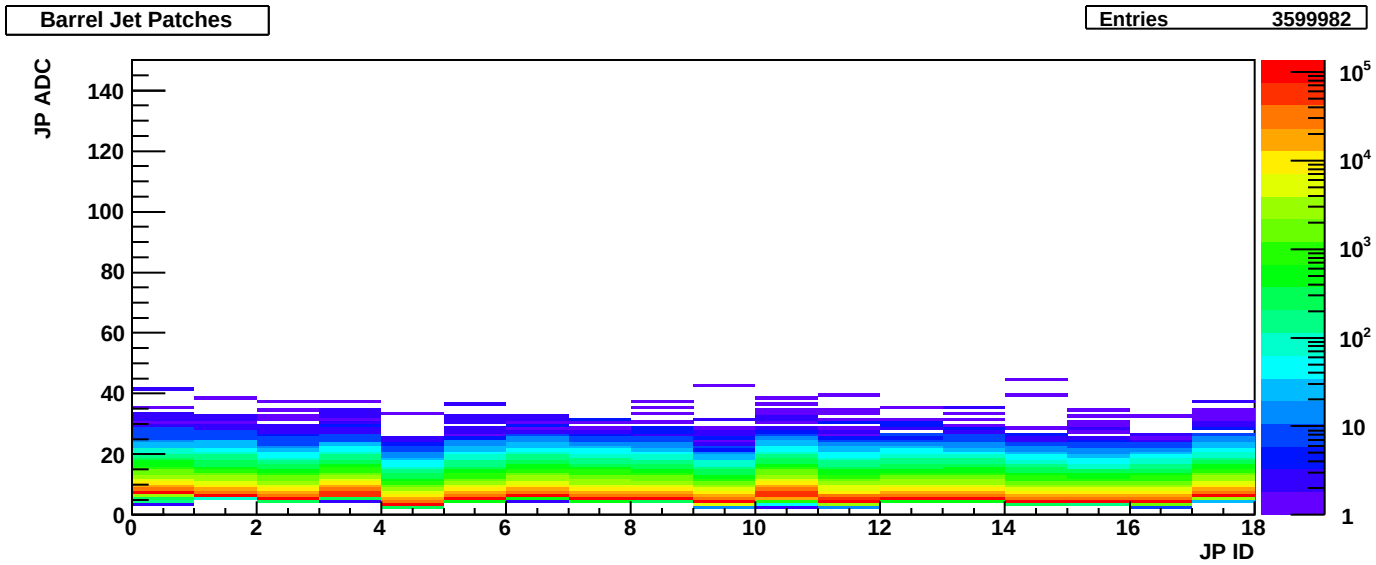
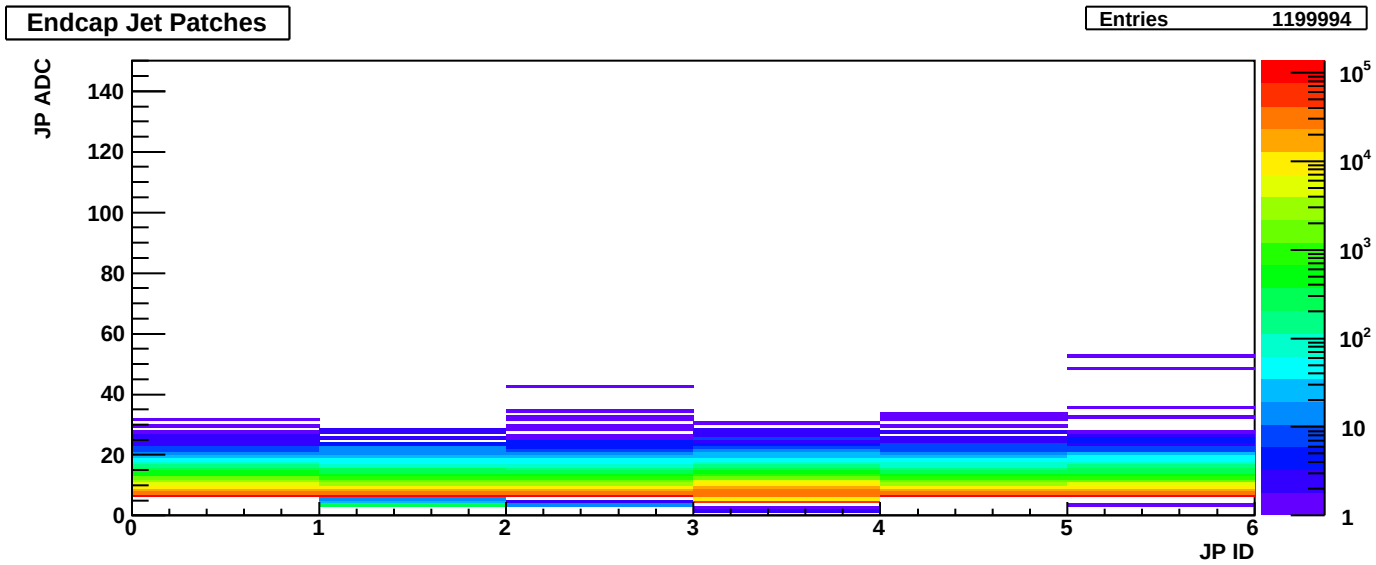

Barrel EMC L0 Input - High Tower
Entries 5.99997e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

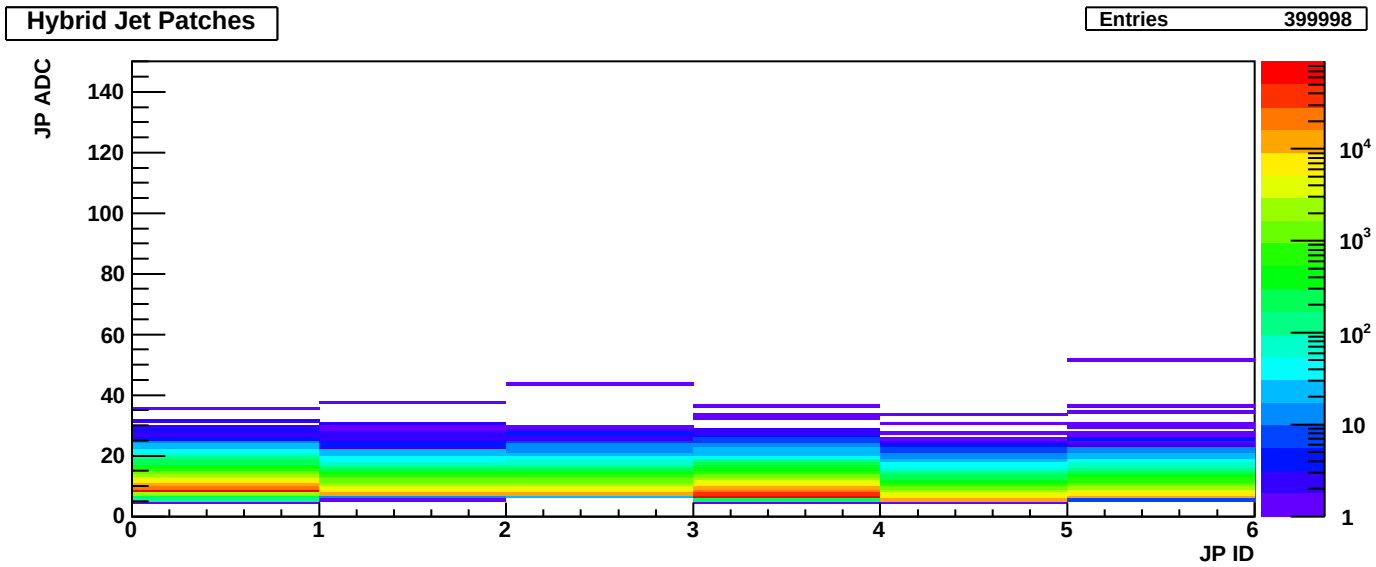

Barrel EMC L0 Input - Patch Sum
Entries 5.99997e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

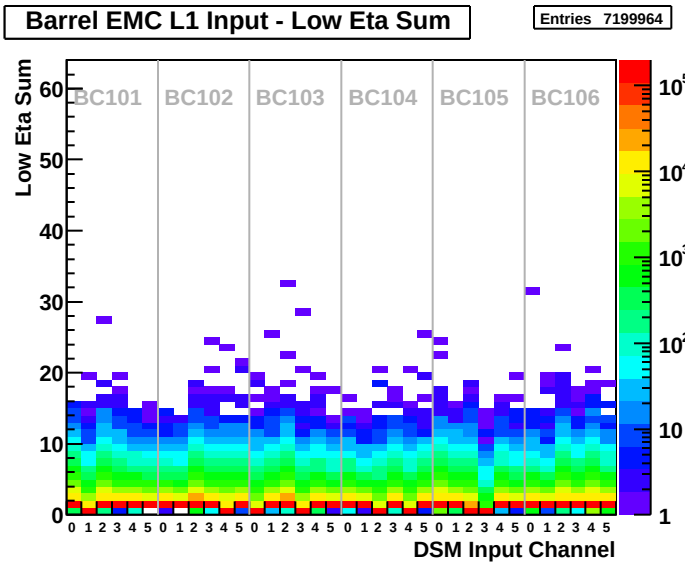

Endcap EMC L0 Input - High Tower
Entries 1.799991e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

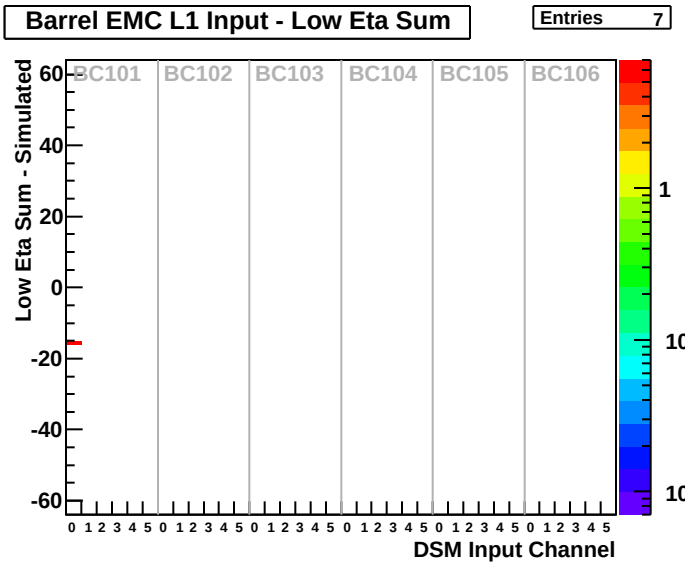

Endcap EMC L0 Input - Patch Sum
Entries 1.799991e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

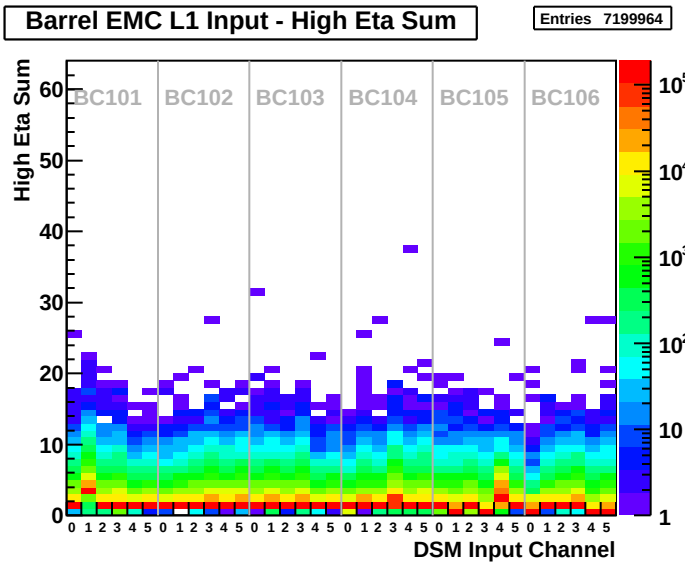

Barrel Jet Patches
Entries 3599982
JP ADC
JP ID

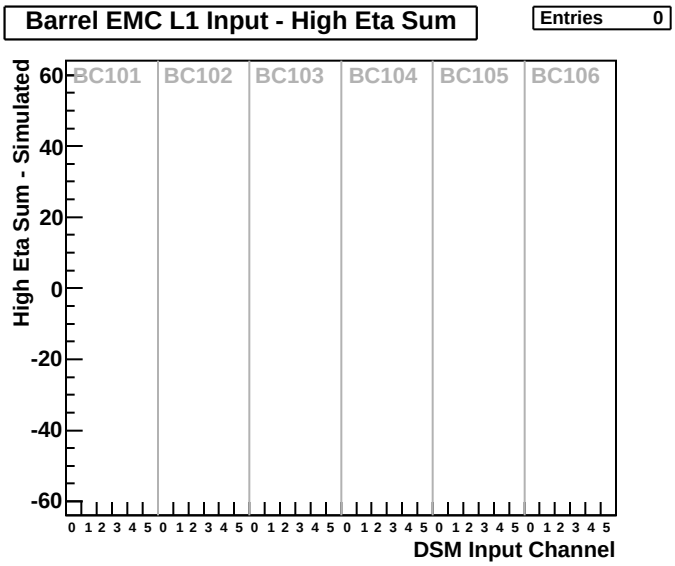

Endcap Jet Patches
Entries 1199994
JP ADC
JP ID

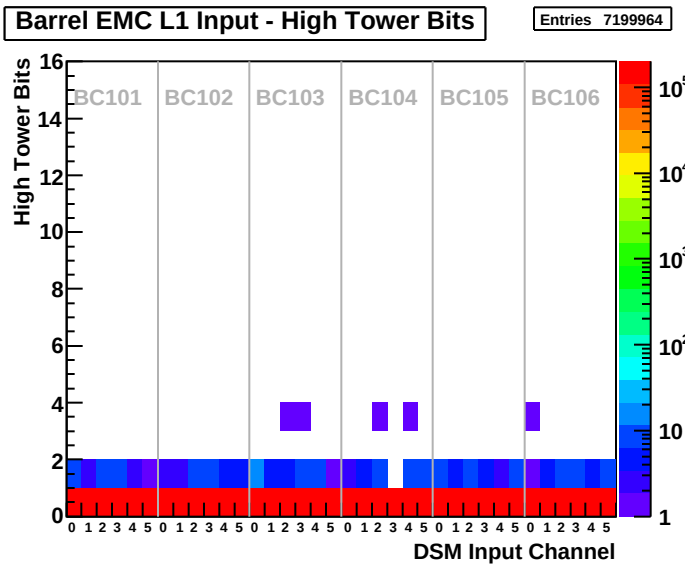

Hybrid Jet Patches
Entries 399998
JP ADC
JP ID

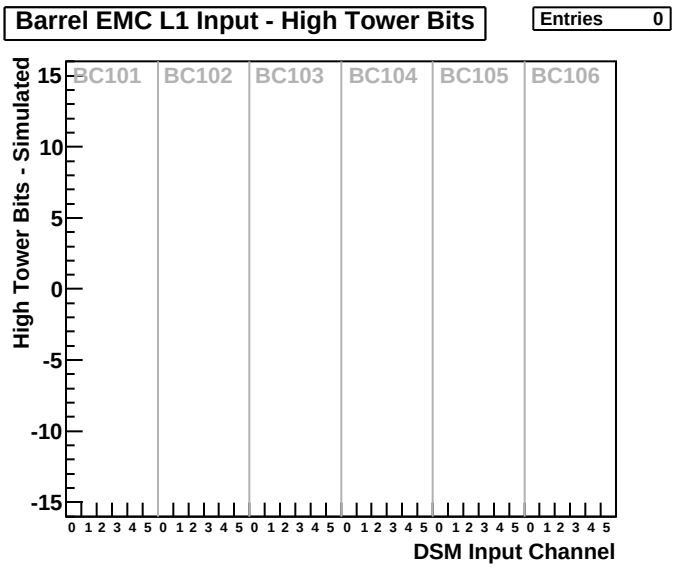

Barrel EMC L1 Input - Low Eta Sum
Entries 7199964
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

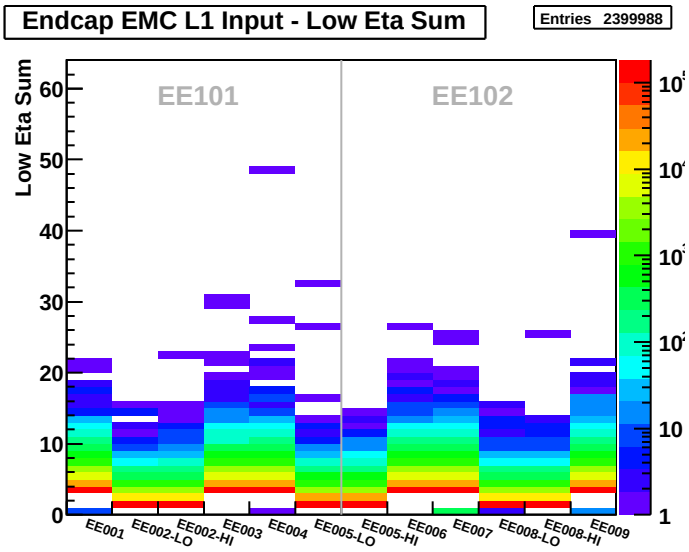

Barrel EMC L1 Input - Low Eta Sum
Entries 7
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

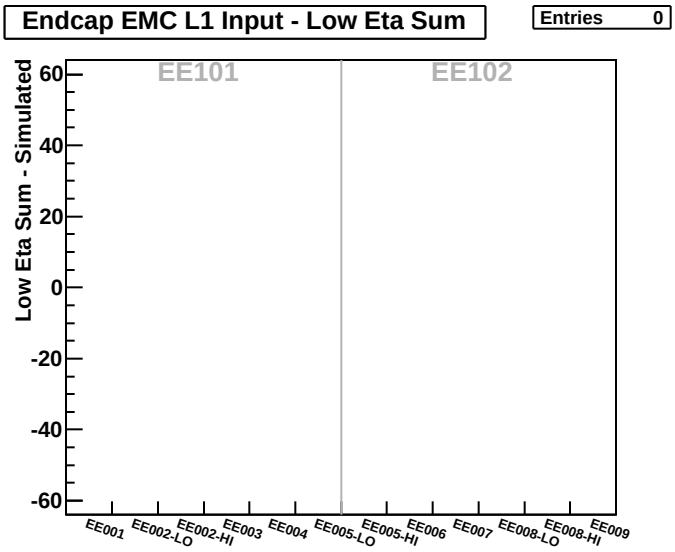

Barrel EMC L1 Input - High Eta Sum
Entries 7199964
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

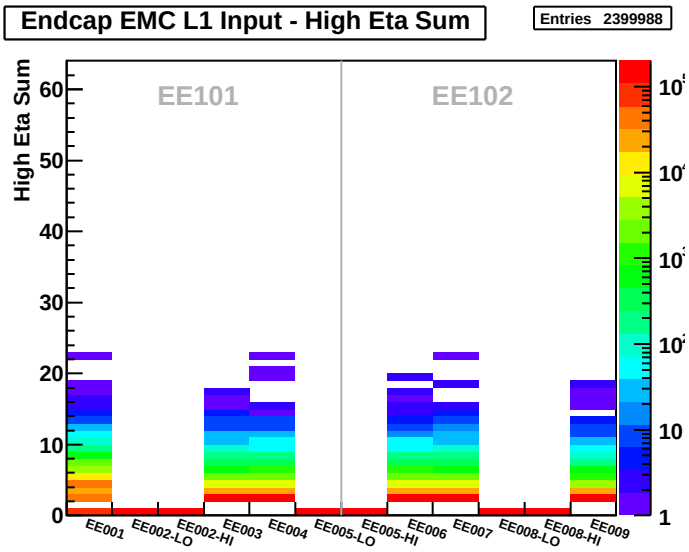

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

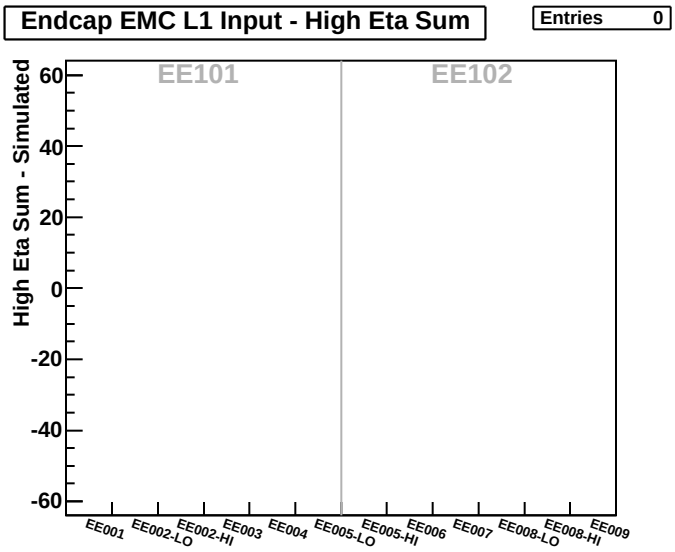

Barrel EMC L1 Input - High Tower Bits
Entries 7199964
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

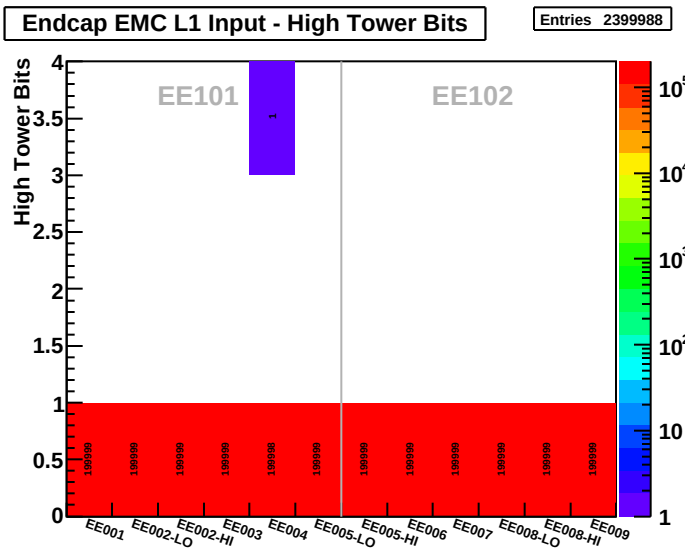

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

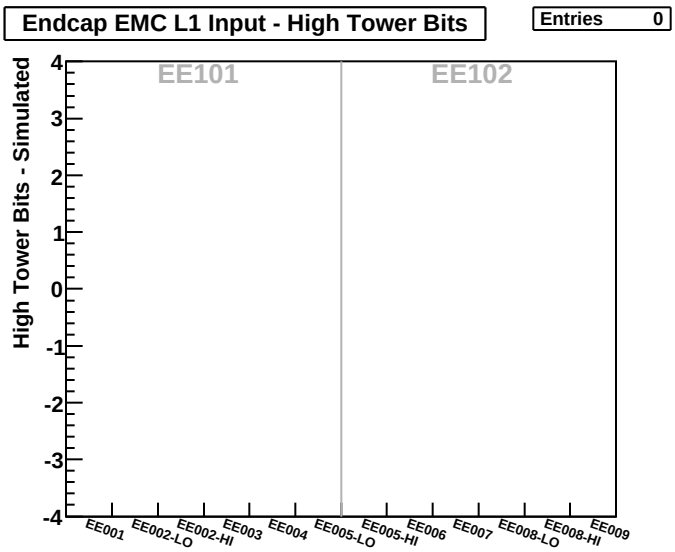

Endcap EMC L1 Input - Low Eta Sum
Entries 2399988
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

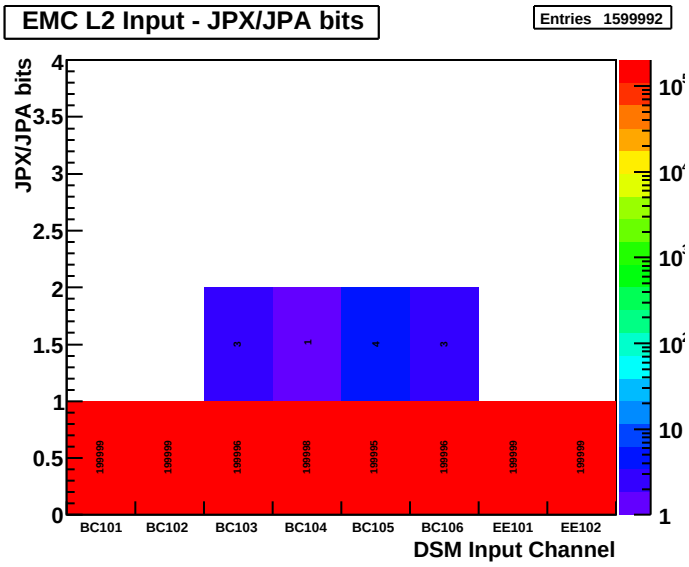

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

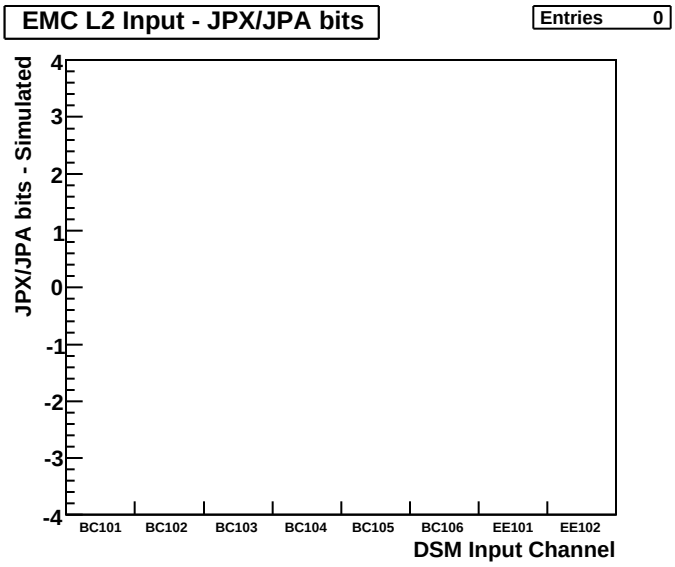

Endcap EMC L1 Input - High Eta Sum
Entries 2399988
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

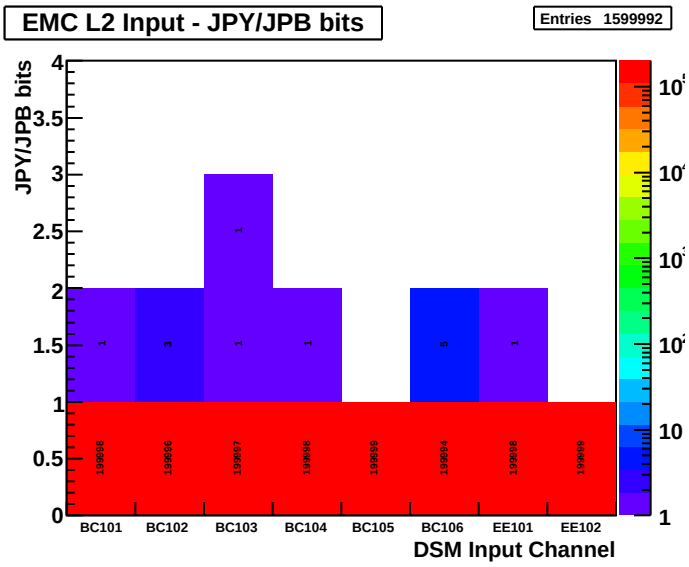

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

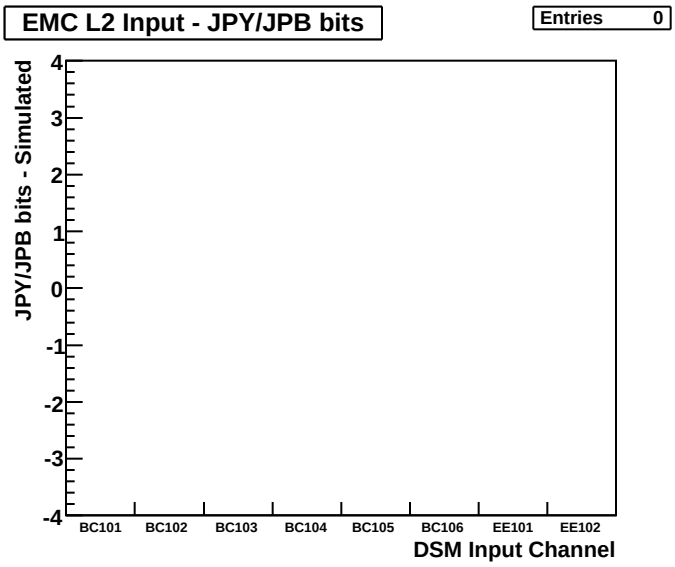

Endcap EMC L1 Input - High Tower Bits
Entries 2399988
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

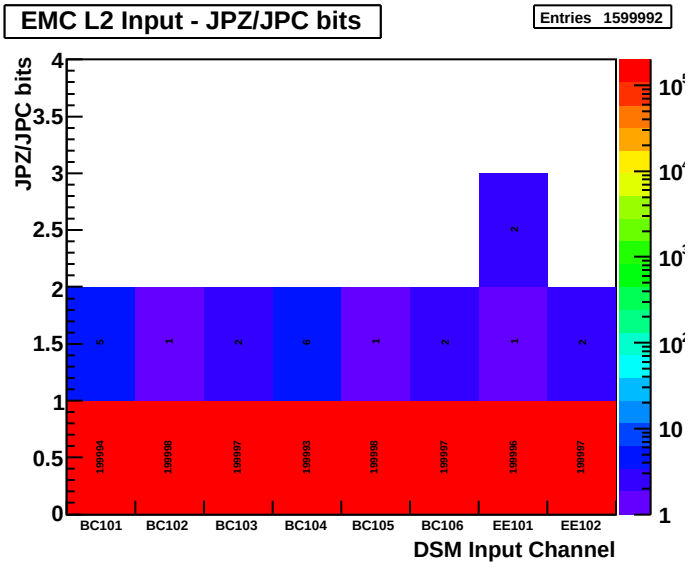

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

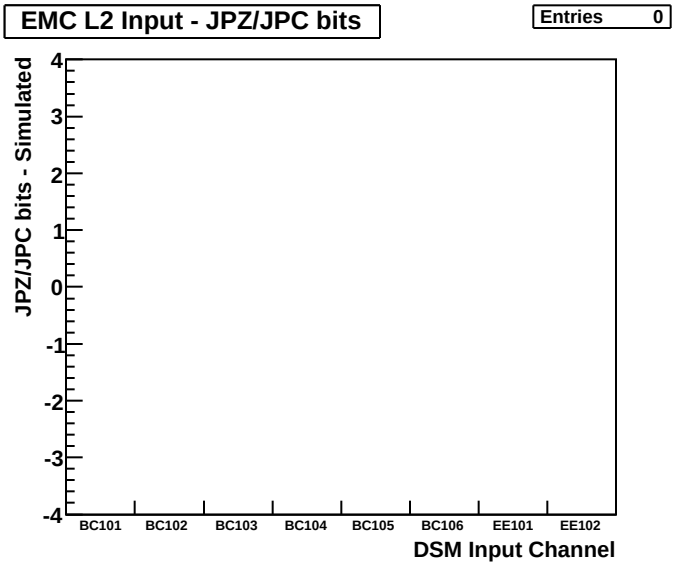

EMC L2 Input - JPX/JPA bits
Entries 1599992
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

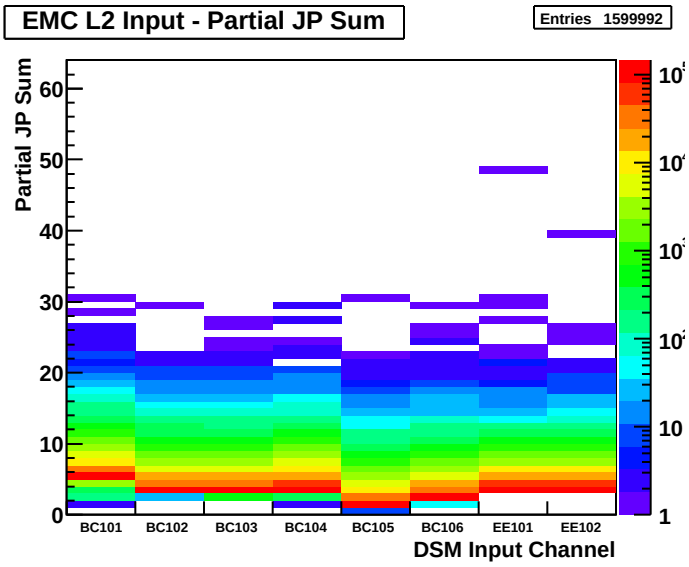

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

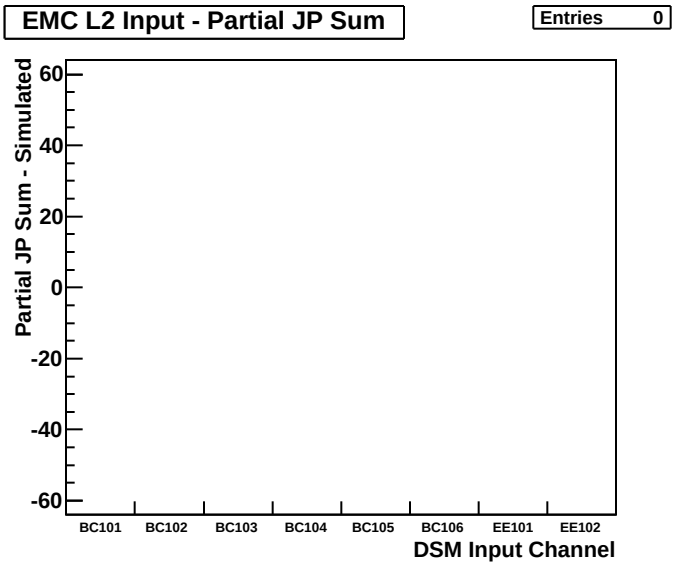

EMC L2 Input - JPY/JPB bits
Entries 1599992
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

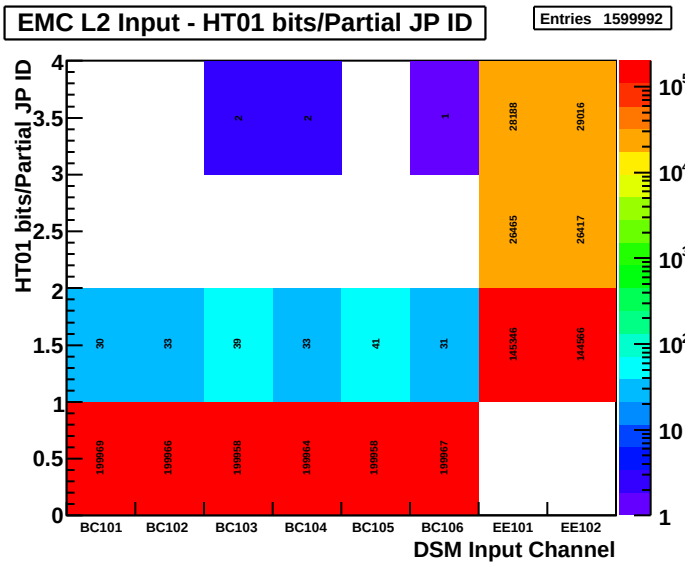

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

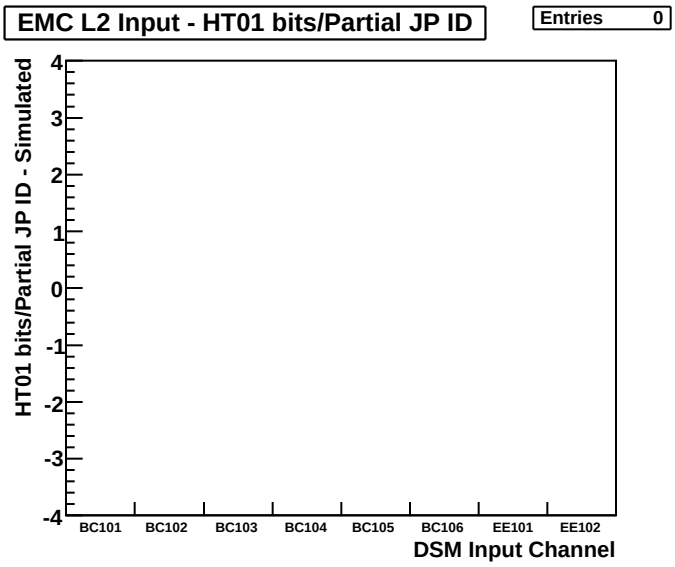

EMC L2 Input - JPZ/JPC bits
Entries 1599992
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

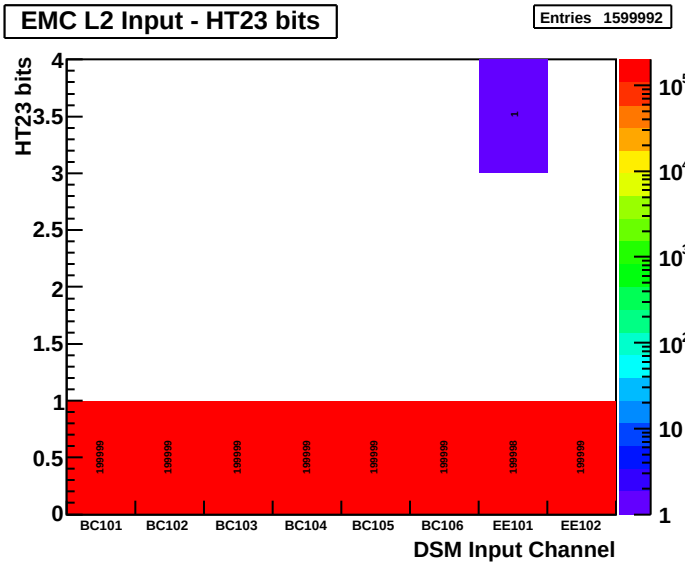

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

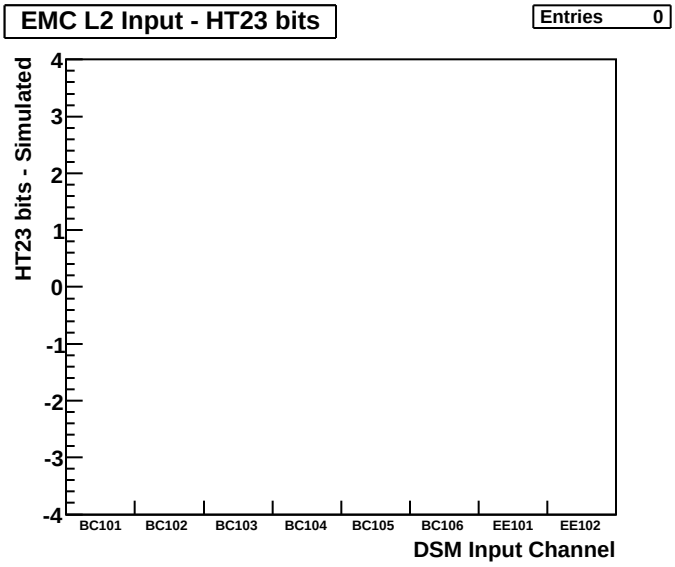

EMC L2 Input - Partial JP Sum
Entries 1599992
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

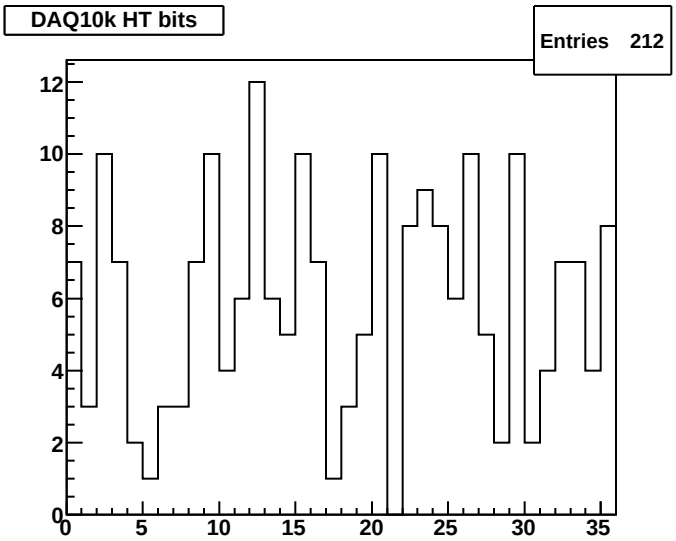

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

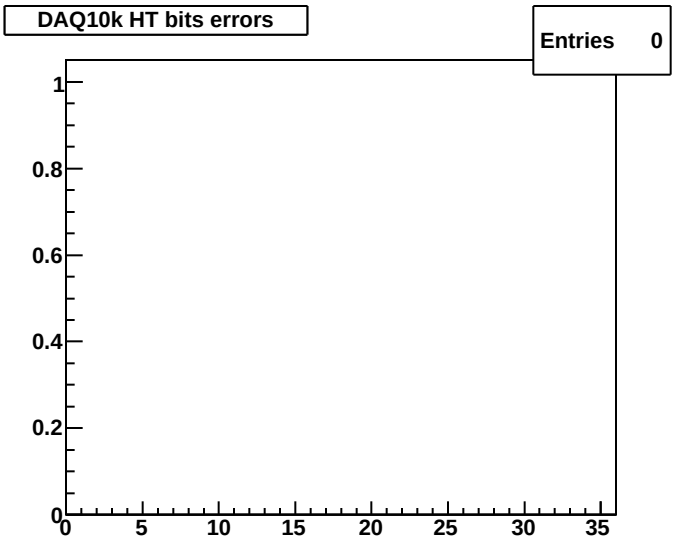

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1599992
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

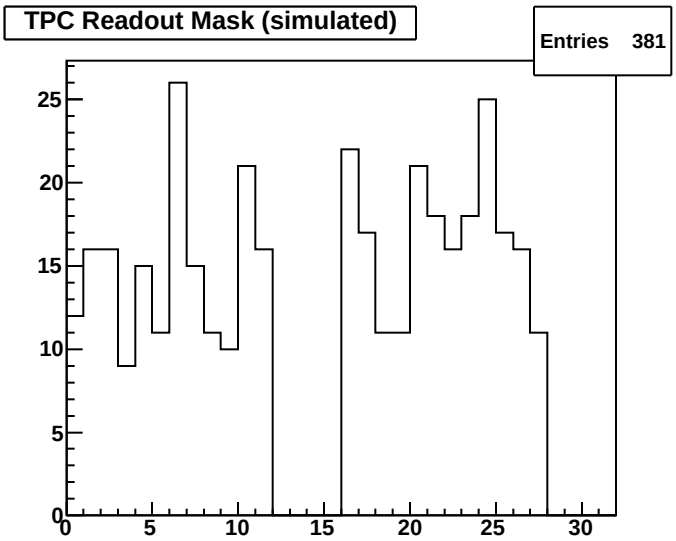

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

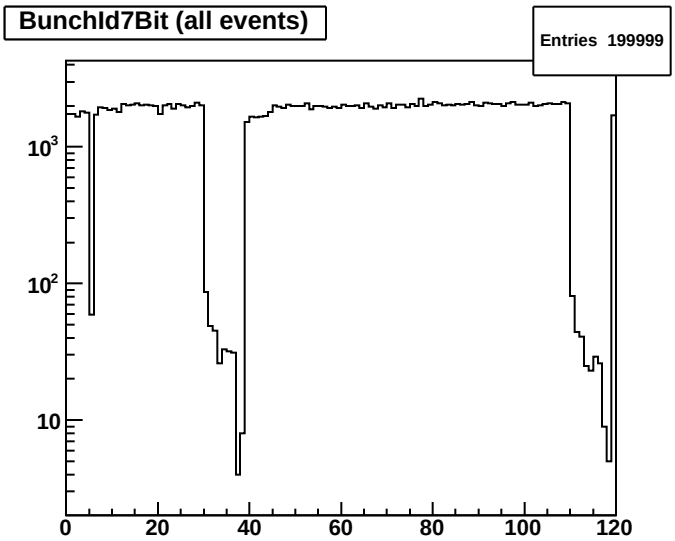

EMC L2 Input - HT23 bits
Entries 1599992
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

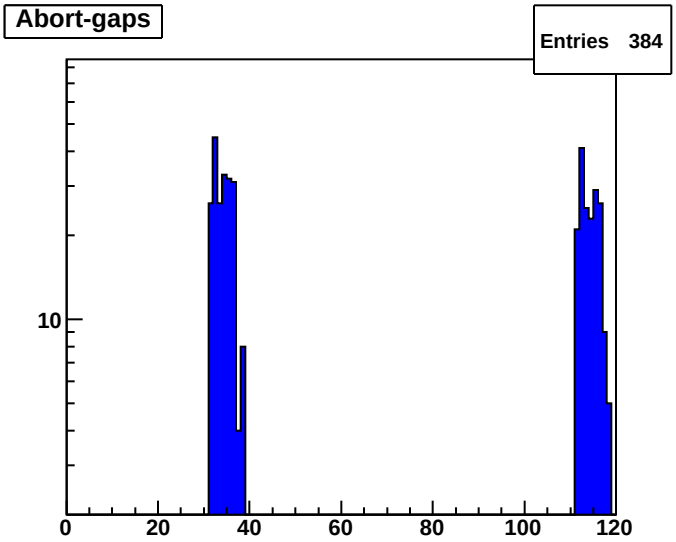

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

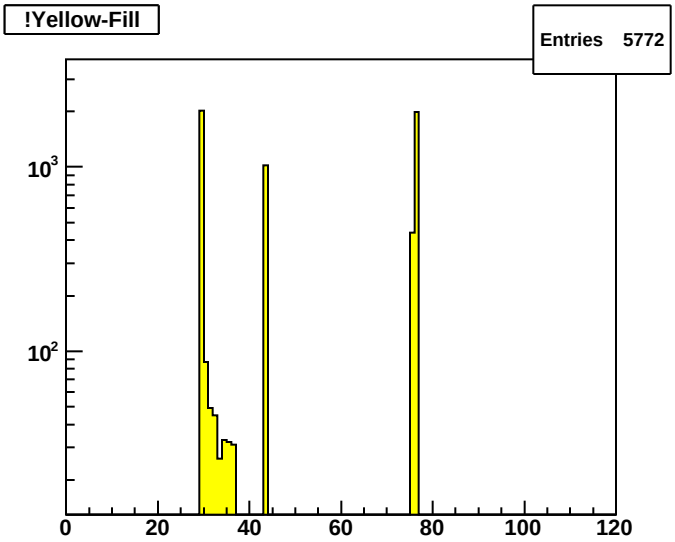

DAQ10k HT bits
Entries 212

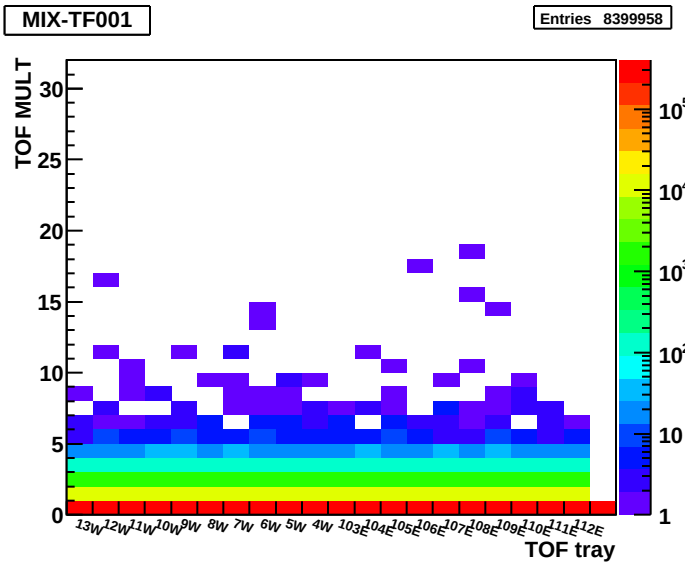

DAQ10k HT bits errors
Entries 0

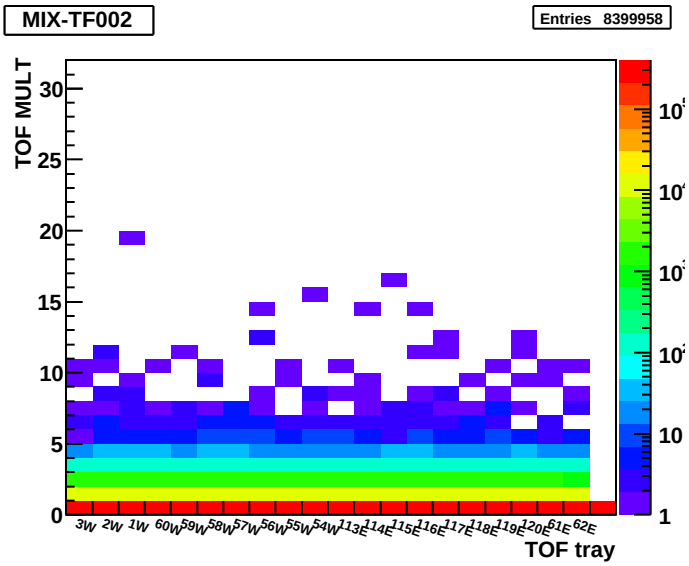

TPC Readout Mask (simulated)
Entries 381

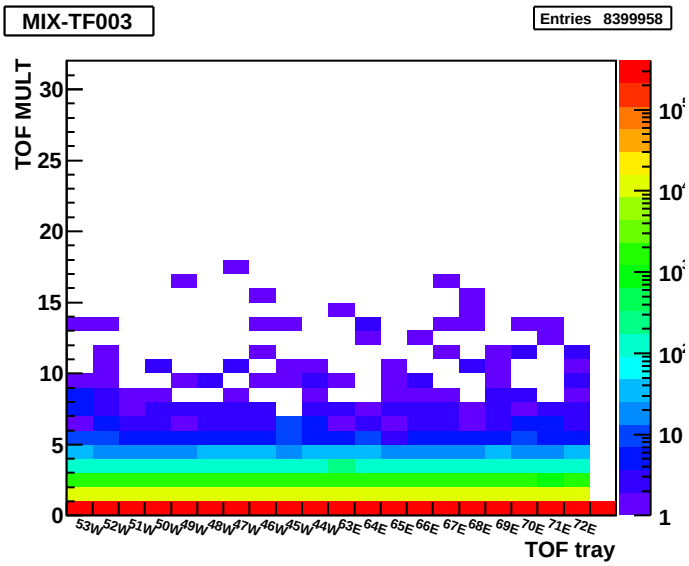

BunchId7Bit (all events)
Entries 199999

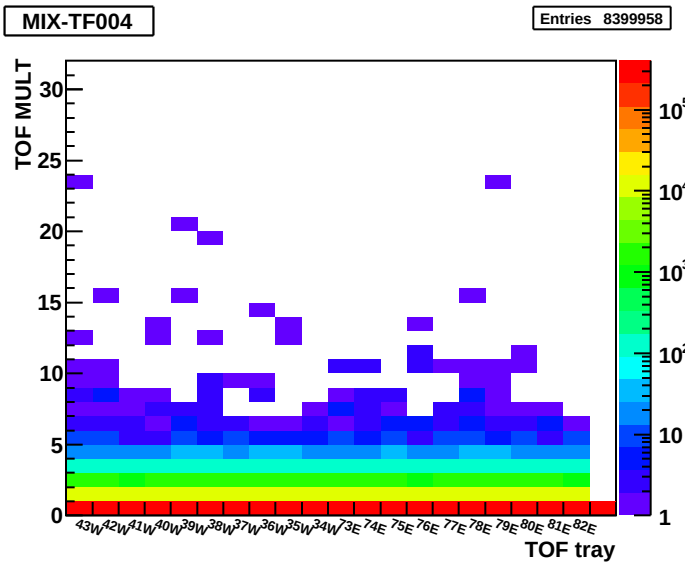

Abort-gaps
Entries 384

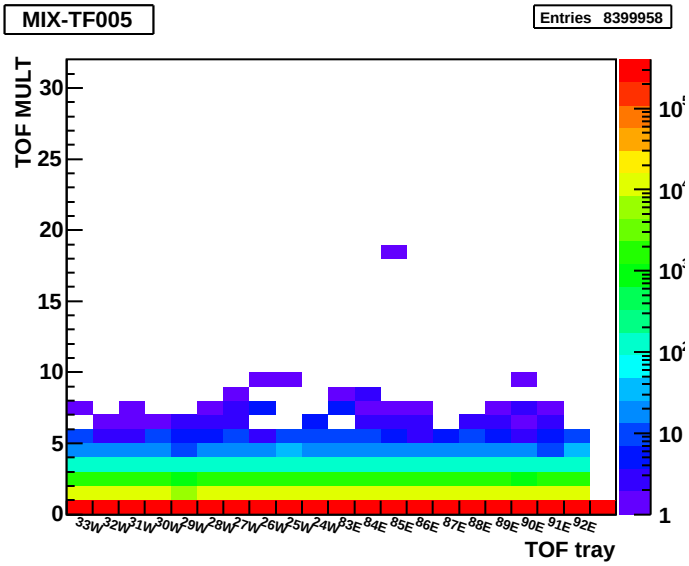

!Yellow-Fill
Entries 5772

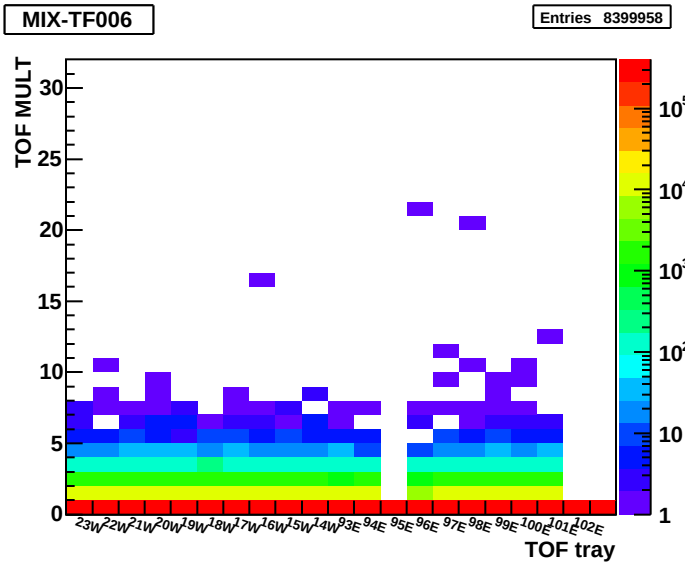

MIX-TF001
Entries 8399958
TOF MULT
TOF tray

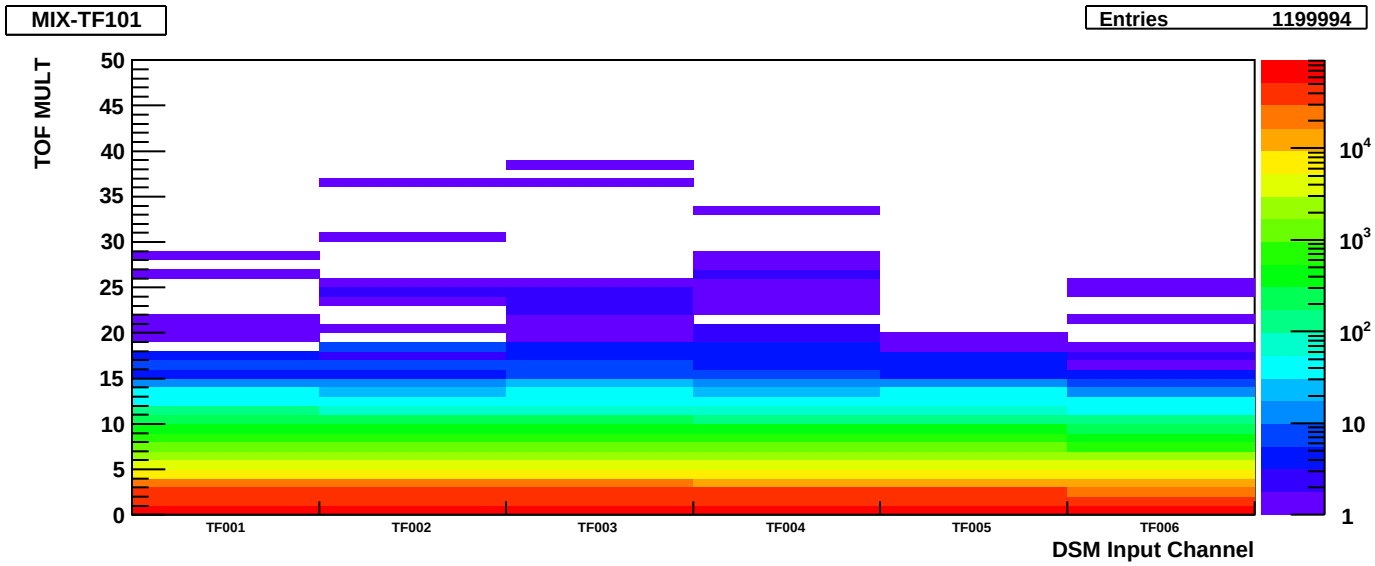

MIX-TF002
Entries 8399958
TOF MULT
TOF tray

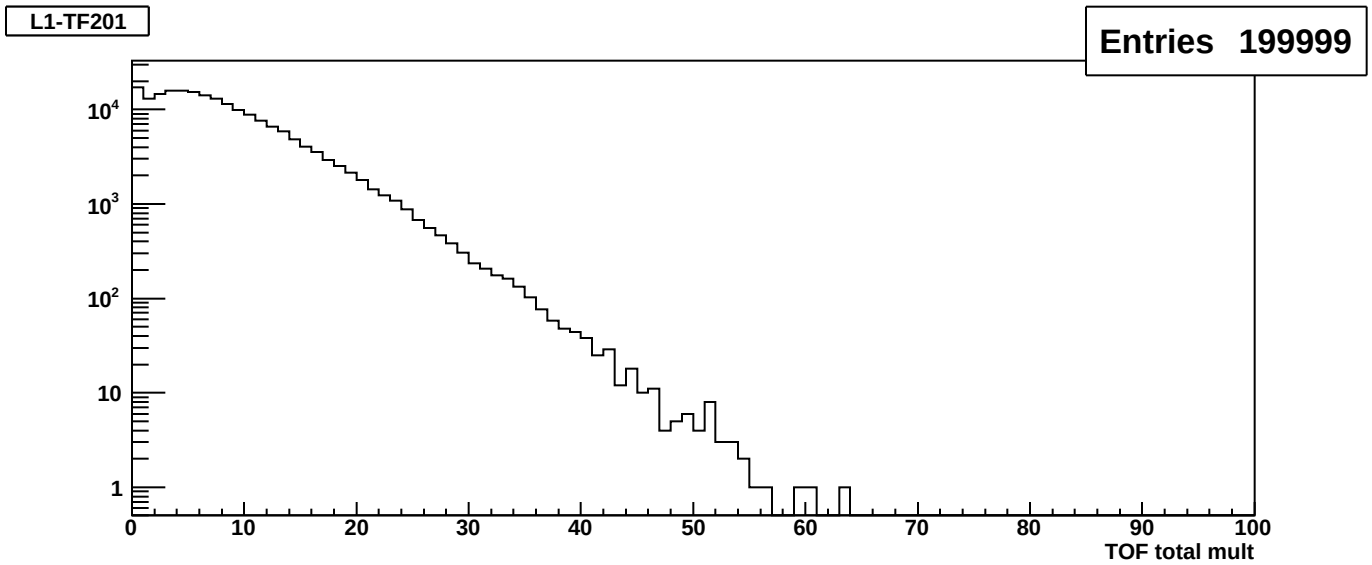

MIX-TF003
Entries 8399958
TOF MULT
TOF tray

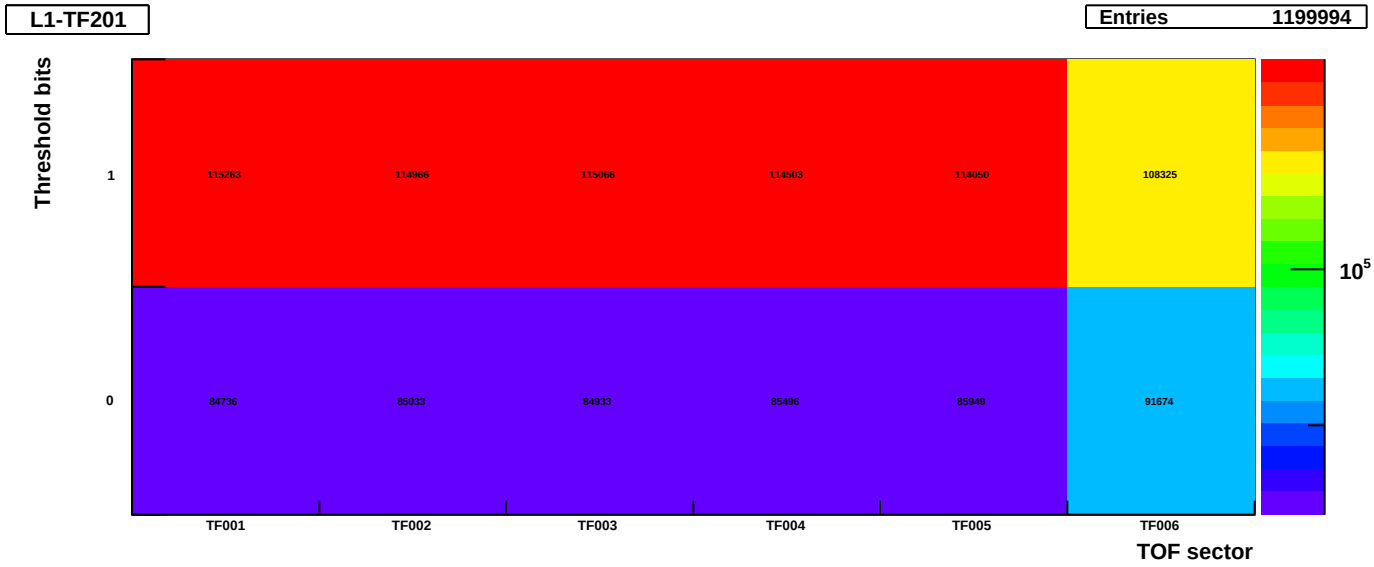

MIX-TF004
Entries 8399958
TOF MULT
TOF tray

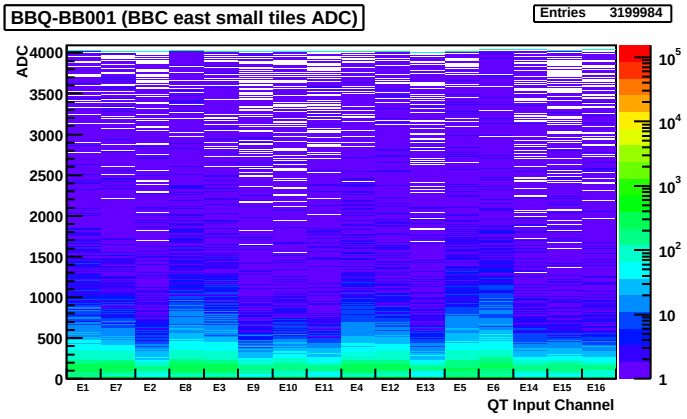

MIX-TF005
Entries 8399958
TOF MULT
TOF tray

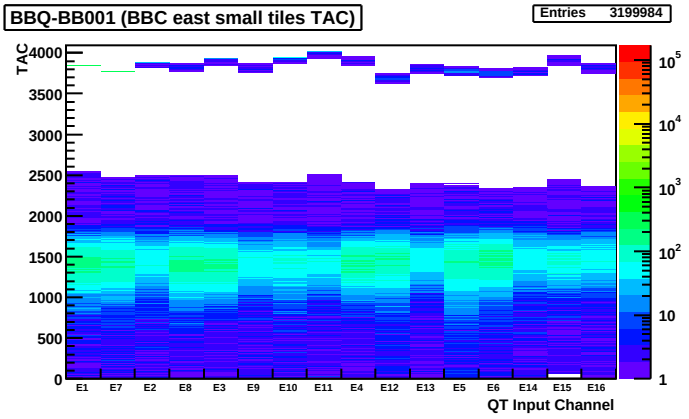

MIX-TF006
Entries 8399958
TOF MULT
TOF tray

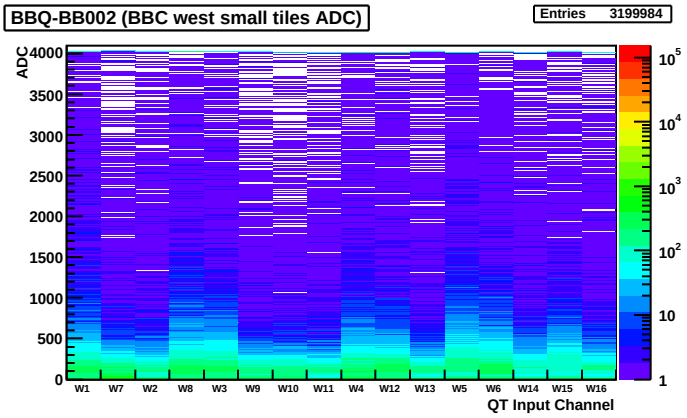

MIX-TF101
Entries 1199994
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

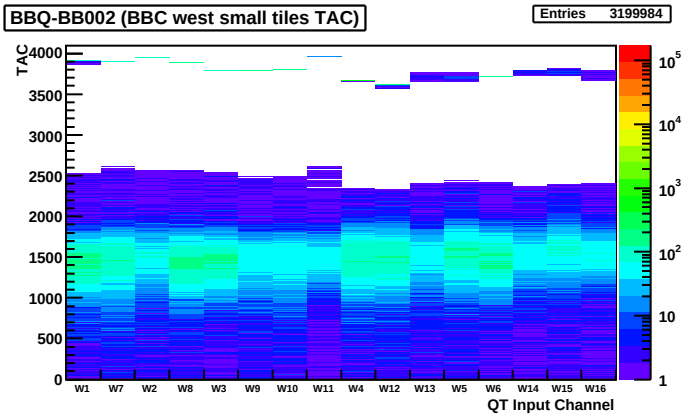

L1-TF201
Entries 199999
TOF total mult

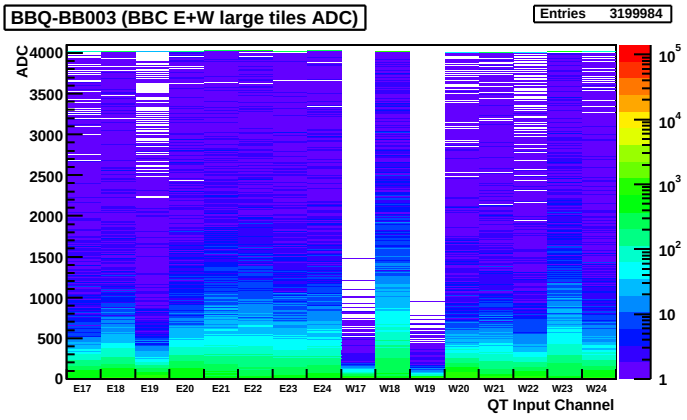

L1-TF201
Entries 1199994
Threshold bits
115263
114966
115066
114503
114050
108325
84736
85033
84933
85496
85949
91674
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

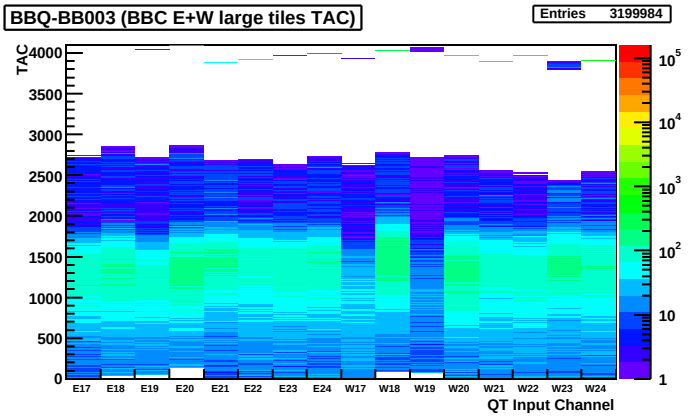

BBQ-BB001 (BBC east small tiles ADC)
Entries 3199984
ADC
QT Input Channel

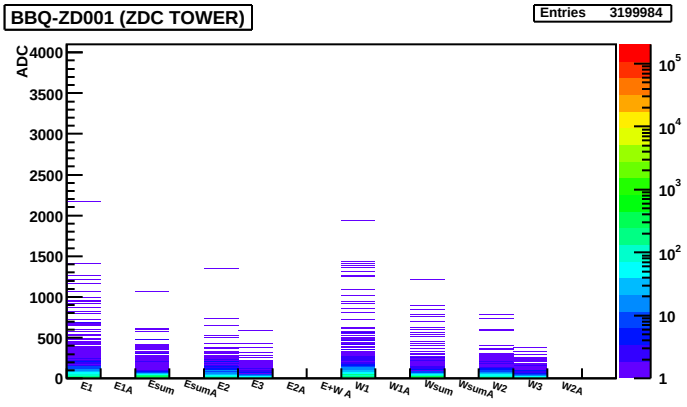

BBQ-BB001 (BBC east small tiles TAC)
Entries 3199984
TAC
QT Input Channel

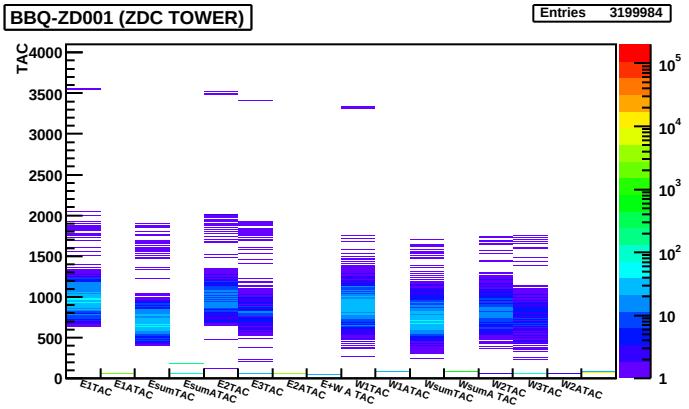

BBQ-BB002 (BBC west small tiles ADC)
Entries 3199984
ADC
QT Input Channel

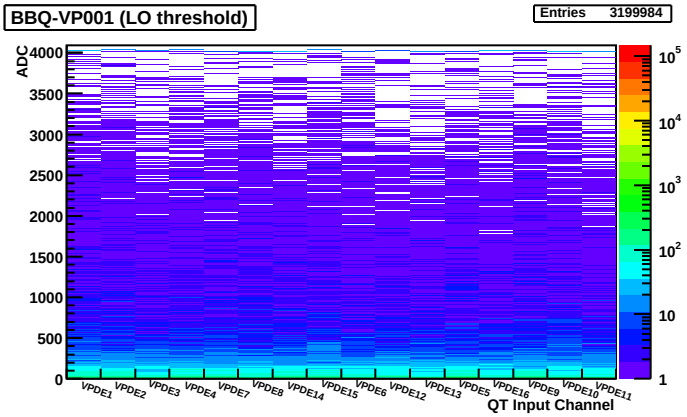

BBQ-BB002 (BBC west small tiles TAC)
Entries 3199984
TAC
QT Input Channel

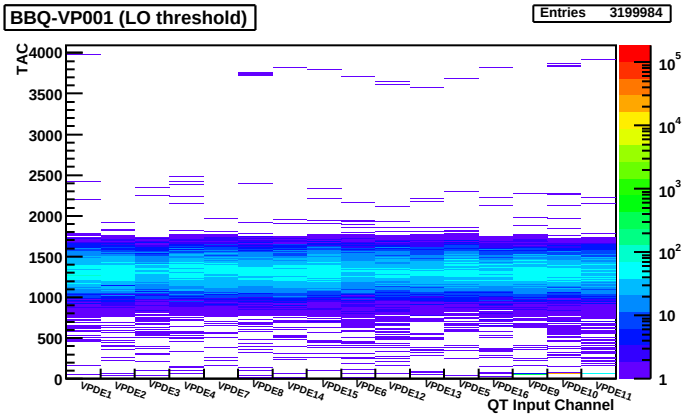

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3199984
ADC
QT Input Channel

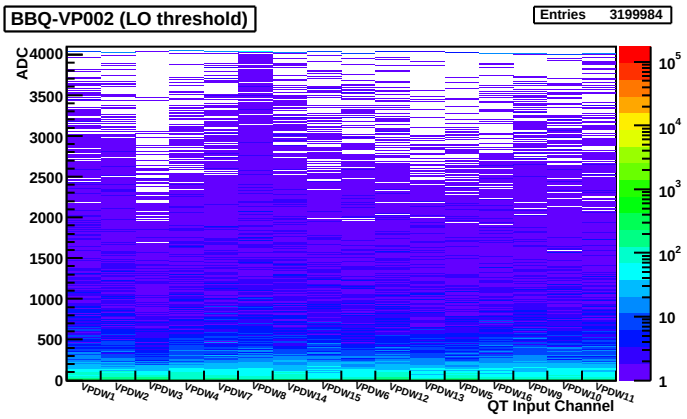

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3199984
TAC
QT Input Channel

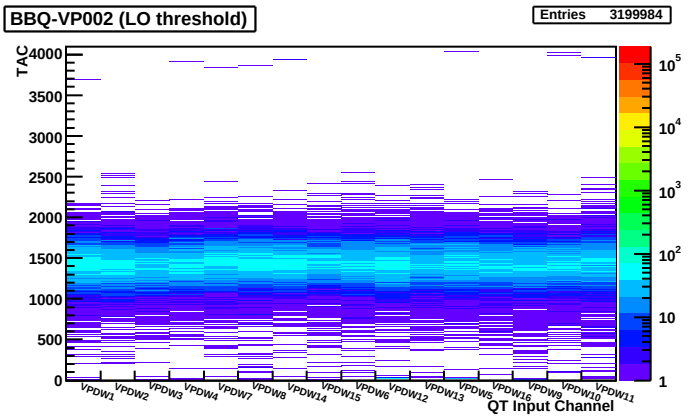

BBQ-ZD001 (ZDC TOWER)
Entries 3199984
ADC

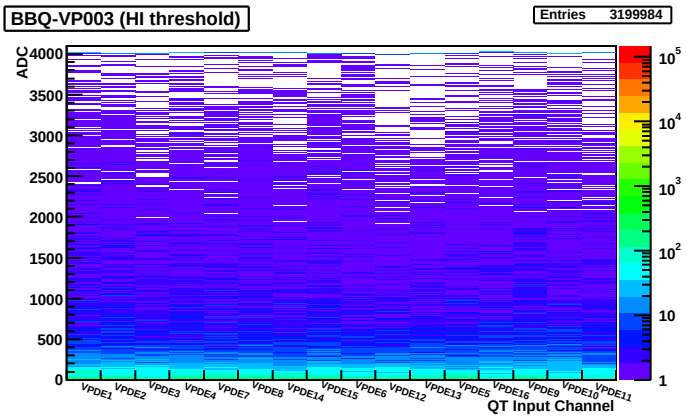

BBQ-ZD001 (ZDC TOWER)
Entries 3199984
TAC

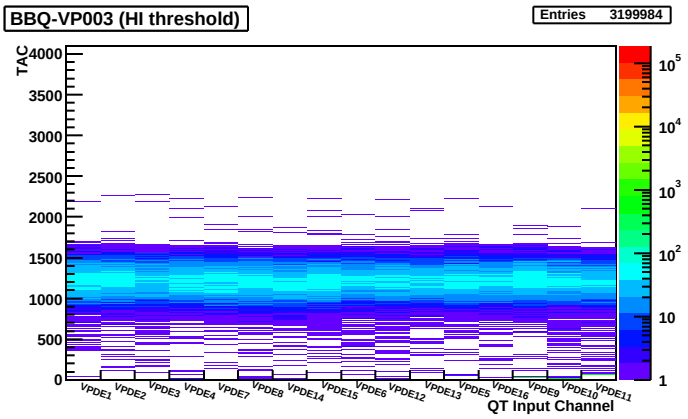

BBQ-VP001 (LO threshold)
Entries 3199984
ADC
QT Input Channel

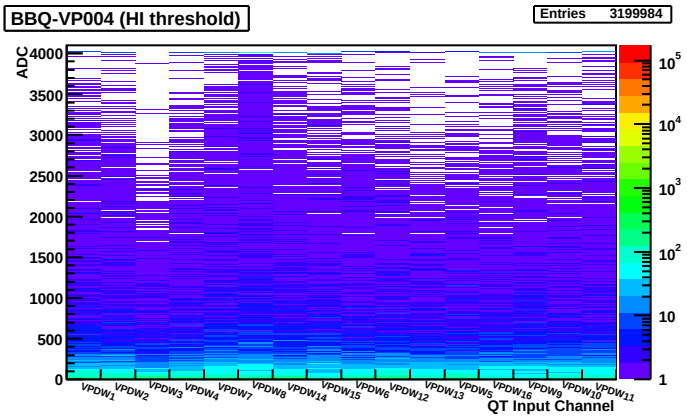

BBQ-VP001 (LO threshold)
Entries 3199984
TAC
QT Input Channel

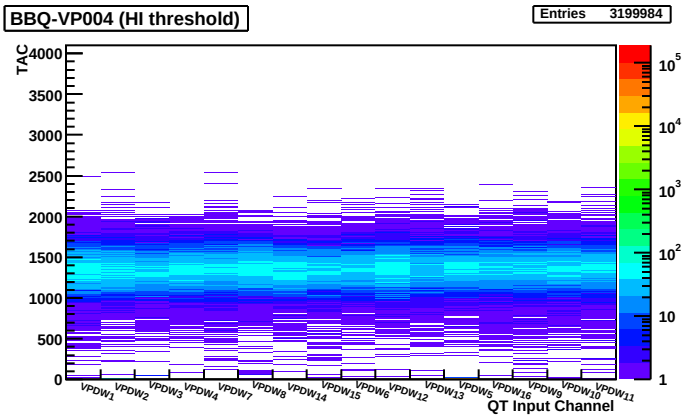

BBQ-VP002 (LO threshold)
Entries 3199984
ADC
QT Input Channel

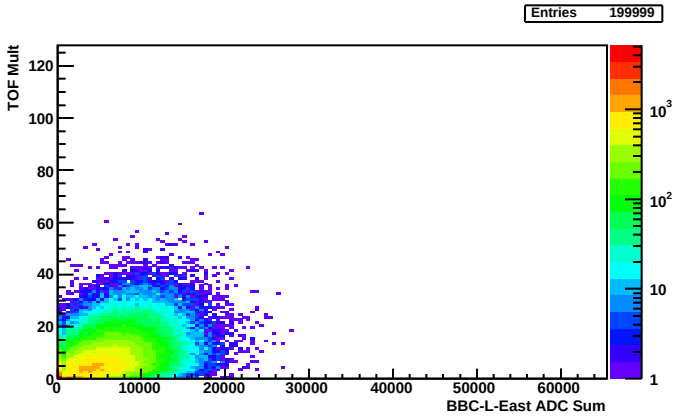

BBQ-VP002 (LO threshold)
Entries 3199984
TAC
QT Input Channel

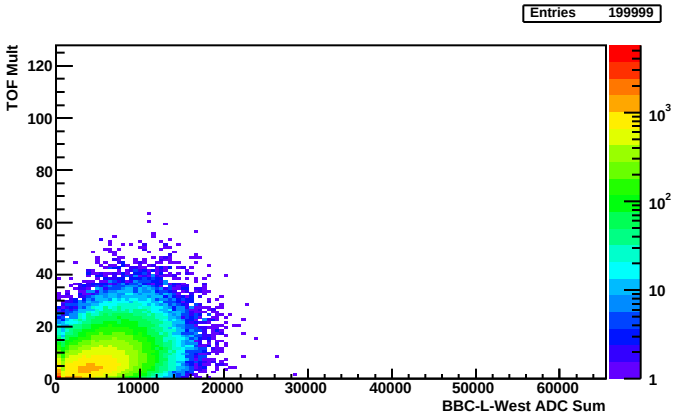

BBQ-VP003 (HI threshold)
Entries 3199984
ADC
QT Input Channel

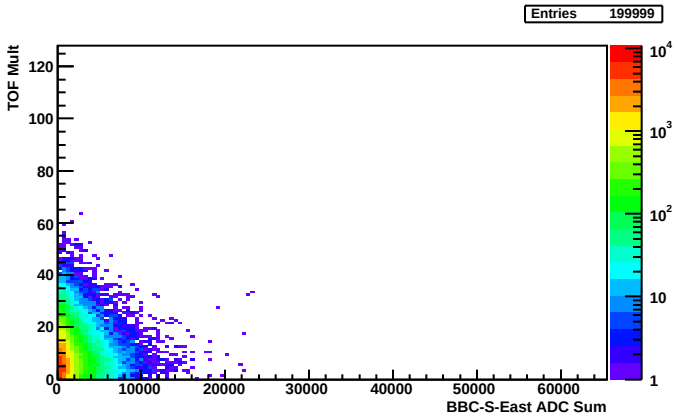

BBQ-VP003 (HI threshold)
Entries 3199984
TAC
QT Input Channel

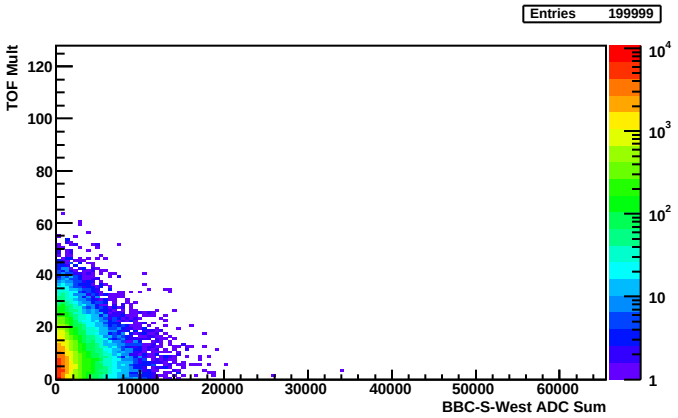

BBQ-VP004 (HI threshold)
Entries 3199984
ADC
QT Input Channel

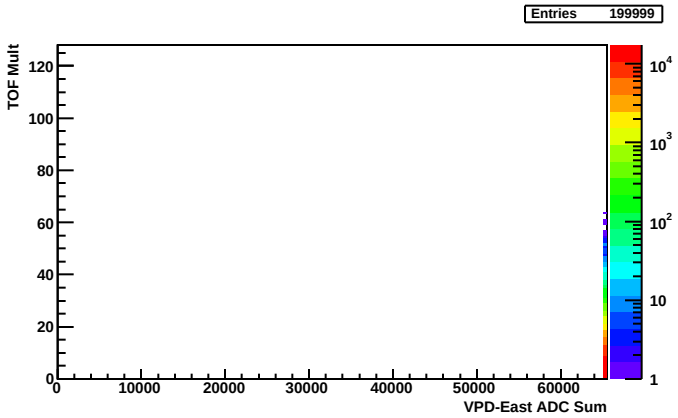

BBQ-VP004 (HI threshold)
Entries 3199984
TAC
QT Input Channel

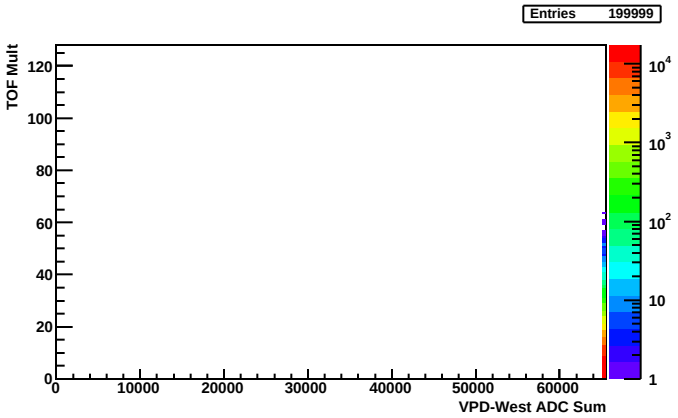

Entries 199999
TOF Mult
BBC-L-East ADC Sum

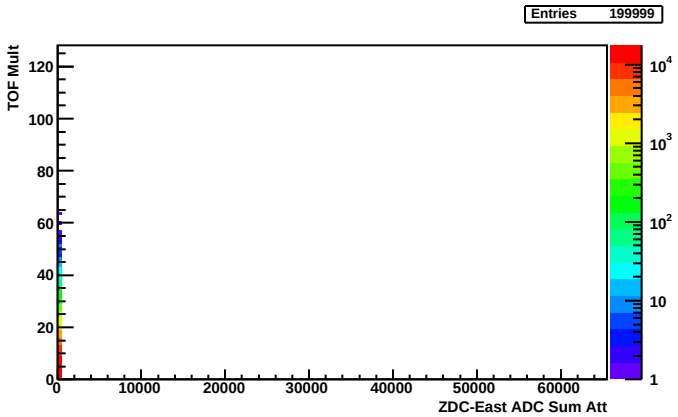

Entries 199999
TOF Mult
BBC-L-West ADC Sum

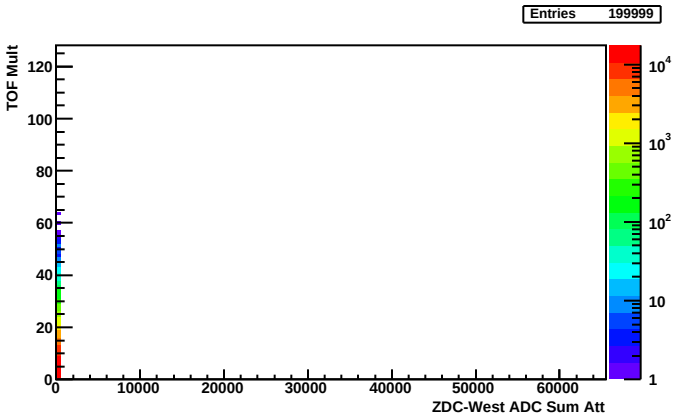

Entries 199999
TOF Mult
BBC-S-East ADC Sum

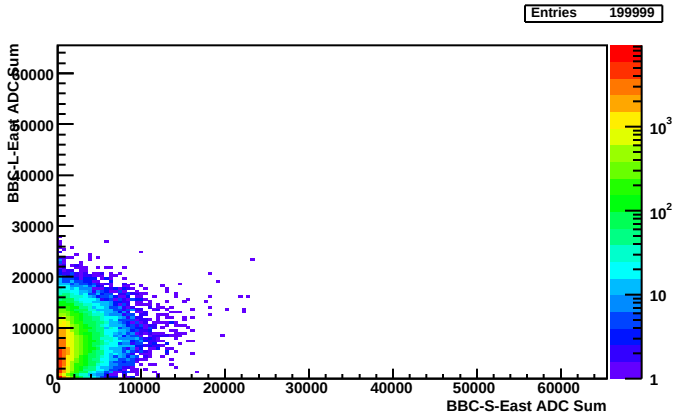

Entries 199999
TOF Mult
BBC-S-West ADC Sum

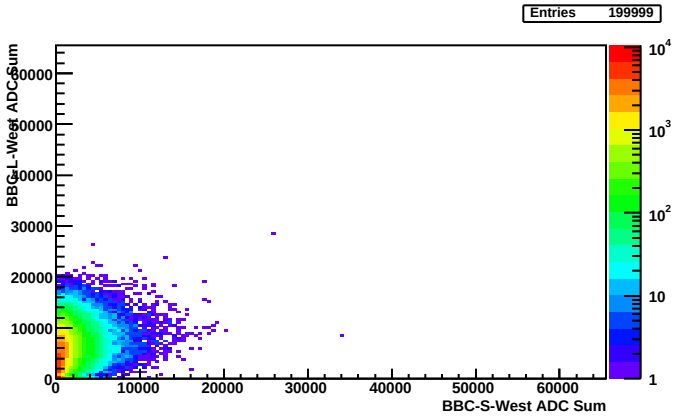

Entries 199999
TOF Mult
VPD-East ADC Sum

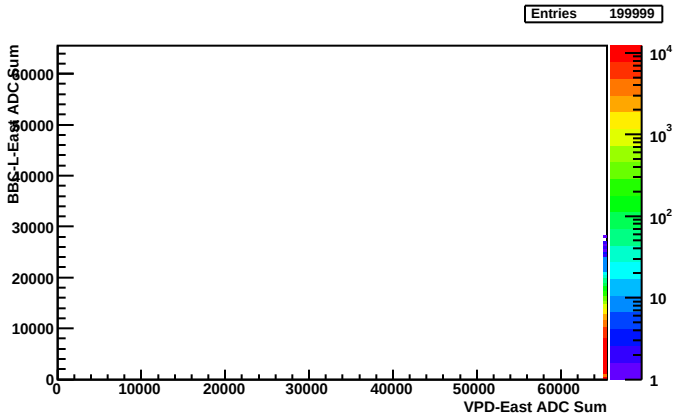

Entries 199999
TOF Mult
VPD-West ADC Sum


Entries 199999
TOF Mult
ZDC-East ADC Sum Att


Entries 199999
TOF Mult
ZDC-West ADC Sum Att


Entries 199999
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 199999
BBC-L-West ADC Sum
BBC-S-West ADC Sum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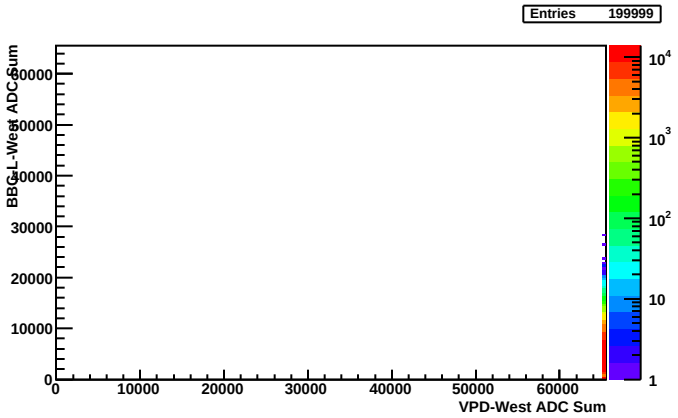
BBC-L-East ADC Sum
VPD-East ADC Sum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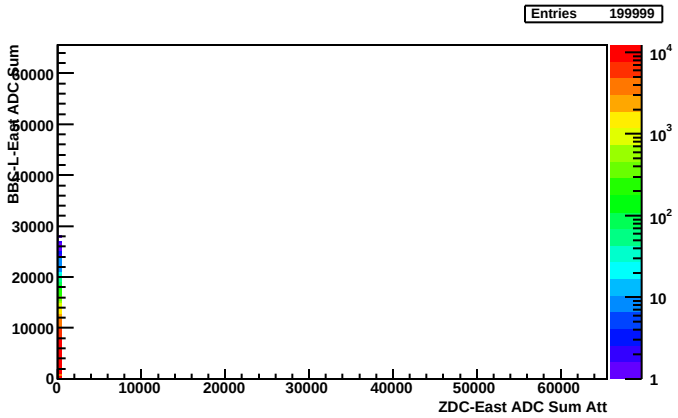
BBC-L-West ADC Sum
VPD-West ADC Sum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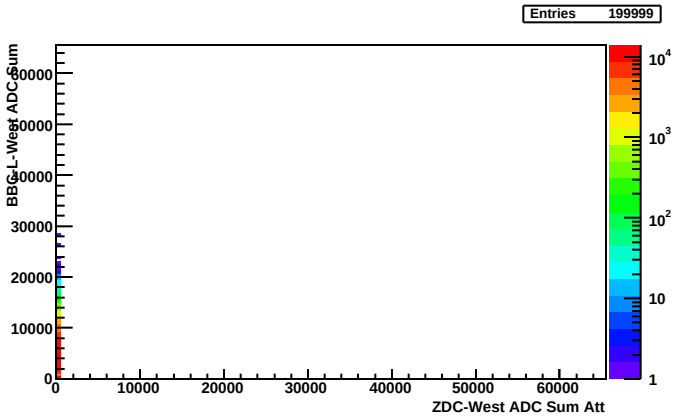
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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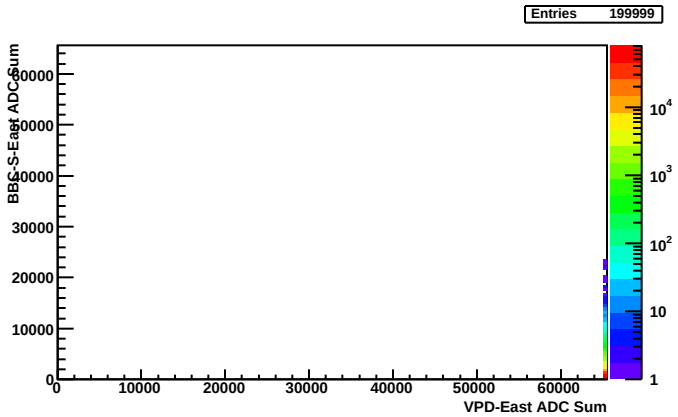
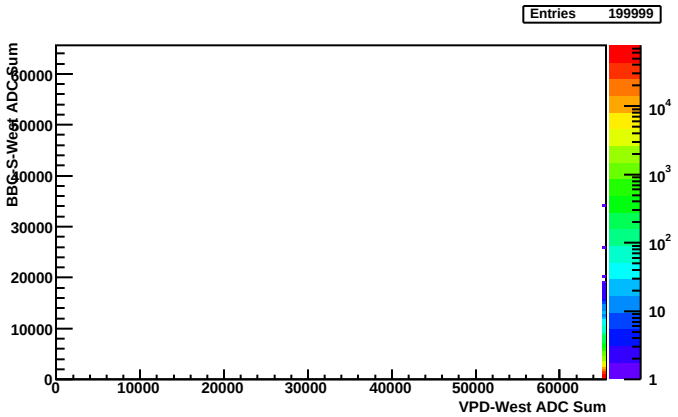
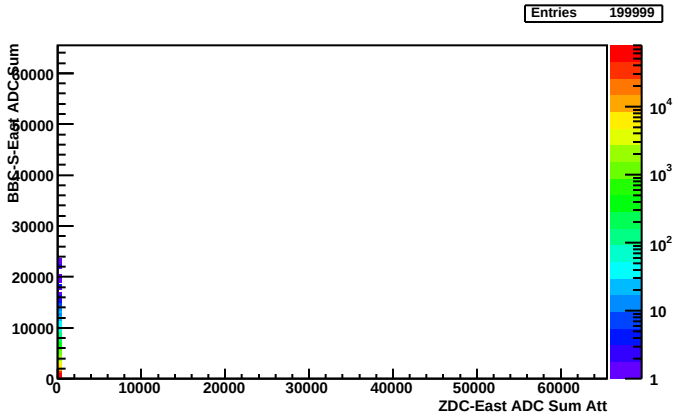
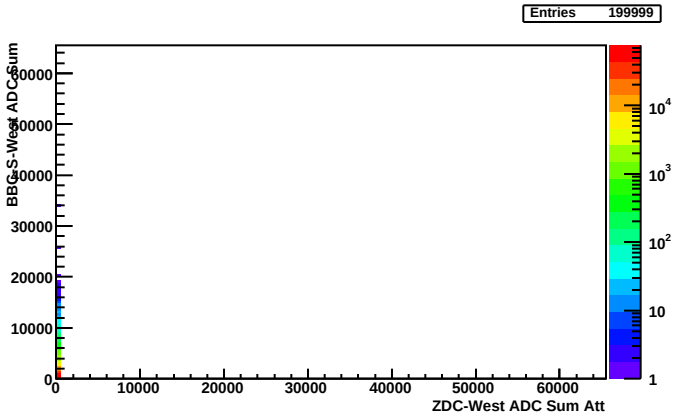
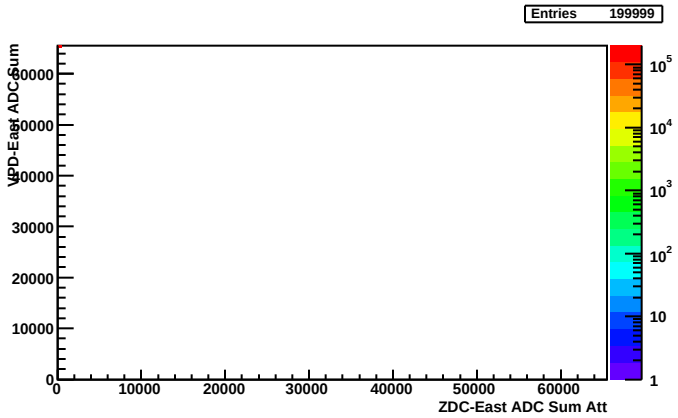
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

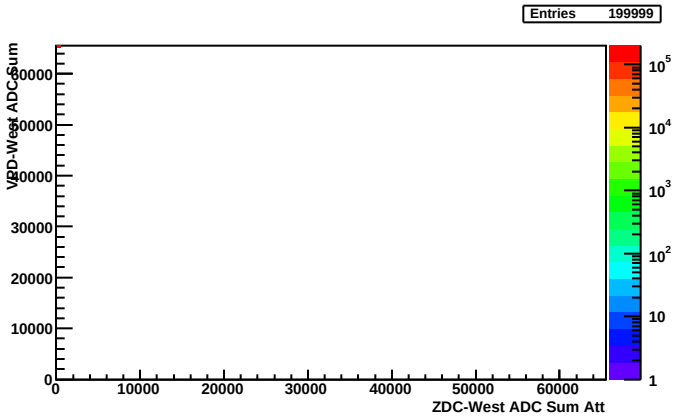

Entries 199999
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 199999
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 199999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 199999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 199999
VPD-East ADC Sum
ZDC-East ADC Sum Att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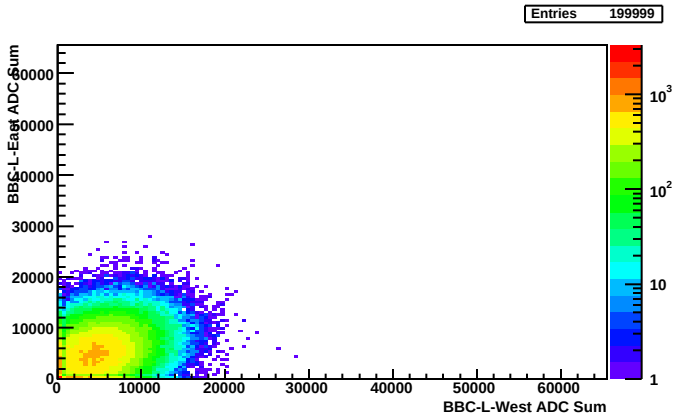
ZDC-West ADC Sum Att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BBC-L-East ADC Sum
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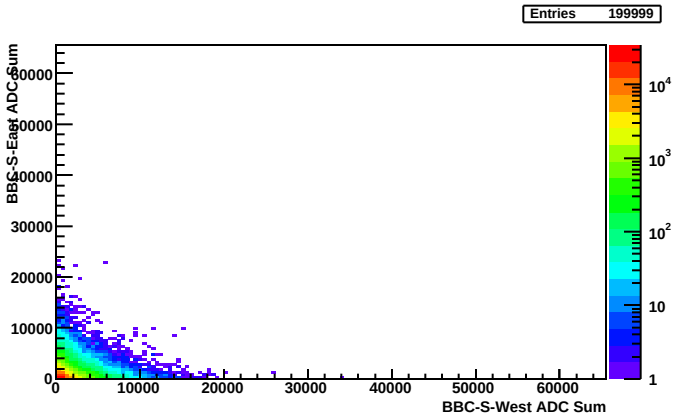
Entries 199999
BBC-S-East ADC Sum
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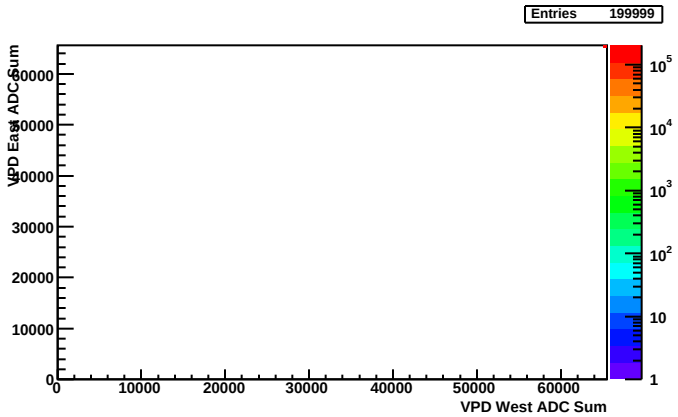
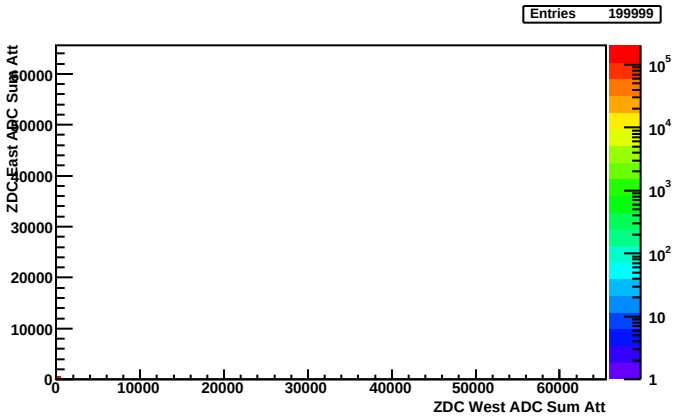
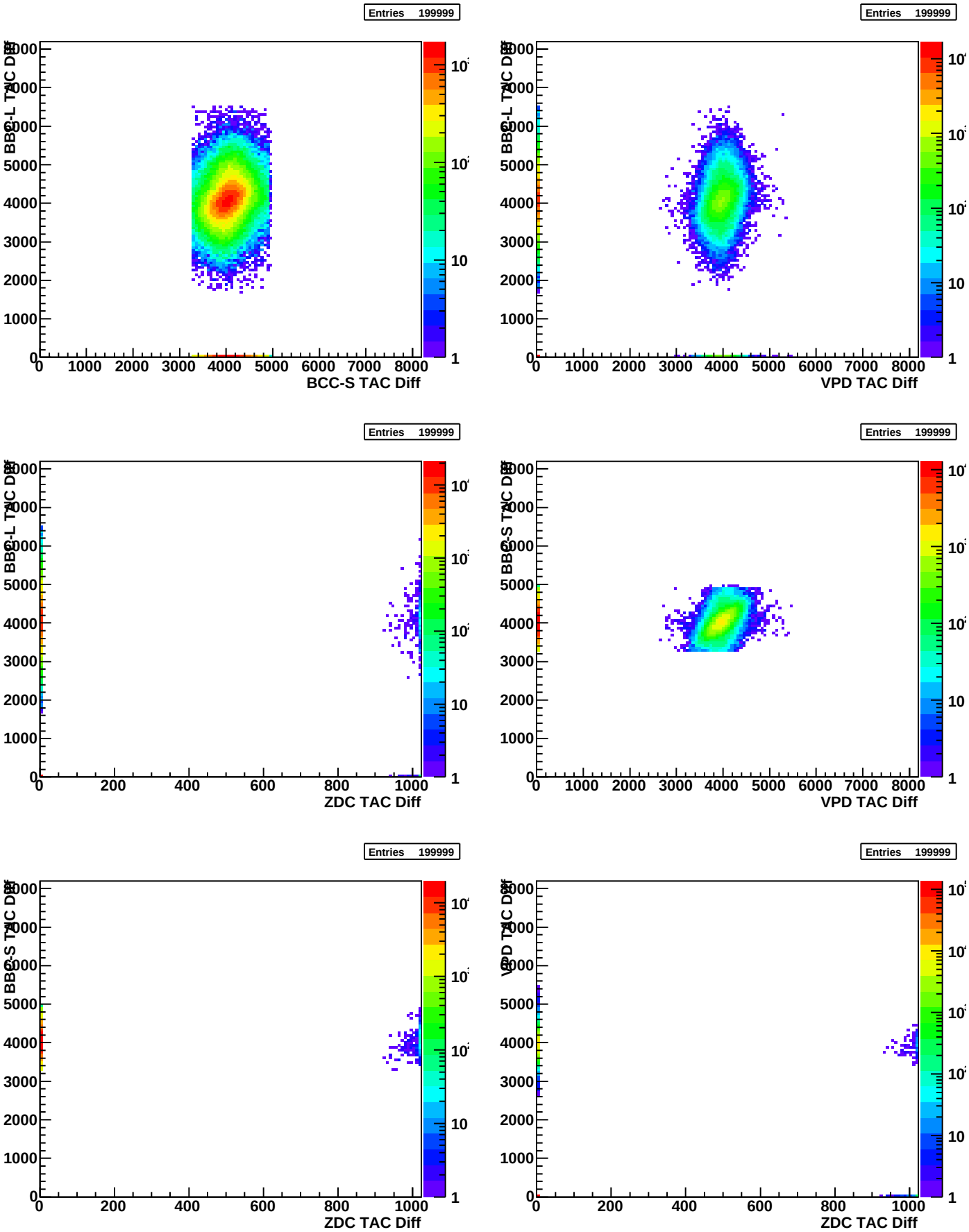
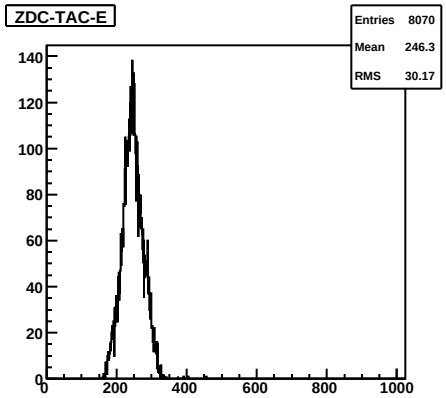
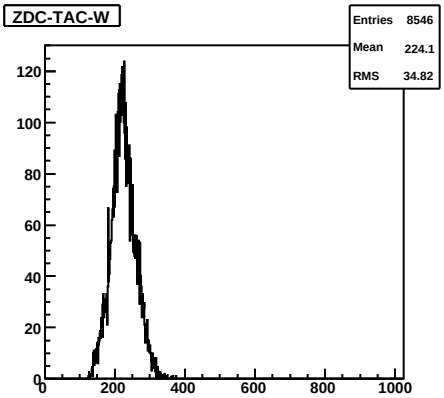
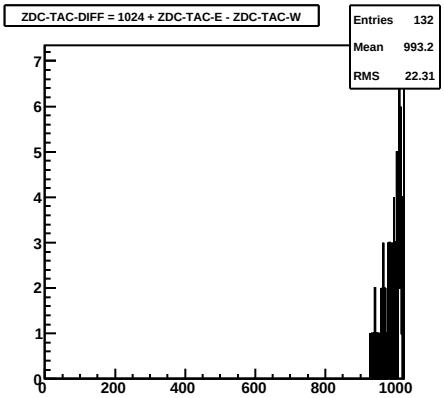

Entries 199999
VPD East ADC Sum
VPD West ADC Sum

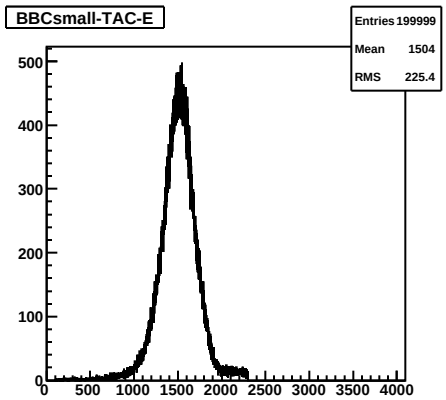

Entries 199999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

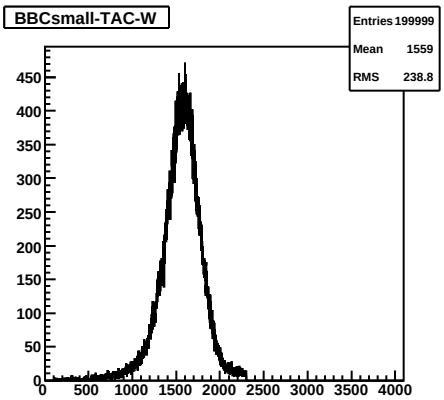

Entries 199999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 199999
BBC-L TAC Diff
VPD TAC Diff
Entries 199999
BBC-L TAC Diff
ZDC TAC Diff
Entries 199999
BBC-S TAC Diff
VPD TAC Diff
Entries 199999
BBC-S TAC Diff
ZDC TAC Diff
Entries 199999
VPD TAC Diff
ZDC TAC Diff

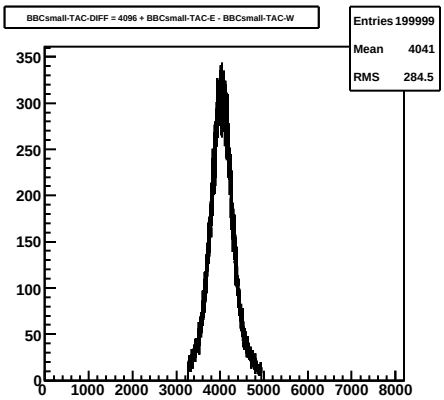

ZDC-TAC-E
Entries 8070
Mean 246.3
RMS 30.17

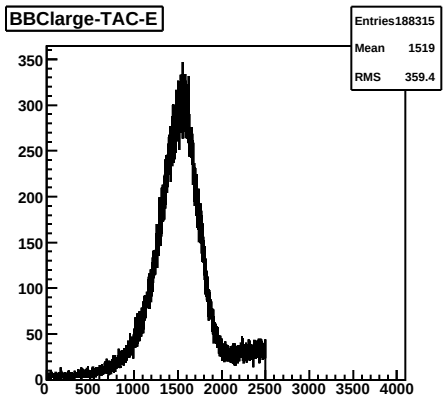

ZDC-TAC-W
Entries 8546
Mean 224.1
RMS 34.82

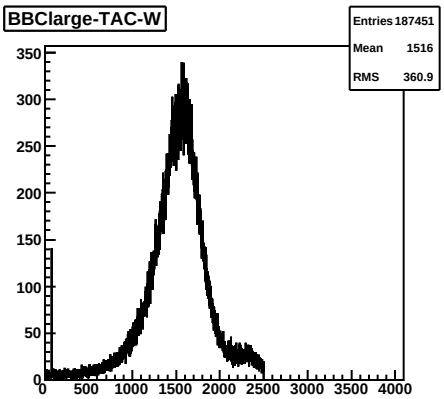

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 132
Mean 993.2
RMS 22.31

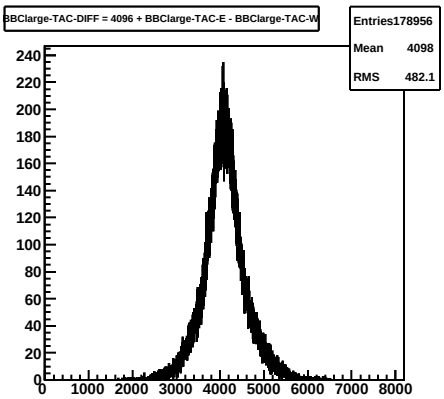

BBCsmall-TAC-E
Entries 199999
Mean 1504
RMS 225.4

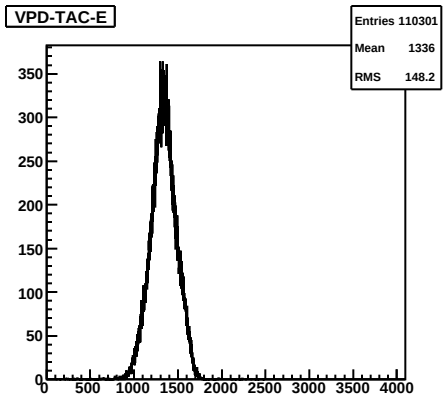

BBCsmall-TAC-W
Entries 199999
Mean 1559
RMS 238.8

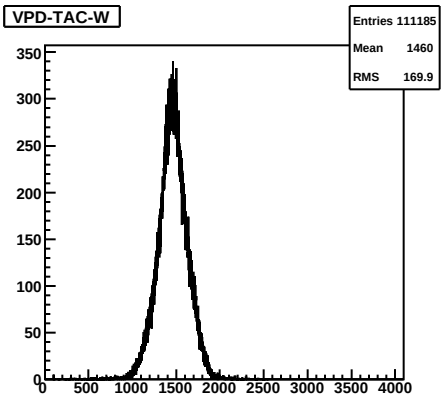

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 199999
Mean 4041
RMS 284.5

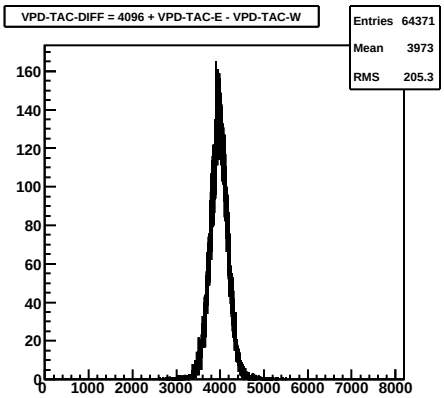

BBClarge-TAC-E
Entries 188315
Mean 1519
RMS 359.4

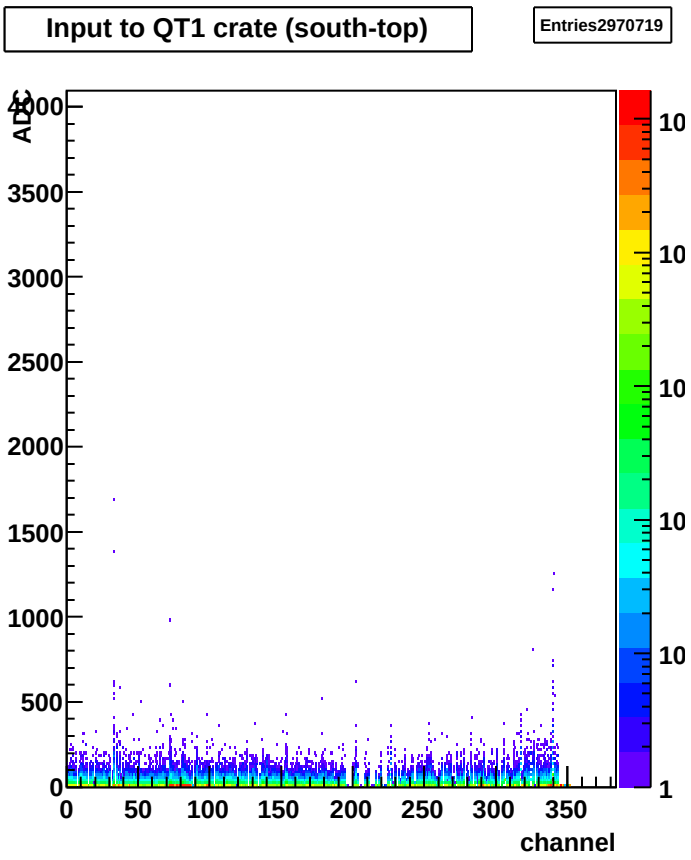

BBClarge-TAC-W
Entries 187451
Mean 1516
RMS 360.9

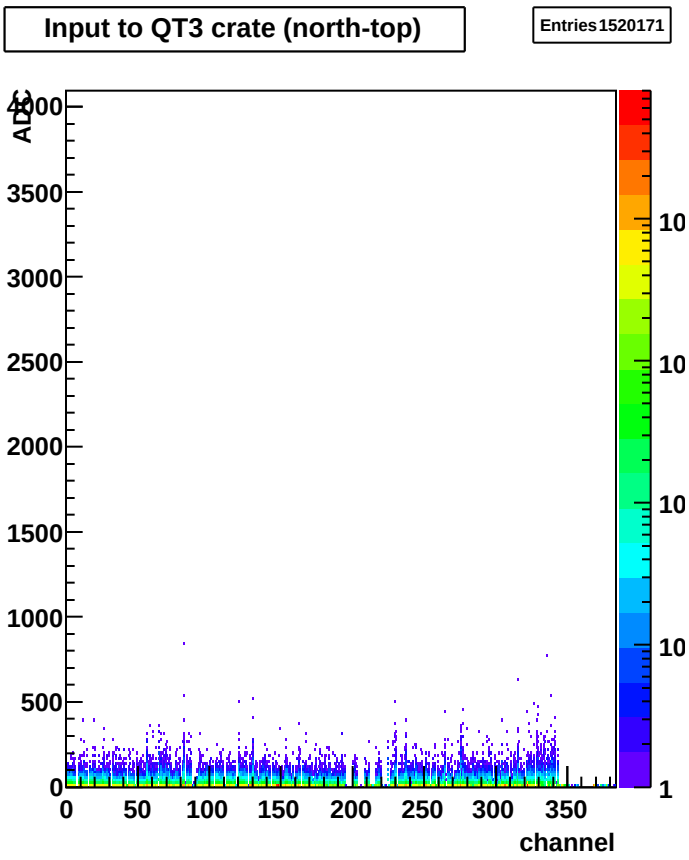

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 178956
Mean 4098
RMS 482.1

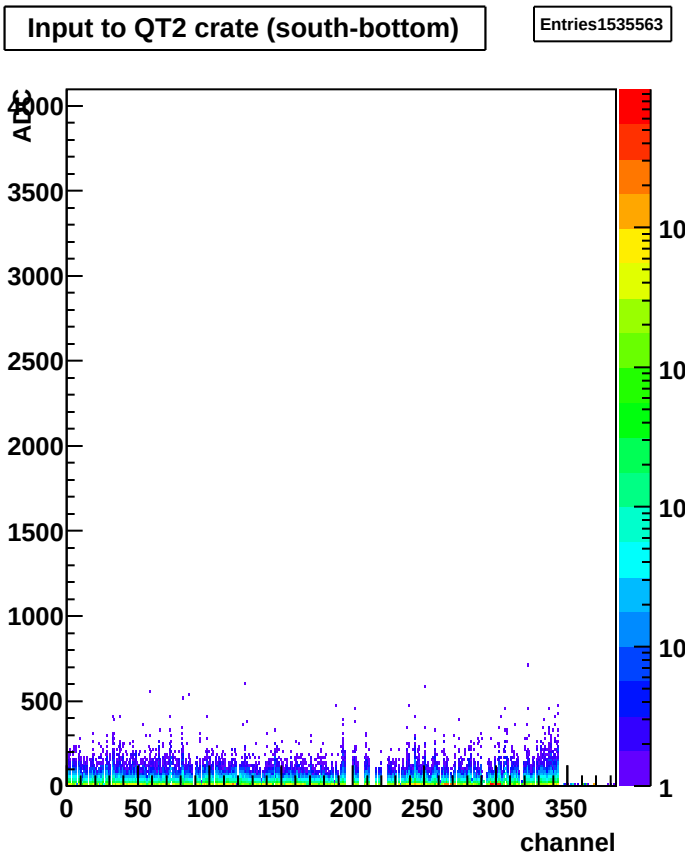

VPD-TAC-E
Entries 110301
Mean 1336
RMS 148.2

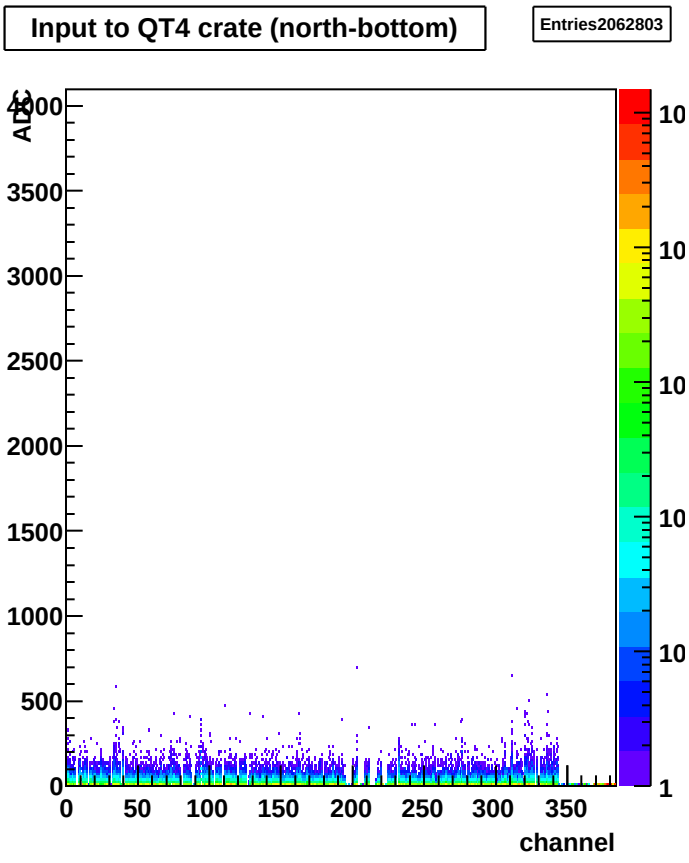

VPD-TAC-W
Entries 111185
Mean 1460
RMS 169.9

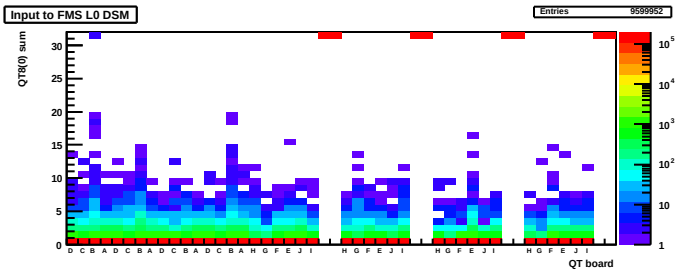

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 64371
Mean 3973
RMS 205.3

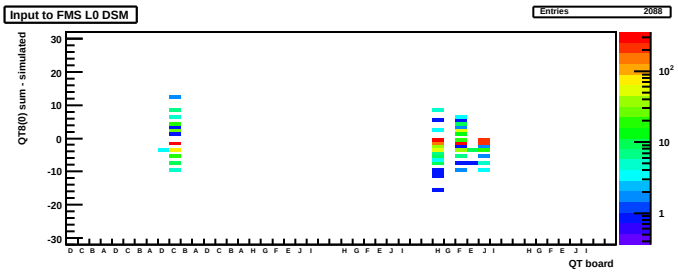

Input to QT1 crate (south-top)
Entries2970719
ADC
channel

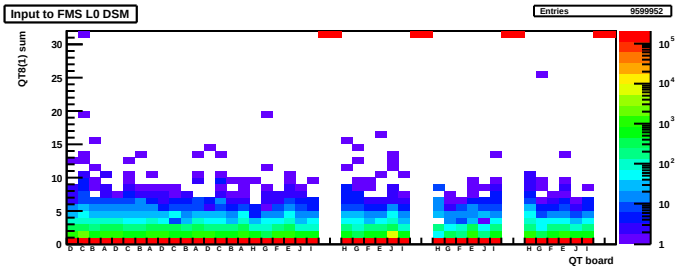

Input to QT3 crate (north-top)
Entries1520171
ADC
channel

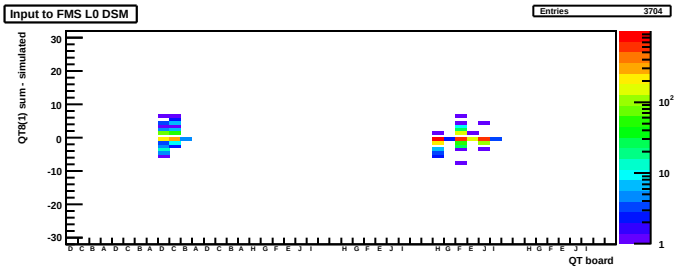

Input to QT2 crate (south-bottom)
Entries1535563
ADC
channel

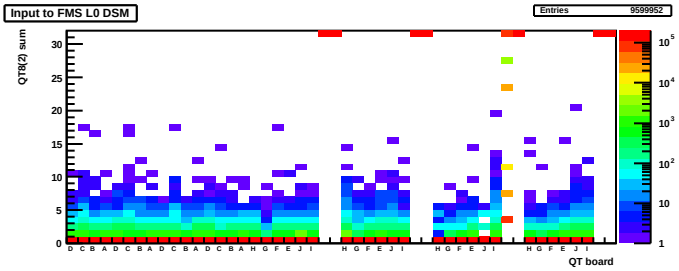

Input to QT4 crate (north-bottom)
Entries2062803
ADC
channel

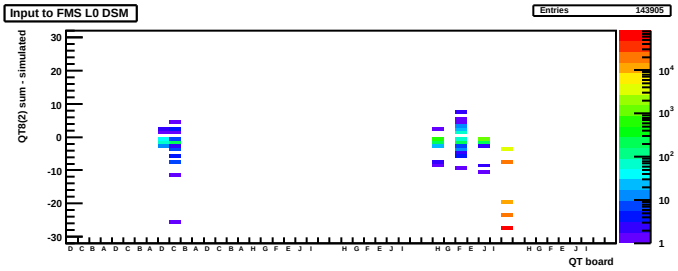

Input to FMS L0 DSM
Entries 9599952
QT8(0) sum
QT board

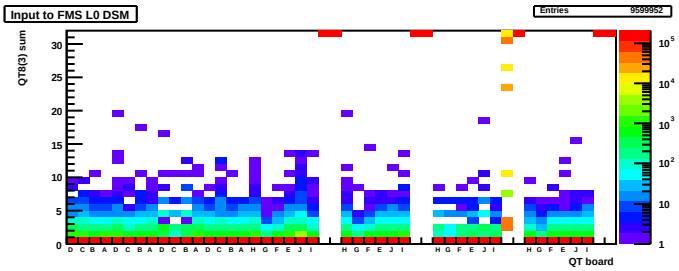

Input to FMS L0 DSM
Entries 2088
QT8(0) sum - simulated
QT board

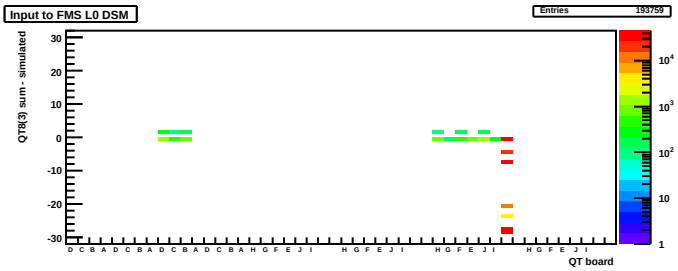

Input to FMS L0 DSM
Entries 9599952
QT8(1) sum
QT board

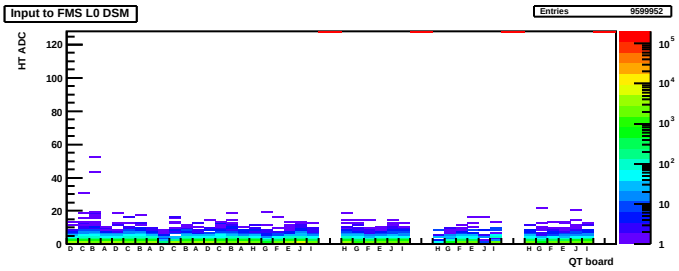

Input to FMS L0 DSM
Entries 3704
QT8(1) sum - simulated
QT board

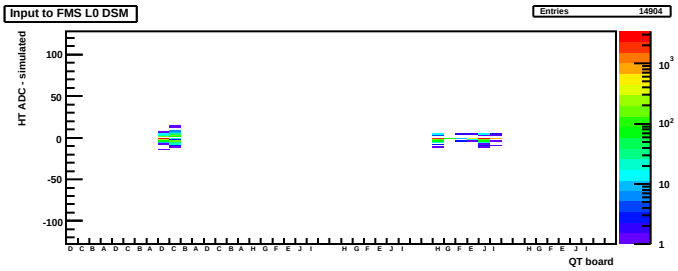

Input to FMS L0 DSM
Entries 9599952
QT8(2) sum
QT board

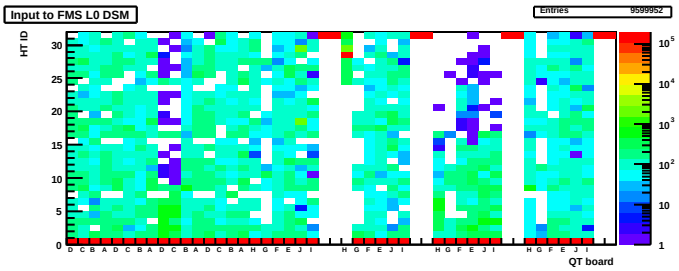

Input to FMS L0 DSM
Entries 143905
QT8(2) sum - simulated
QT board

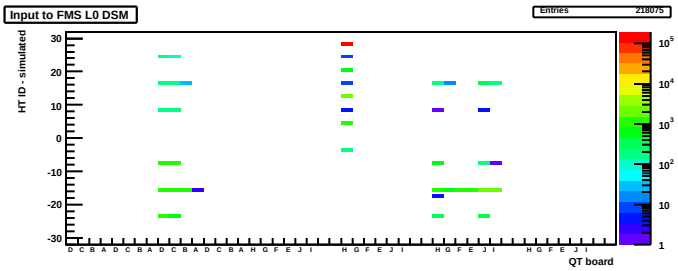

Input to FMS L0 DSM
Entries 9599952
QT8(3) sum
QT board

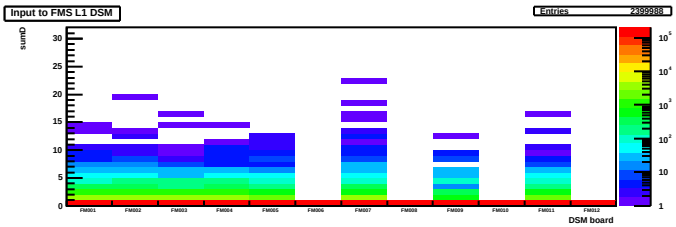

Input to FMS L0 DSM
Entries 193759
QT8(3) sum - simulated
QT board

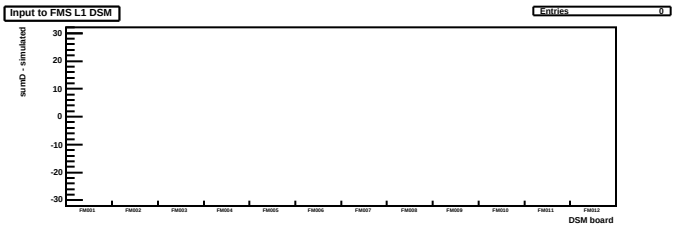

Input to FMS L0 DSM
Entries 9599952
HT ADC
QT board

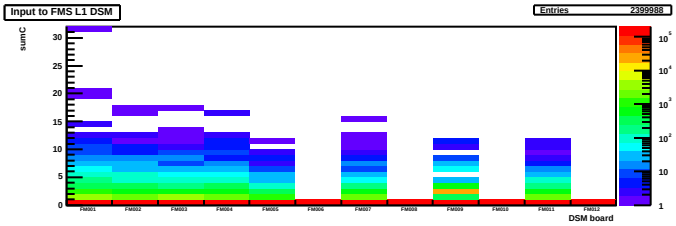

Input to FMS L0 DSM
Entries 14904
HT ADC - simulated
QT board

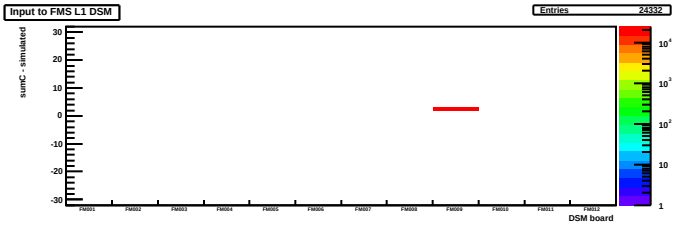

Input to FMS L0 DSM
Entries 9599952
HT ID
QT board

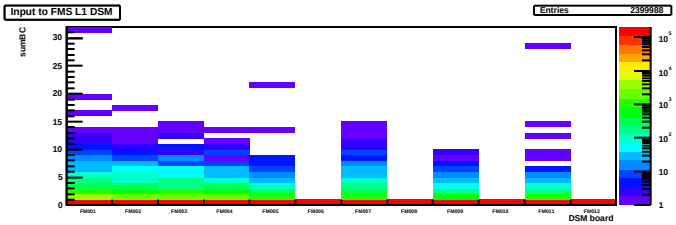

Input to FMS L0 DSM
Entries 218075
HT ID - simulated
QT board

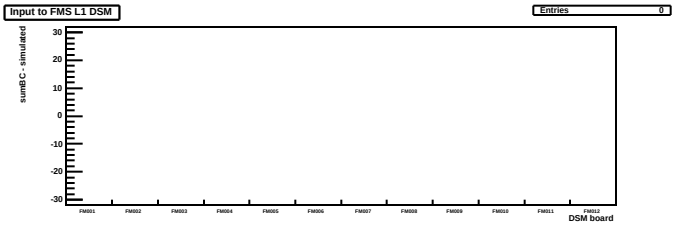

Input to FMS L1 DSM
Entries 2399988
sumD
DSM board

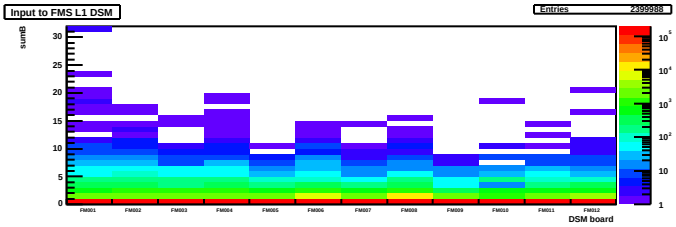

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

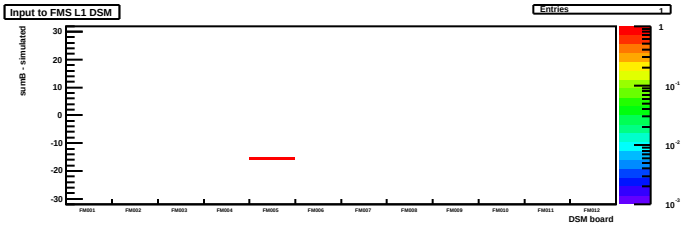

Input to FMS L1 DSM
Entries 2399988
sumC
DSM board

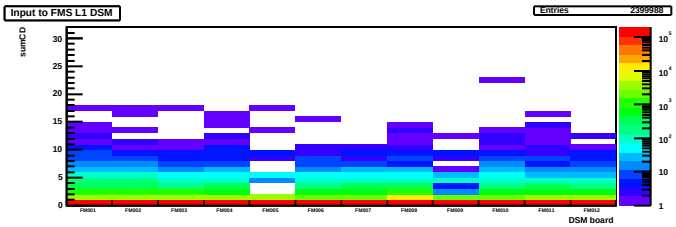

Input to FMS L1 DSM
Entries 24332
sumC - simulated
DSM board

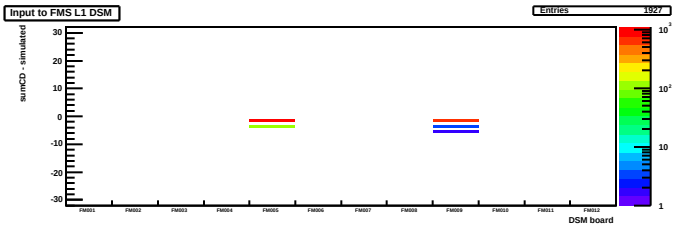

Input to FMS L1 DSM
Entries 2399988
sumBC
DSM board

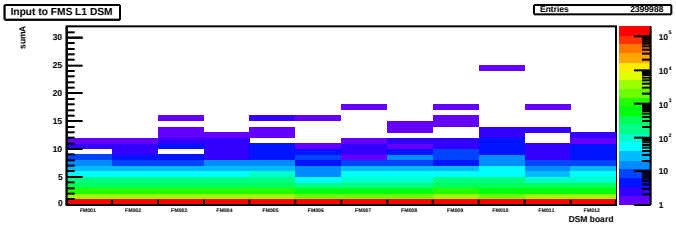

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

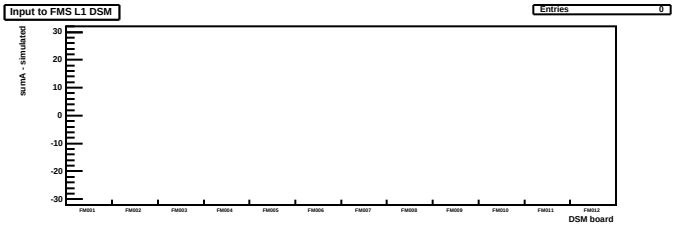

Input to FMS L1 DSM
Entries 2399988
sumB
DSM board

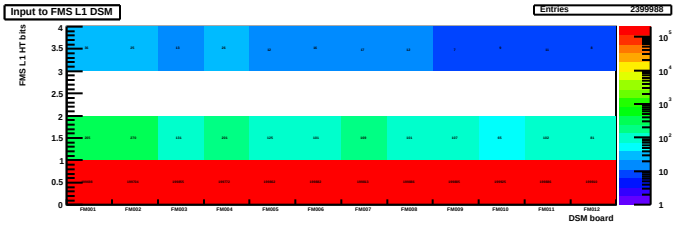

Input to FMS L1 DSM
Entries 1
sumB - simulated
DSM board

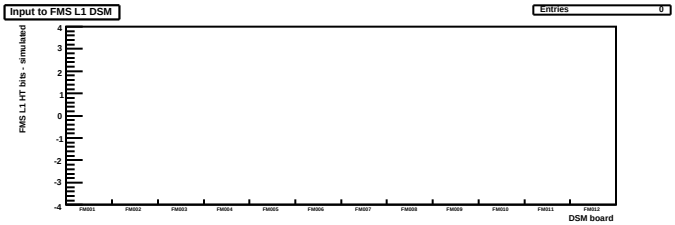

Input to FMS L1 DSM
Entries 2399988
sumCD
DSM board

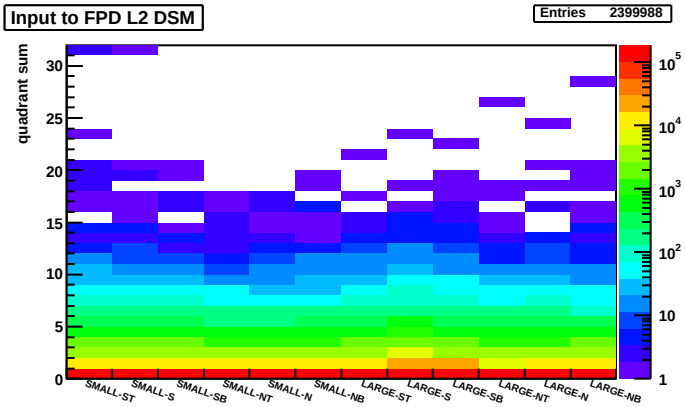

Input to FMS L1 DSM
Entries 1927
sumCD - simulated
DSM board

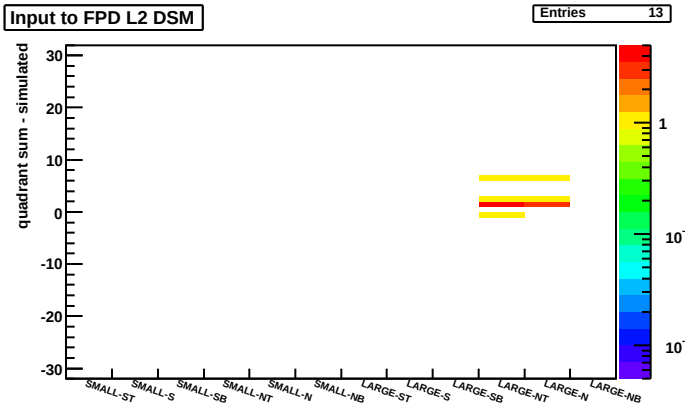

Input to FMS L1 DSM
Entries 2399988
sumA
DSM board

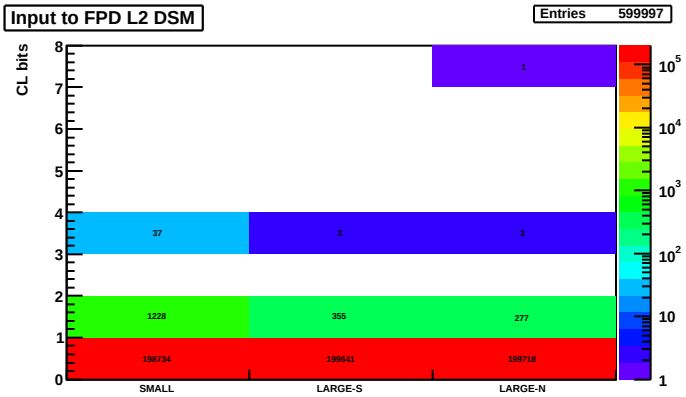

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

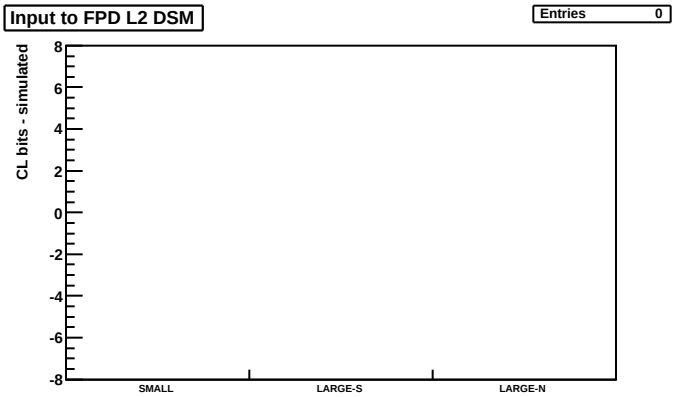

Input to FMS L1 DSM
Entries 2399988
FMS L1 HT bits
DSM board

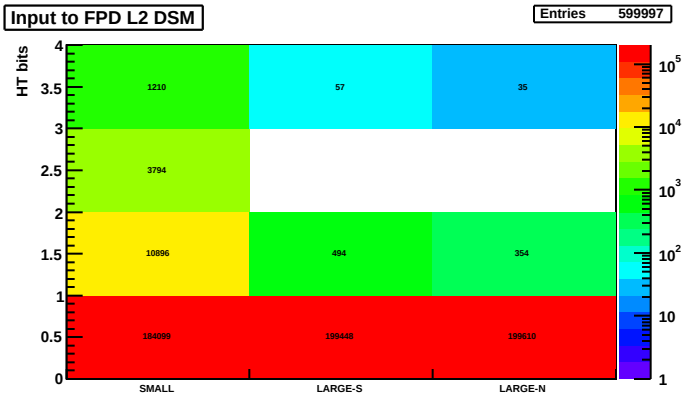

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

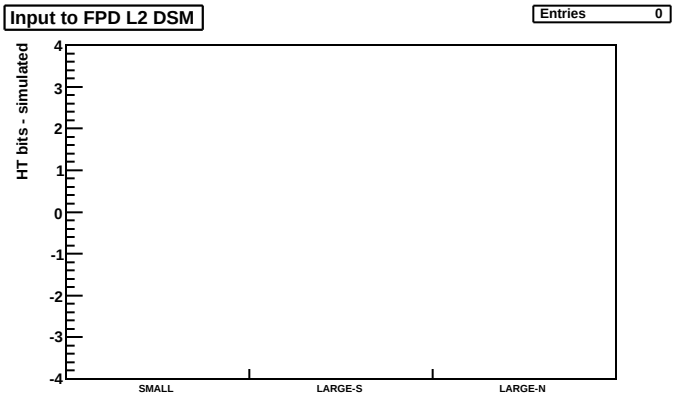

Input to FPD L2 DSM
Entries 2399988
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

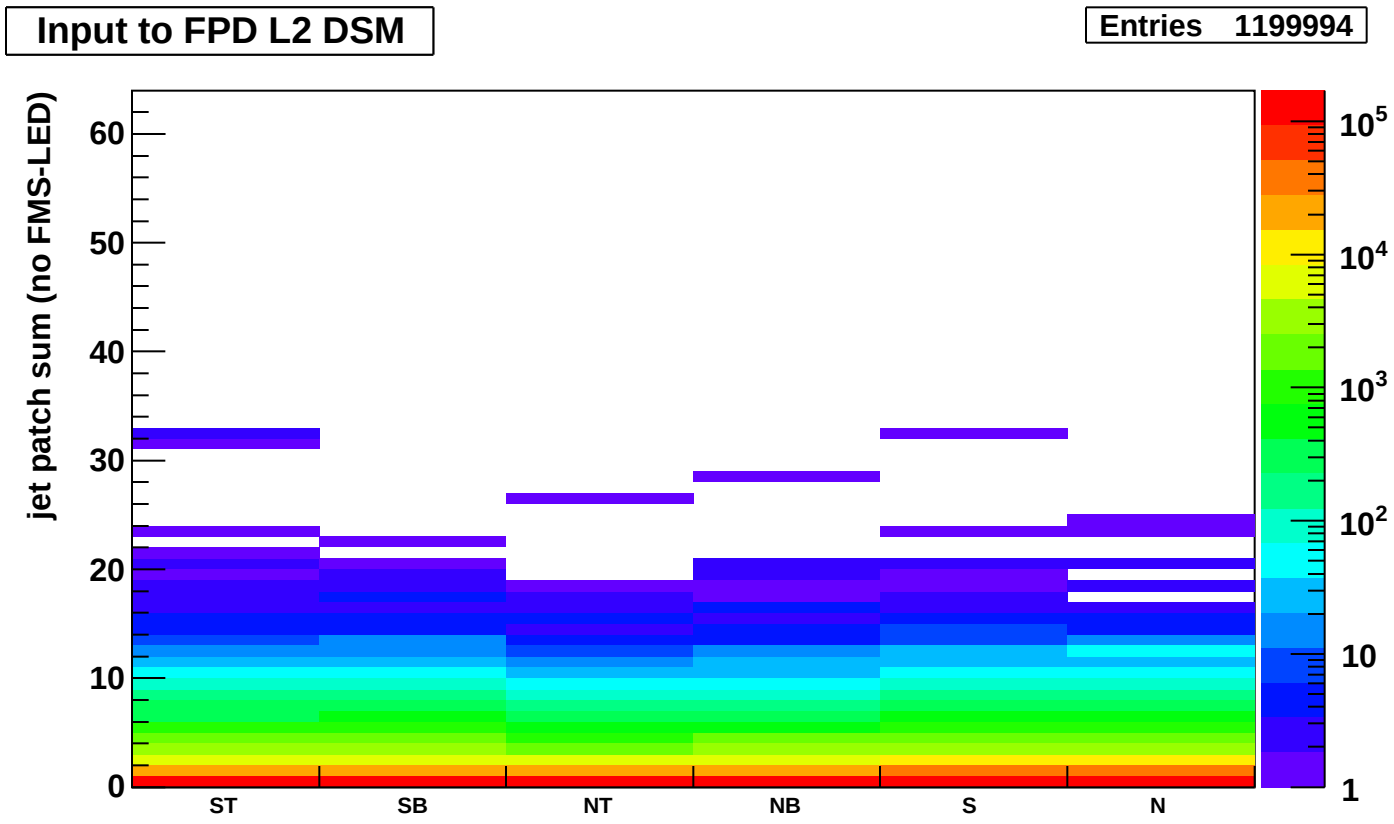

Input to FPD L2 DSM
Entries 13
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

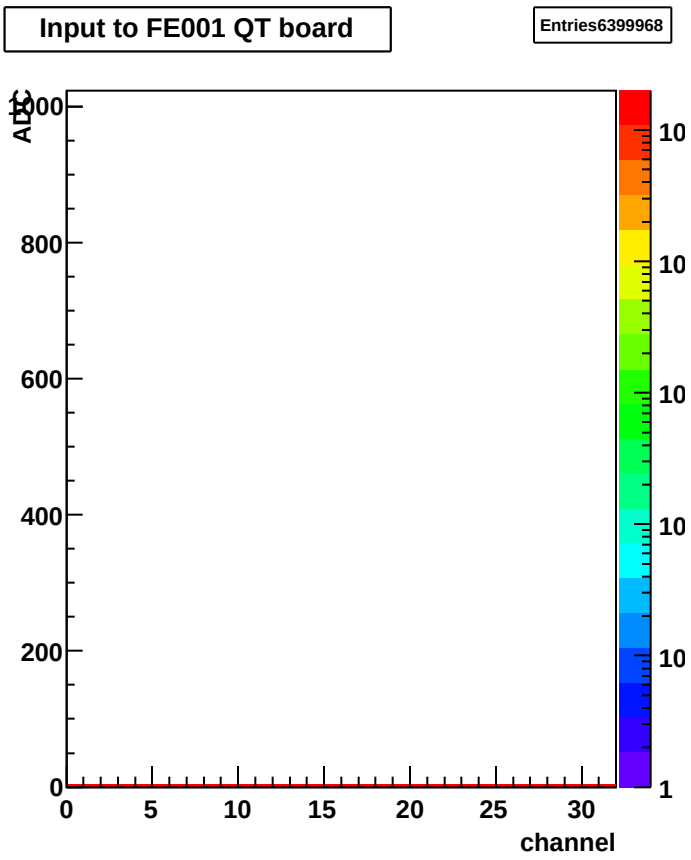

Input to FPD L2 DSM
Entries 599997
CL bits
1
37
3
3
1228
355
277
198734
199641
199718
SMALL
LARGE-S
LARGE-N

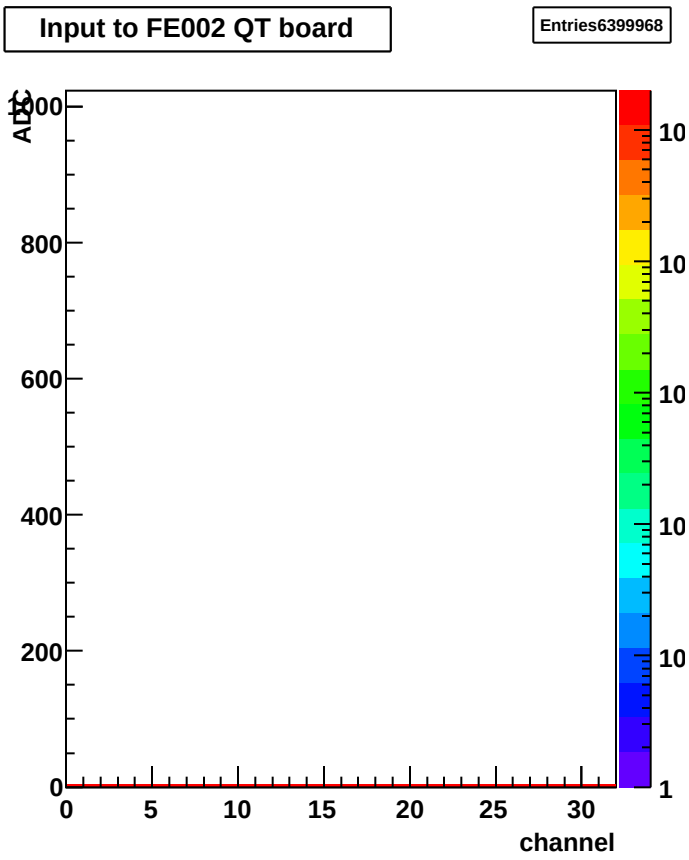

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

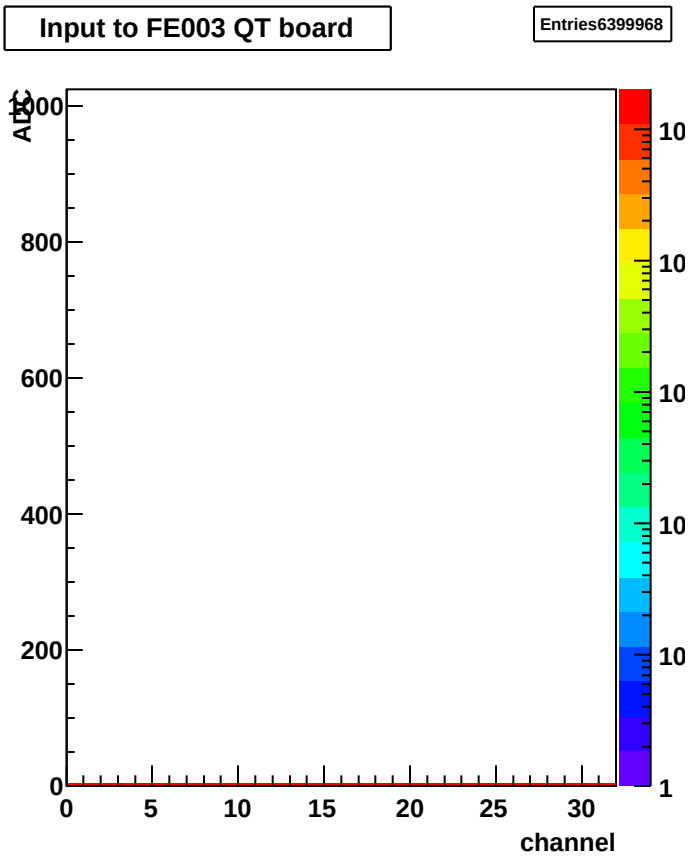

Input to FPD L2 DSM
Entries 599997
HT bits
1210
57
35
3794
10896
494
354
184099
199448
199610
SMALL
LARGE-S
LARGE-N

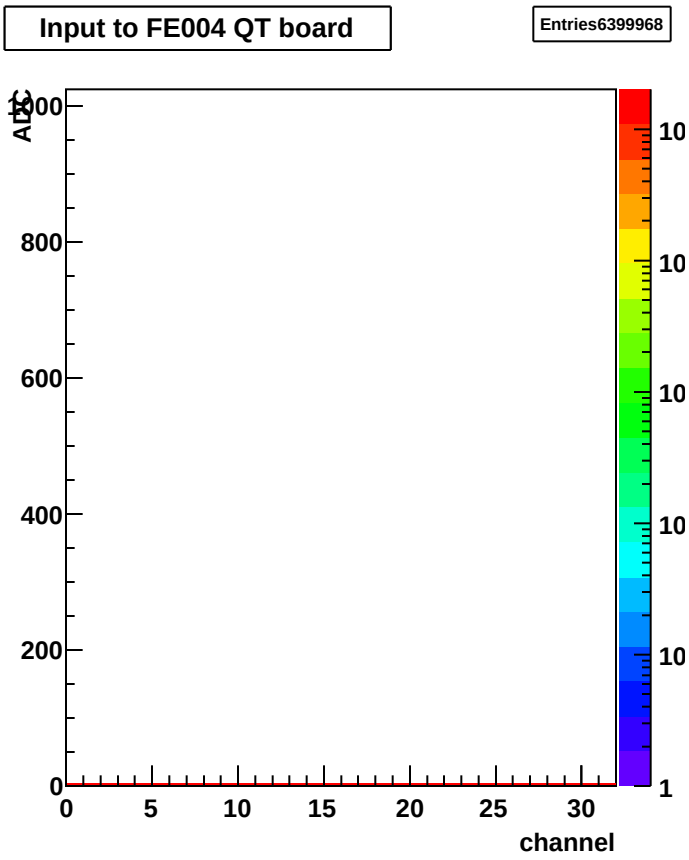

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

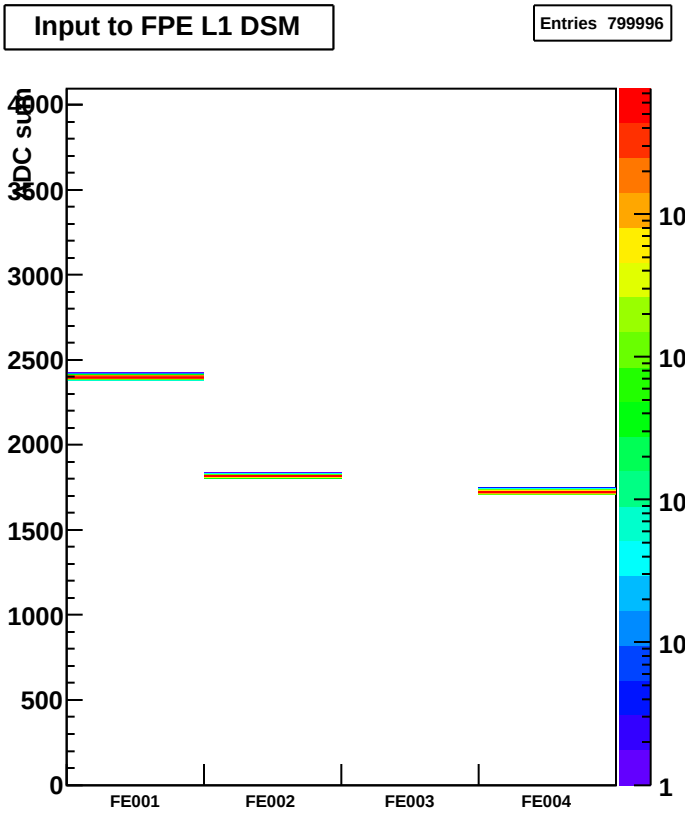

Input to FPD L2 DSM
Entries 1199994
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

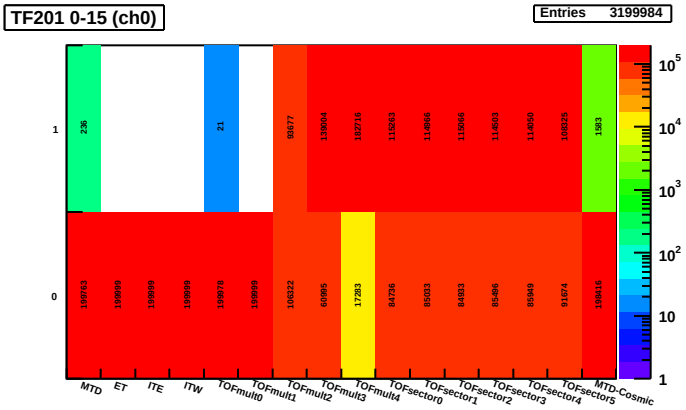

Input to FE001 QT board
Entries6399968
ADC
channel

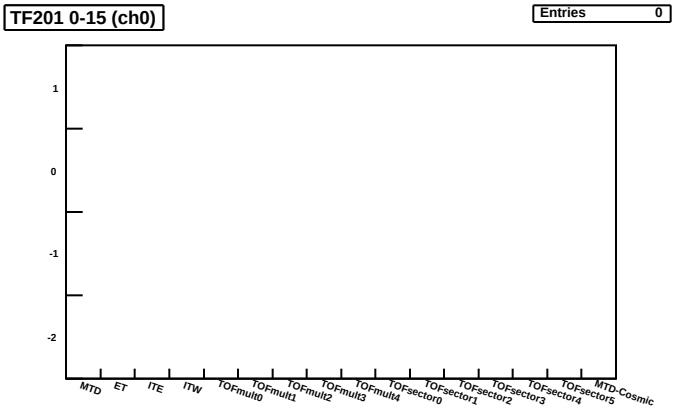

Input to FE002 QT board
Entries6399968
ADC
channel

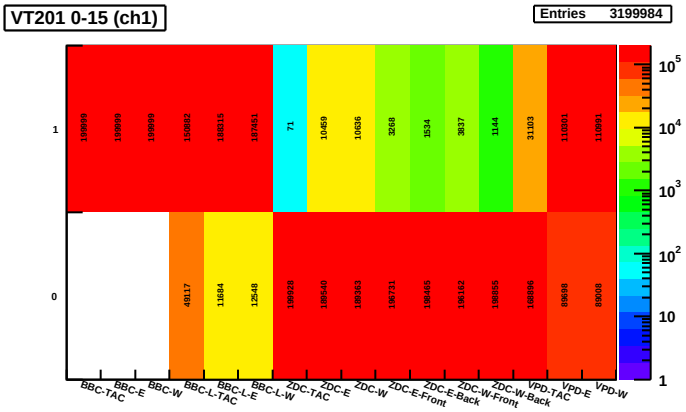

Input to FE003 QT board
Entries6399968
ADC
channel

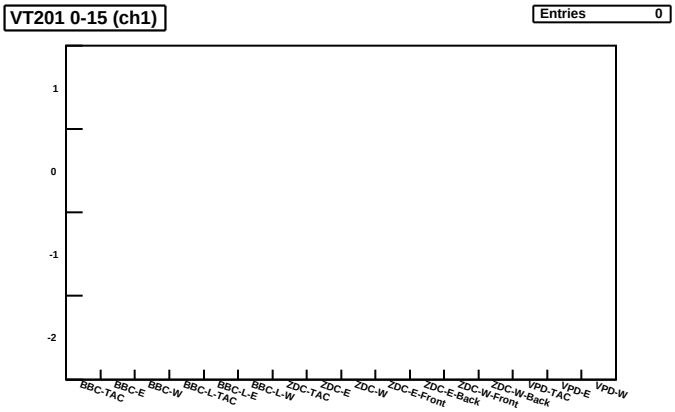

Input to FE004 QT board
Entries6399968
ADC
channel

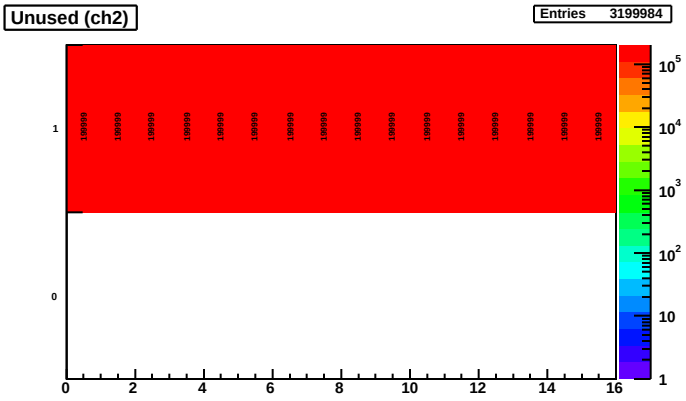

Input to FPE L1 DSM
Entries 799996
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

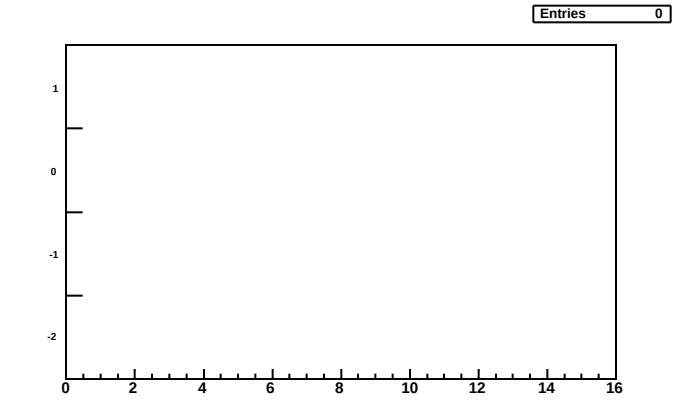

TF201 0-15 (ch0)
Entries 3199984
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

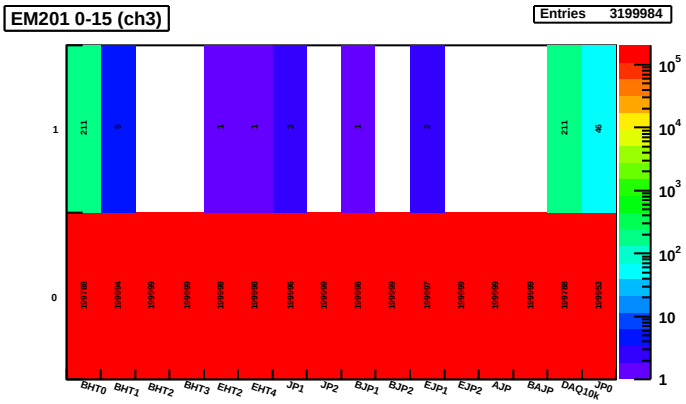

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 3199984
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 3199984


Entries 0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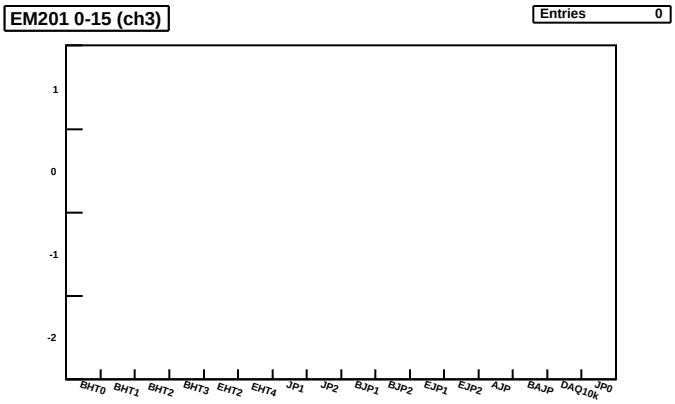
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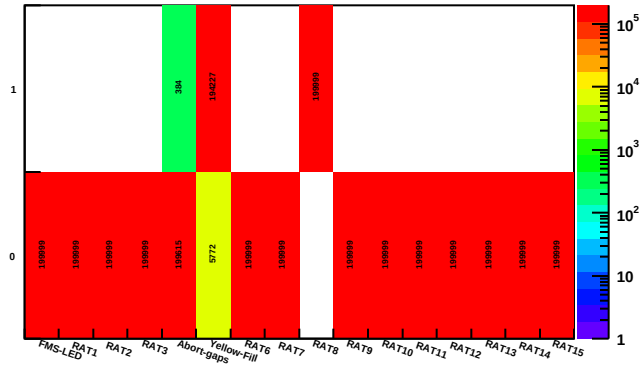
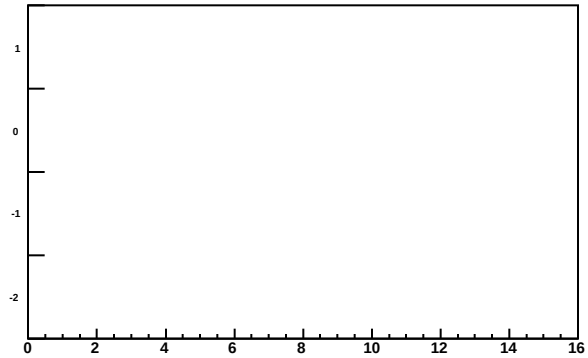
RAT board (ch4)





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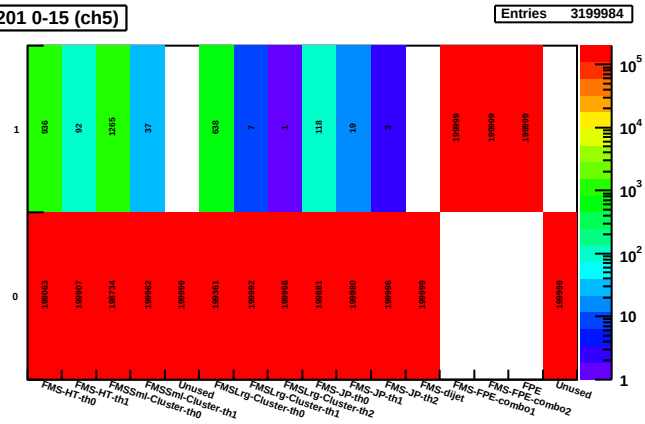

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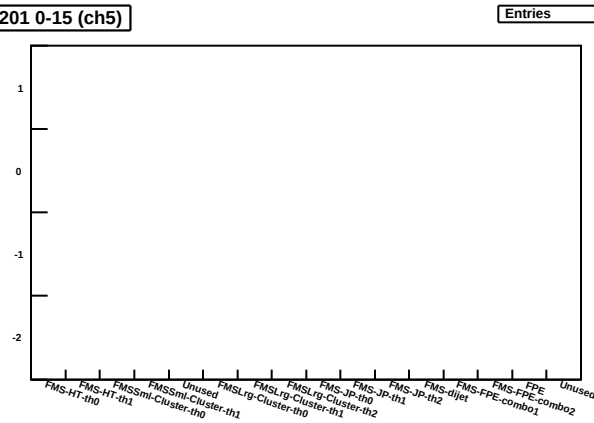

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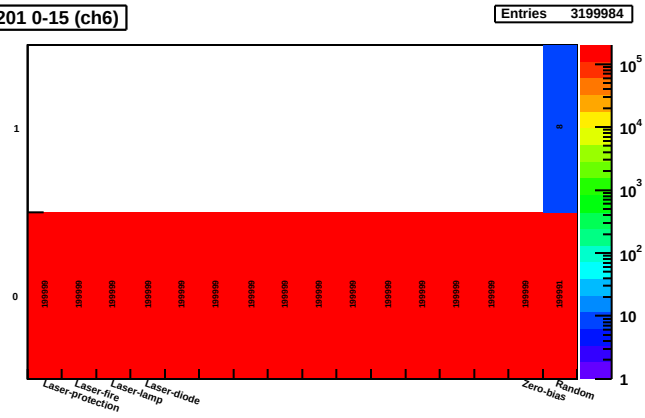

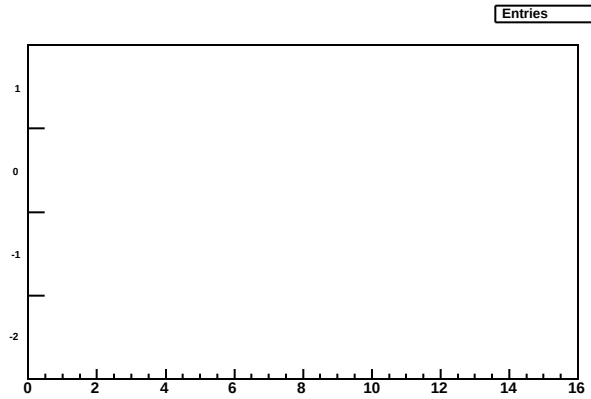
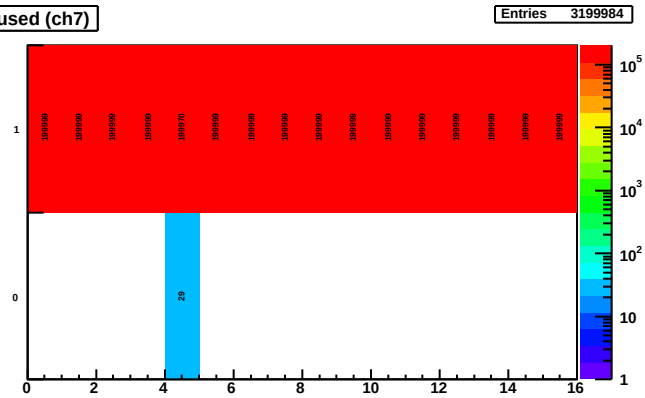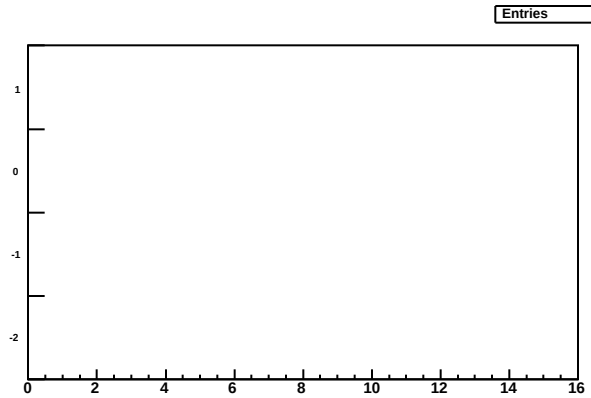
Unused (ch7)