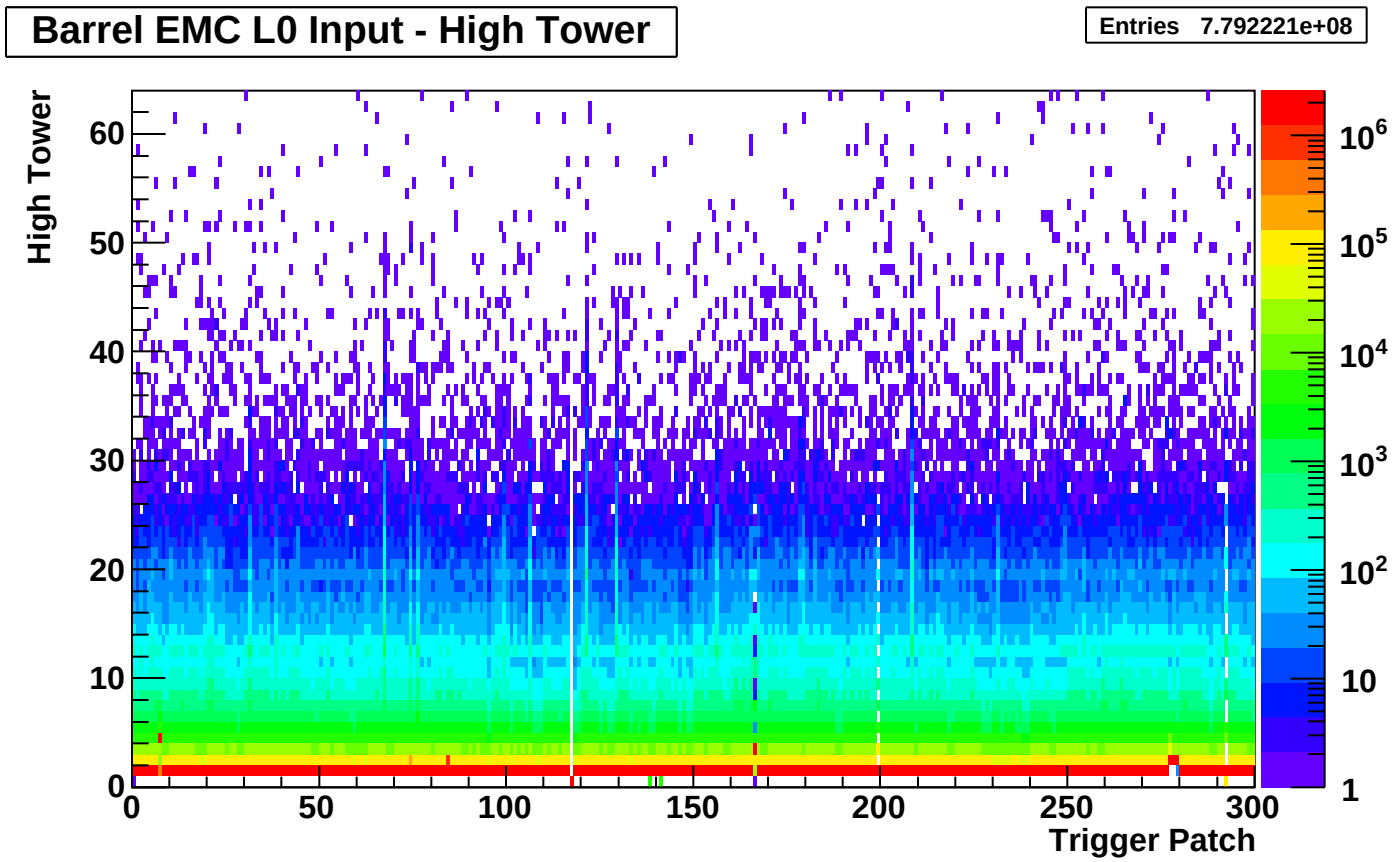
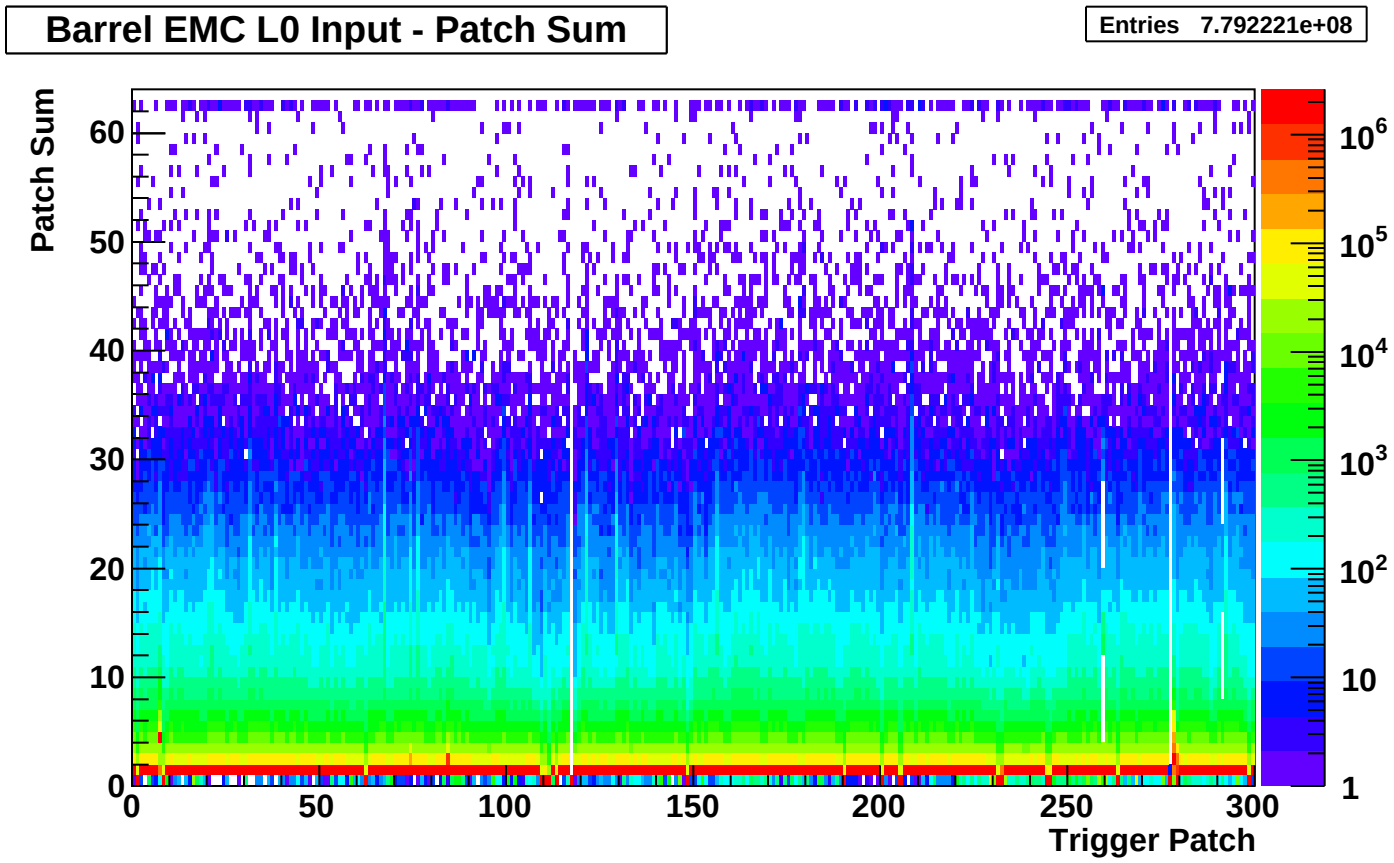
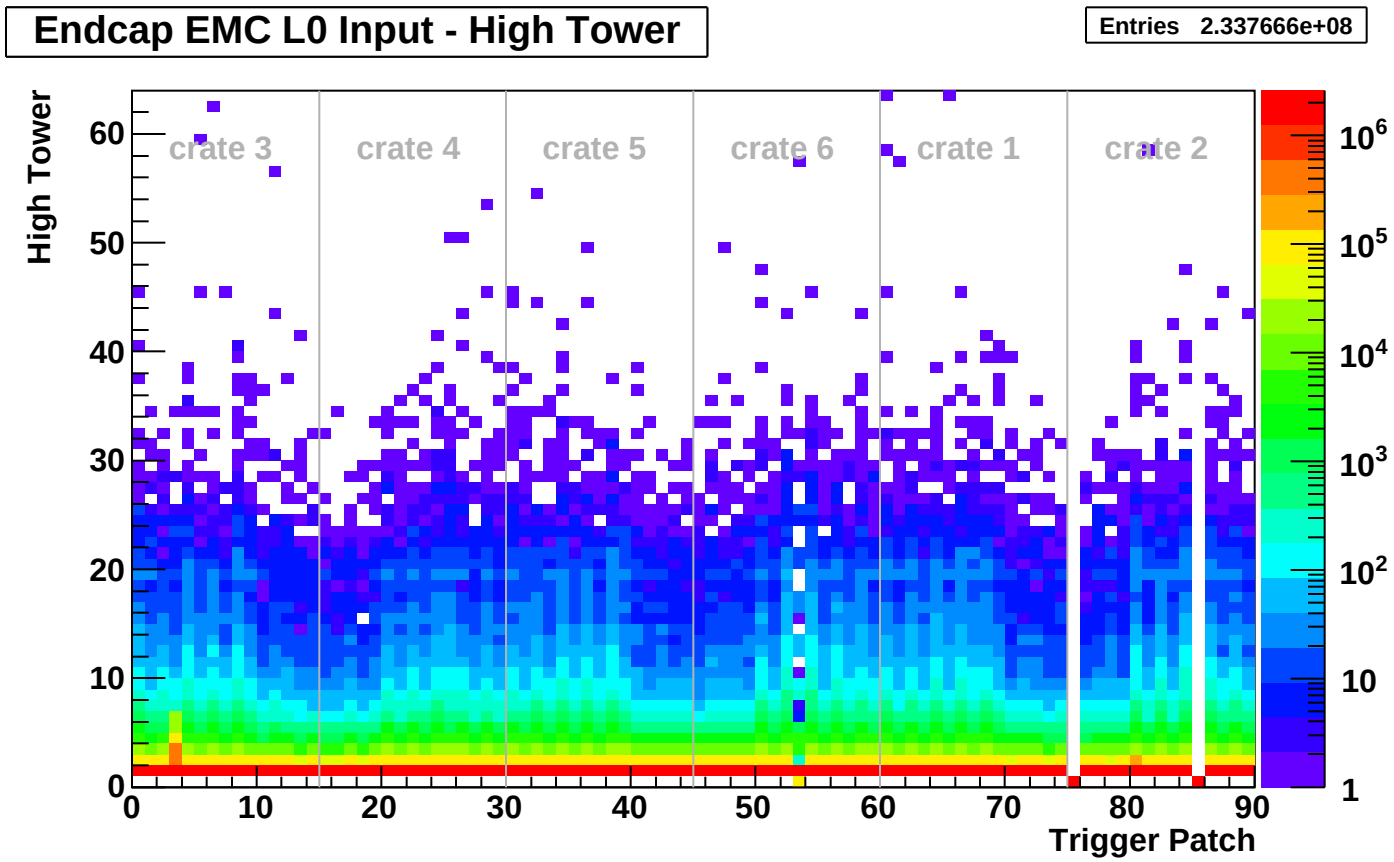
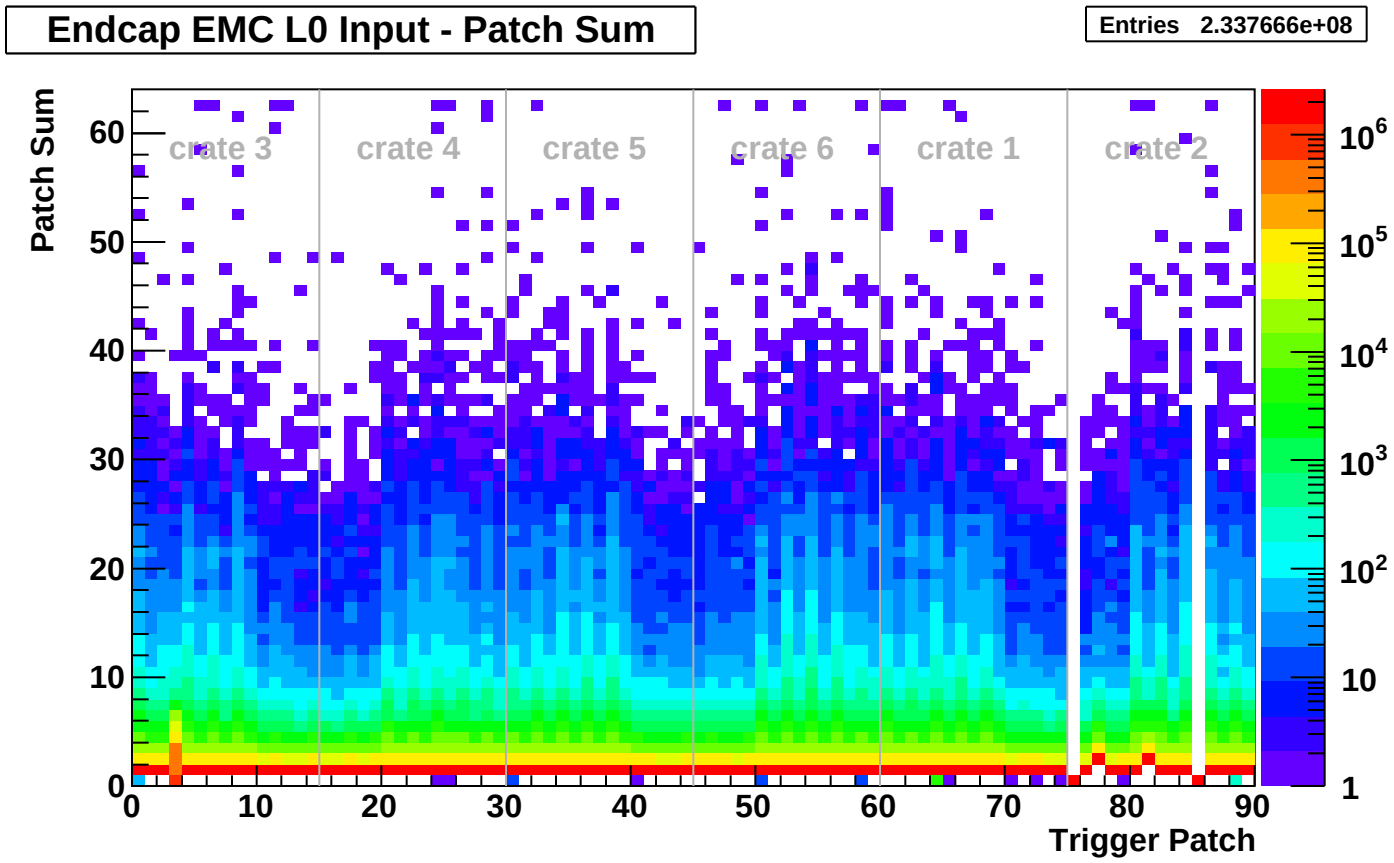
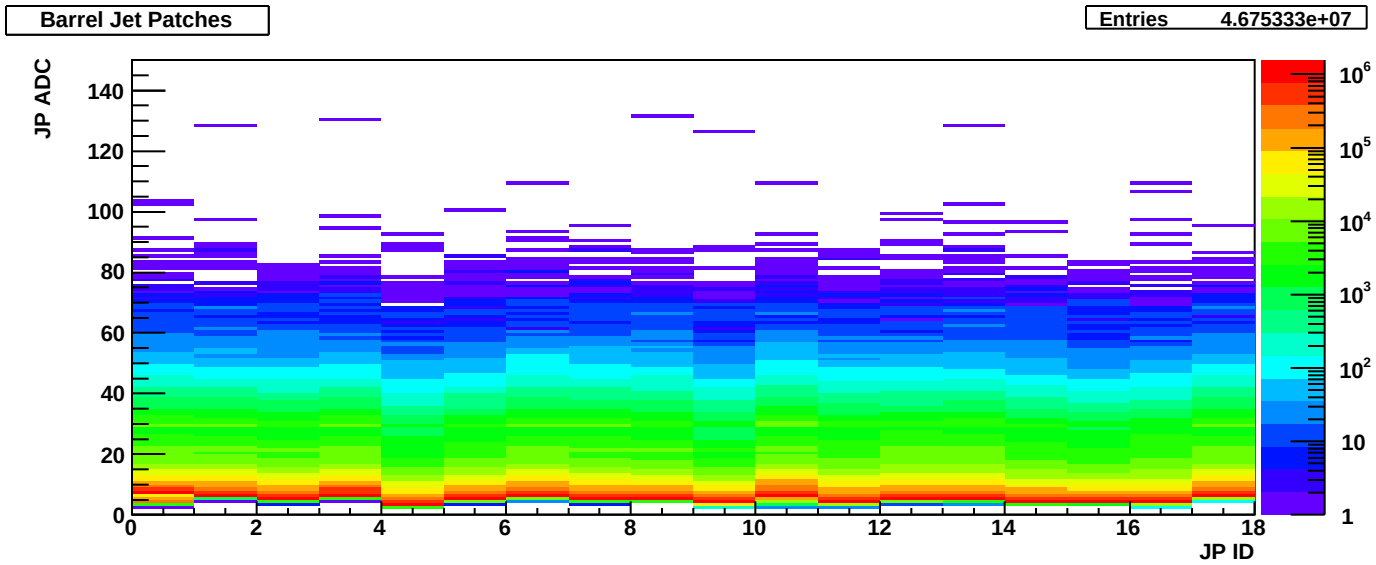
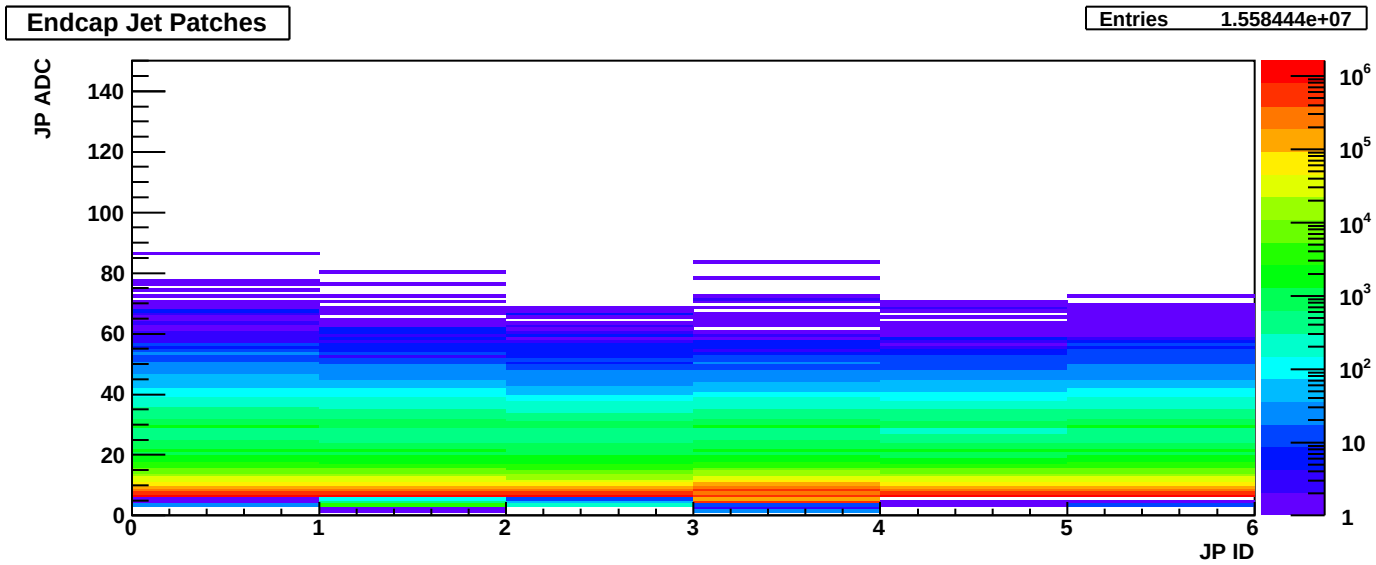

Barrel EMC L0 Input - High Tower
Entries 7.792221e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

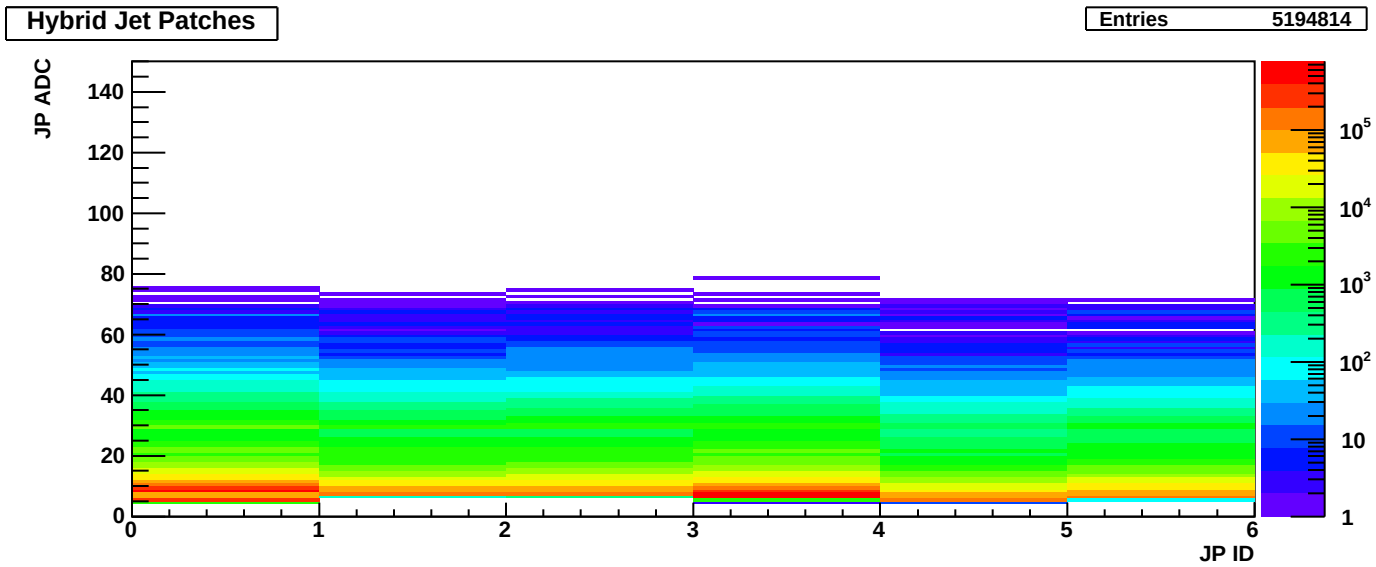

Barrel EMC L0 Input - Patch Sum
Entries 7.792221e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

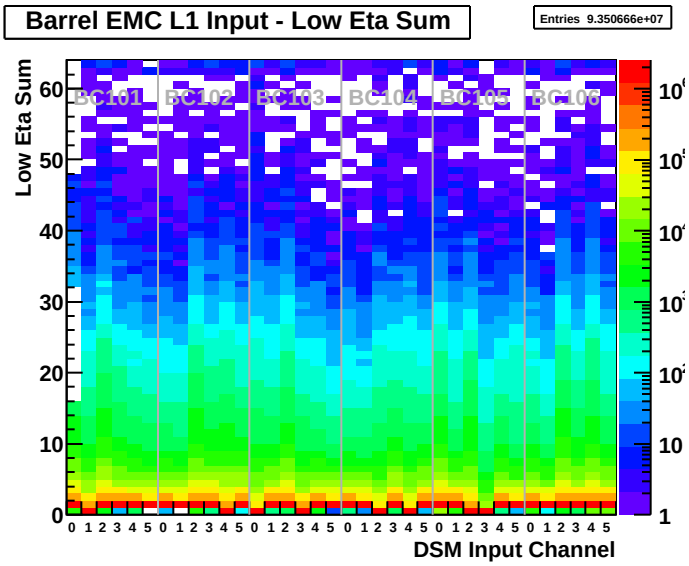

Endcap EMC L0 Input - High Tower
Entries 2.337666e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

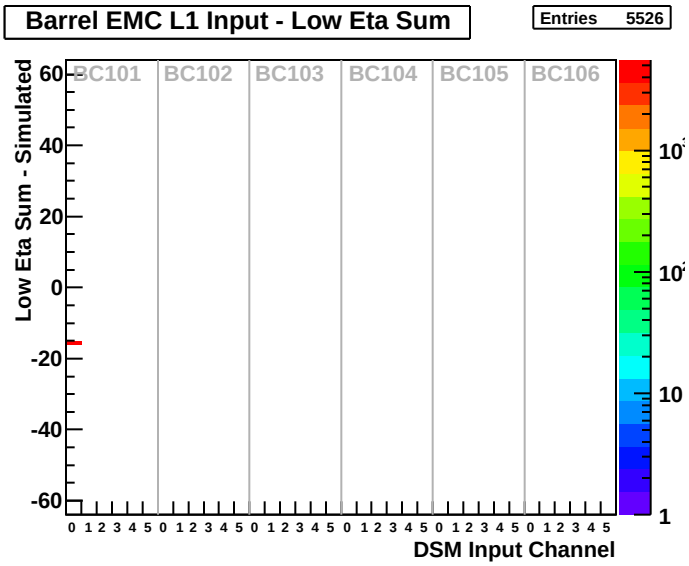

Endcap EMC L0 Input - Patch Sum
Entries 2.337666e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

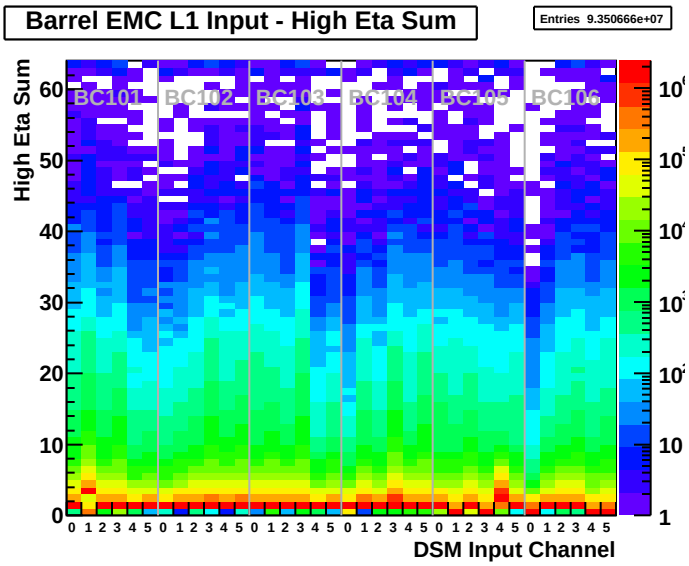

Barrel Jet Patches
Entries 4.675333e+07
JP ADC
JP ID

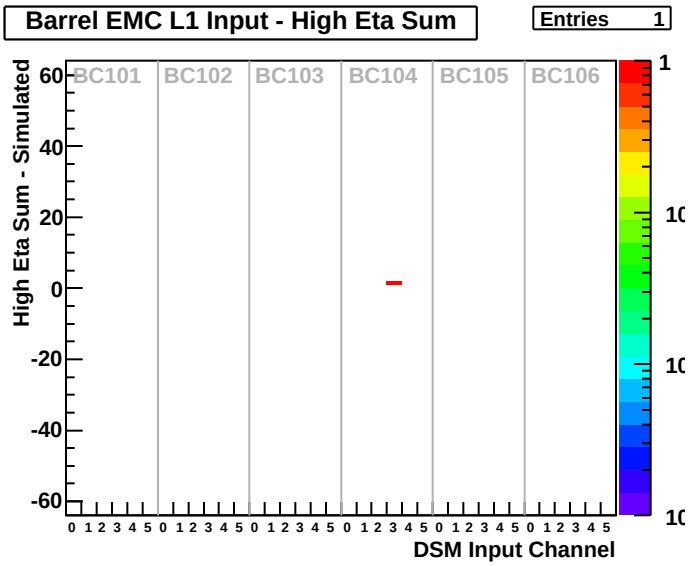

Endcap Jet Patches
Entries 1.558444e+07
JP ADC
JP ID

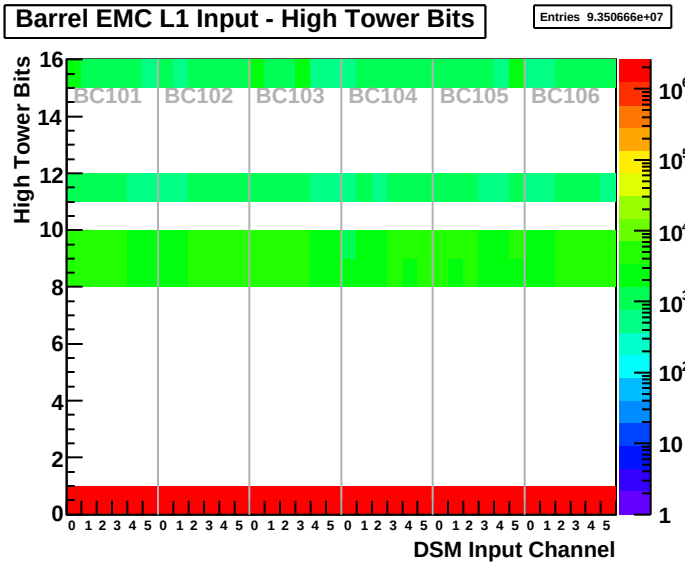

Hybrid Jet Patches
Entries 5194814
JP ADC
JP ID

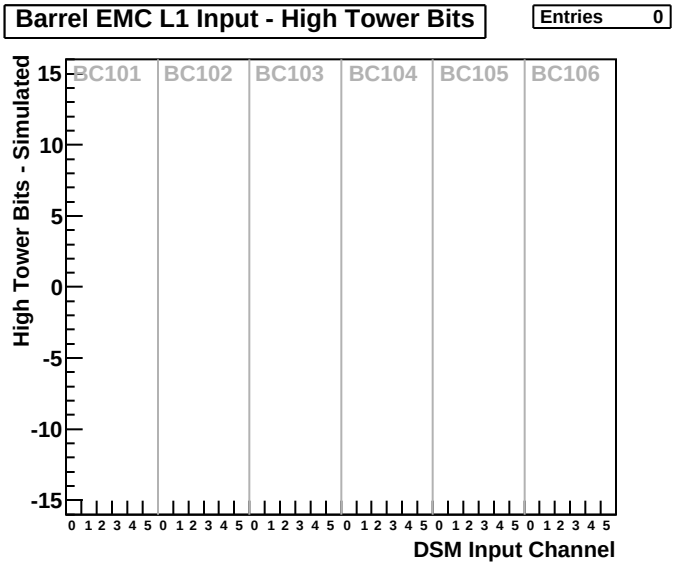

Barrel EMC L1 Input - Low Eta Sum
Entries 9.350666e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

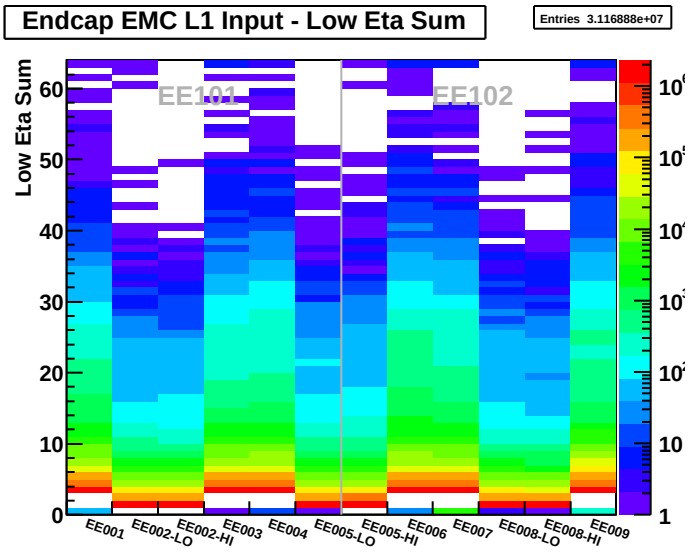

Barrel EMC L1 Input - Low Eta Sum
Entries 5526
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

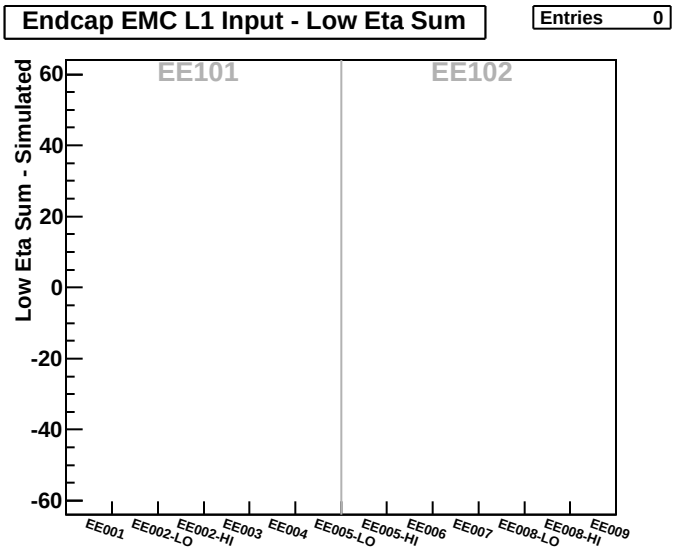

Barrel EMC L1 Input - High Eta Sum
Entries 9.350666e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

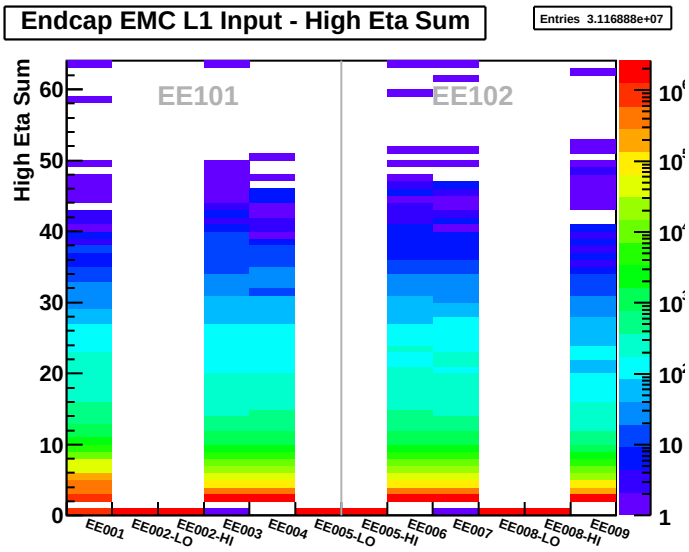

Barrel EMC L1 Input - High Eta Sum
Entries 1
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

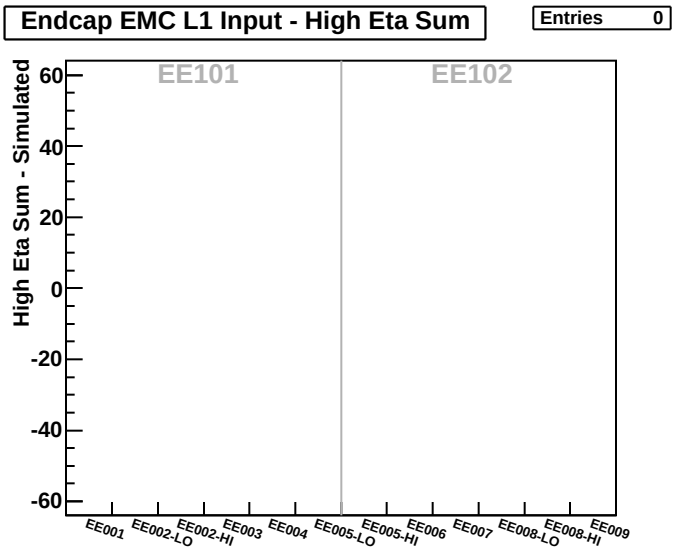

Barrel EMC L1 Input - High Tower Bits
Entries 9.350666e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

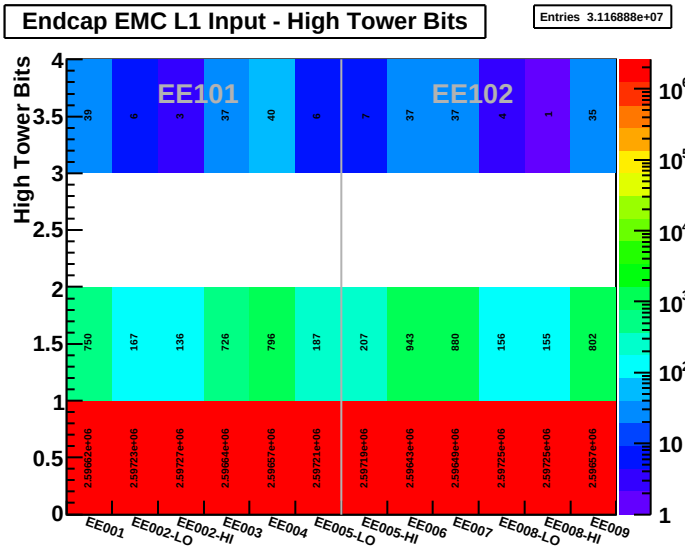

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

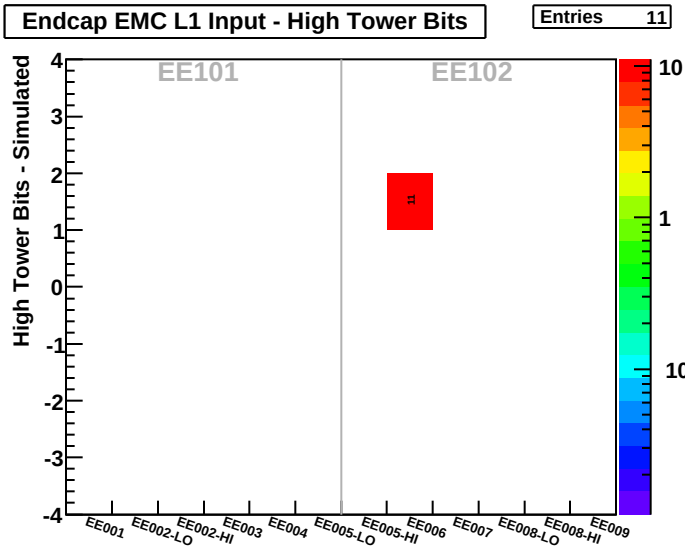

Endcap EMC L1 Input - Low Eta Sum
Entries 3.116888e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

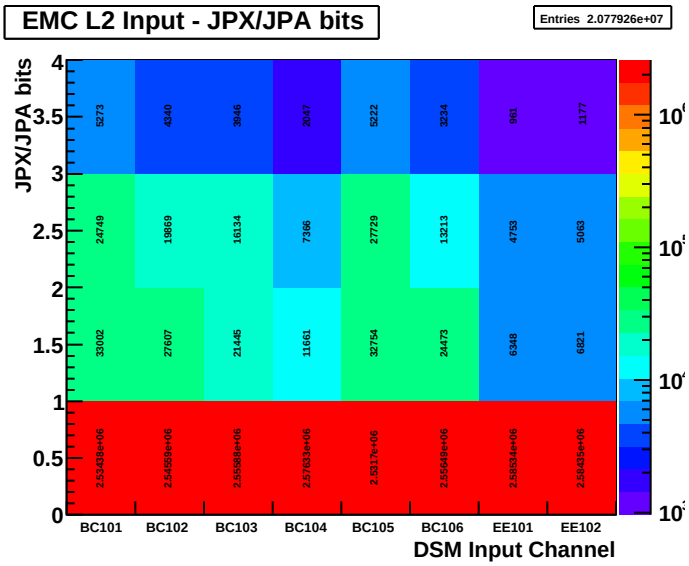

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

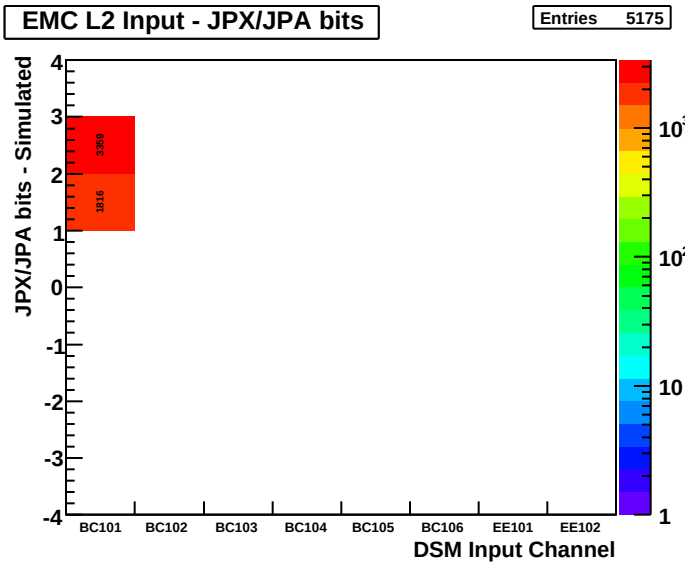

Endcap EMC L1 Input - High Eta Sum
Entries 3.116888e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

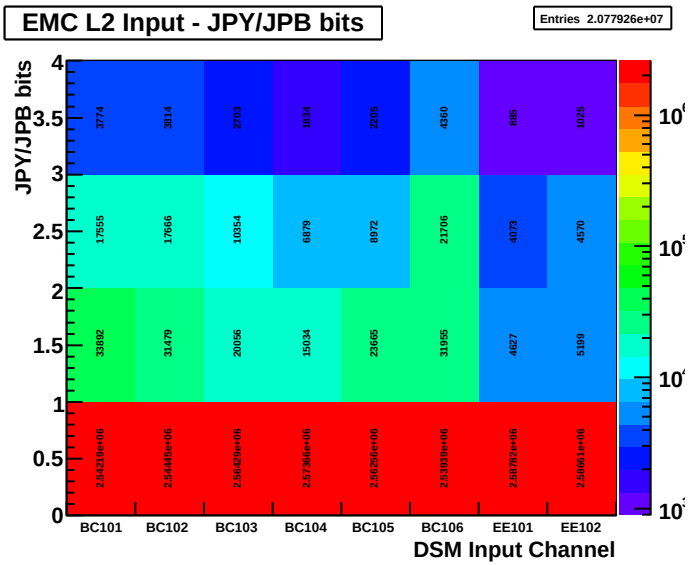

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

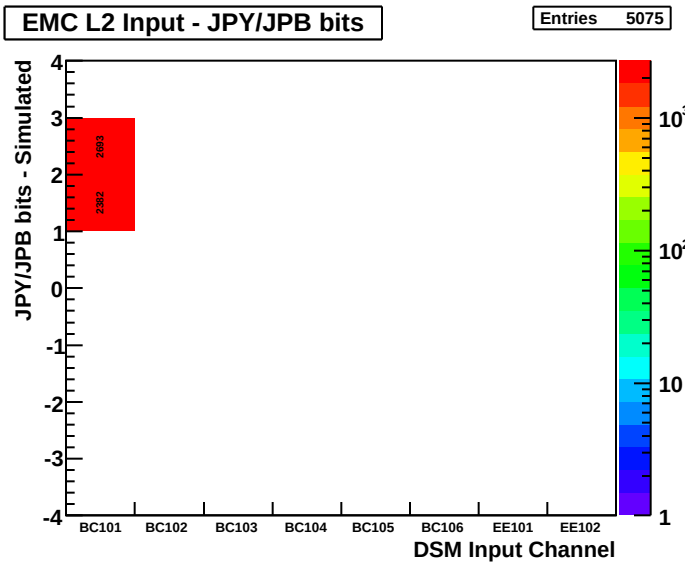

Endcap EMC L1 Input - High Tower Bits
Entries 3.116888e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

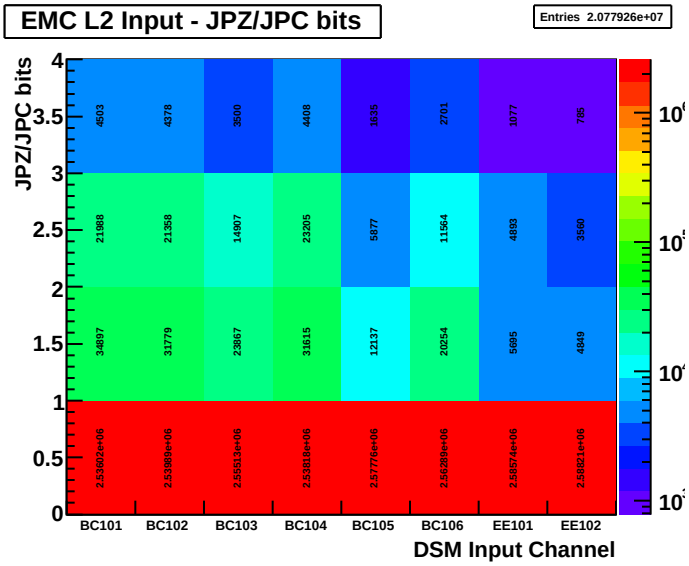

Endcap EMC L1 Input - High Tower Bits
Entries 11
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

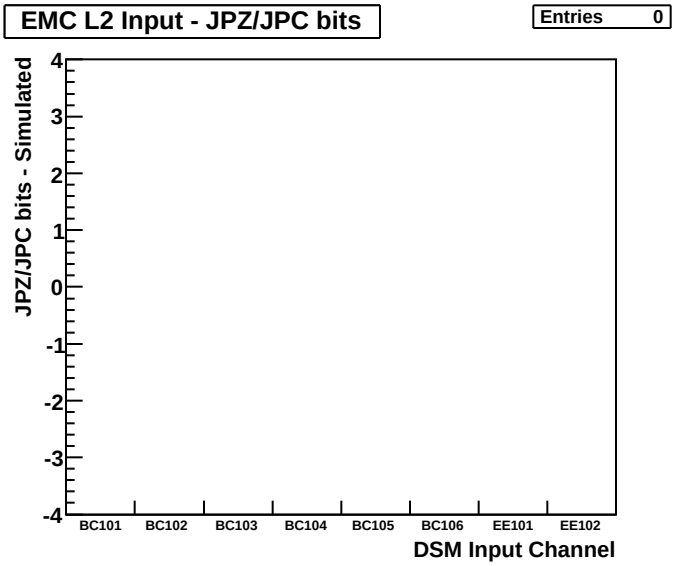

EMC L2 Input - JPX/JPA bits
Entries 2.077926e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5273 4340 3946 2047 5222 3234 961 1177
24749 19869 16134 7366 27729 13213 4753 5063
33002 27607 21445 11661 32754 24473 6348 6821
2.53438e+06 2.54559e+06 2.55580e+06 2.57633e+06 2.5317e+06 2.55649e+06 2.58534e+06 2.58425e+06

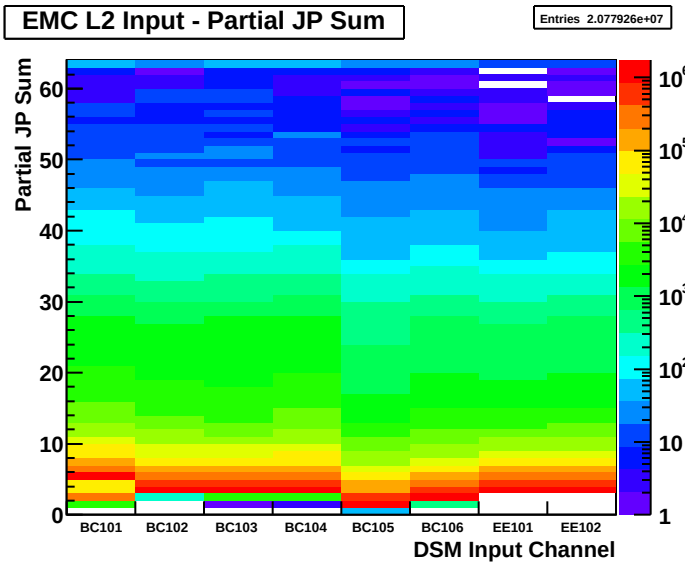

EMC L2 Input - JPX/JPA bits
Entries 5175
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3359
1816

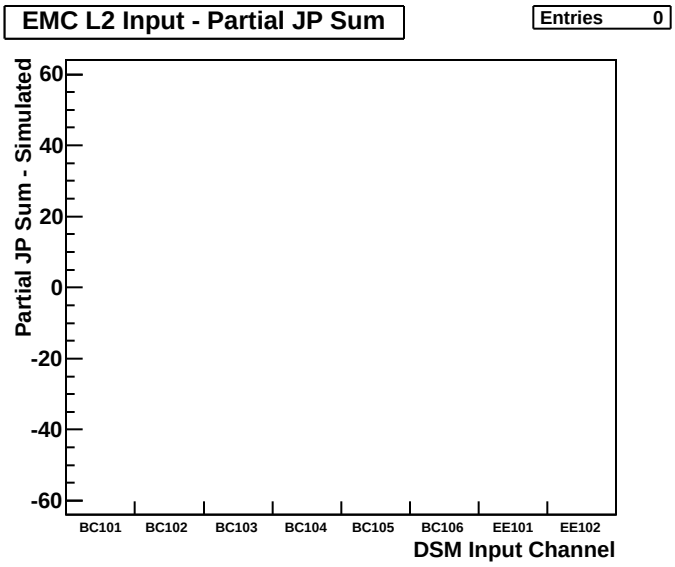

EMC L2 Input - JPY/JPB bits
Entries 2.077926e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3774 3814 2703 1834 2205 4360 885 1025
17555 17666 10354 6879 8972 21706 4073 4570
33892 31479 20056 15034 23665 31955 4627 5399
2.54219e+06 2.54445e+06 2.56420e+06 2.57366e+06 2.56256e+06 2.53939e+06 2.58792e+06 2.58661e+06

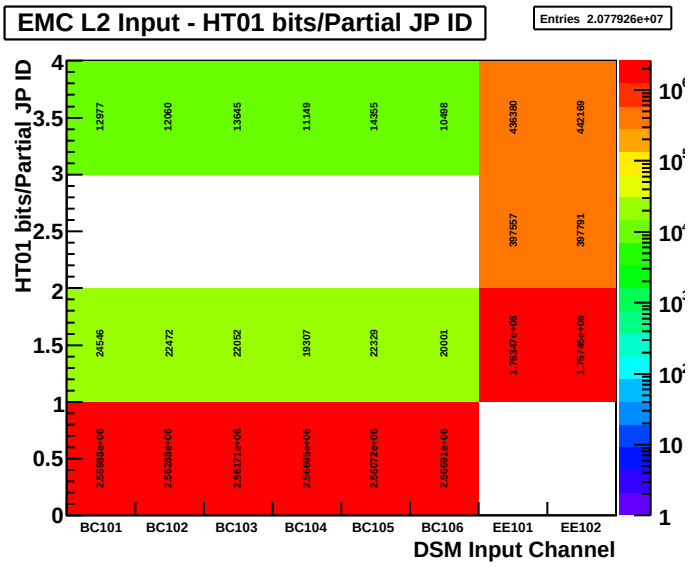

EMC L2 Input - JPY/JPB bits
Entries 5075
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2693
2382

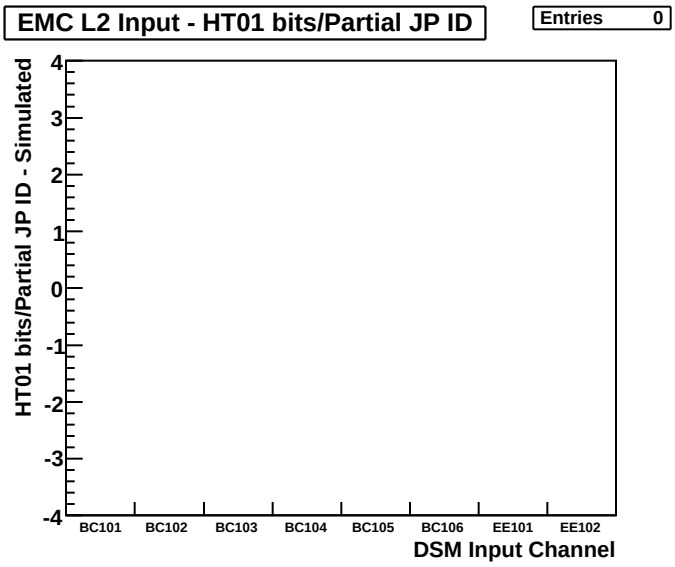

EMC L2 Input - JPZ/JPC bits
Entries 2.077926e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4503 4378 3500 4408 1635 2701 1077 785
21988 21358 14907 23205 5877 11564 4893 3560
34897 31779 23867 31615 12137 20254 5695 4849
2.53602e+06 2.53989e+06 2.55513e+06 2.53818e+06 2.57776e+06 2.56289e+06 2.58574e+06 2.58821e+06

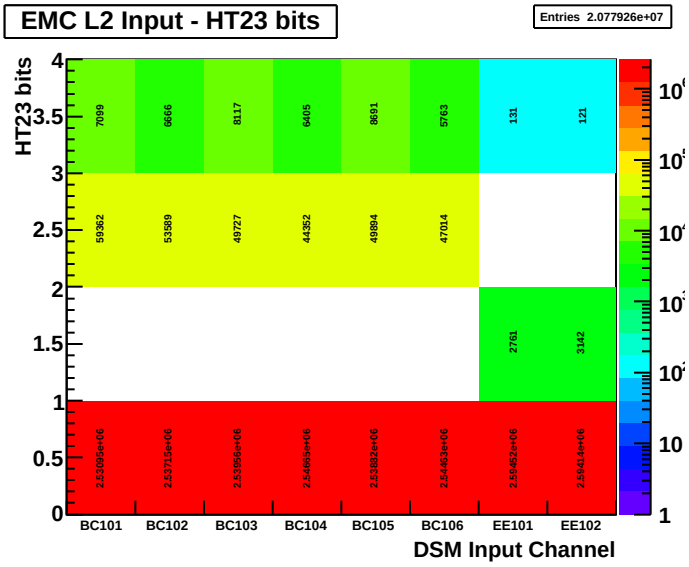

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

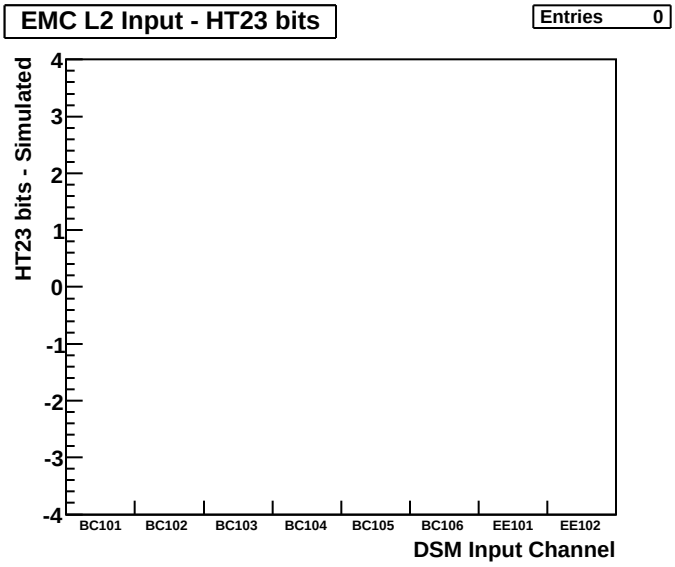

EMC L2 Input - Partial JP Sum
Entries 2.077926e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

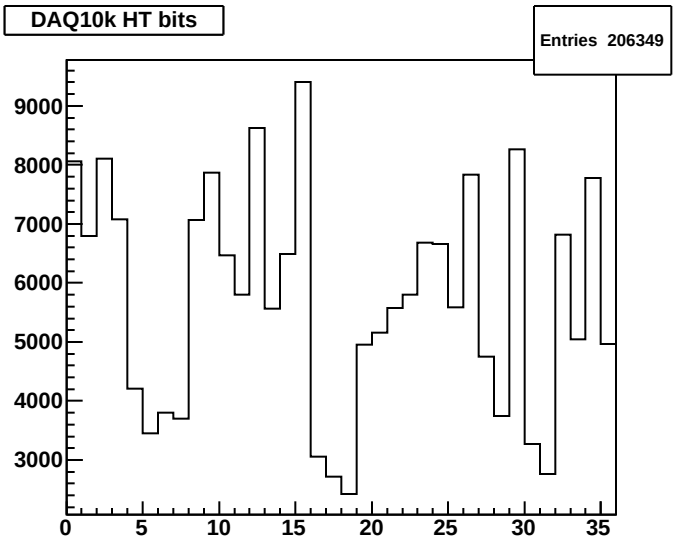

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

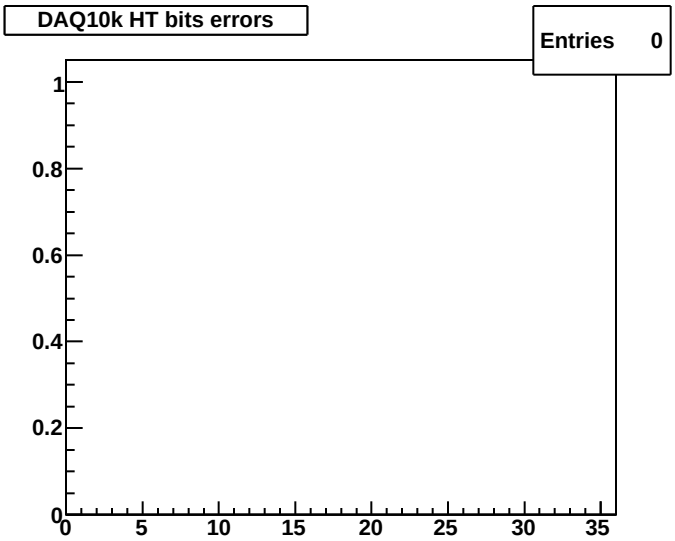

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2.077926e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

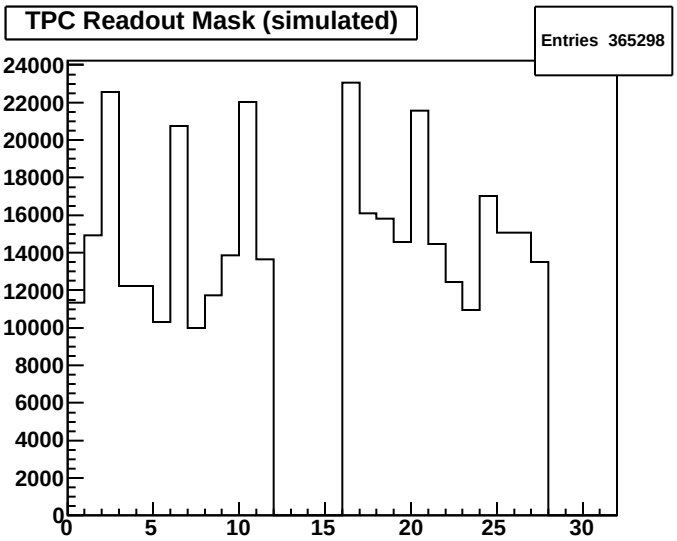

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

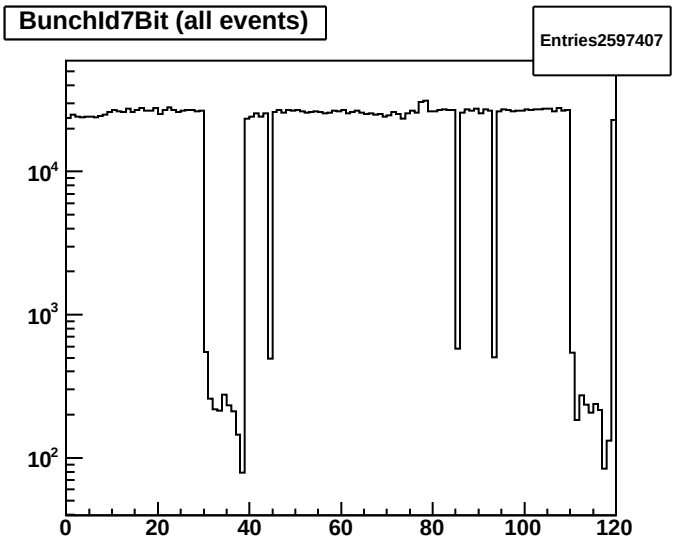

EMC L2 Input - HT23 bits
Entries 2.077926e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

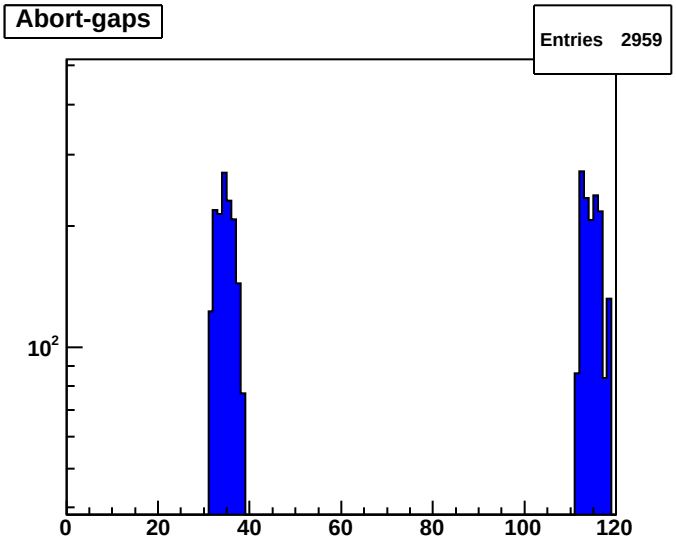

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

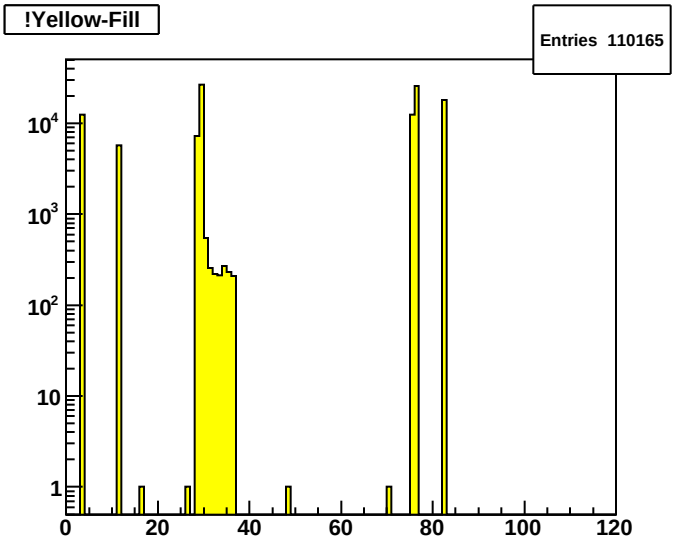

DAQ10k HT bits
Entries 206349

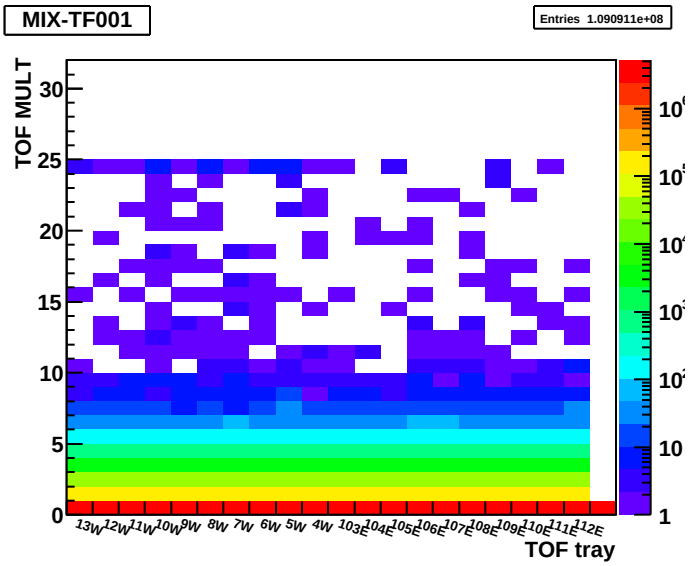

DAQ10k HT bits errors
Entries 0

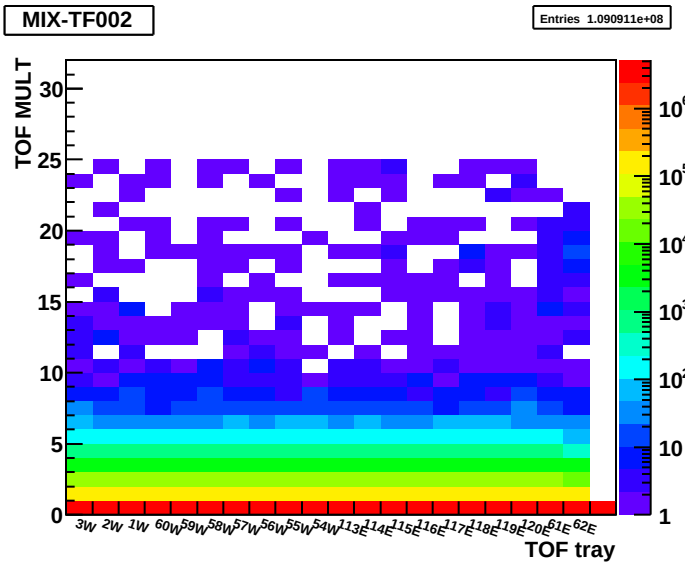

TPC Readout Mask (simulated)
Entries 365298

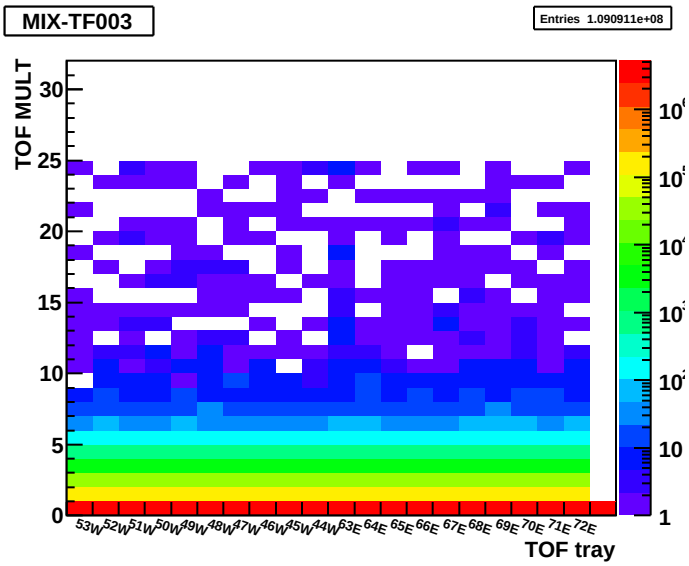

BunchId7Bit (all events)
Entries2597407

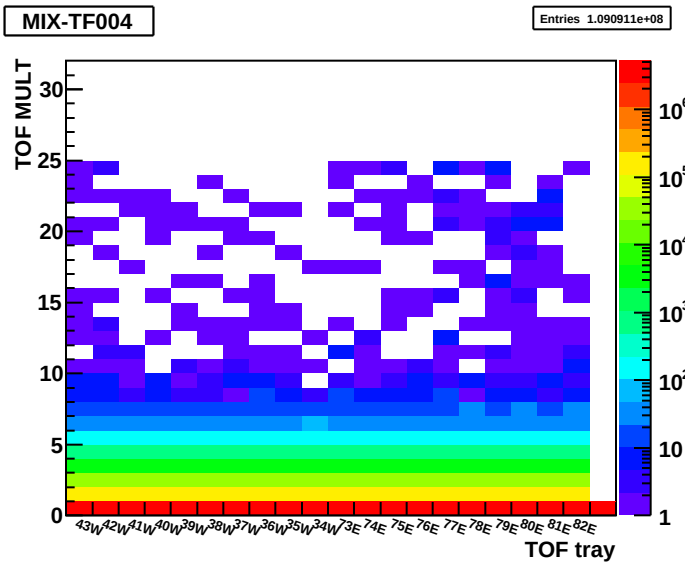

Abort-gaps
Entries 2959

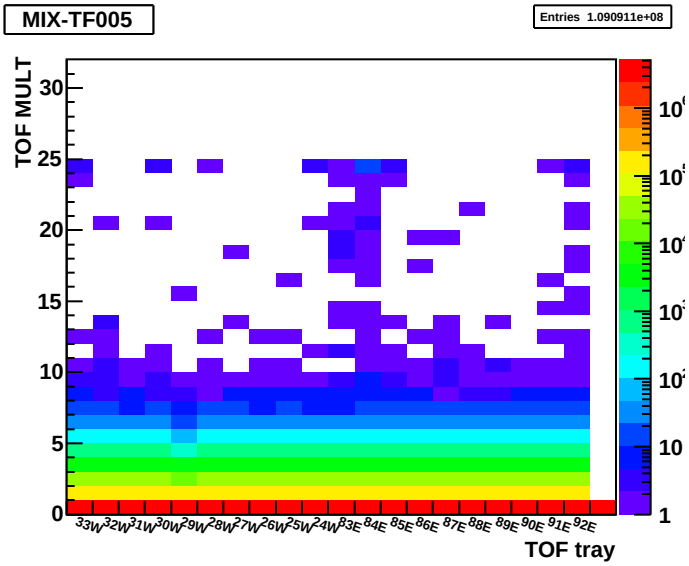

!Yellow-Fill
Entries 110165

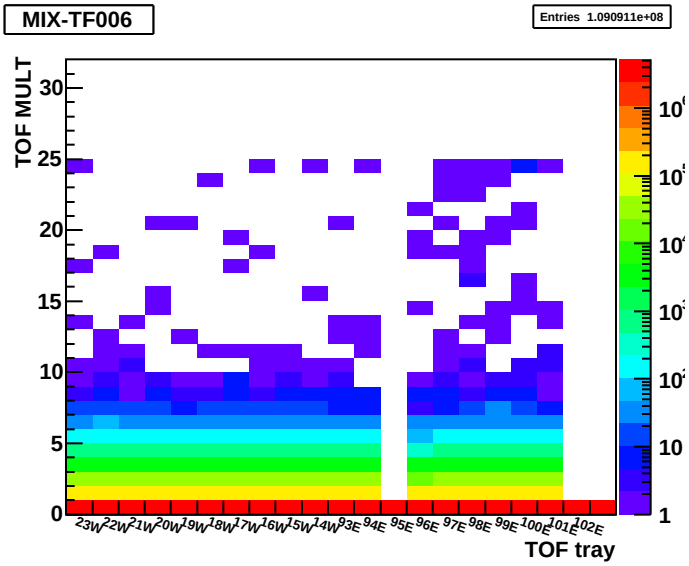

MIX-TF001
Entries 1.090911e+08
TOF MULT
TOF tray

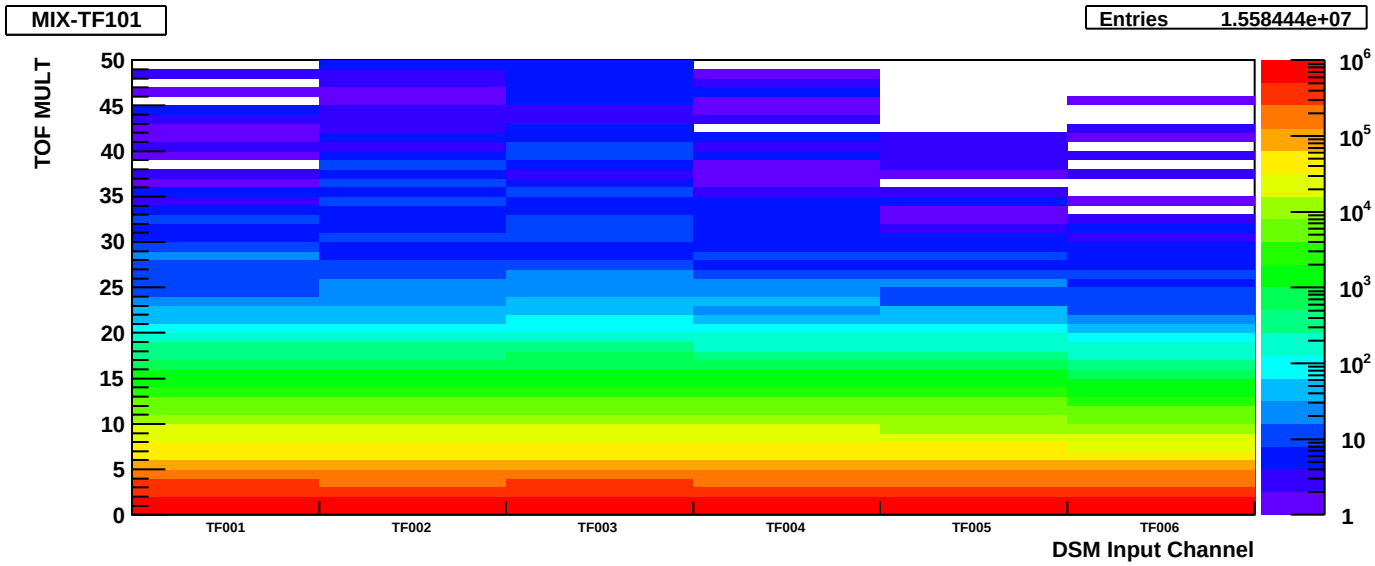

MIX-TF002
Entries 1.090911e+08
TOF MULT
TOF tray

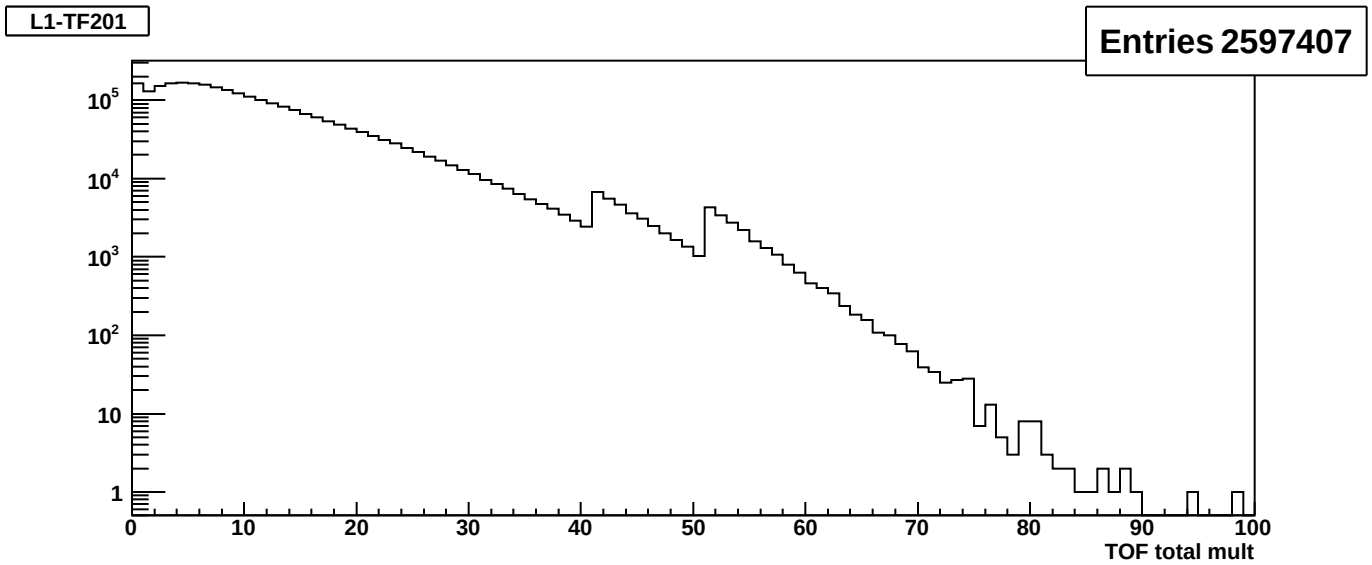

MIX-TF003
Entries 1.090911e+08
TOF MULT
TOF tray

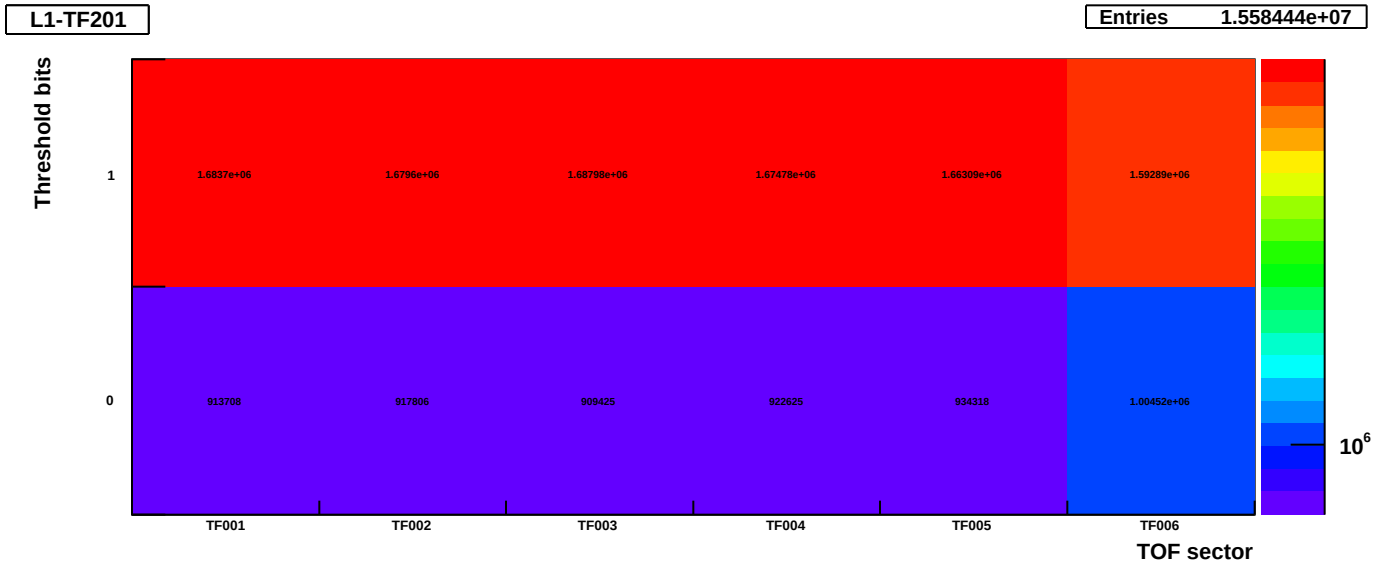

MIX-TF004
Entries 1.090911e+08
TOF MULT
TOF tray

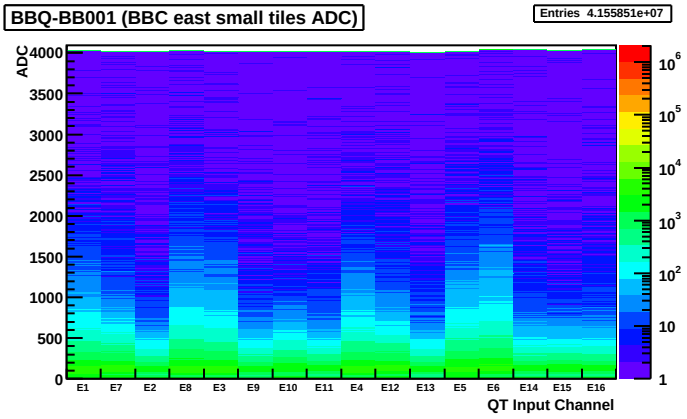

MIX-TF005
Entries 1.090911e+08
TOF MULT
TOF tray

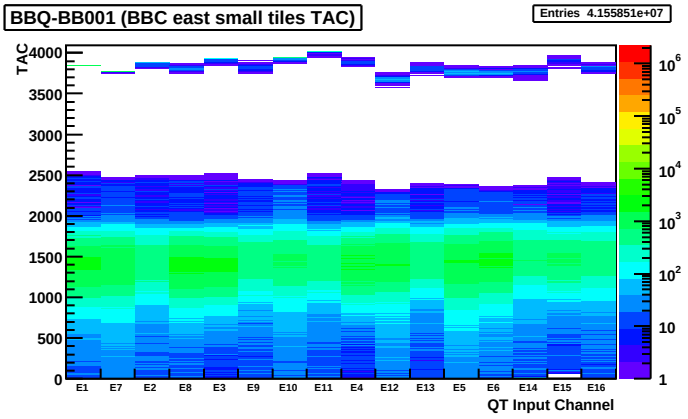

MIX-TF006
Entries 1.090911e+08
TOF MULT
TOF tray

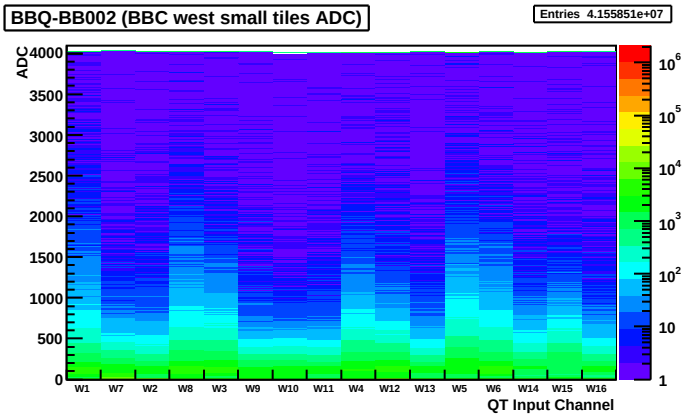

MIX-TF101
Entries 1.558444e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

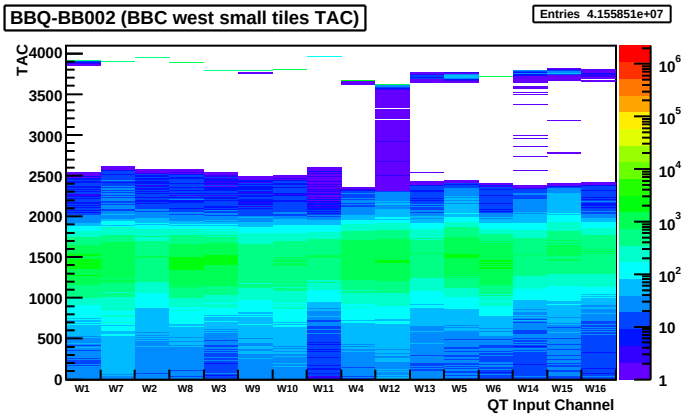

L1-TF201
Entries 2597407
TOF total mult

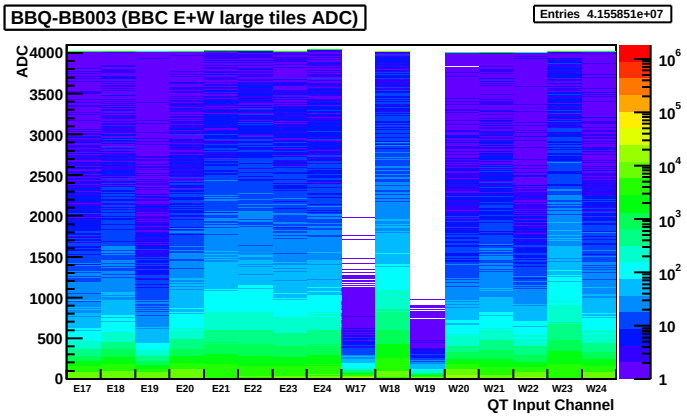

L1-TF201
Entries 1.558444e+07
Threshold bits
1.6837e+06
1.6796e+06
1.68798e+06
1.67478e+06
1.66309e+06
1.59289e+06
913708
917806
909425
922625
934318
1.00452e+06
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

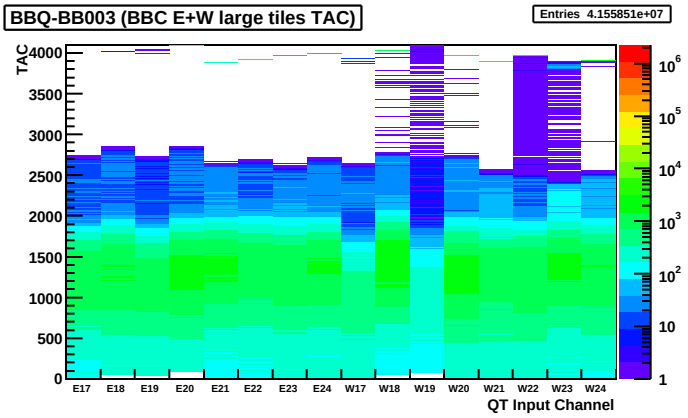

BBQ-BB001 (BBC east small tiles ADC)
Entries 4.155851e+07
ADC
QT Input Channel

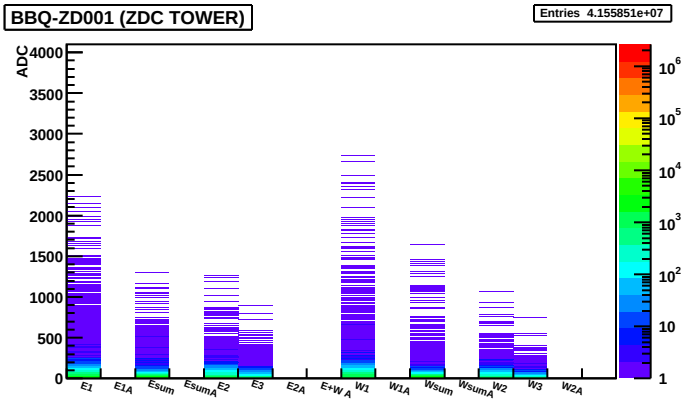

BBQ-BB001 (BBC east small tiles TAC)
Entries 4.155851e+07
TAC
QT Input Channel

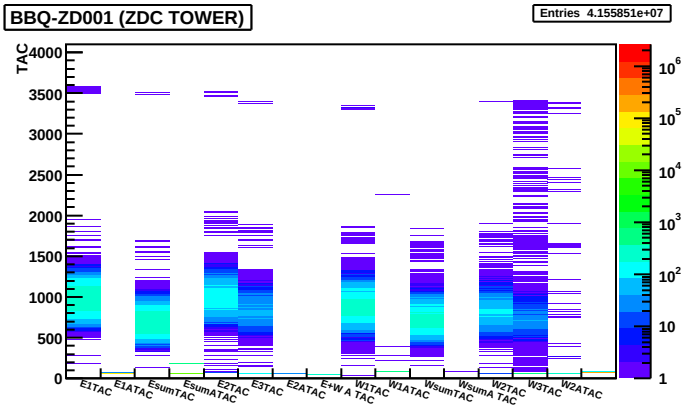

BBQ-BB002 (BBC west small tiles ADC)
Entries 4.155851e+07
ADC
QT Input Channel

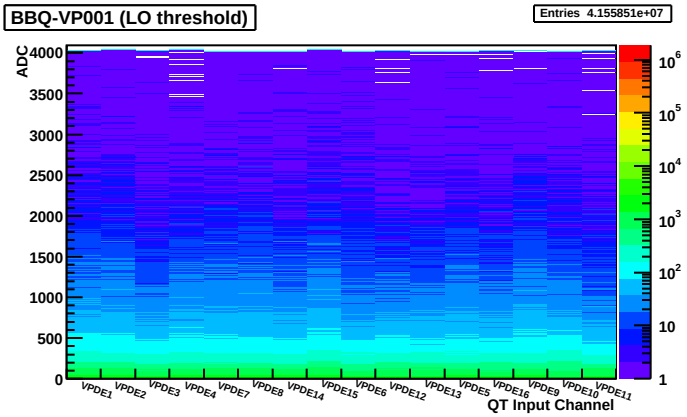

BBQ-BB002 (BBC west small tiles TAC)
Entries 4.155851e+07
TAC
QT Input Channel

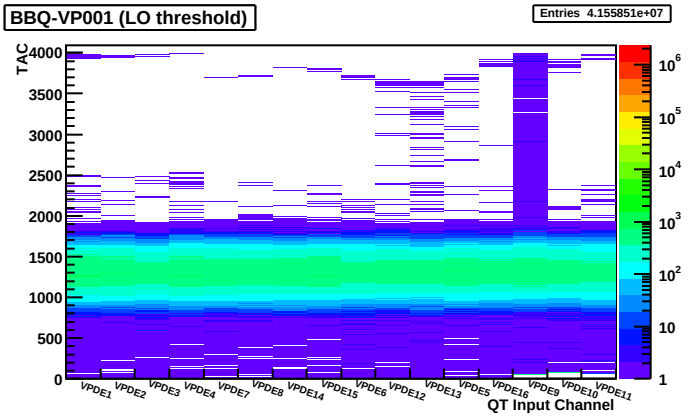

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4.155851e+07
ADC
QT Input Channel

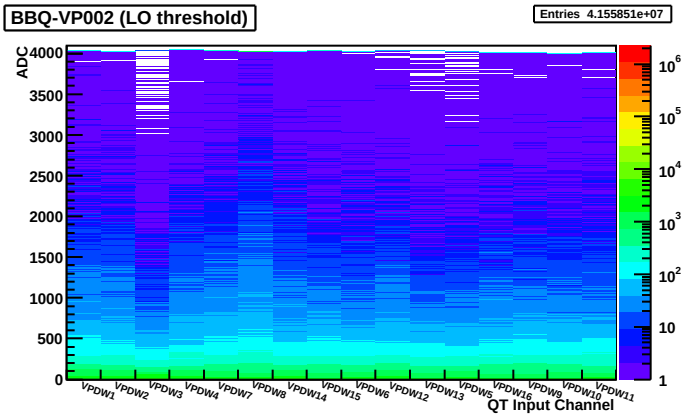

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4.155851e+07
TAC
QT Input Channel

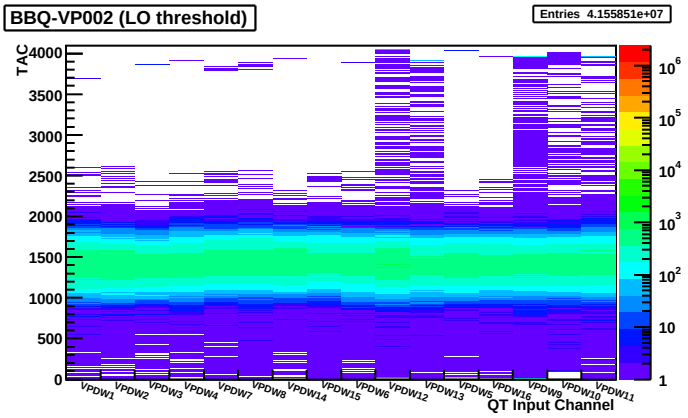

BBQ-ZD001 (ZDC TOWER)
Entries 4.155851e+07
ADC

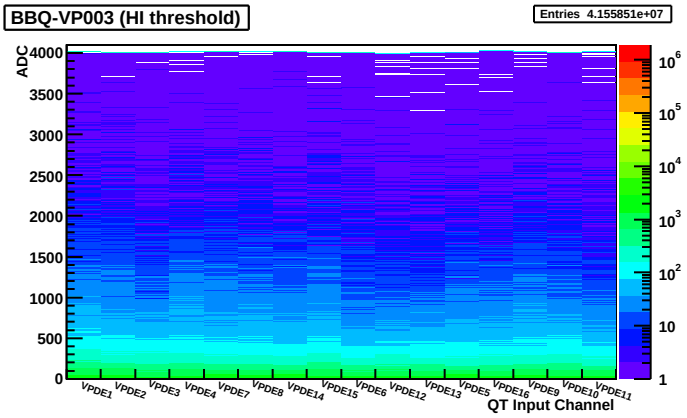

BBQ-ZD001 (ZDC TOWER)
Entries 4.155851e+07
TAC

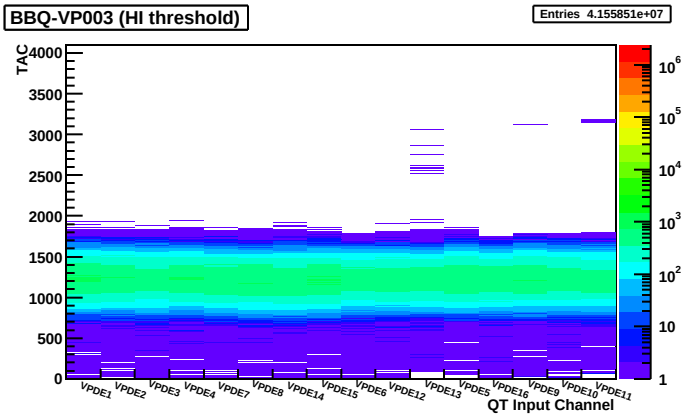

BBQ-VP001 (LO threshold)
Entries 4.155851e+07
ADC
QT Input Channel

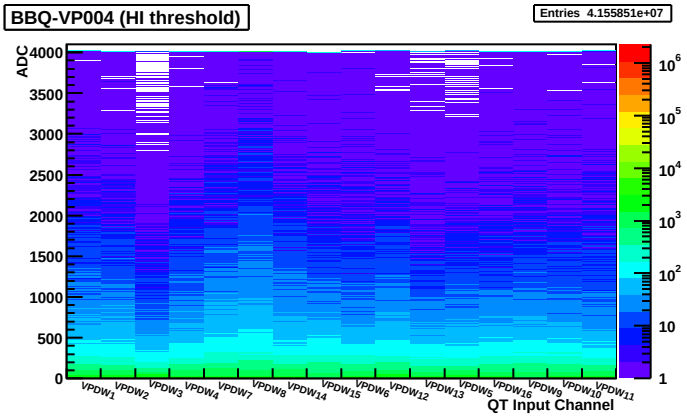

BBQ-VP001 (LO threshold)
Entries 4.155851e+07
TAC
QT Input Channel

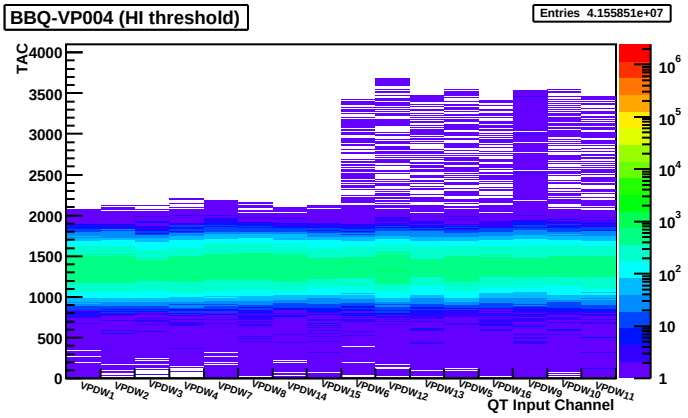

BBQ-VP002 (LO threshold)
Entries 4.155851e+07
ADC
QT Input Channel

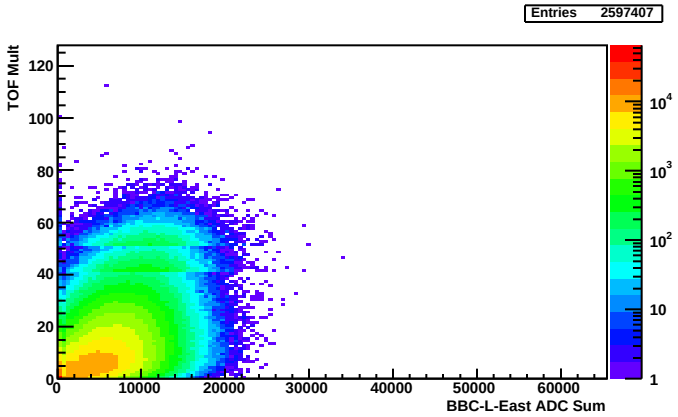

BBQ-VP002 (LO threshold)
Entries 4.155851e+07
TAC
QT Input Channel

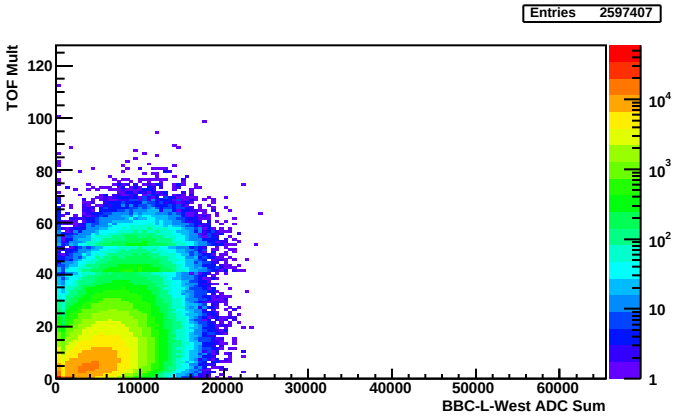

BBQ-VP003 (HI threshold)
Entries 4.155851e+07
ADC
QT Input Channel

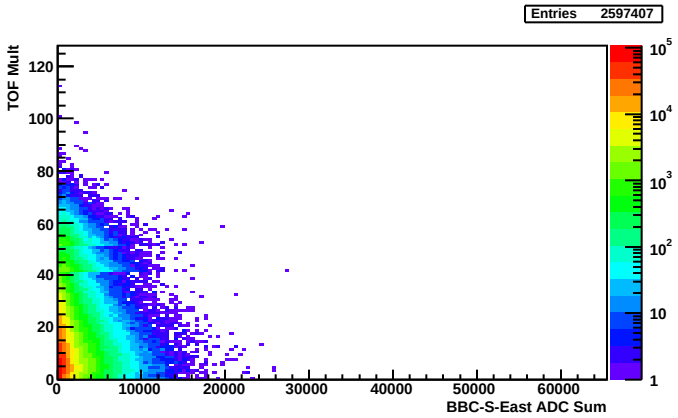

BBQ-VP003 (HI threshold)
Entries 4.155851e+07
TAC
QT Input Channel

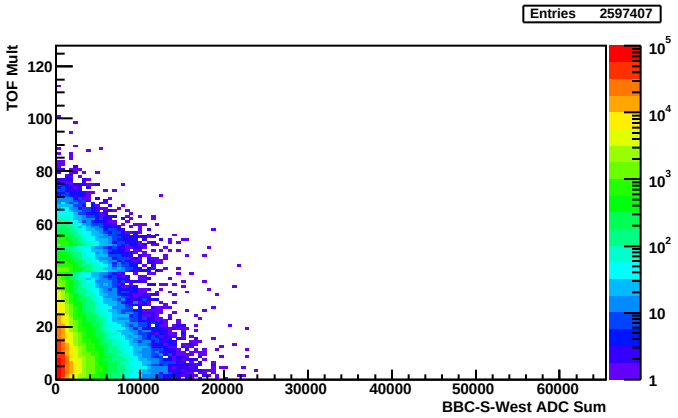

BBQ-VP004 (HI threshold)
Entries 4.155851e+07
ADC
QT Input Channel

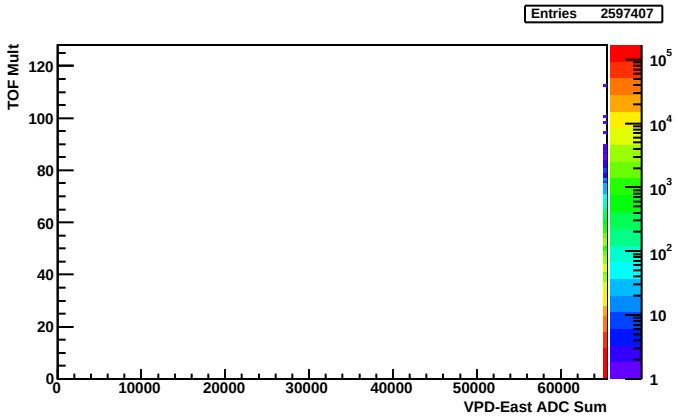

BBQ-VP004 (HI threshold)
Entries 4.155851e+07
TAC
QT Input Channel

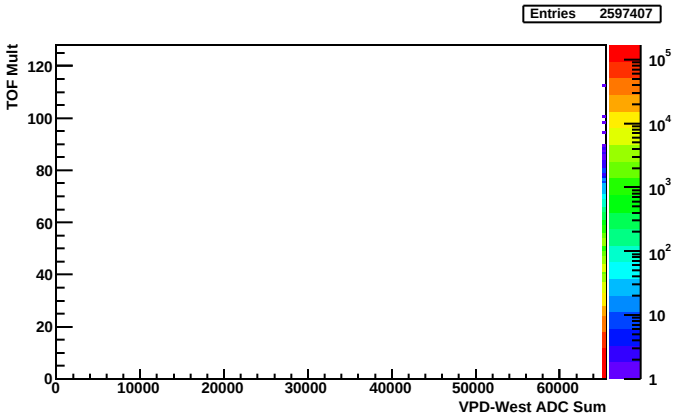

Entries 2597407
TOF Mult
BBC-L-East ADC Sum

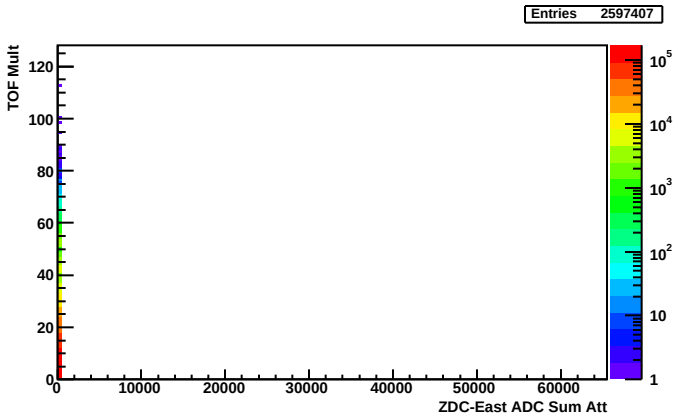

Entries 2597407
TOF Mult
BBC-L-West ADC Sum

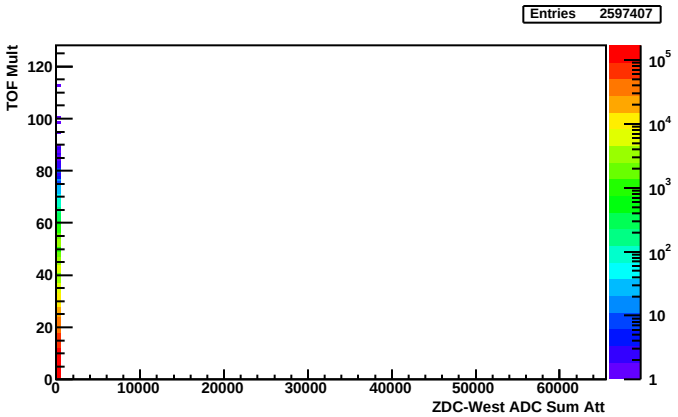

Entries 2597407
TOF Mult
BBC-S-East ADC Sum

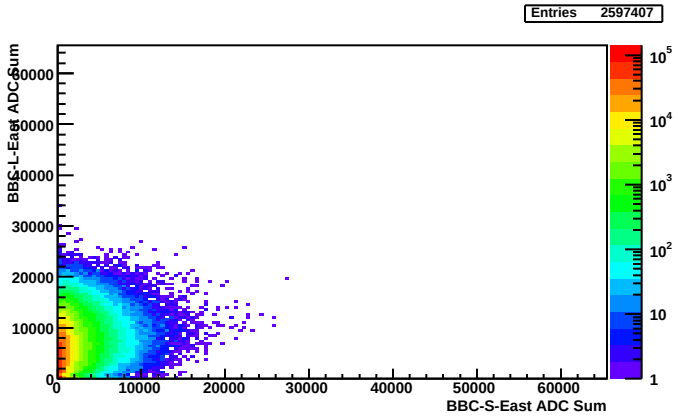

Entries 2597407
TOF Mult
BBC-S-West ADC Sum

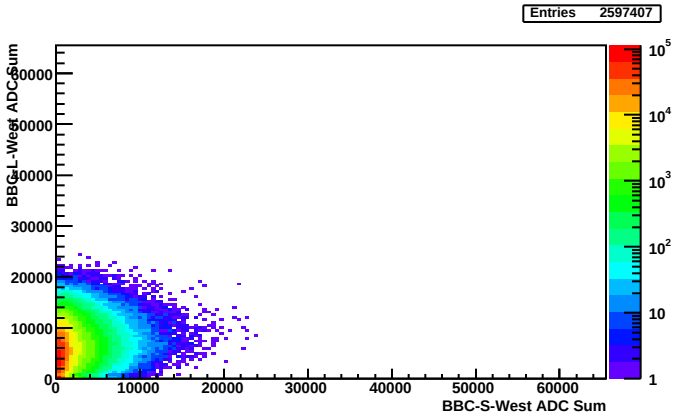

Entries 2597407
TOF Mult
VPD-East ADC Sum

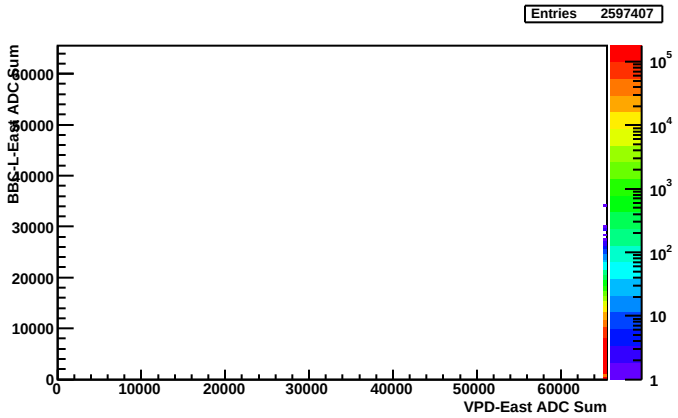

Entries 2597407
TOF Mult
VPD-West ADC Sum

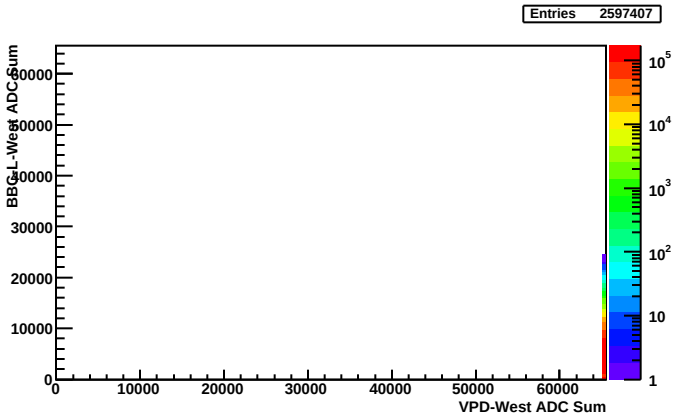

Entries 2597407
TOF Mult
ZDC-East ADC Sum Att

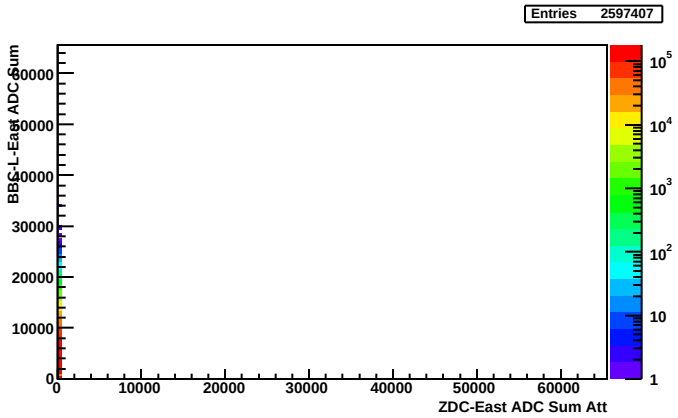

Entries 2597407
TOF Mult
ZDC-West ADC Sum Att

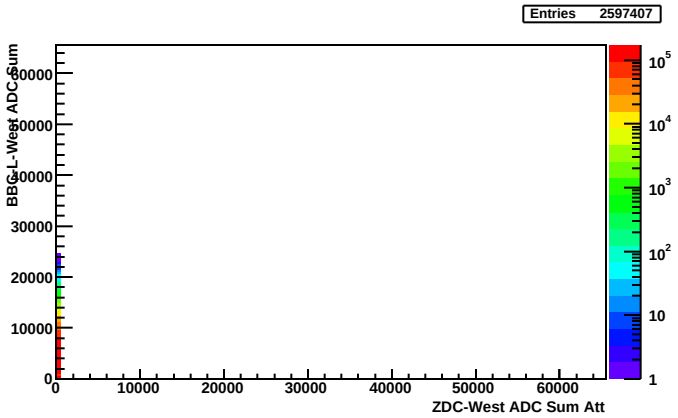

Entries 2597407
BBC-L-East ADC Sum
BBC-S-East ADC Sum

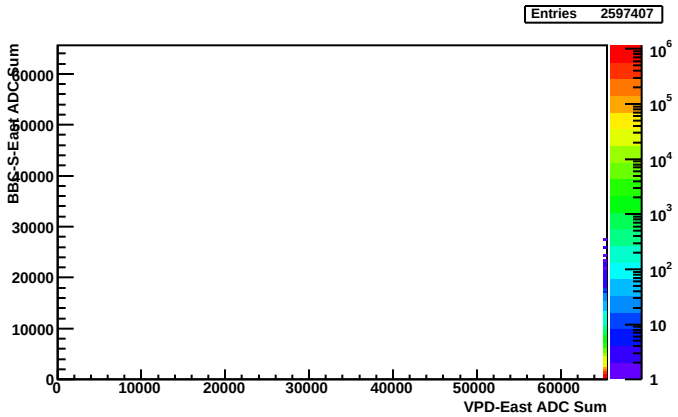

Entries 2597407
BBC-L-West ADC Sum
BBC-S-West ADC Sum

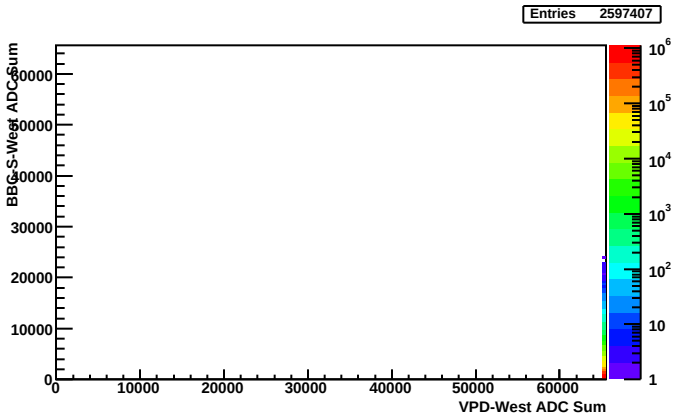

Entries 2597407
BBC-L-East ADC Sum
VPD-East ADC Sum

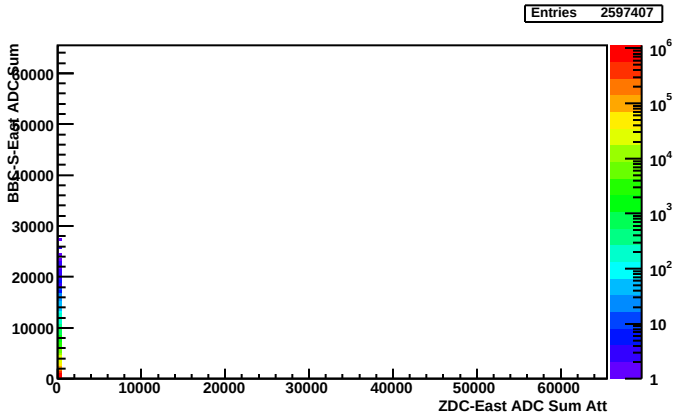

Entries 2597407
BBC-L-West ADC Sum
VPD-West ADC Sum

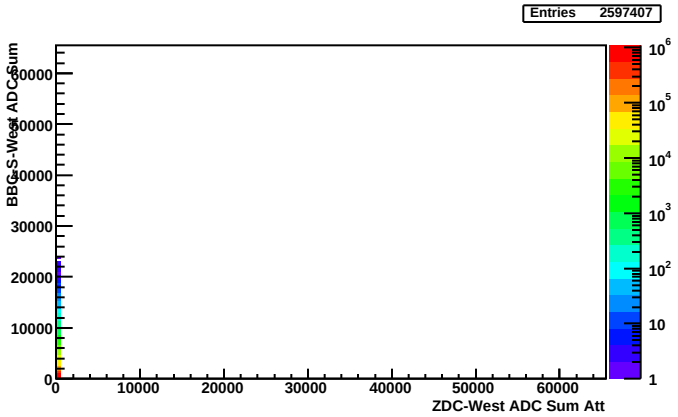

Entries 2597407
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

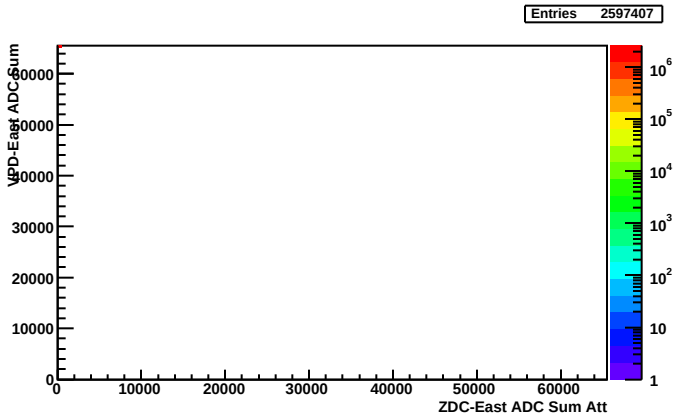

Entries 2597407
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

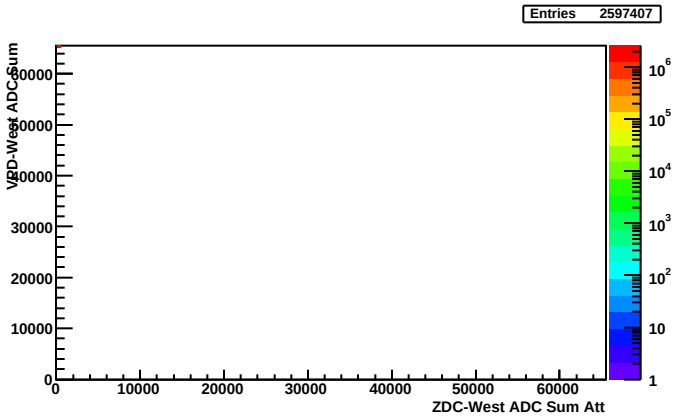

Entries 2597407
BBC-S-East ADC Sum
VPD-East ADC Sum

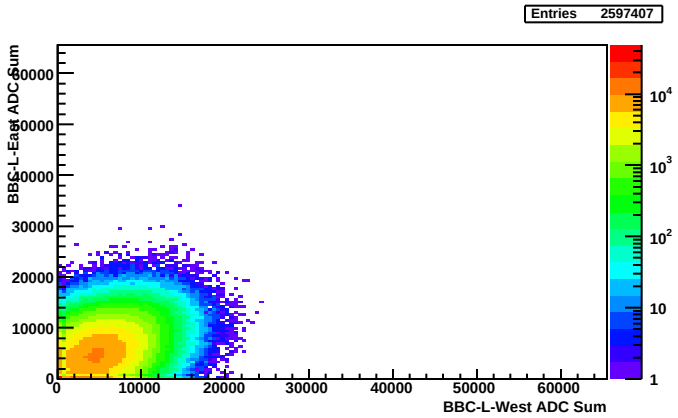

Entries 2597407
BBC-S-West ADC Sum
VPD-West ADC Sum

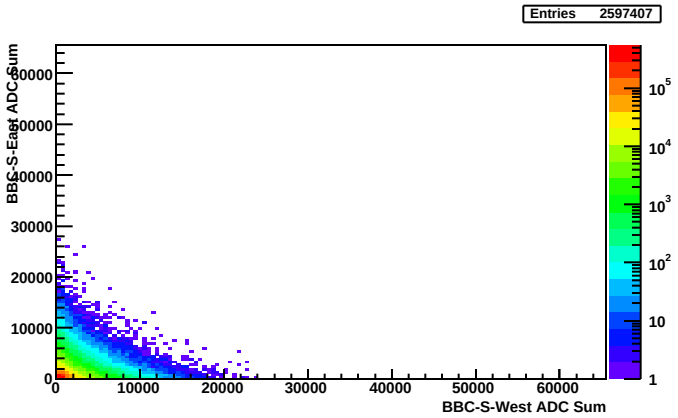

Entries 2597407
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

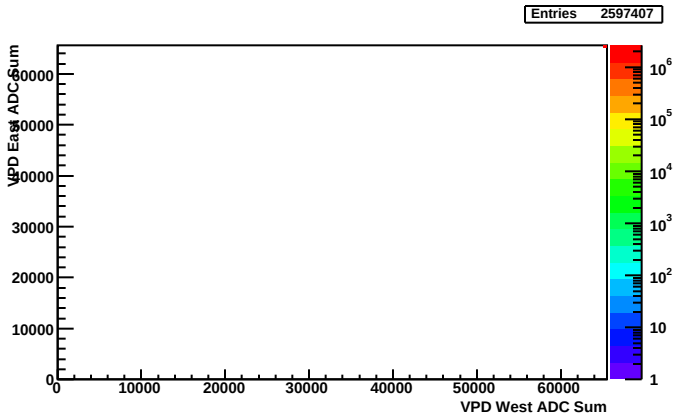

Entries 2597407
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

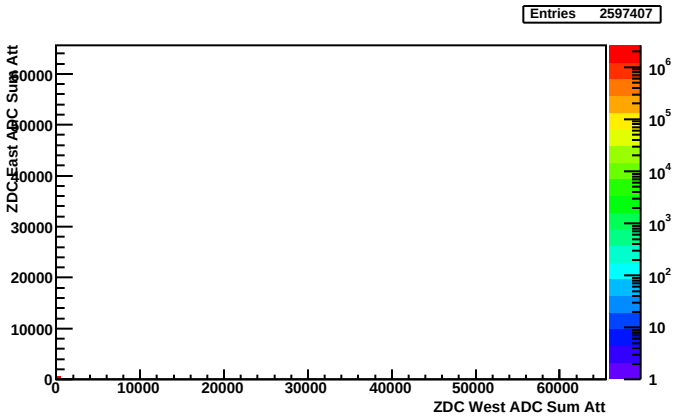

Entries 2597407
VPD-East ADC Sum
ZDC-East ADC Sum Att

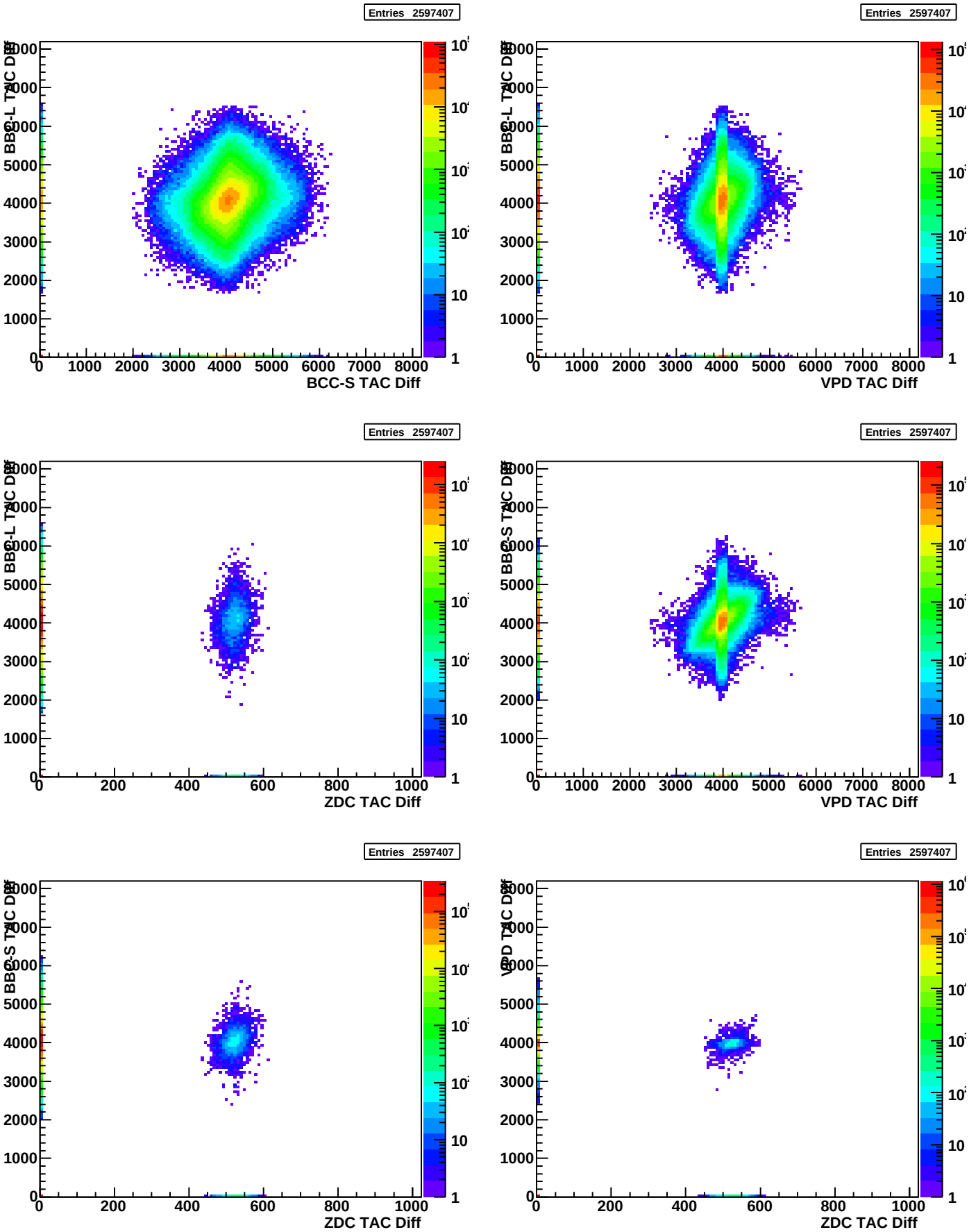

Entries 2597407
VPD-West ADC Sum
ZDC-West ADC Sum Att

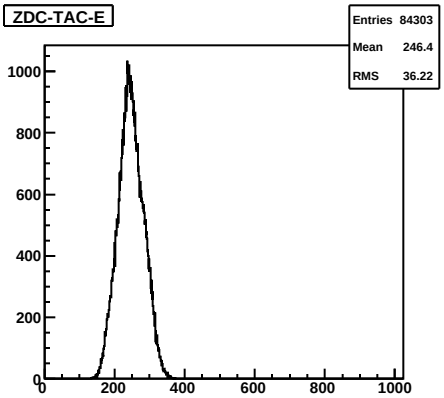

Entries 2597407
BBC-L-East ADC Sum
BBC-L-West ADC Sum

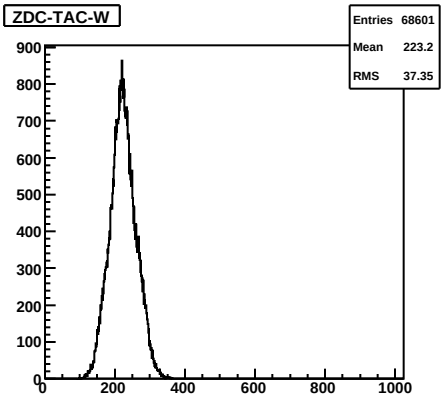

Entries 2597407
BBC-S-East ADC Sum
BBC-S-West ADC Sum

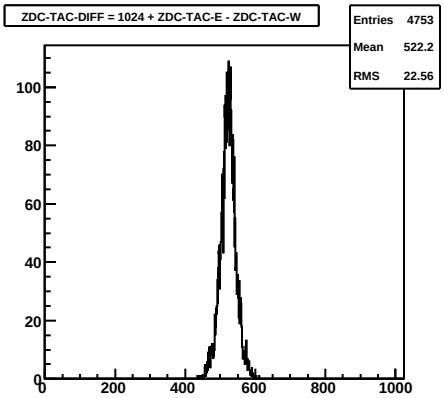

Entries 2597407
VPD East ADC Sum
VPD West ADC Sum

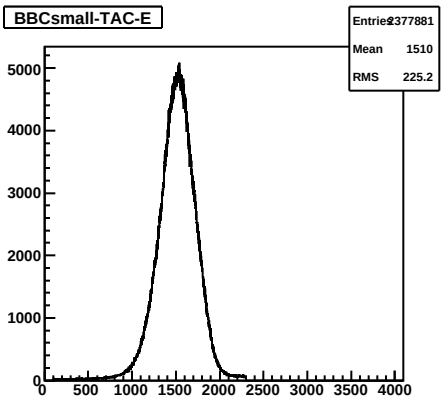

Entries 2597407
ZDC East ADC Sum Att
ZDC West ADC Sum Att

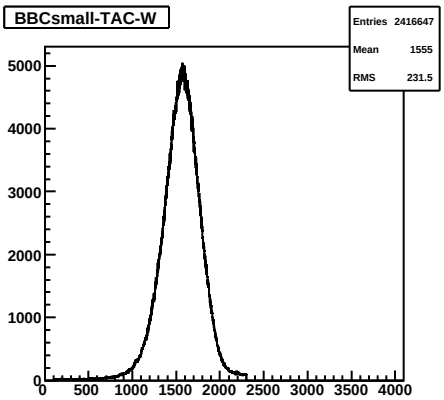

Entries 2597407
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2597407
BBC-L TAC Diff
VPD TAC Diff
Entries 2597407
BBC-L TAC Diff
ZDC TAC Diff
Entries 2597407
BBC-S TAC Diff
VPD TAC Diff
Entries 2597407
BBC-S TAC Diff
ZDC TAC Diff
Entries 2597407
VPD TAC Diff
ZDC TAC Diff

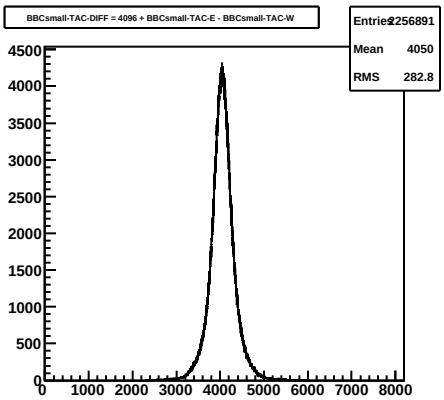

ZDC-TAC-E
Entries 84303
Mean 246.4
RMS 36.22

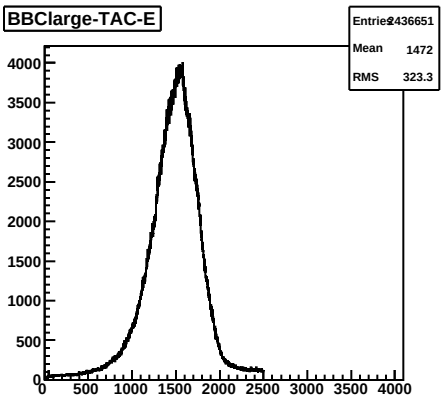

ZDC-TAC-W
Entries 68601
Mean 223.2
RMS 37.35

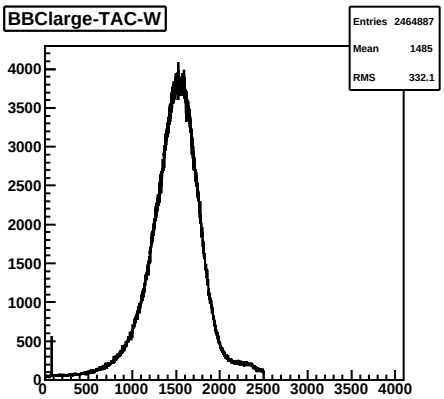

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 4753
Mean 522.2
RMS 22.56

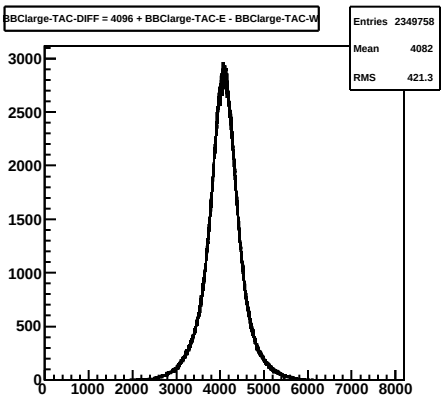

BBCsmall-TAC-E
Entries 2377881
Mean 1510
RMS 225.2

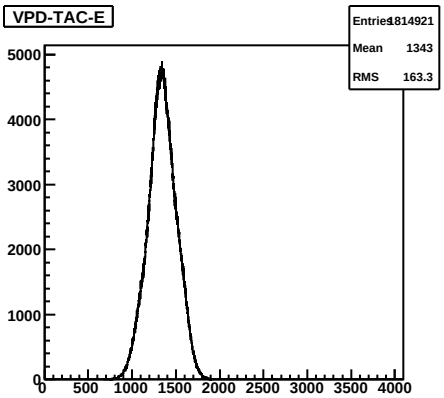

BBCsmall-TAC-W
Entries 2416647
Mean 1555
RMS 231.5

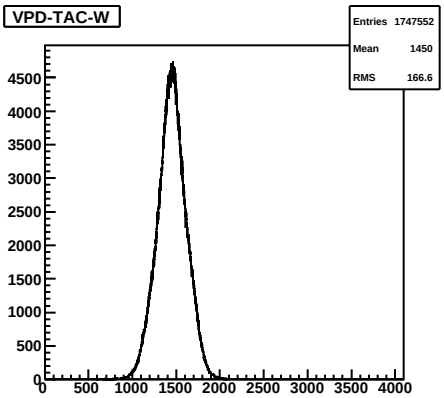

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2256891
Mean 4050
RMS 282.8

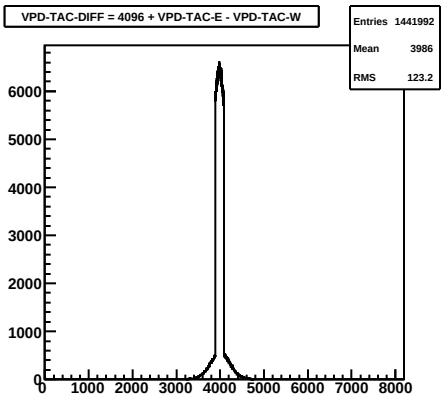

BBClarge-TAC-E
Entries 2436651
Mean 1472
RMS 323.3

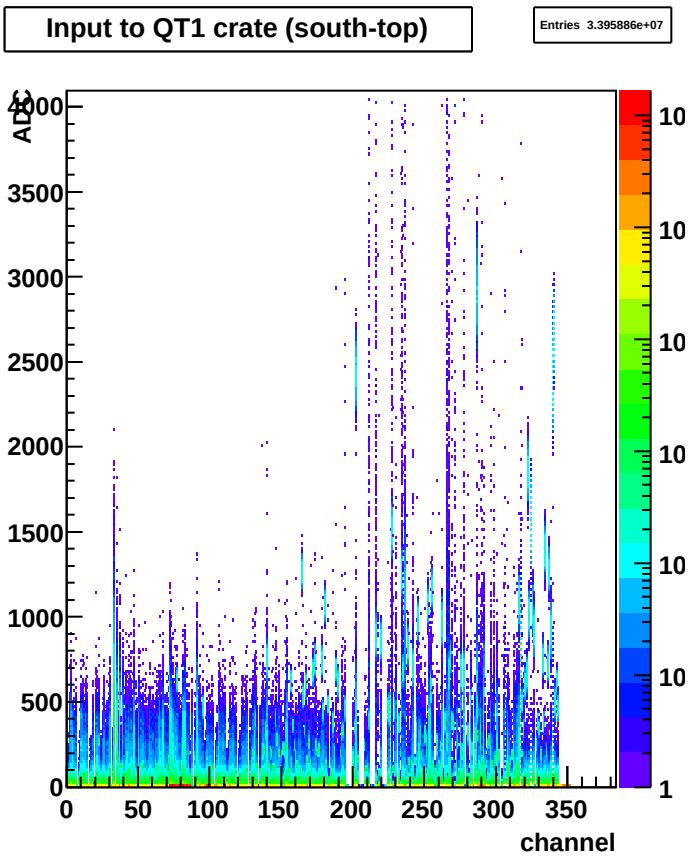

BBClarge-TAC-W
Entries 2464887
Mean 1485
RMS 332.1

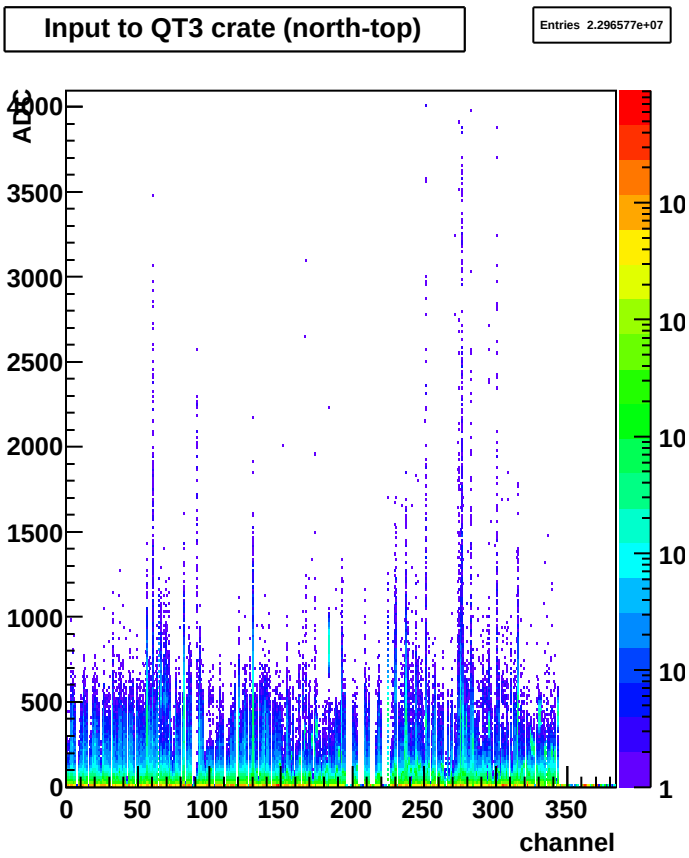

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2349758
Mean 4082
RMS 421.3

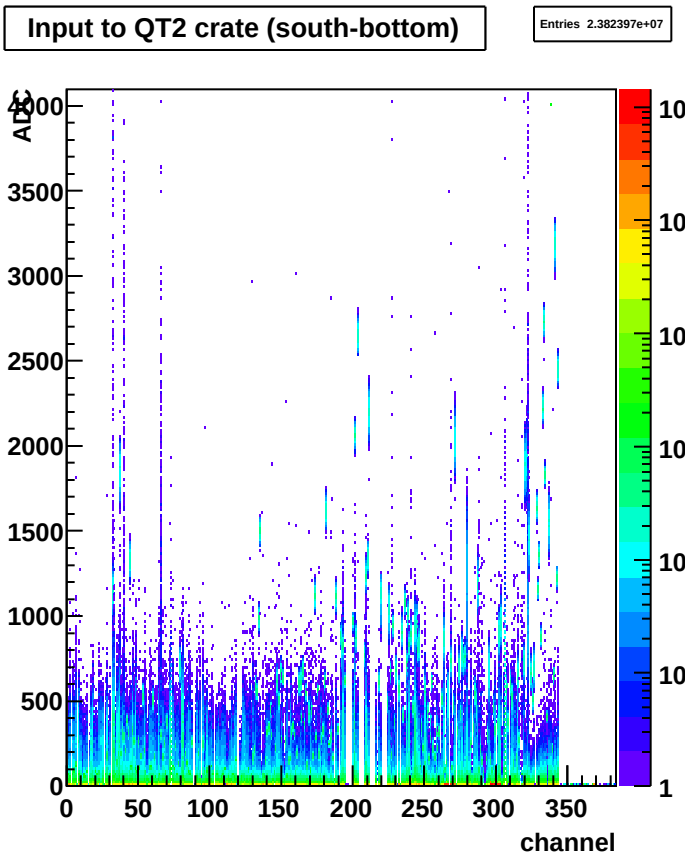

VPD-TAC-E
Entries 1814921
Mean 1343
RMS 163.3

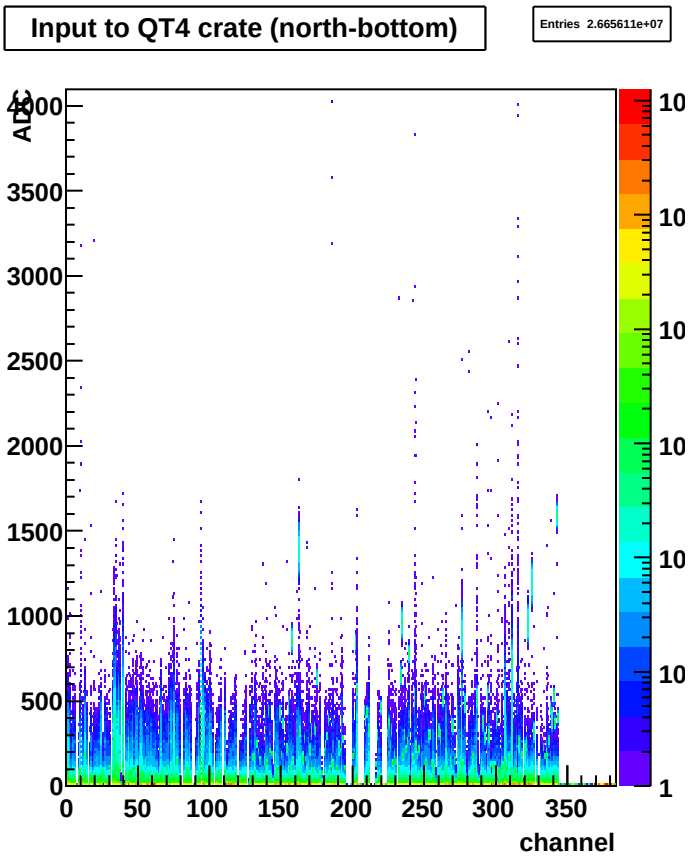

VPD-TAC-W
Entries 1747552
Mean 1450
RMS 166.6

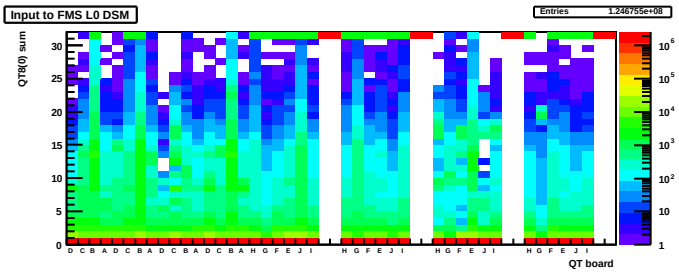

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1441992
Mean 3986
RMS 123.2

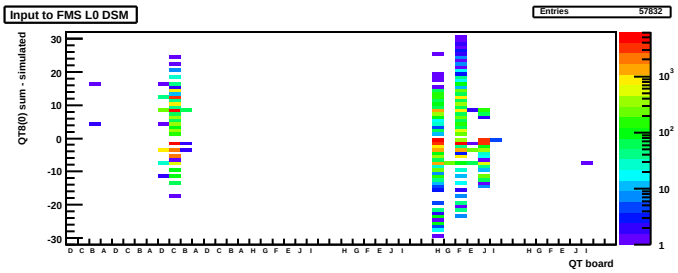

Input to QT1 crate (south-top)
Entries 3.395886e+07
ADC
channel

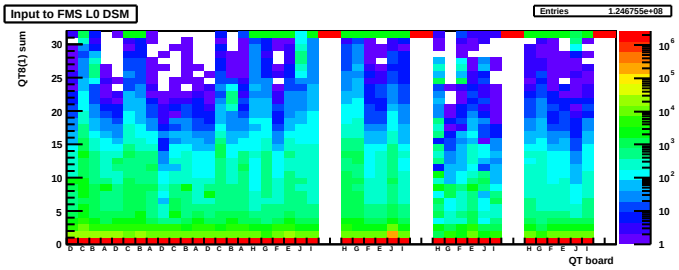

Input to QT3 crate (north-top)
Entries 2.296577e+07
ADC
channel

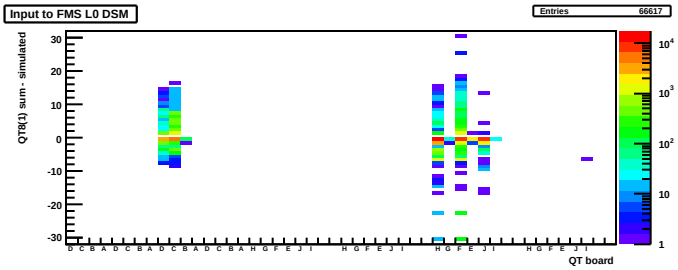

Input to QT2 crate (south-bottom)
Entries 2.382397e+07
ADC
channel

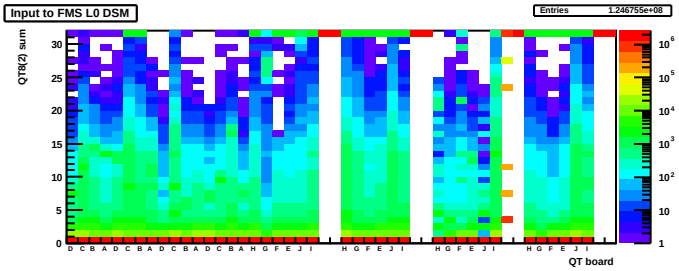

Input to QT4 crate (north-bottom)
Entries 2.665611e+07
ADC
channel

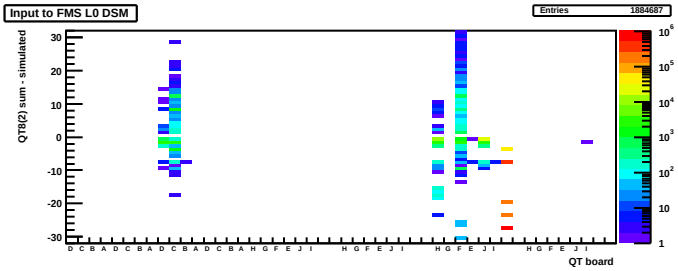

Input to FMS L0 DSM
Entries 1.246755e+08
QT8(0) sum
QT board

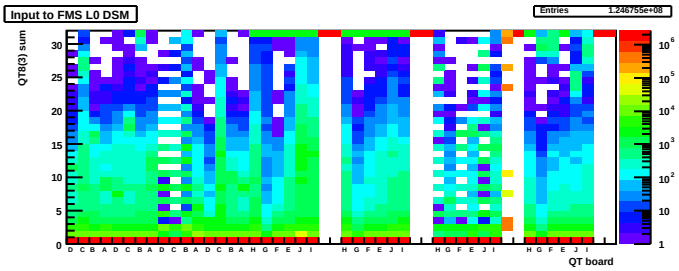

Input to FMS L0 DSM
Entries 57832
QT8(0) sum - simulated
QT board

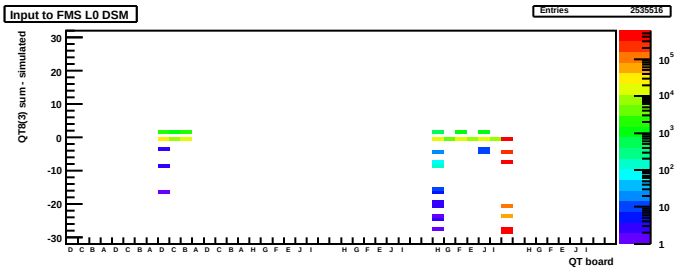

Input to FMS L0 DSM
Entries 1.246755e+08
QT8(1) sum
QT board

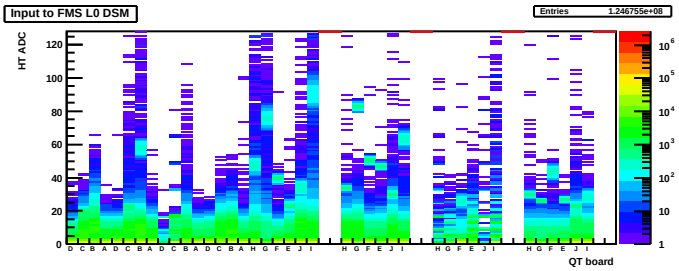

Input to FMS L0 DSM
Entries 66617
QT8(1) sum - simulated
QT board

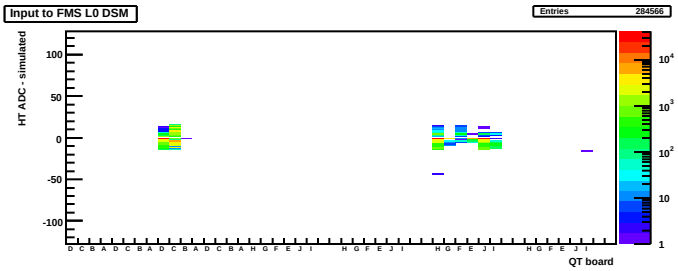

Input to FMS L0 DSM
Entries 1.246755e+08
QT8(2) sum
QT board

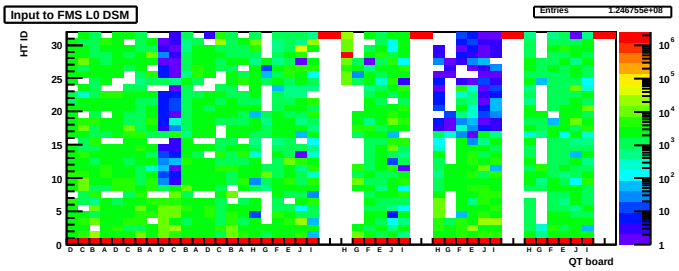

Input to FMS L0 DSM
Entries 1884687
QT8(2) sum - simulated
QT board

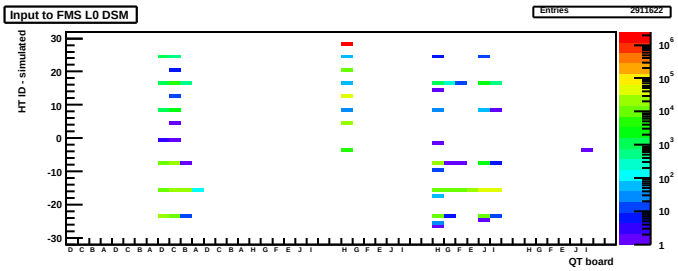

Input to FMS L0 DSM
Entries 1.246755e+08
QT8(3) sum
QT board

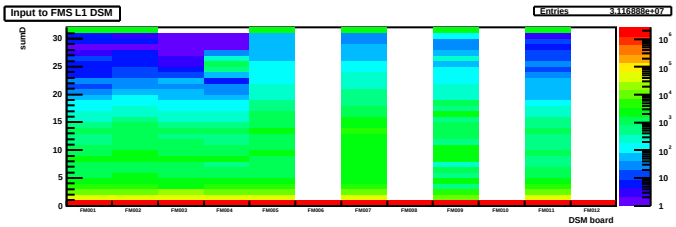

Input to FMS L0 DSM
Entries 2535516
QT8(3) sum - simulated
QT board

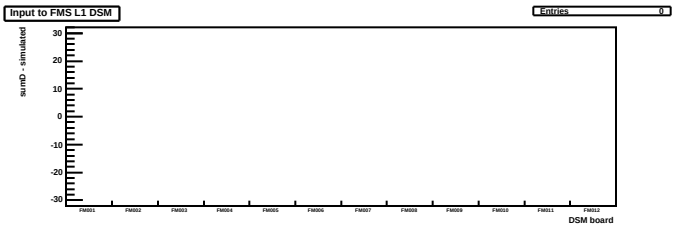

Input to FMS L0 DSM
Entries 1.246755e+08
HT ADC
QT board

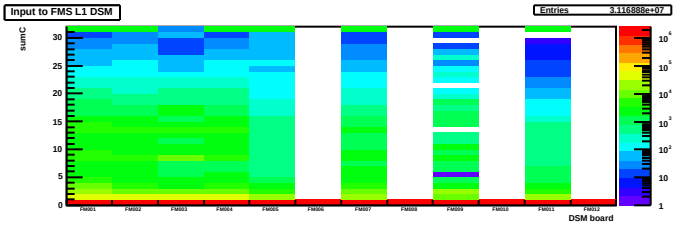

Input to FMS L0 DSM
Entries 284566
HT ADC - simulated
QT board

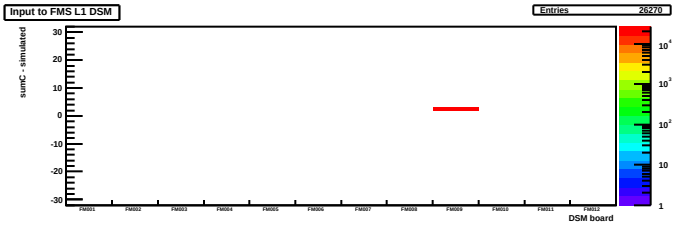

Input to FMS L0 DSM
Entries 1.246755e+08
HT ID
QT board

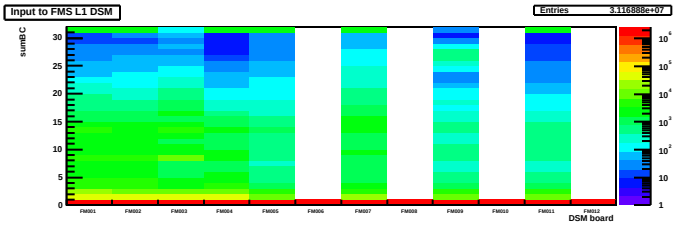

Input to FMS L0 DSM
Entries 2911622
HT ID - simulated
QT board

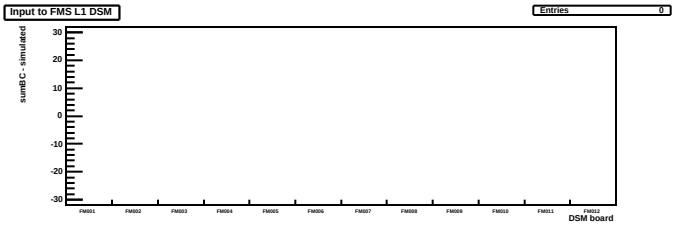

Input to FMS L1 DSM
Entries 3.116888e+07
sumD
DSM board

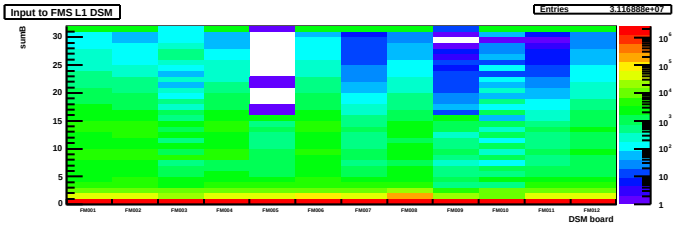

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

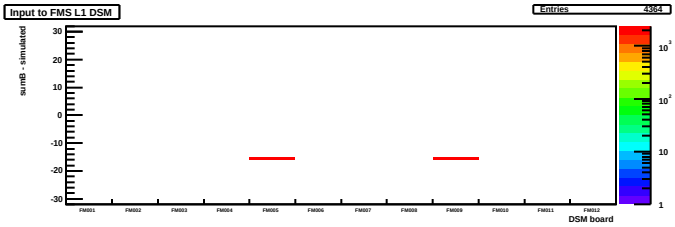

Input to FMS L1 DSM
Entries 3.116888e+07
sumC
DSM board

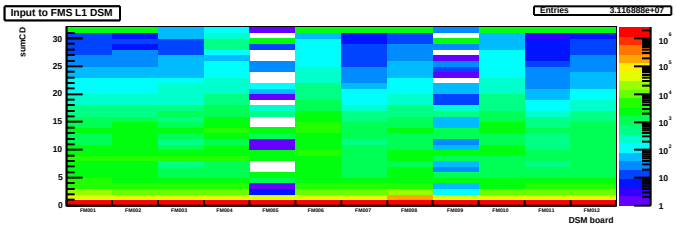

Input to FMS L1 DSM
Entries 26270
sumC - simulated
DSM board

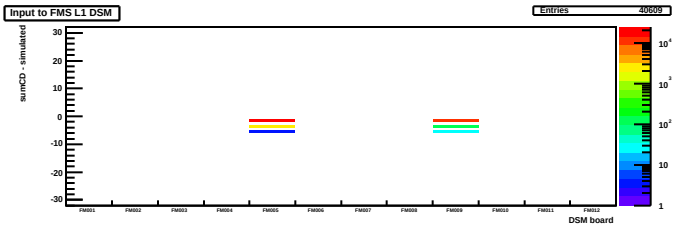

Input to FMS L1 DSM
Entries 3.116888e+07
sumBC
DSM board

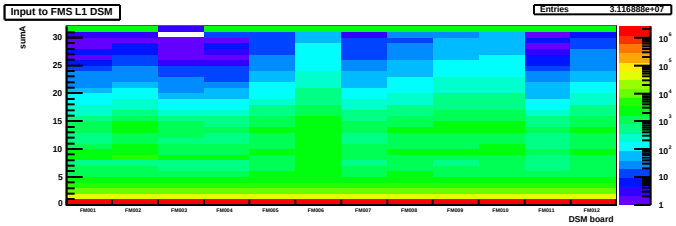

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

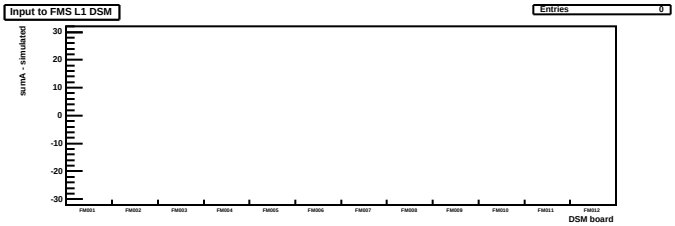

Input to FMS L1 DSM
Entries 3.116888e+07
sumB
DSM board

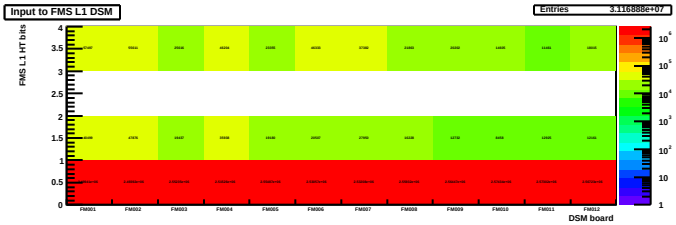

Input to FMS L1 DSM
Entries 4364
sumB - simulated
DSM board

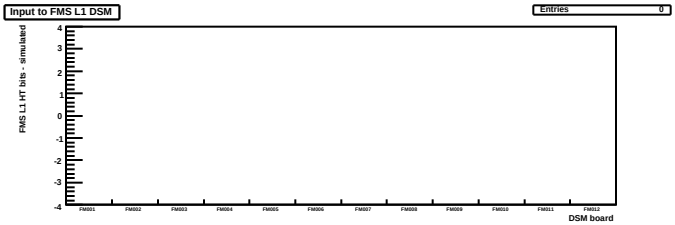

Input to FMS L1 DSM
Entries 3.116888e+07
sumCD
DSM board

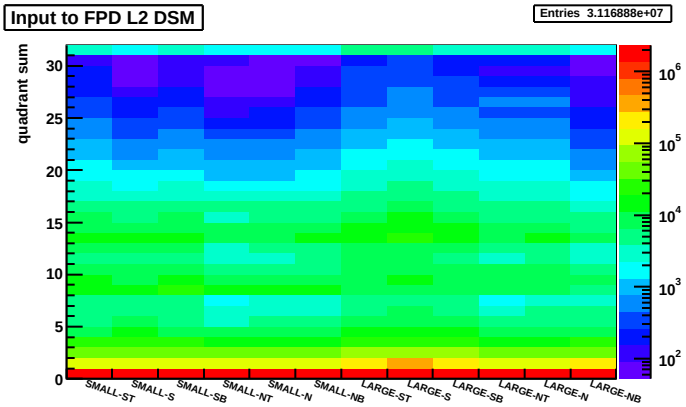

Input to FMS L1 DSM
Entries 40609
sumCD - simulated
DSM board

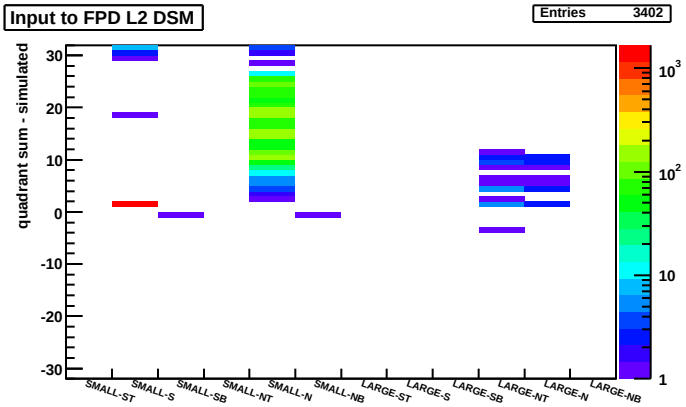

Input to FMS L1 DSM
Entries 3.116888e+07
sumA
DSM board

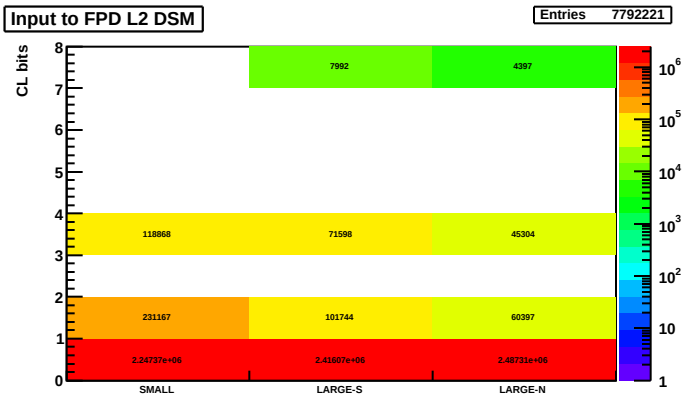

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

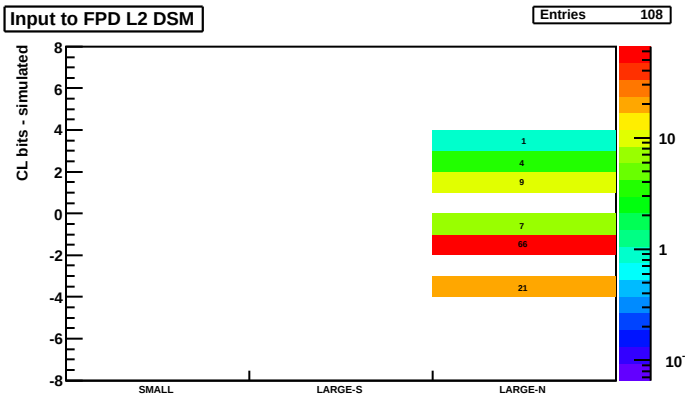

Input to FMS L1 DSM
Entries 3.116888e+07
FMS L1 HT bits
DSM board

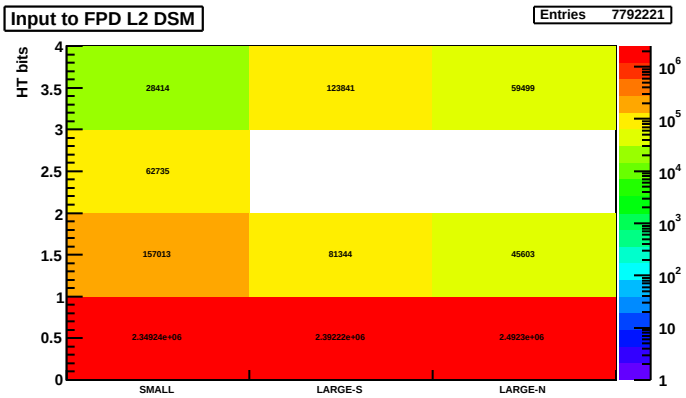

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

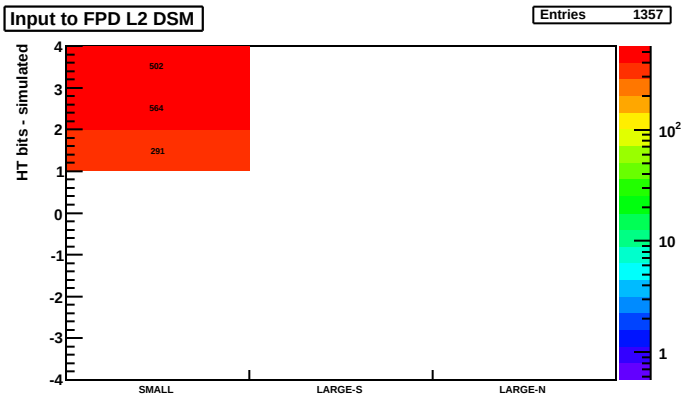

Input to FPD L2 DSM
Entries 3.116888e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

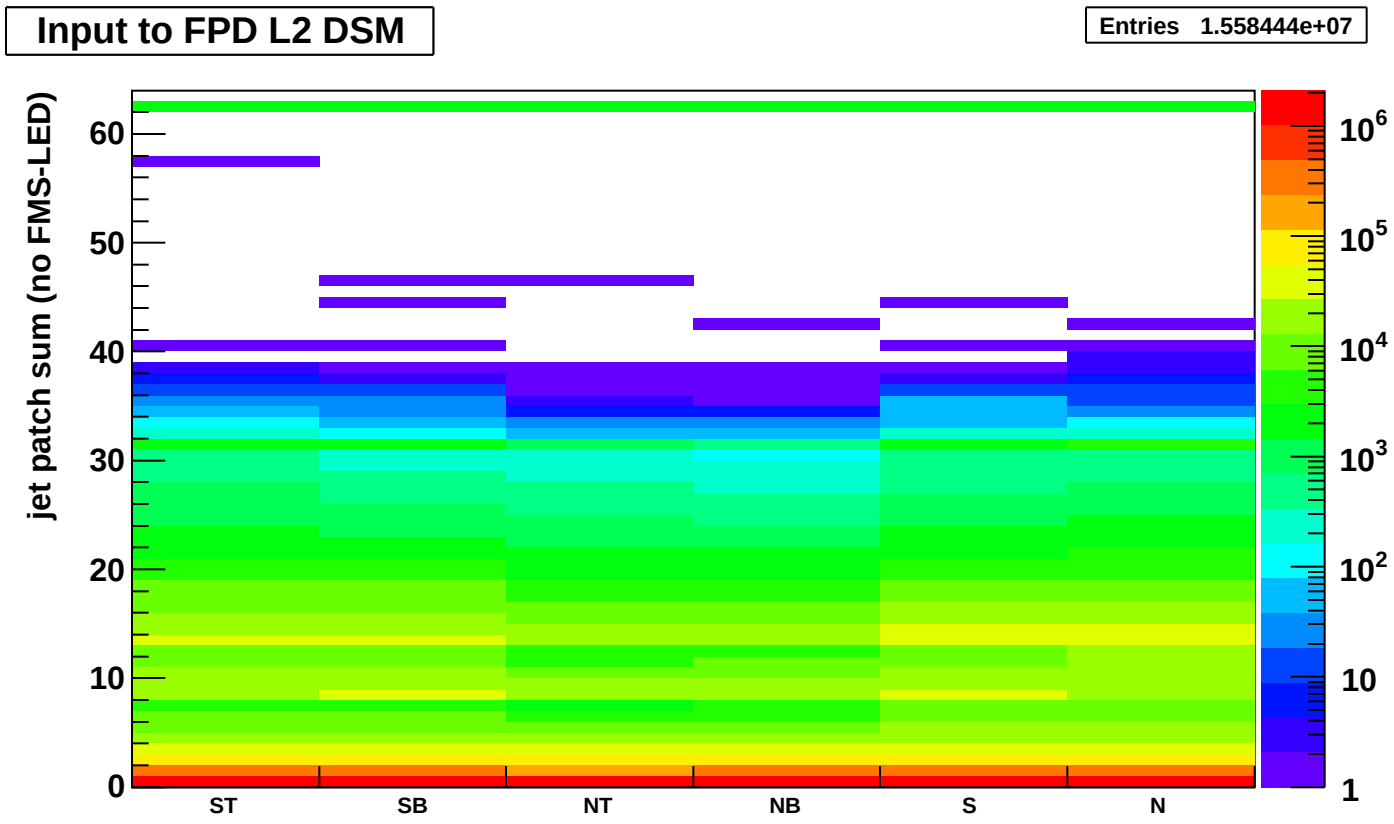

Input to FPD L2 DSM
Entries 3402
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

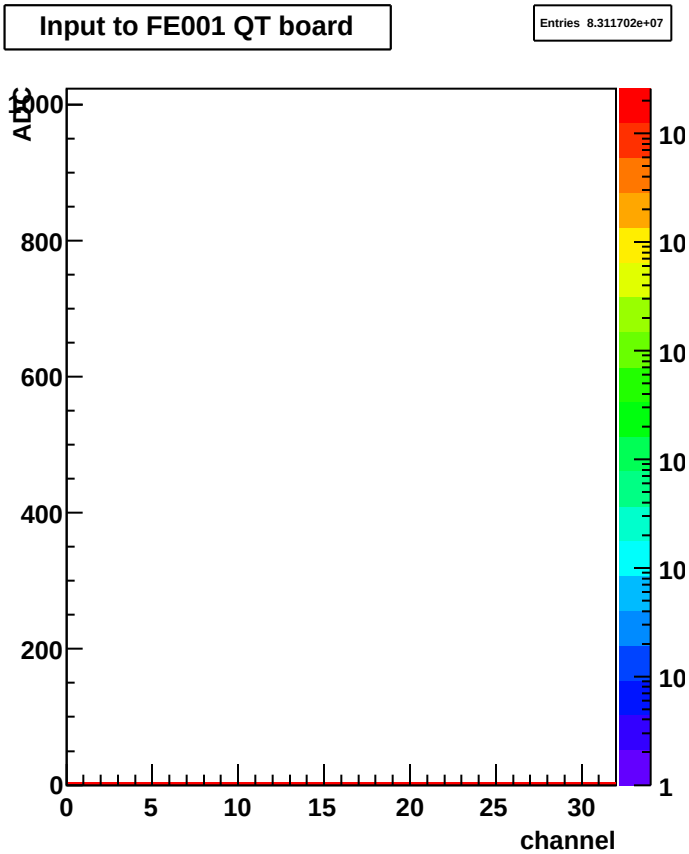

Input to FPD L2 DSM
Entries 7792221
CL bits
7992
4397
118868
71598
45304
231167
101744
60397
2.24737e+06
2.41607e+06
2.48731e+06
SMALL
LARGE-S
LARGE-N

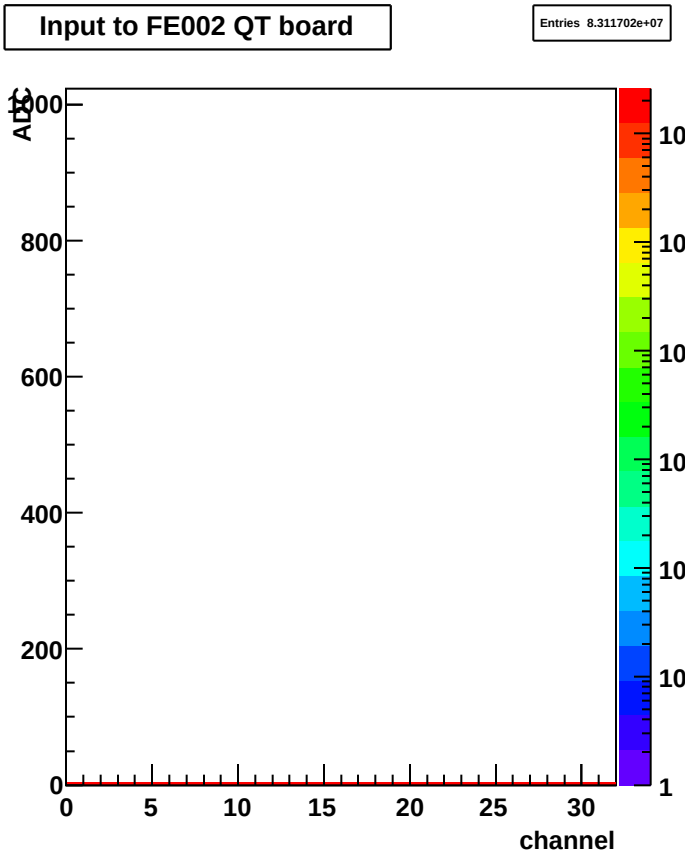

Input to FPD L2 DSM
Entries 108
CL bits - simulated
1
4
9
7
66
21
SMALL
LARGE-S
LARGE-N

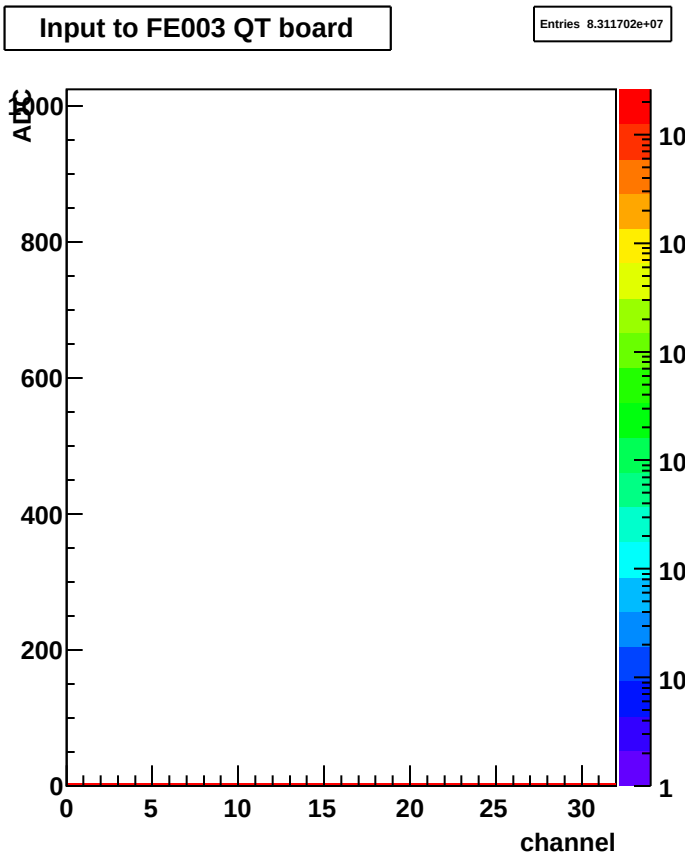

Input to FPD L2 DSM
Entries 7792221
HT bits
28414
123841
59499
62735
157013
81344
45603
2.34924e+06
2.39222e+06
2.4923e+06
SMALL
LARGE-S
LARGE-N

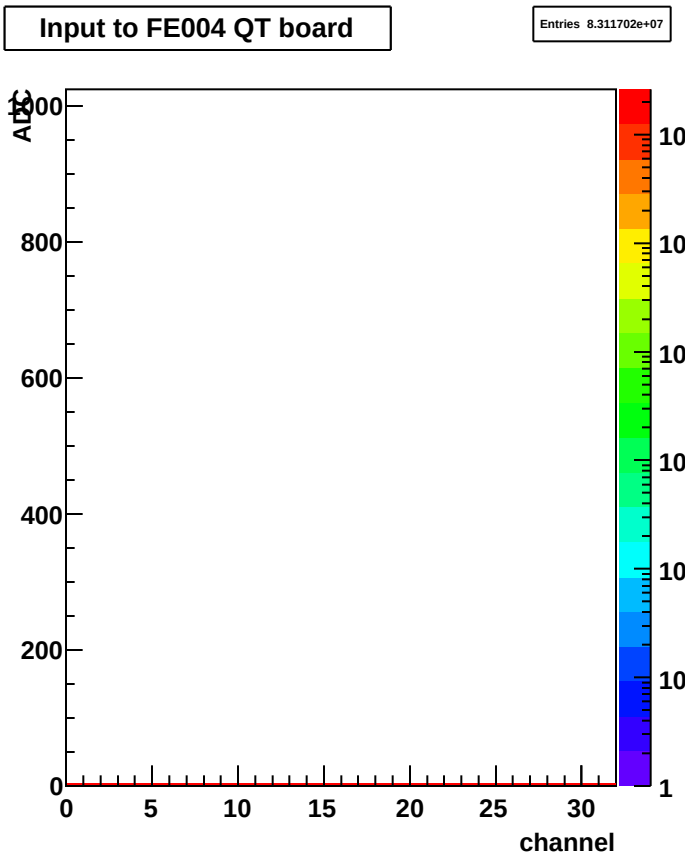

Input to FPD L2 DSM
Entries 1357
HT bits - simulated
502
564
291
SMALL
LARGE-S
LARGE-N

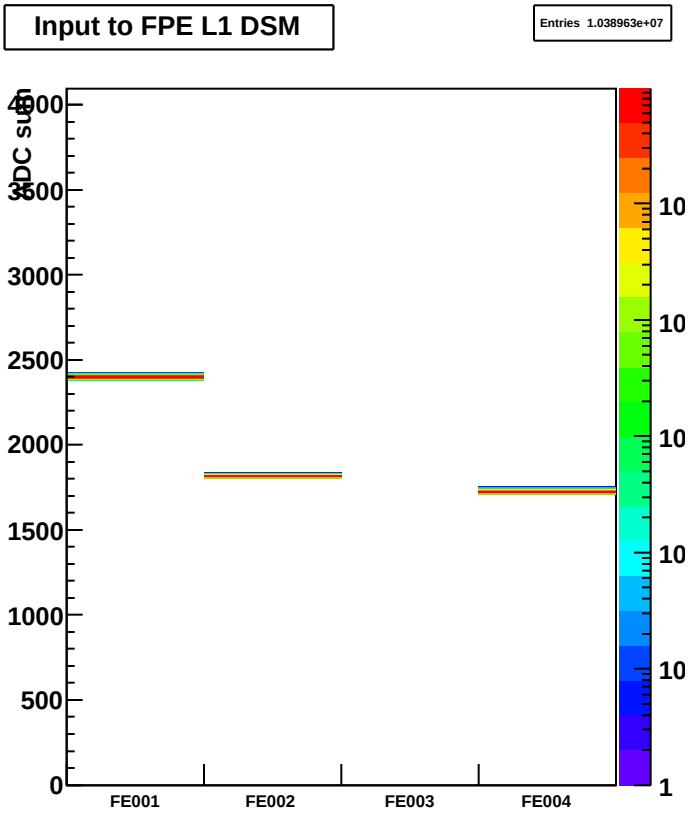

Input to FPD L2 DSM
Entries 1.558444e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

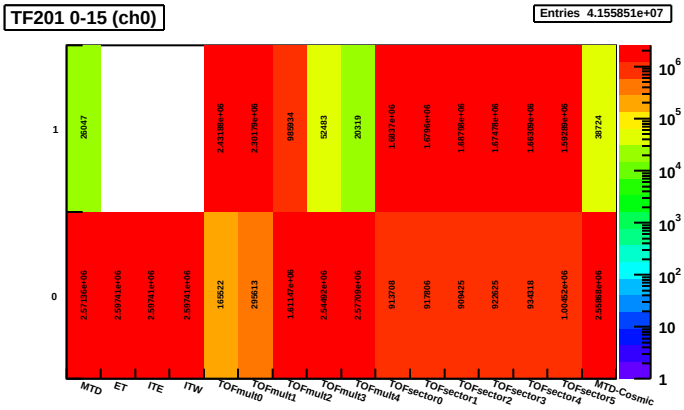

Input to FE001 QT board
Entries 8.311702e+07
ADC
channel

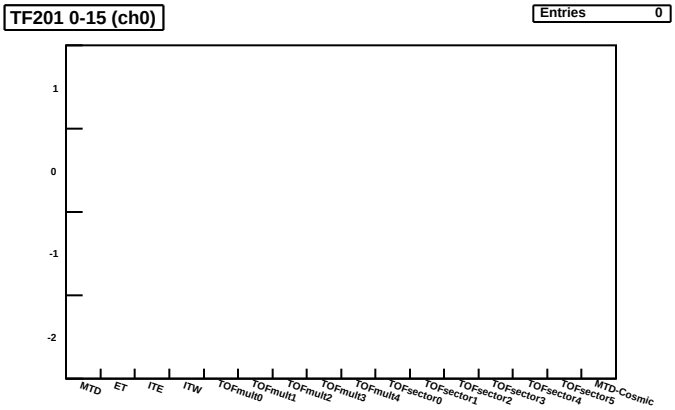

Input to FE002 QT board
Entries 8.311702e+07
ADC
channel

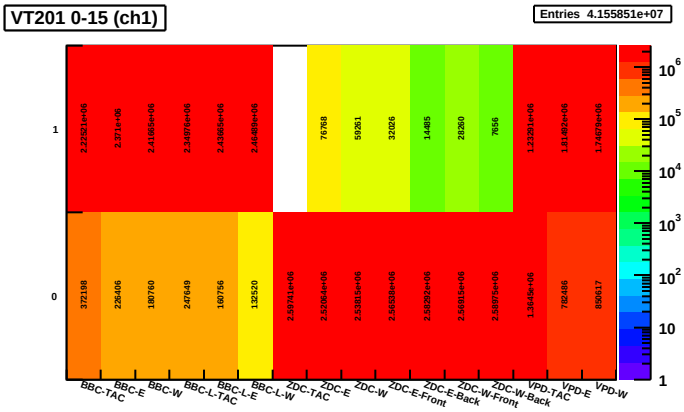

Input to FE003 QT board
Entries 8.311702e+07
ADC
channel


Input to FE004 QT board
Entries 8.311702e+07
ADC
channel


Input to FPE L1 DSM
Entries 1.038963e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 4.155851e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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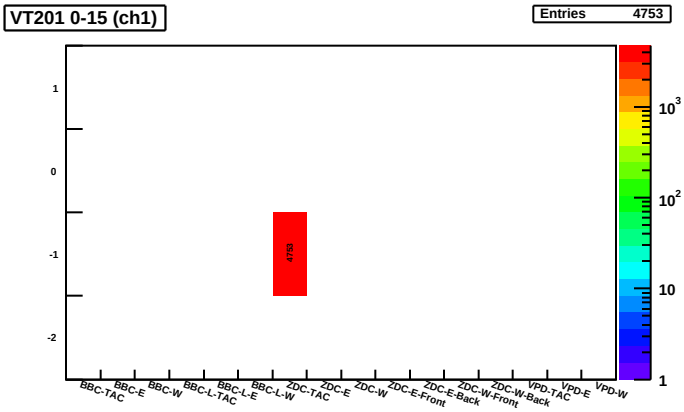
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4753
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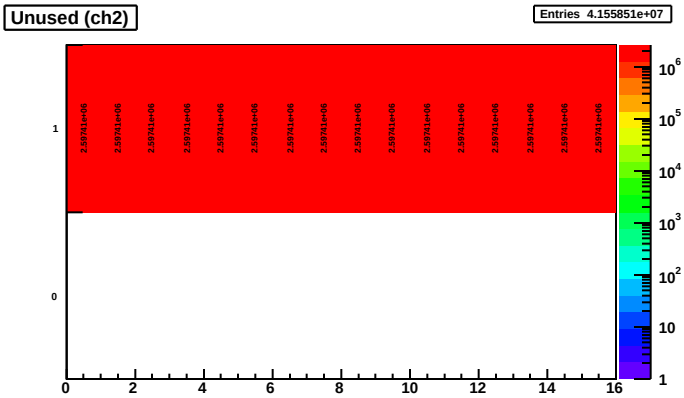
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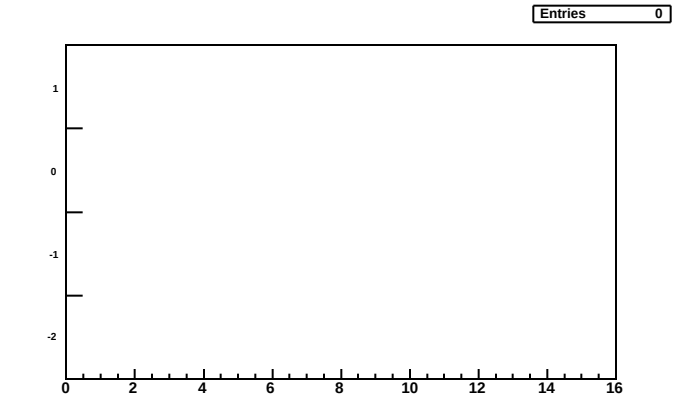
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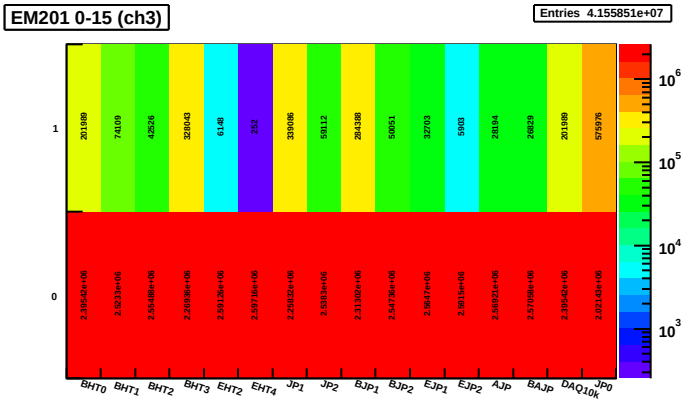
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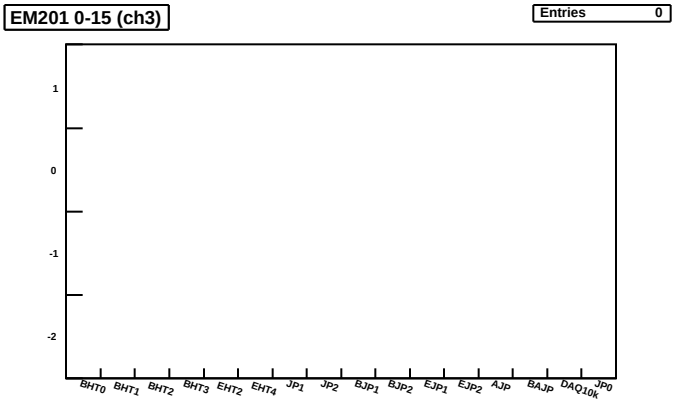
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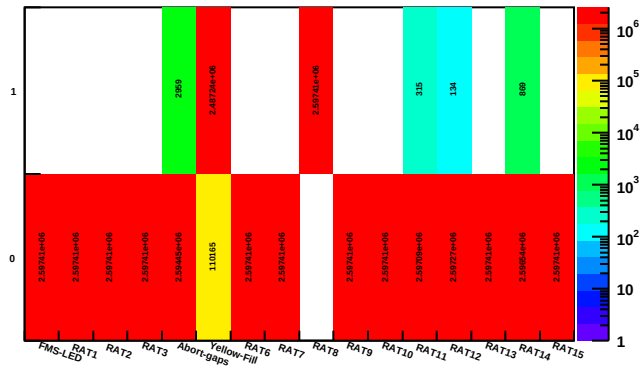
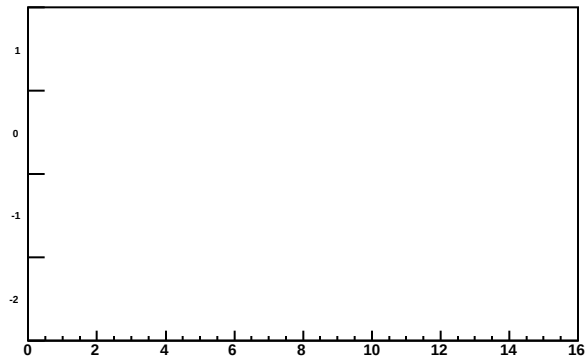
RAT board (ch4)





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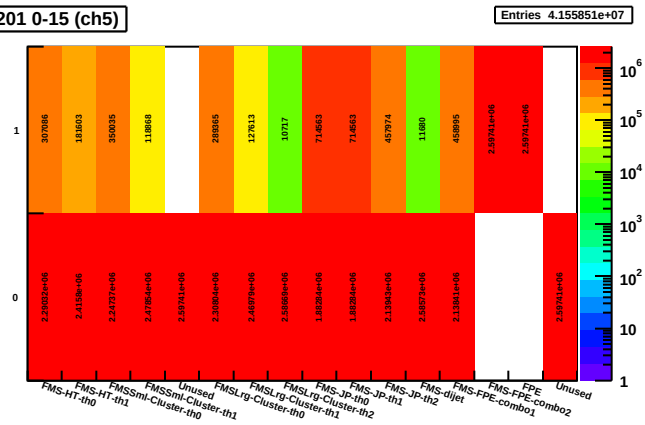

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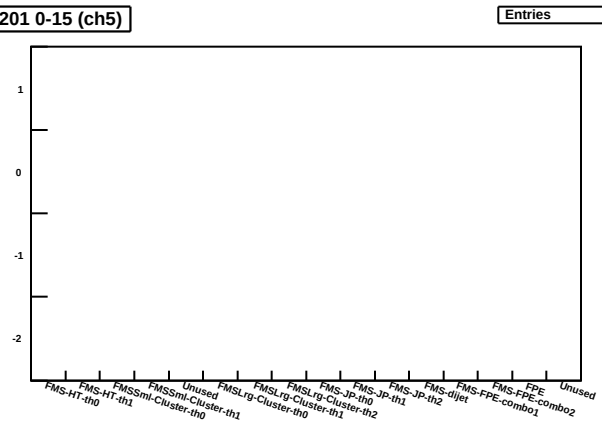
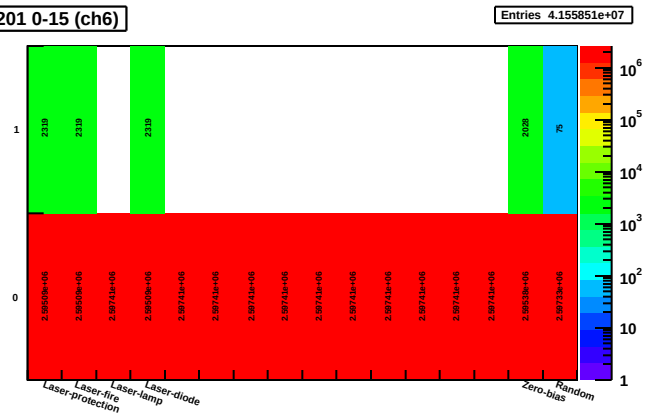
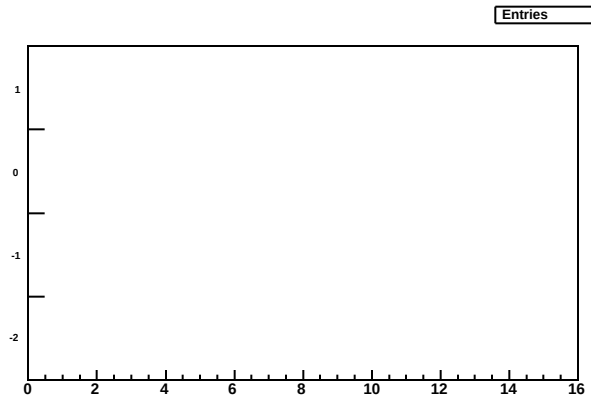
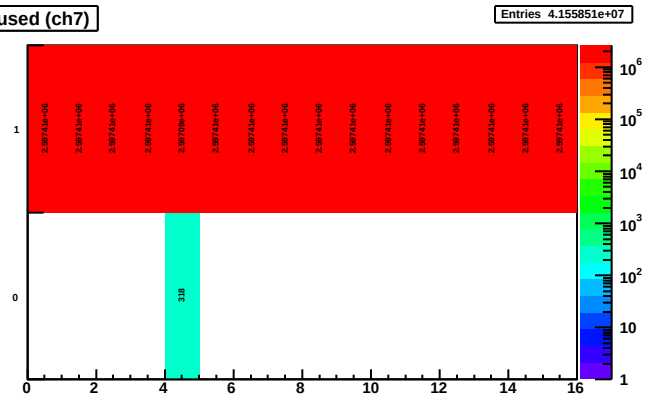
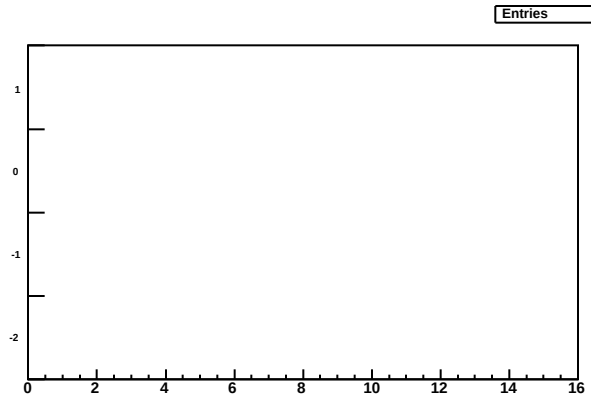


ST201 0-15 (ch6)





Unused (ch7)