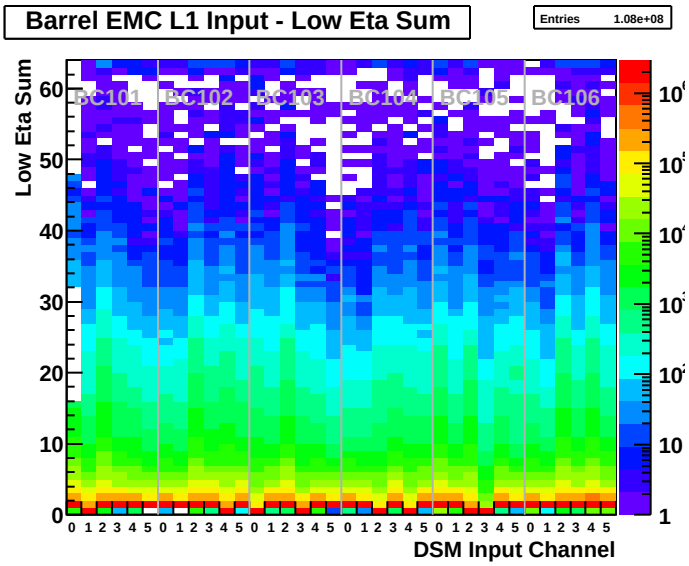
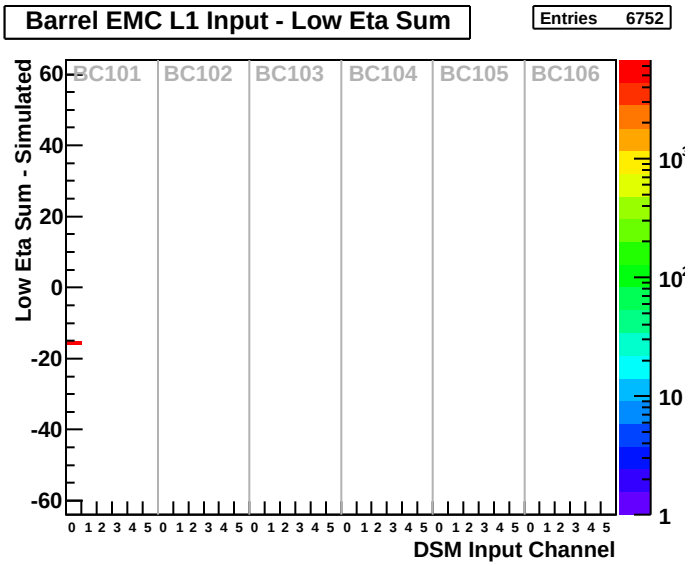
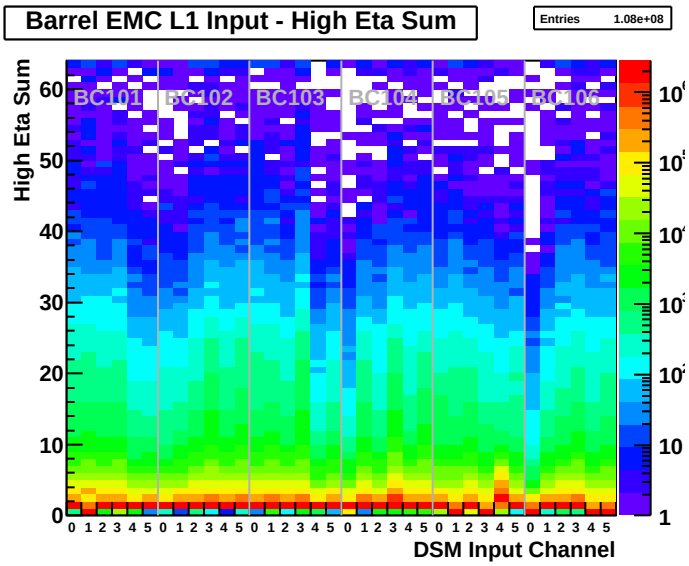
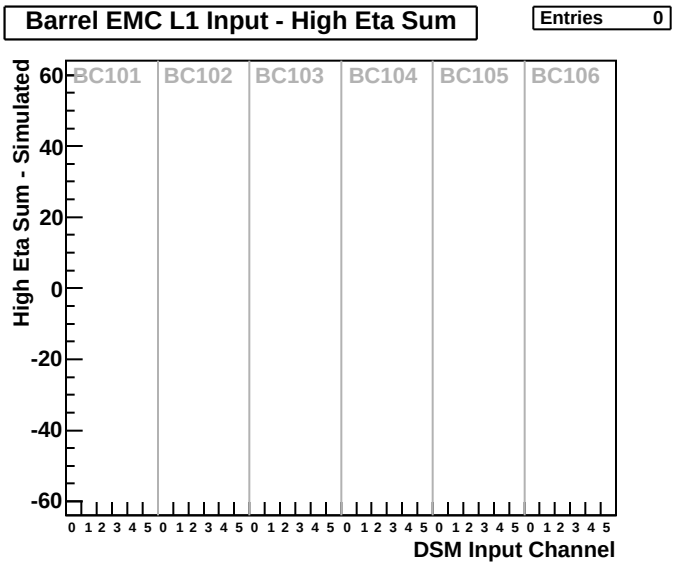
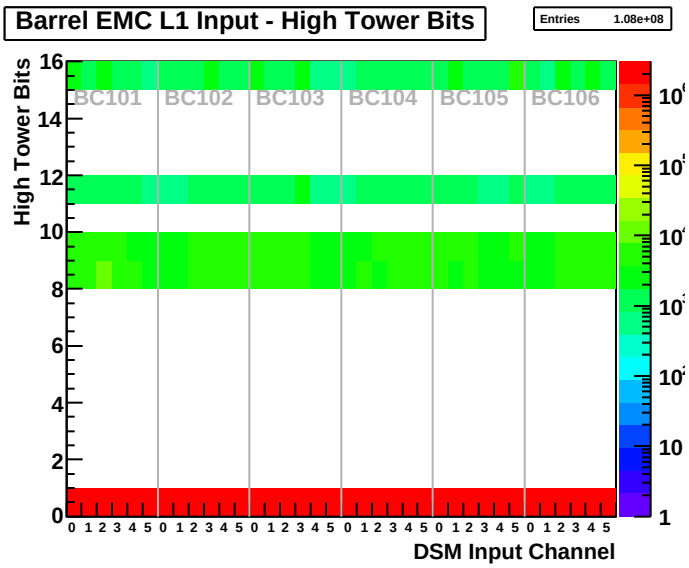
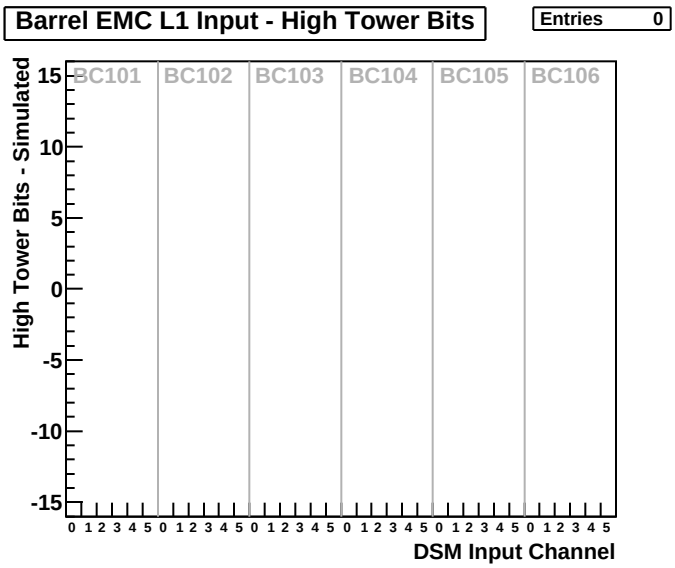

Barrel EMC L1 Input - Low Eta Sum
Entries 1.08e+08
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

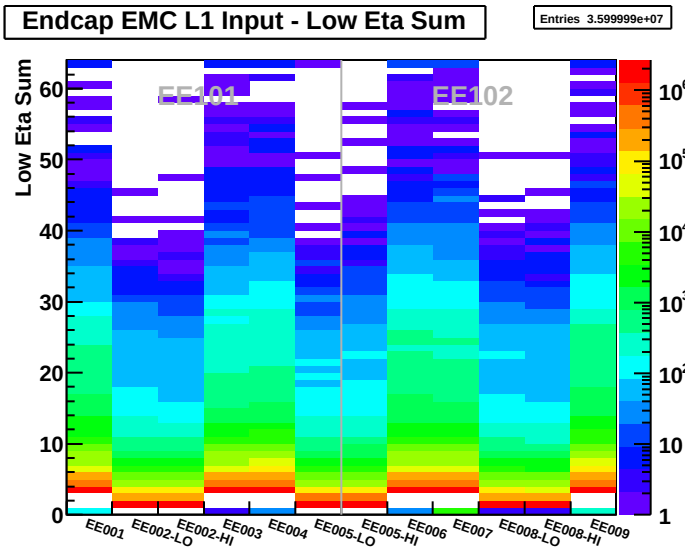

Barrel EMC L1 Input - Low Eta Sum
Entries 6752
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

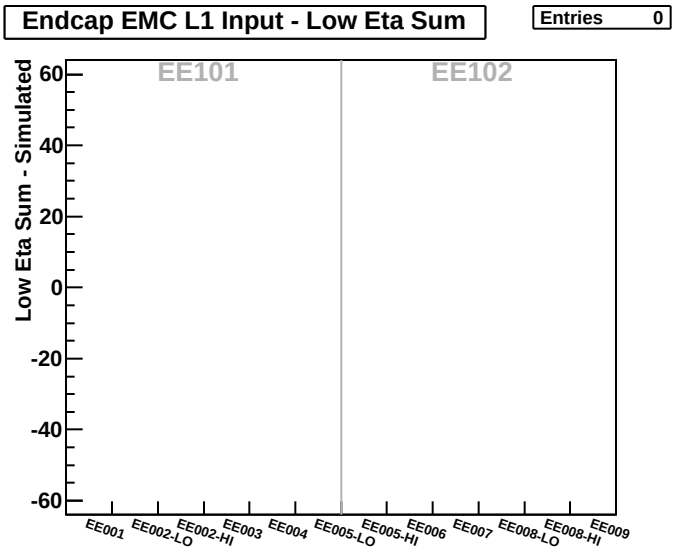

Barrel EMC L1 Input - High Eta Sum
Entries 1.08e+08
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

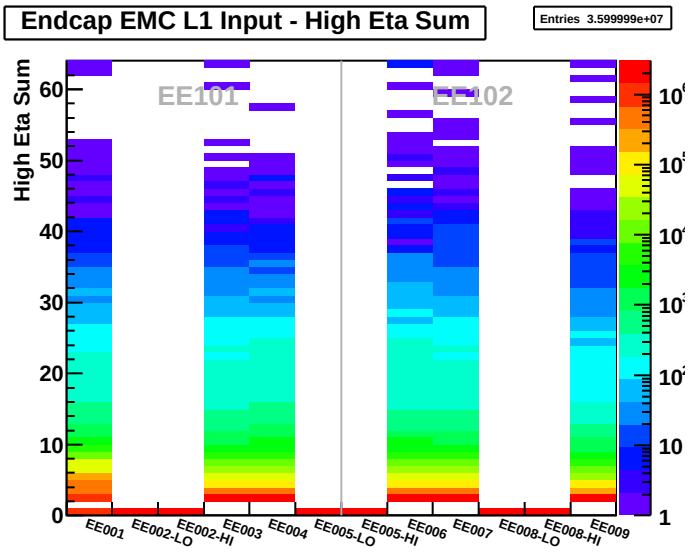

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

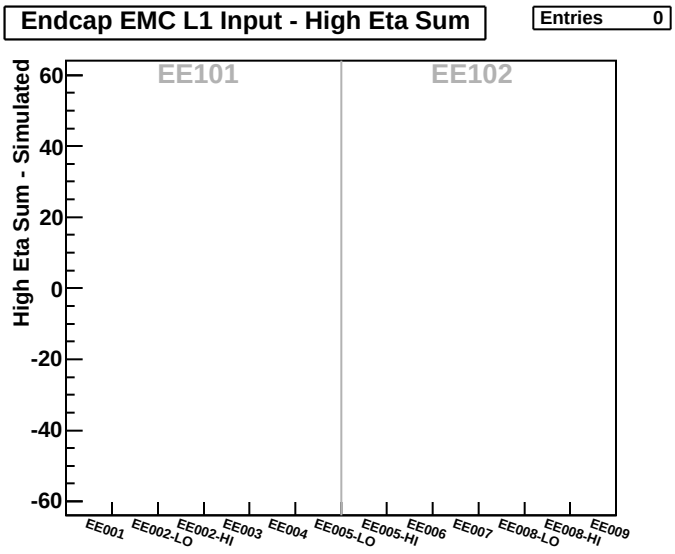

Barrel EMC L1 Input - High Tower Bits
Entries 1.08e+08
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

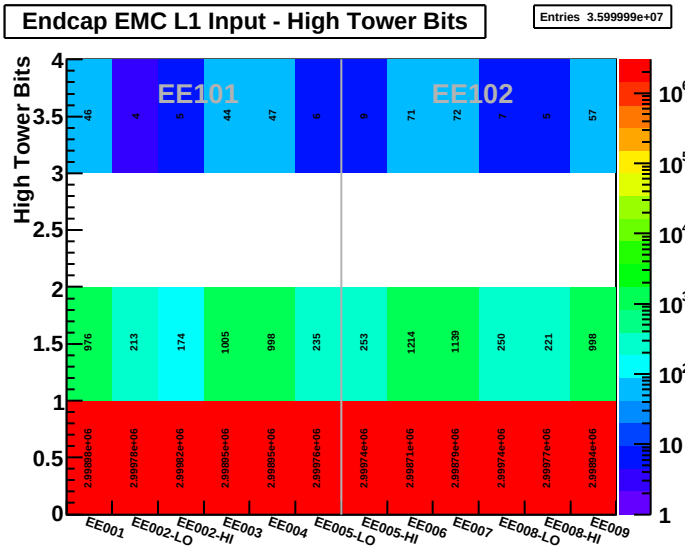

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

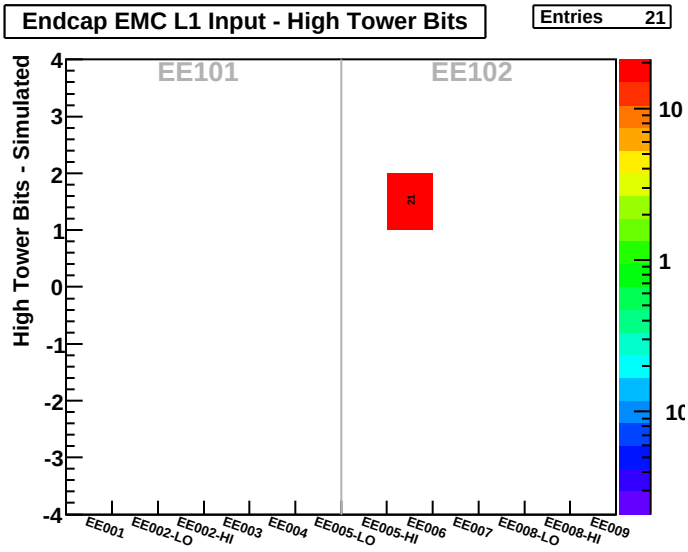

Endcap EMC L1 Input - Low Eta Sum
Entries 3.599999e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

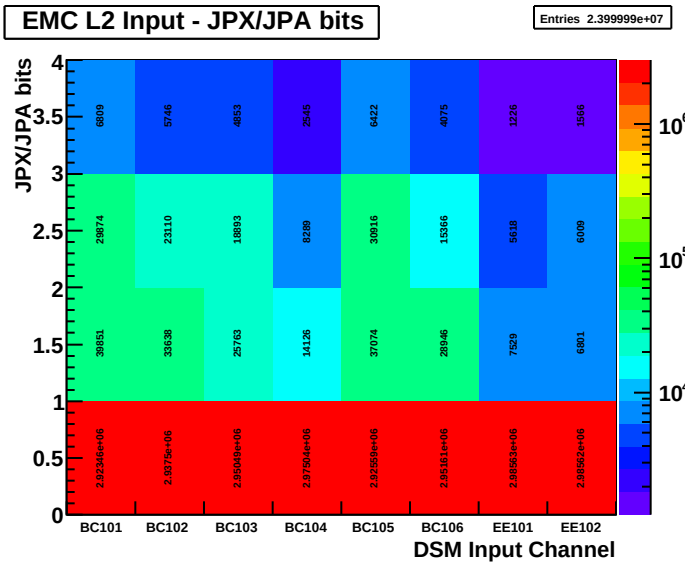

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

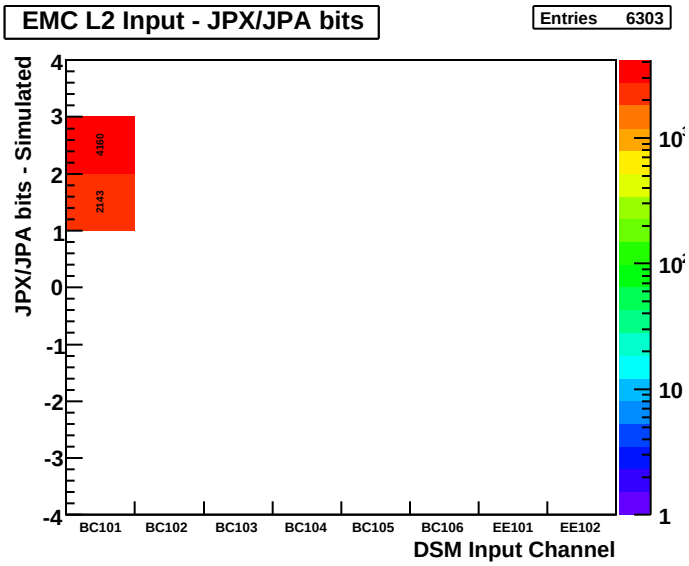

Endcap EMC L1 Input - High Eta Sum
Entries 3.599999e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

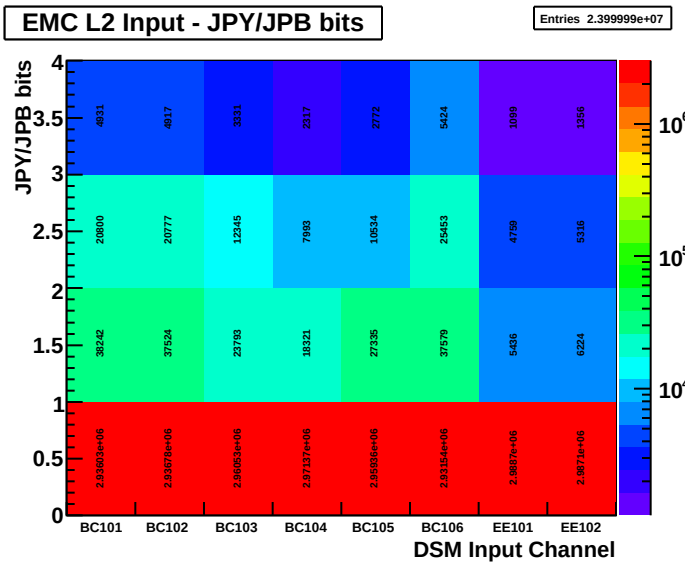

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

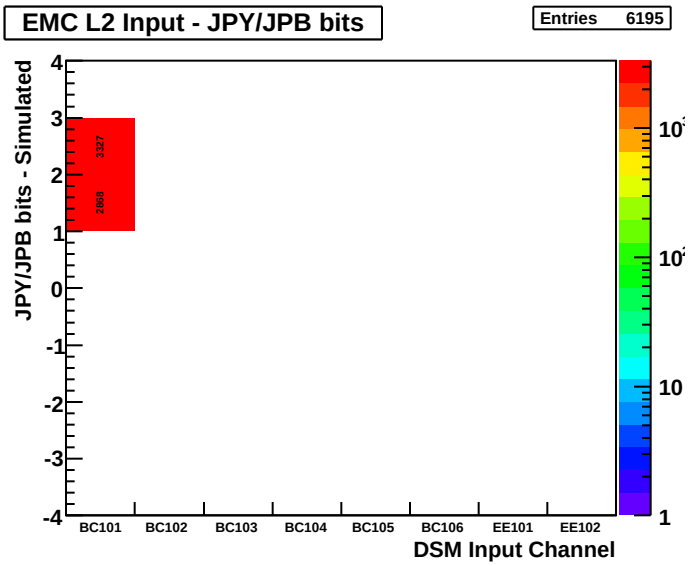

Endcap EMC L1 Input - High Tower Bits
Entries 3.599999e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

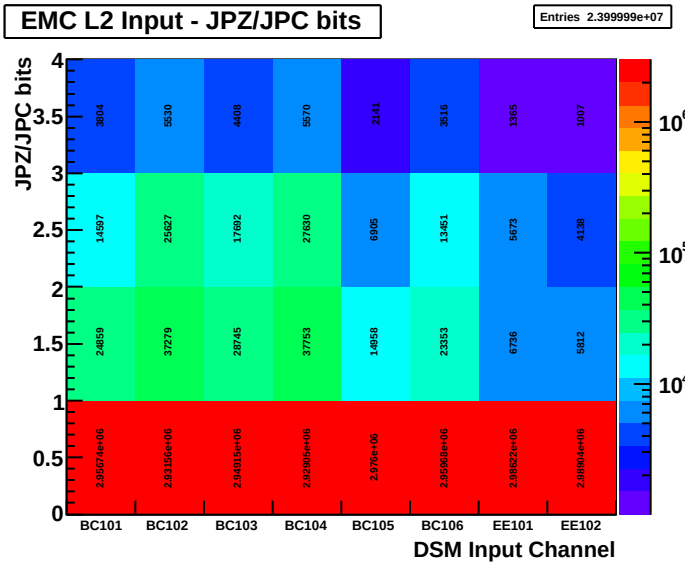

Endcap EMC L1 Input - High Tower Bits
Entries 21
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

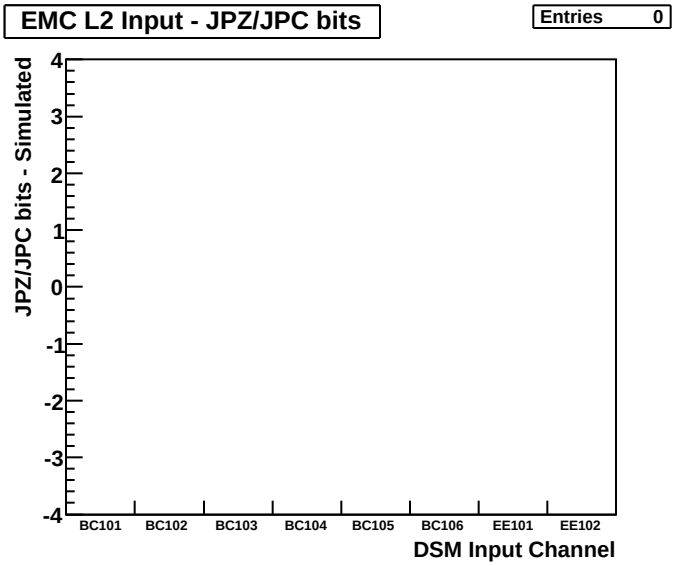

EMC L2 Input - JPX/JPA bits
Entries 2.399999e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6809 5746 4853 2545 6422 4075 1226 1566
29874 23110 18893 8289 30916 15366 5618 6009
39851 33638 25763 14126 37074 29946 7529 6801
2.92346e+06 2.9375e+06 2.95049e+06 2.97504e+06 2.92559e+06 2.95161e+06 2.98553e+06 2.98562e+06

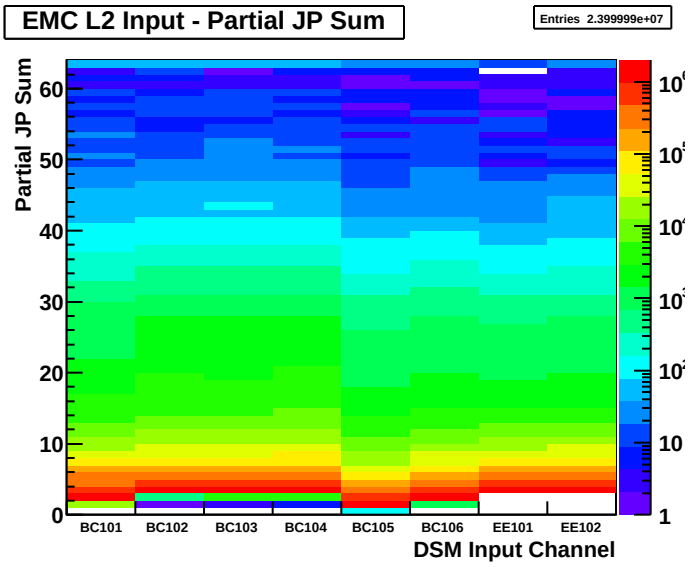

EMC L2 Input - JPX/JPA bits
Entries 6303
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4160
2143

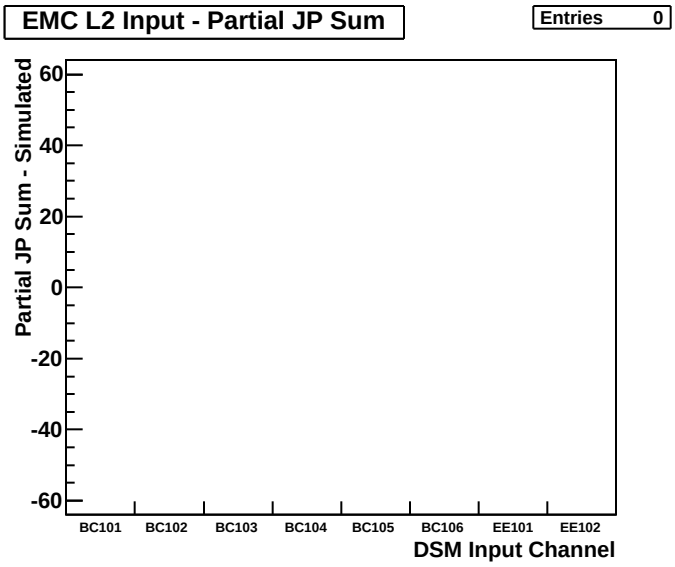

EMC L2 Input - JPY/JPB bits
Entries 2.399999e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4931 4917 3331 2317 2772 5424 1099 1356
20800 20777 12345 7993 10534 25453 4759 5316
38242 37524 23793 18321 27335 37579 5436 6224
2.93603e+06 2.93678e+06 2.96053e+06 2.97137e+06 2.95936e+06 2.93154e+06 2.9887e+06 2.9871e+06

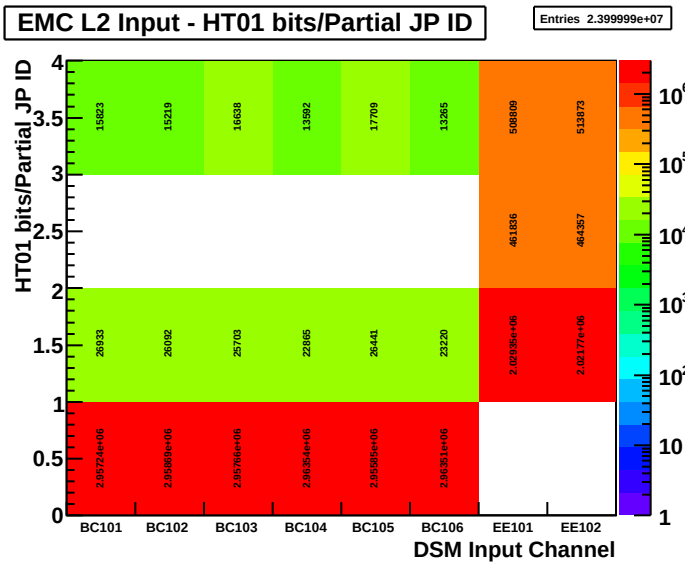

EMC L2 Input - JPY/JPB bits
Entries 6195
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3327
2868

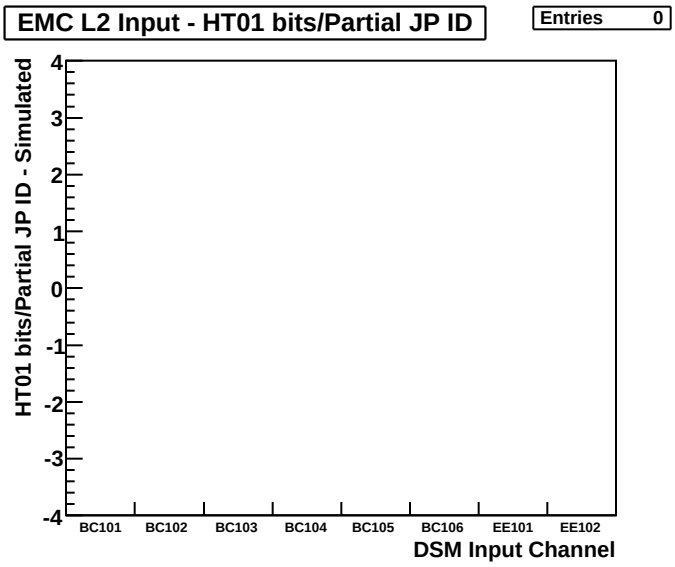

EMC L2 Input - JPZ/JPC bits
Entries 2.399999e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3804 5530 4408 5570 2141 3516 1365 1007
14997 25627 17692 27630 6905 13451 5673 4138
24859 37279 28745 37753 14958 23353 6736 5812
2.95674e+06 2.93156e+06 2.94015e+06 2.92905e+06 2.976e+06 2.95968e+06 2.98522e+06 2.98904e+06

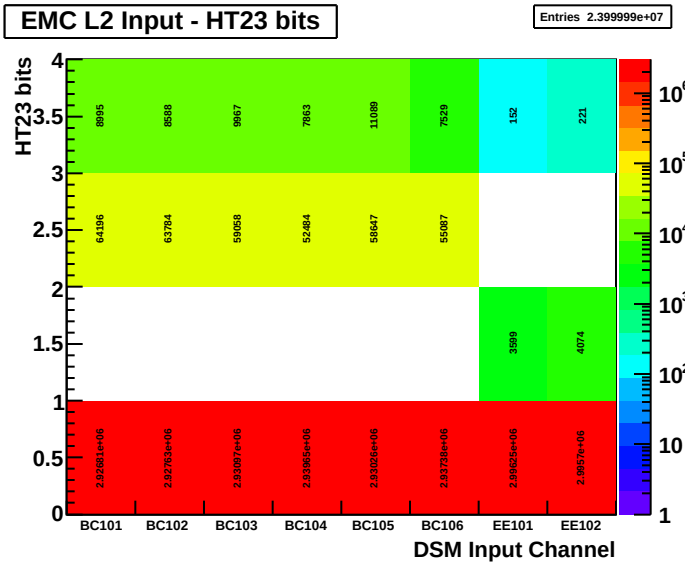

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

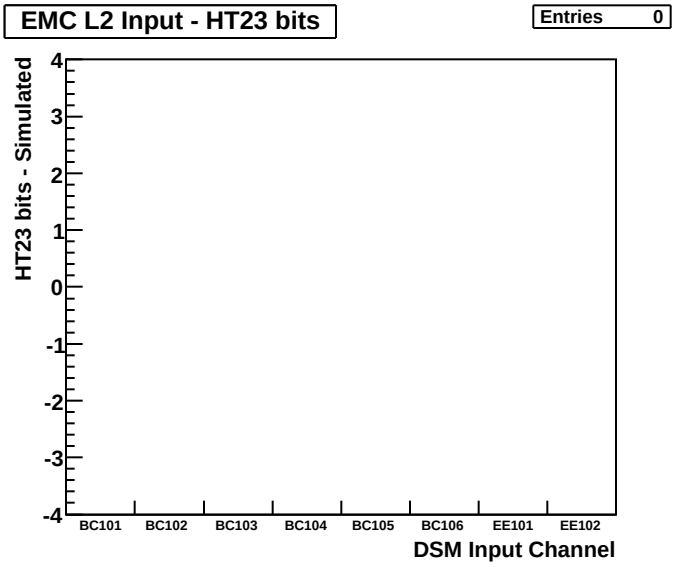

EMC L2 Input - Partial JP Sum
Entries 2.399999e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

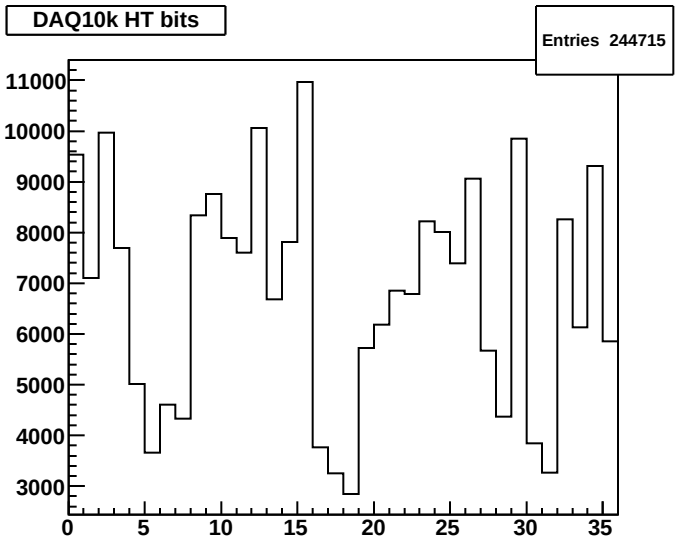

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

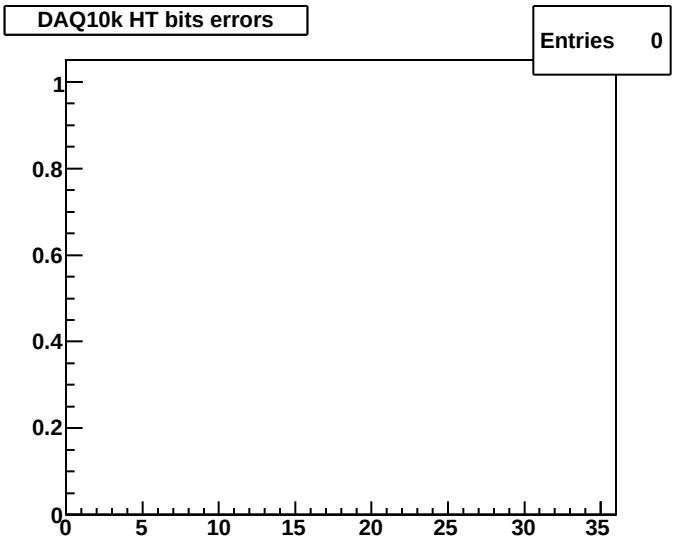

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2.399999e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

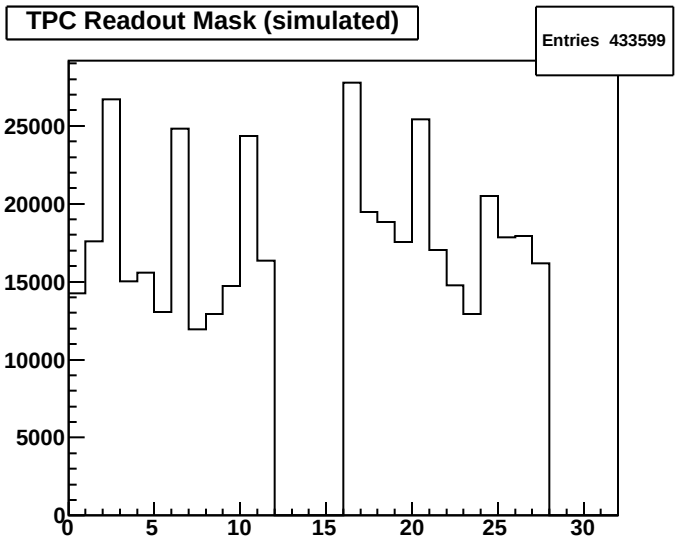

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

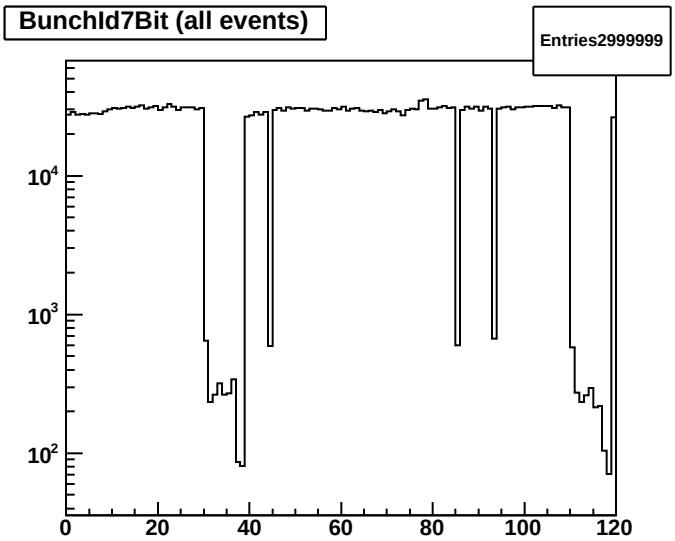

EMC L2 Input - HT23 bits
Entries 2.399999e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

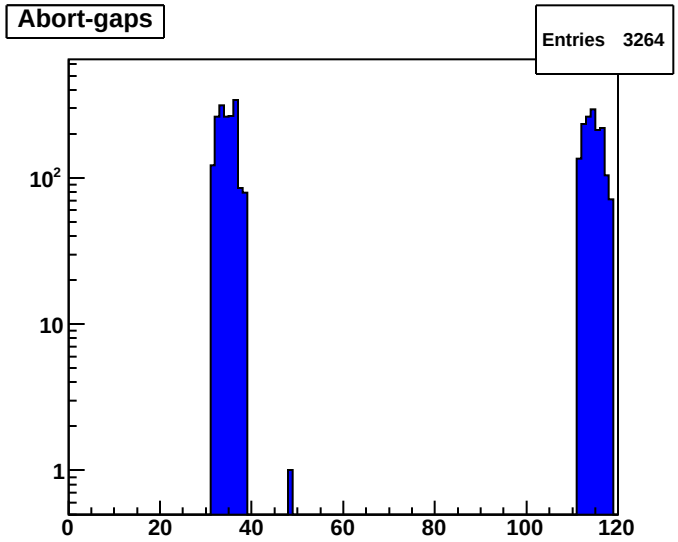

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

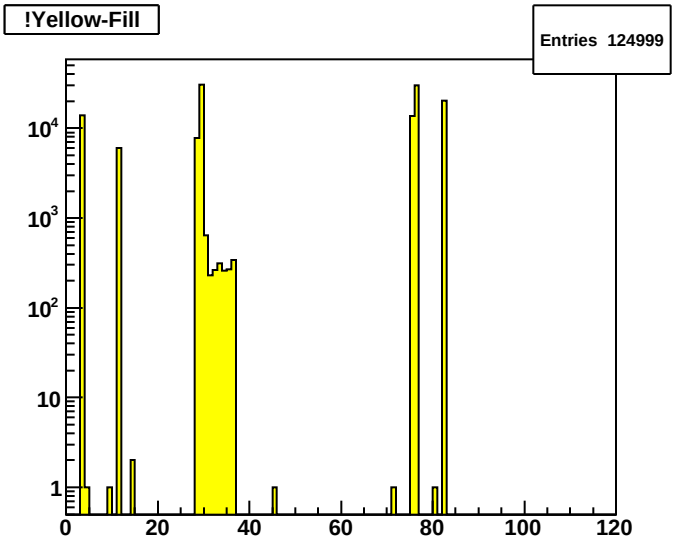

DAQ10k HT bits
Entries 244715

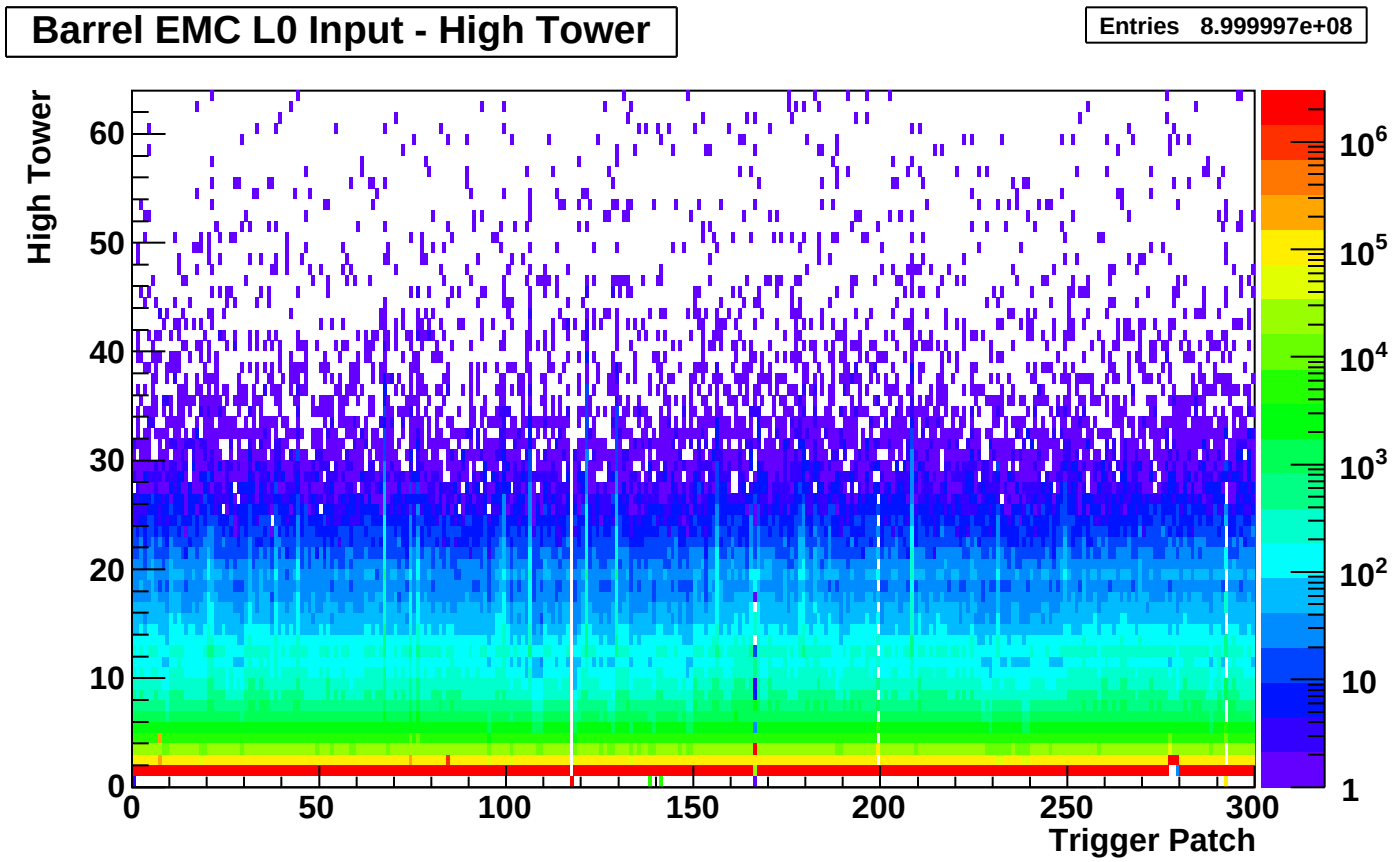

DAQ10k HT bits errors
Entries 0

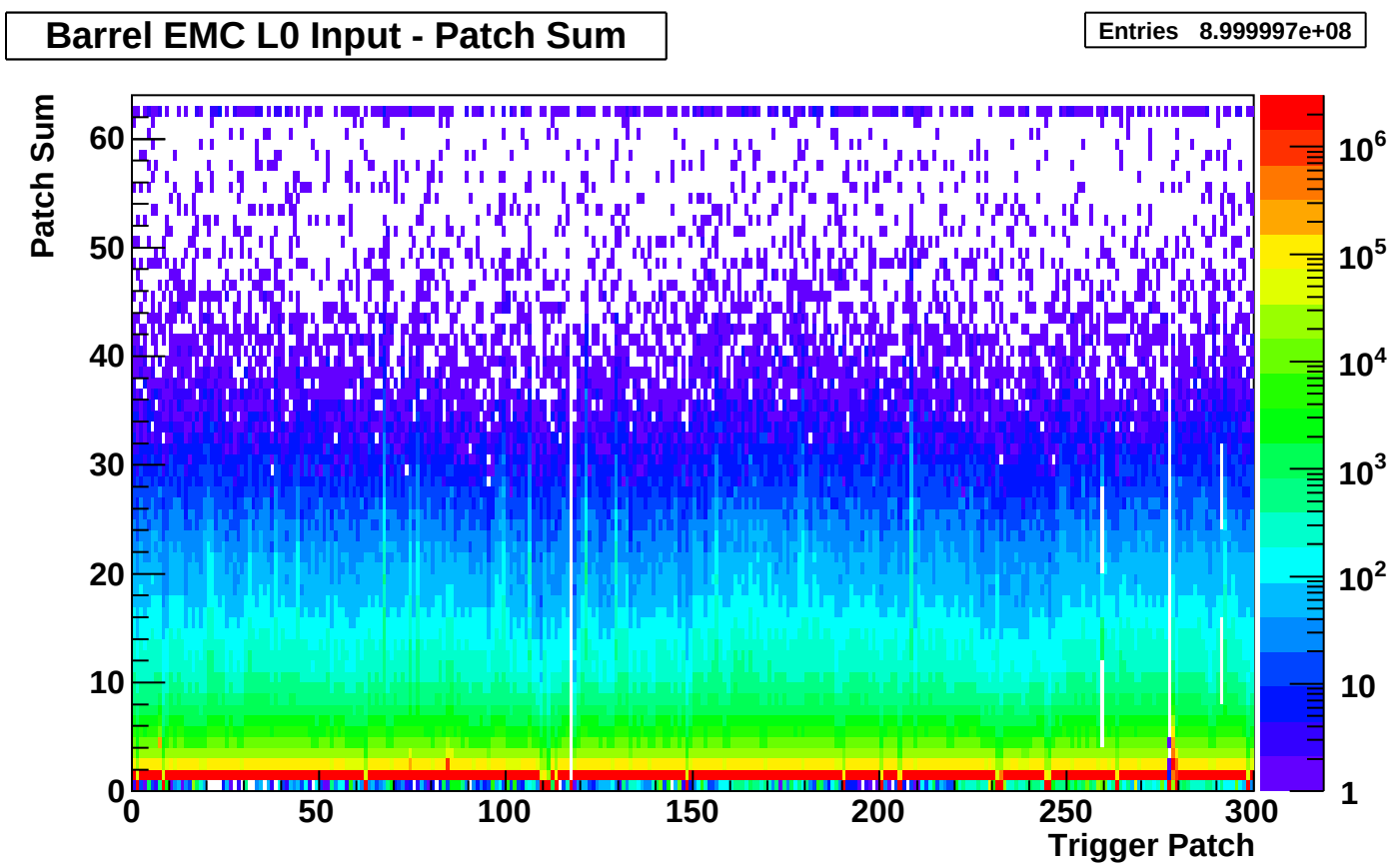

TPC Readout Mask (simulated)
Entries 433599

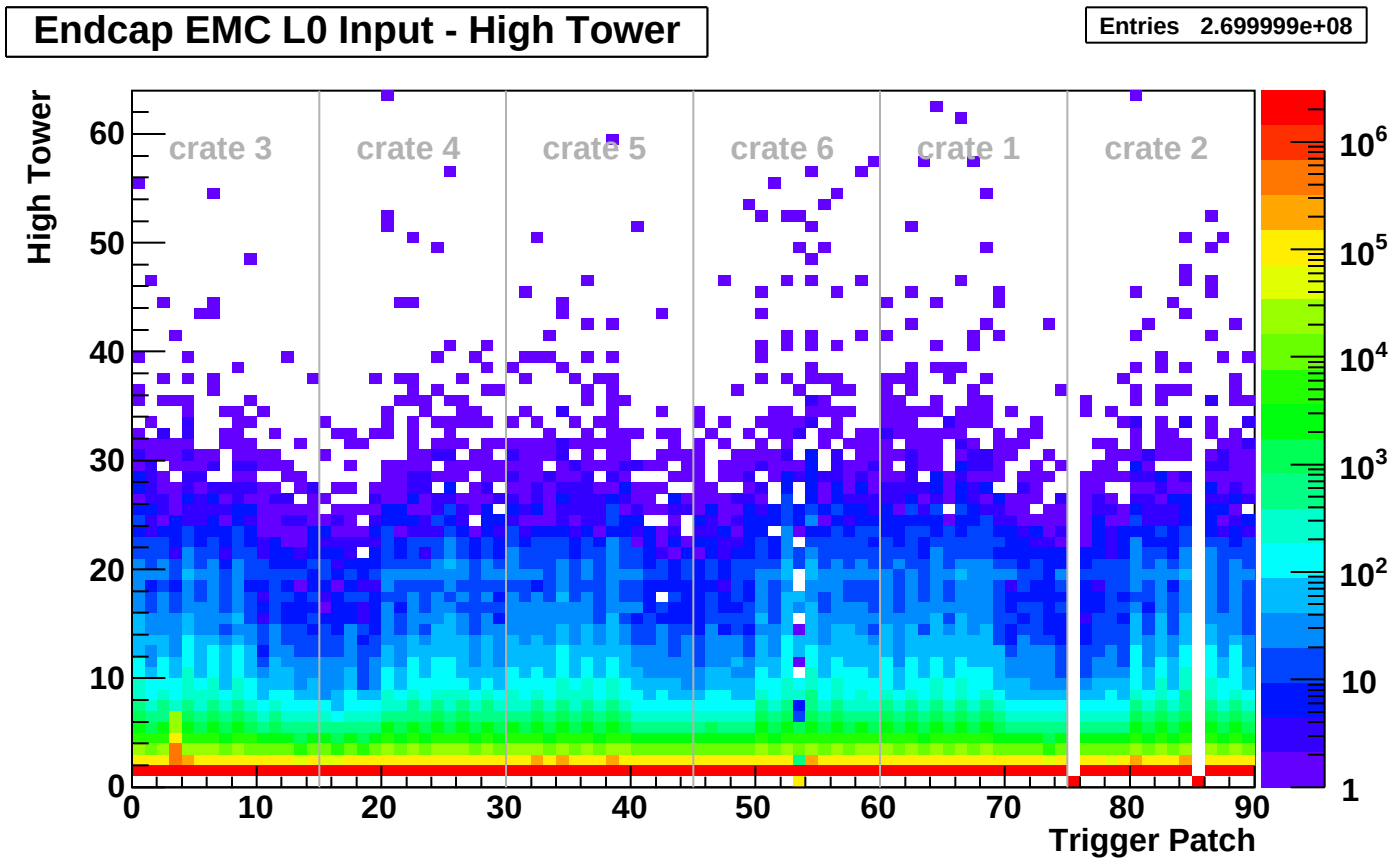

BunchId7Bit (all events)
Entries2999999

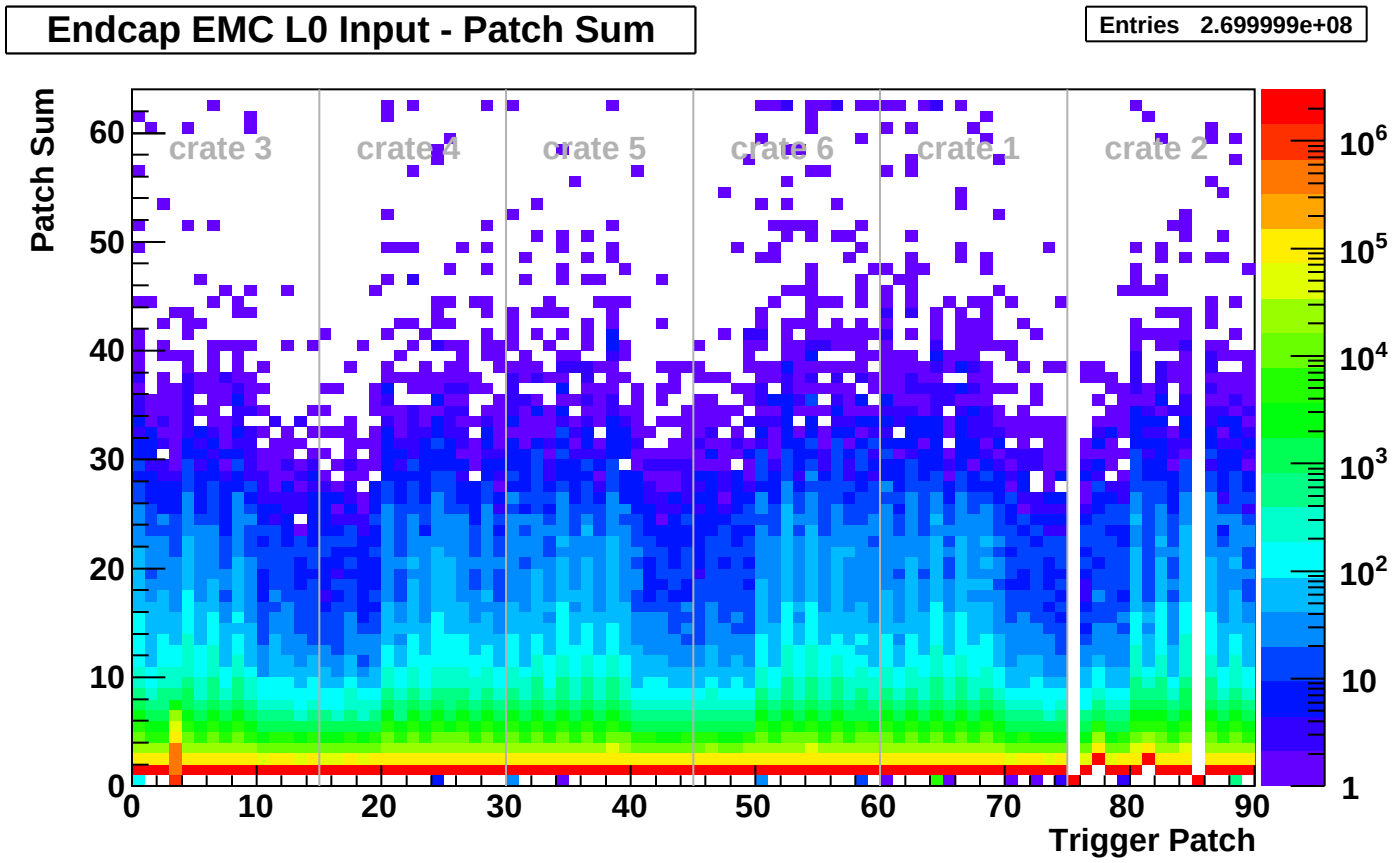

Abort-gaps
Entries 3264

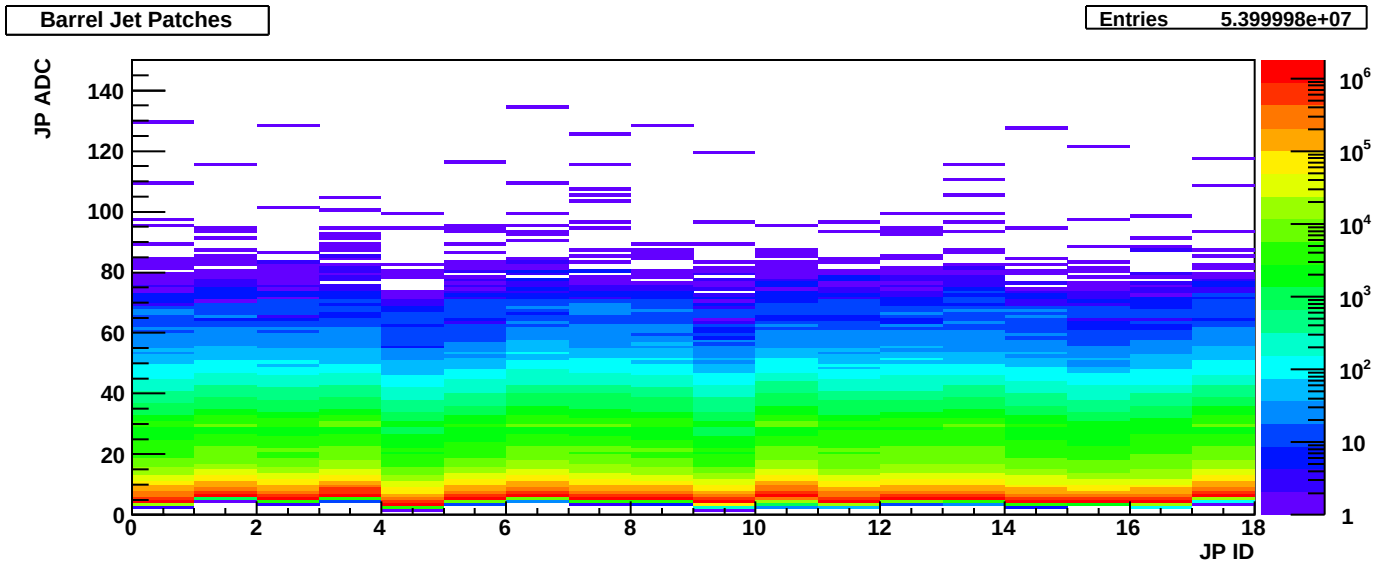

!Yellow-Fill
Entries 124999

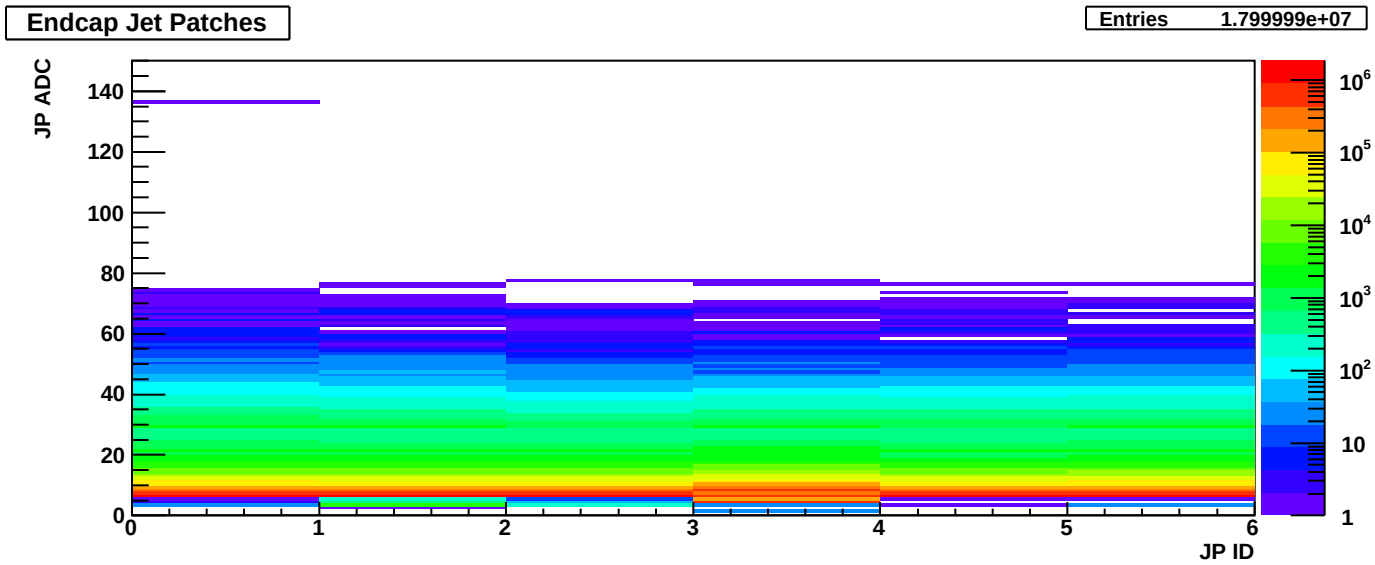

Barrel EMC L0 Input - High Tower
Entries 8.999997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

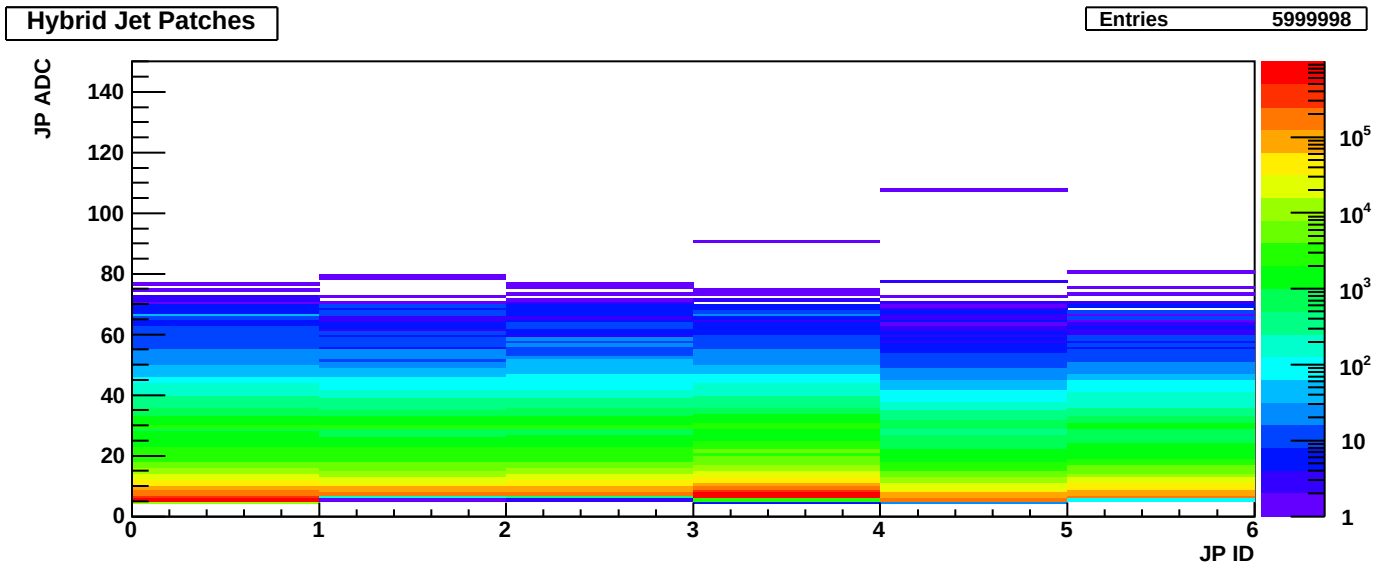

Barrel EMC L0 Input - Patch Sum
Entries 8.999997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

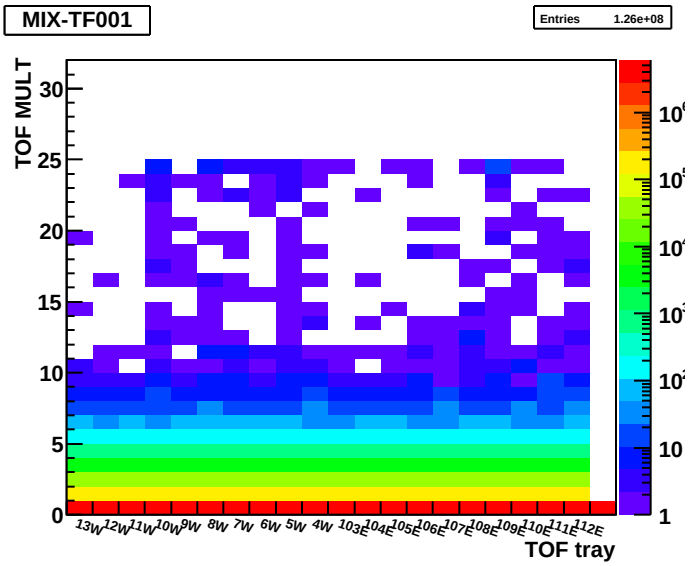

Endcap EMC L0 Input - High Tower
Entries 2.699999e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

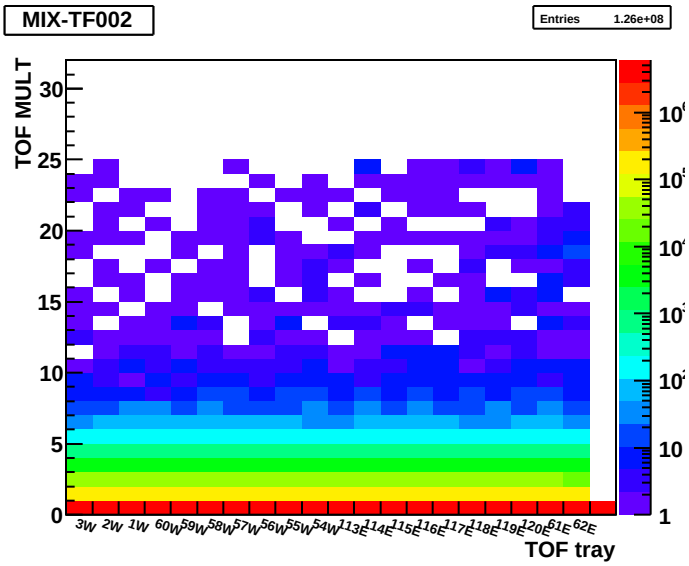

Endcap EMC L0 Input - Patch Sum
Entries 2.699999e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

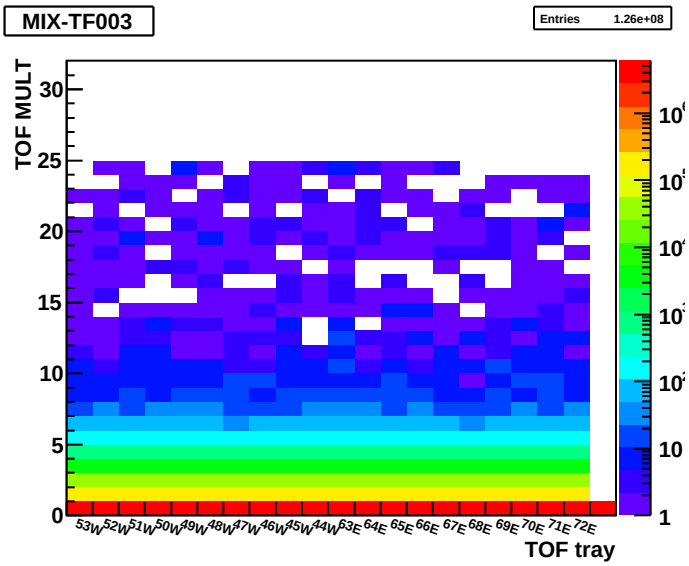

Barrel Jet Patches
Entries 5.399998e+07
JP ADC
JP ID

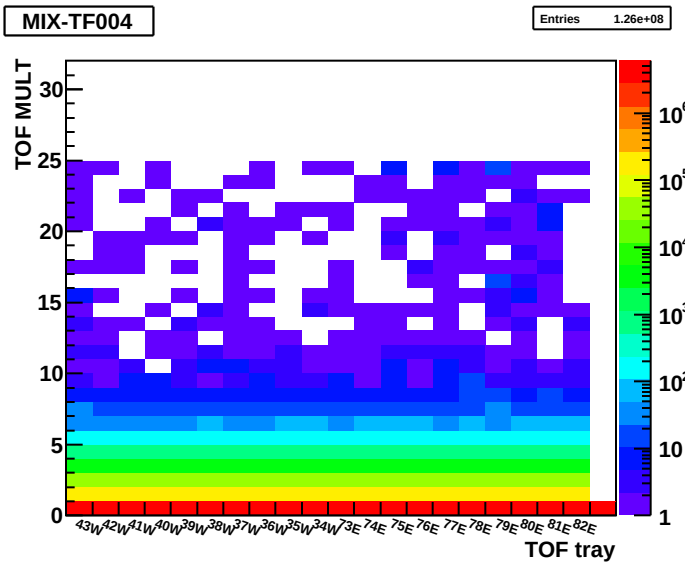

Endcap Jet Patches
Entries 1.799999e+07
JP ADC
JP ID

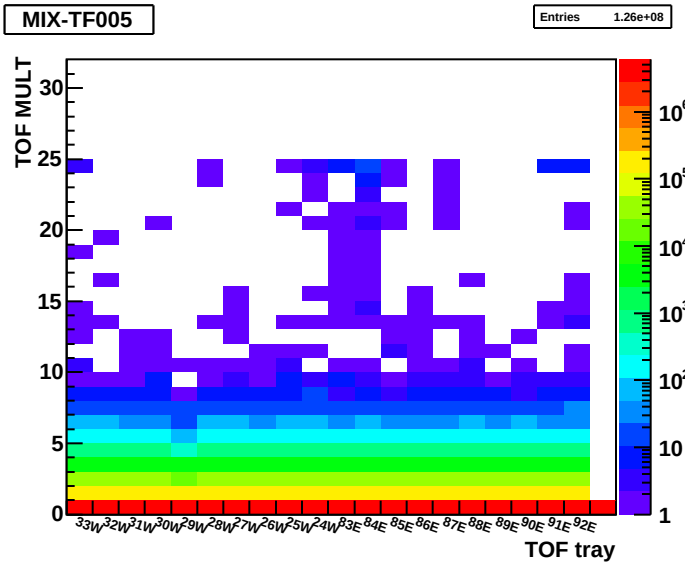

Hybrid Jet Patches
Entries 5999998
JP ADC
JP ID

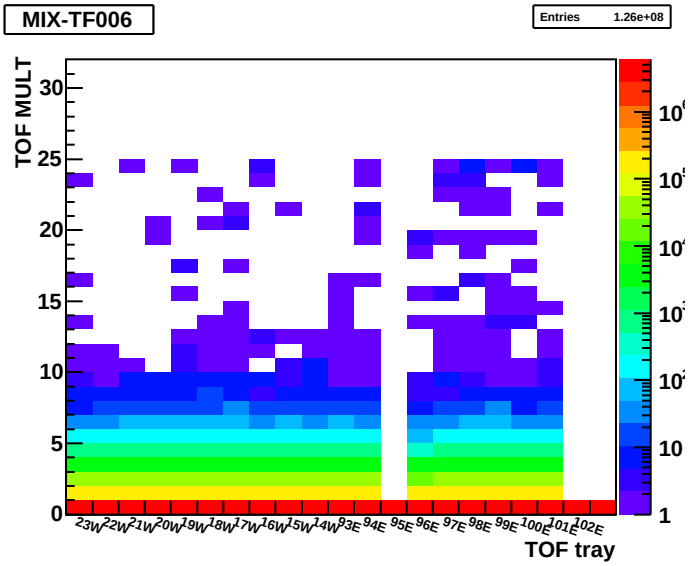

MIX-TF001
Entries 1.26e+08
TOF MULT
TOF tray

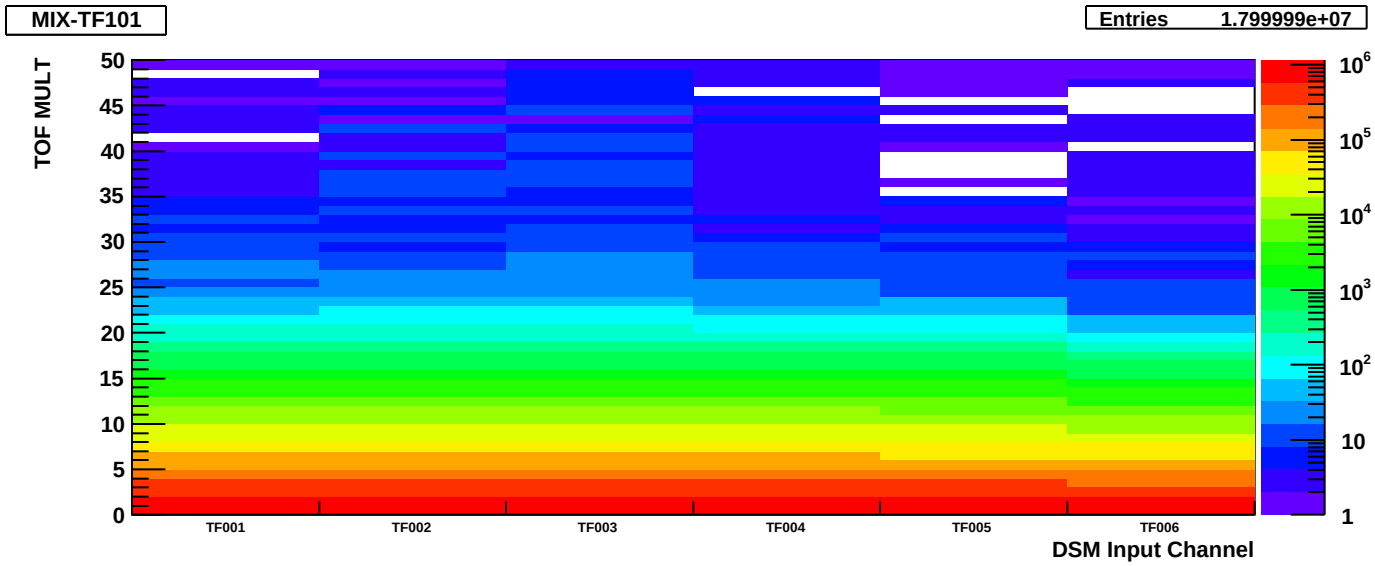

MIX-TF002
Entries 1.26e+08
TOF MULT
TOF tray

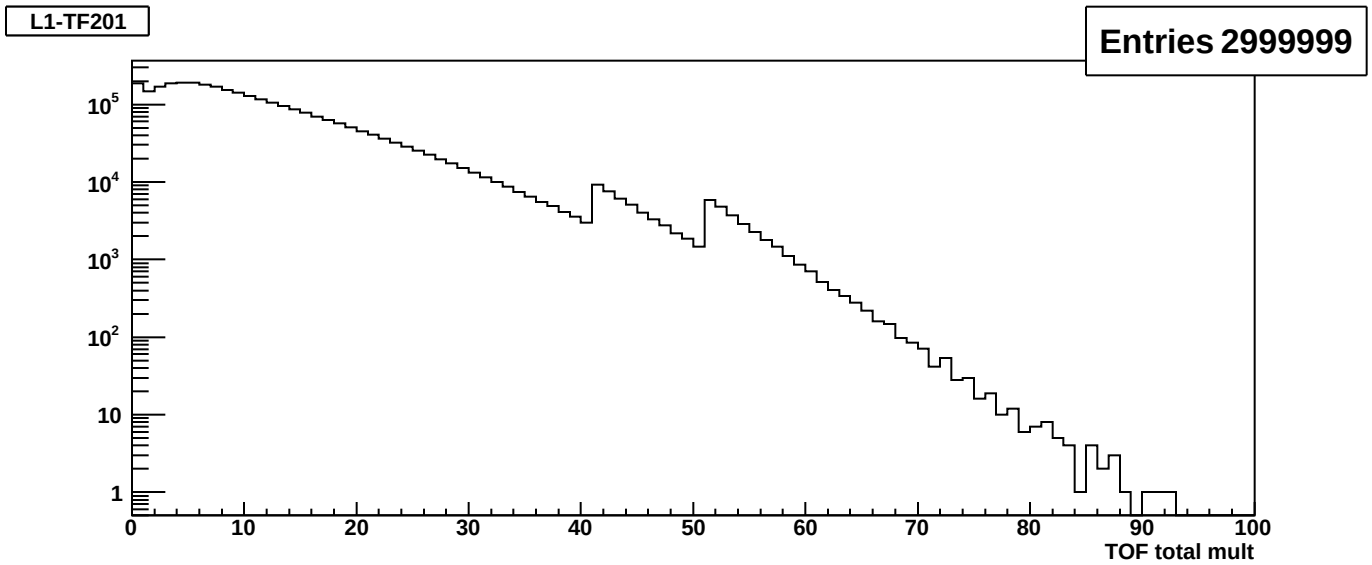

MIX-TF003
Entries 1.26e+08
TOF MULT
TOF tray

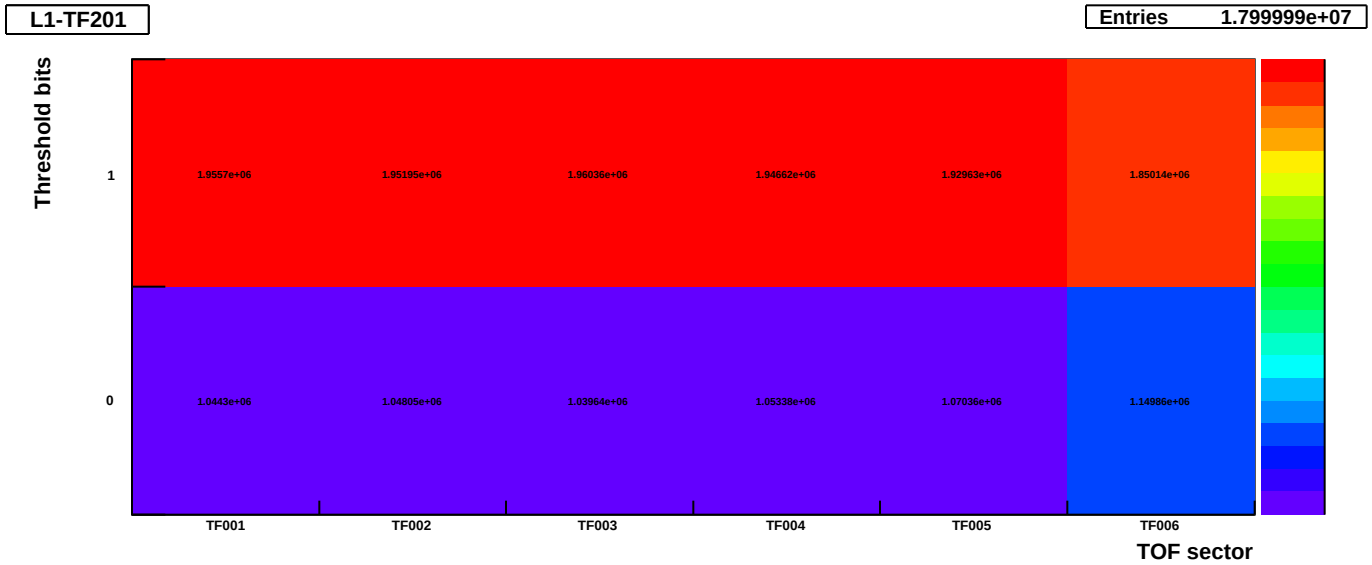

MIX-TF004
Entries 1.26e+08
TOF MULT
TOF tray

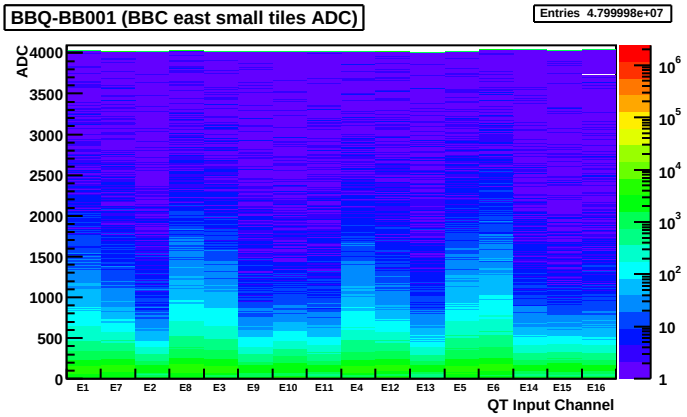

MIX-TF005
Entries 1.26e+08
TOF MULT
TOF tray

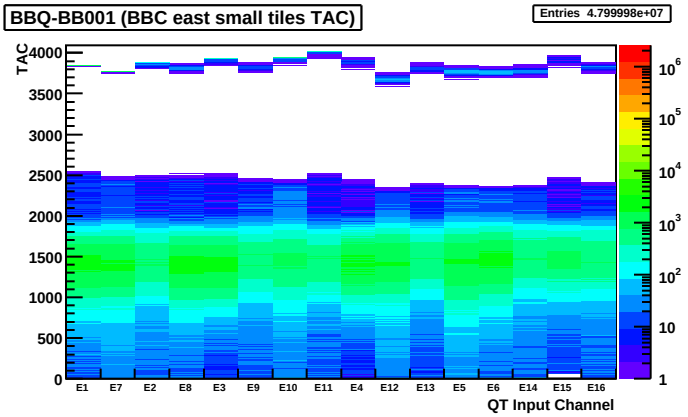

MIX-TF006
Entries 1.26e+08
TOF MULT
TOF tray

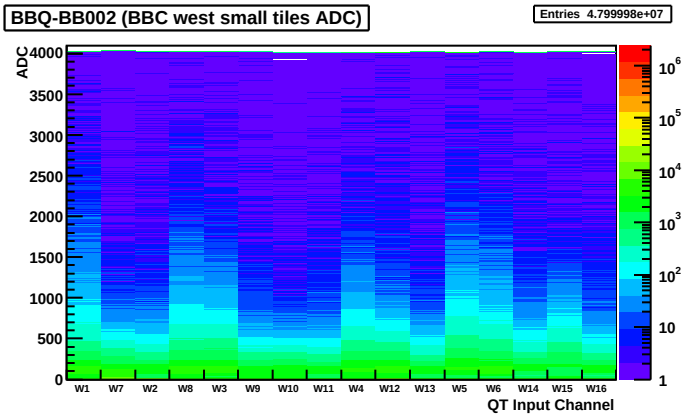

MIX-TF101
Entries 1.799999e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

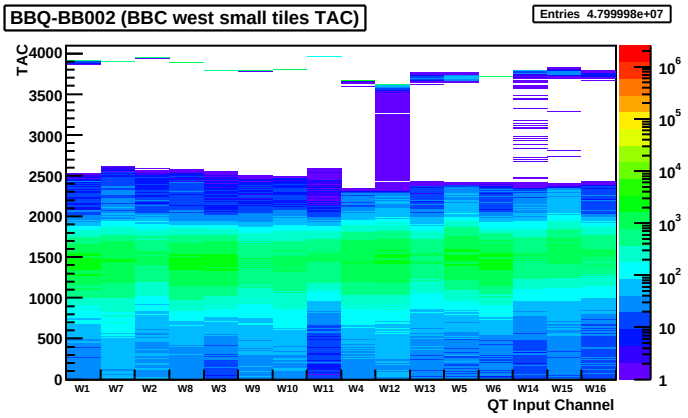

L1-TF201
Entries 2999999
TOF total mult

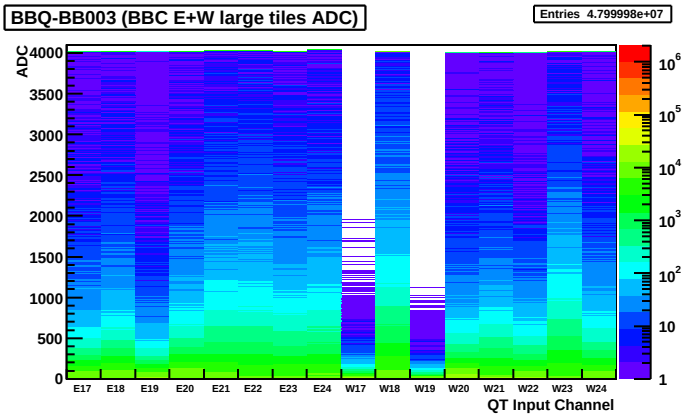

L1-TF201
Entries 1.799999e+07
Threshold bits
1.9557e+06
1.95195e+06
1.96036e+06
1.94662e+06
1.92963e+06
1.85014e+06
1.0443e+06
1.04805e+06
1.03964e+06
1.05338e+06
1.07036e+06
1.14986e+06
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

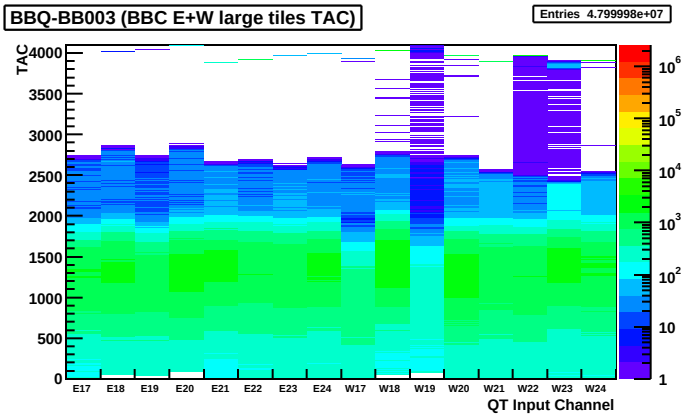

BBQ-BB001 (BBC east small tiles ADC)
Entries 4.799998e+07
ADC
QT Input Channel

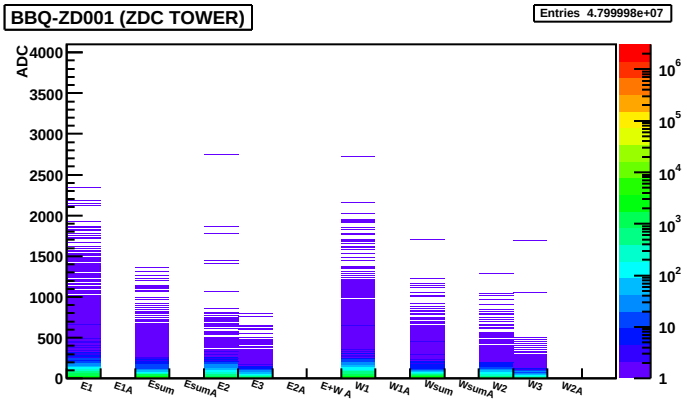

BBQ-BB001 (BBC east small tiles TAC)
Entries 4.799998e+07
TAC
QT Input Channel

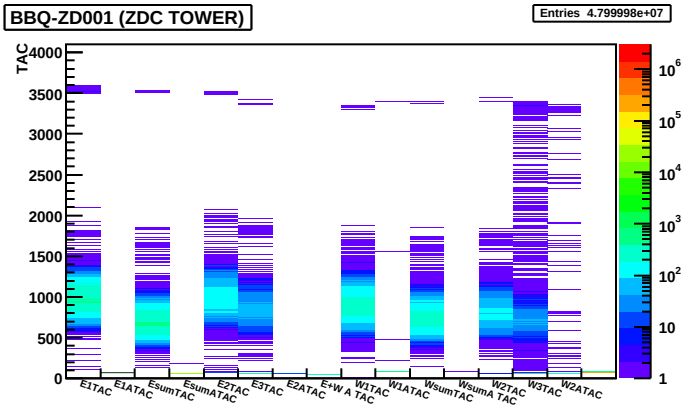

BBQ-BB002 (BBC west small tiles ADC)
Entries 4.799998e+07
ADC
QT Input Channel

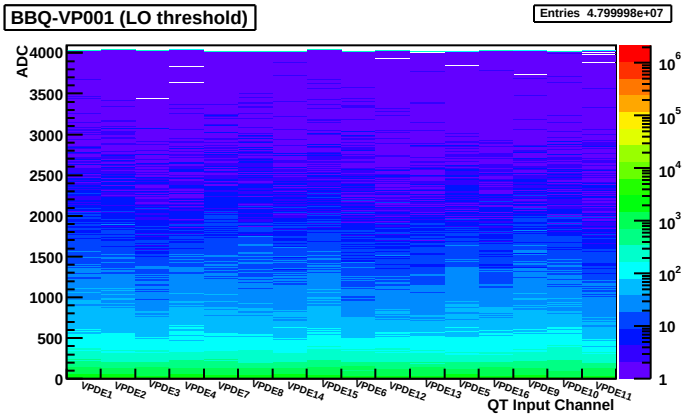

BBQ-BB002 (BBC west small tiles TAC)
Entries 4.799998e+07
TAC
QT Input Channel

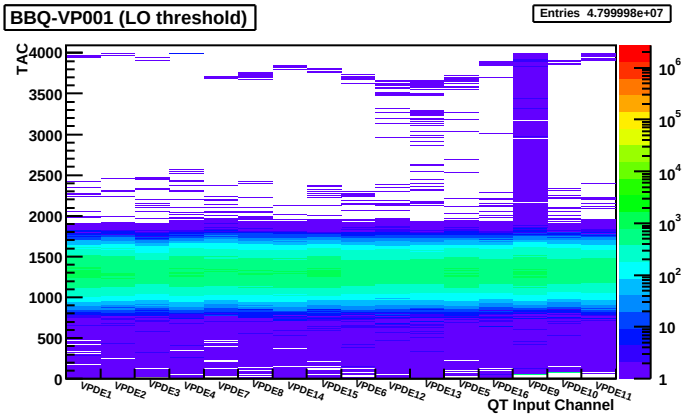

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4.799998e+07
ADC
QT Input Channel

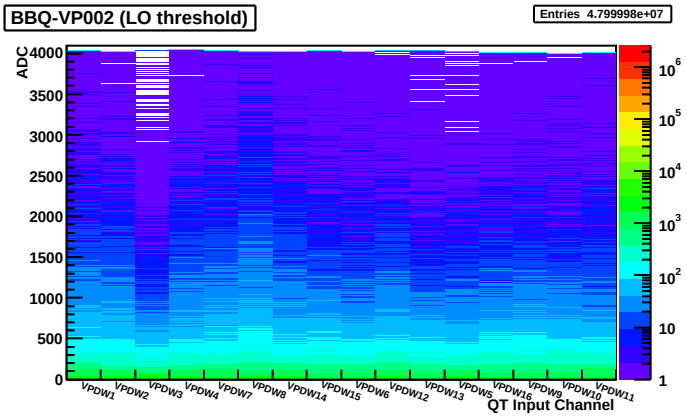

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4.799998e+07
TAC
QT Input Channel

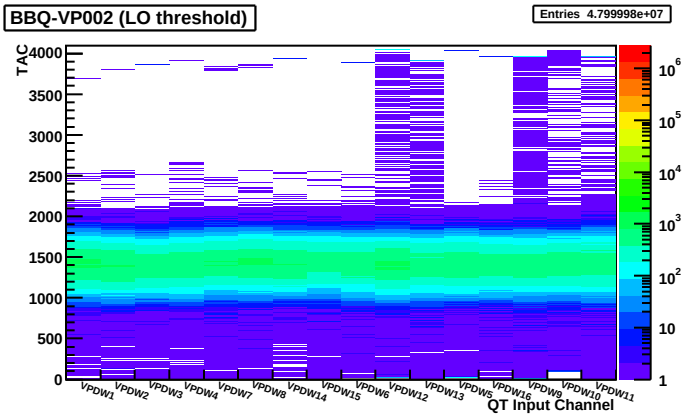

BBQ-ZD001 (ZDC TOWER)
Entries 4.799998e+07
ADC

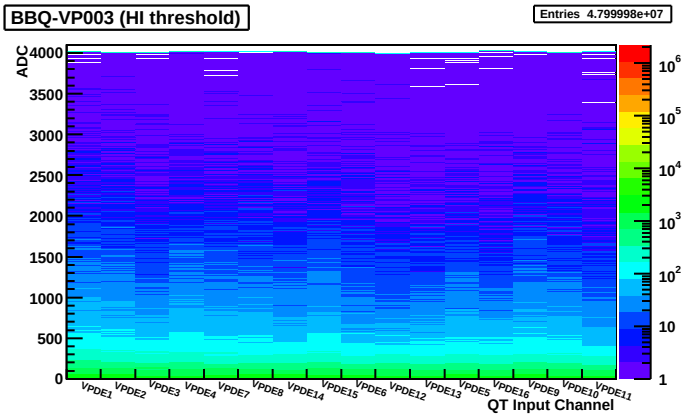

BBQ-ZD001 (ZDC TOWER)
Entries 4.799998e+07
TAC

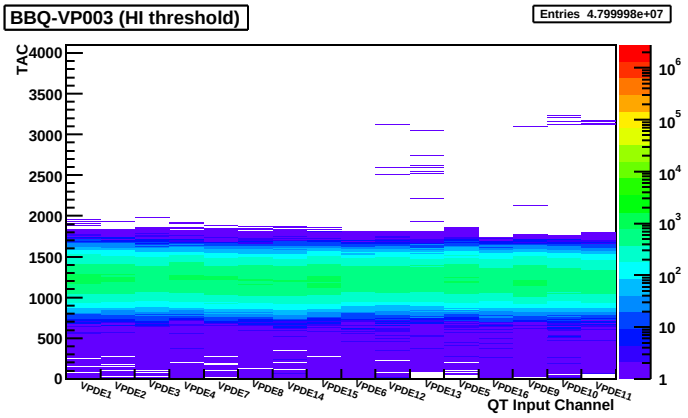

BBQ-VP001 (LO threshold)
Entries 4.799998e+07
ADC
QT Input Channel

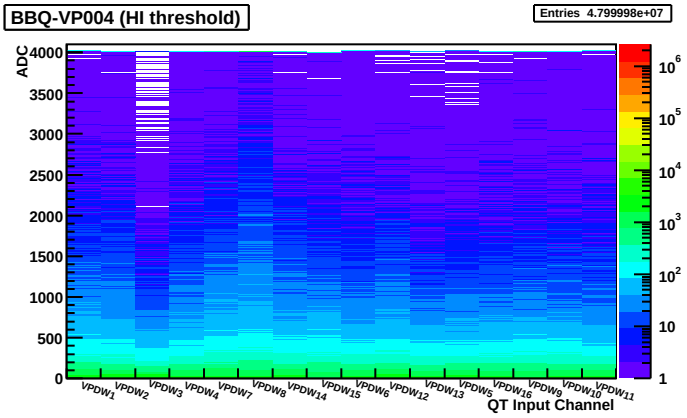

BBQ-VP001 (LO threshold)
Entries 4.799998e+07
TAC
QT Input Channel

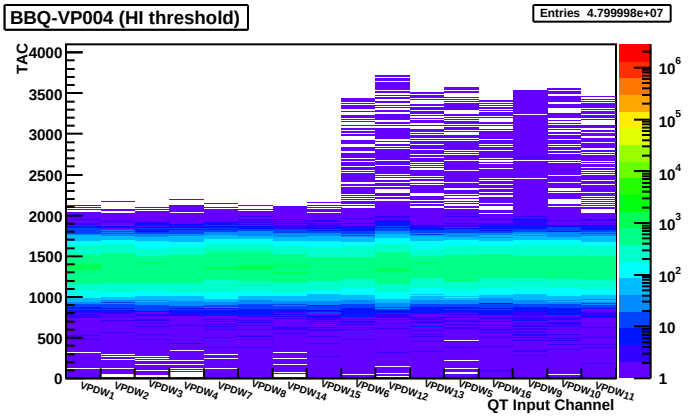

BBQ-VP002 (LO threshold)
Entries 4.799998e+07
ADC
QT Input Channel

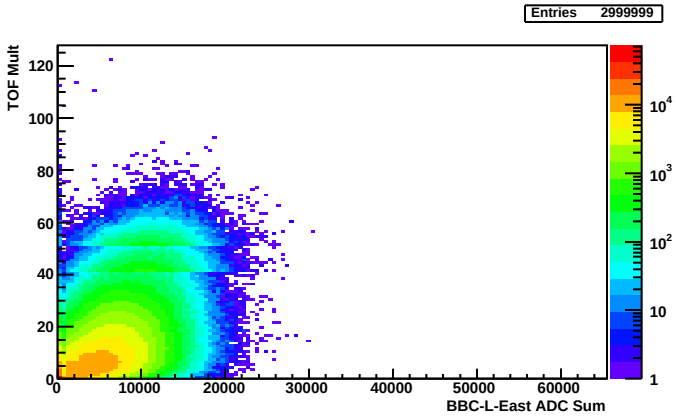

BBQ-VP002 (LO threshold)
Entries 4.799998e+07
TAC
QT Input Channel

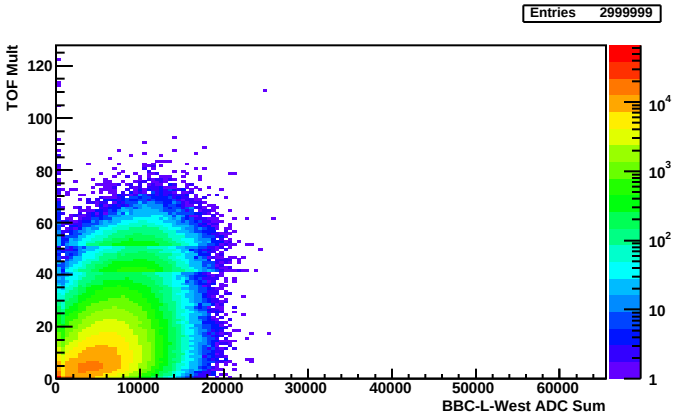

BBQ-VP003 (HI threshold)
Entries 4.799998e+07
ADC
QT Input Channel

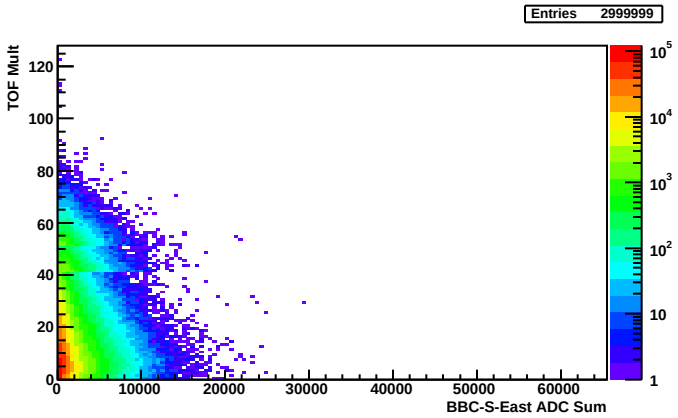

BBQ-VP003 (HI threshold)
Entries 4.799998e+07
TAC
QT Input Channel

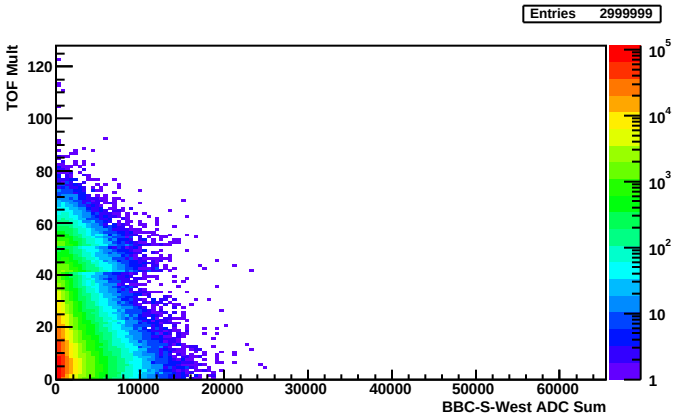

BBQ-VP004 (HI threshold)
Entries 4.799998e+07
ADC
QT Input Channel

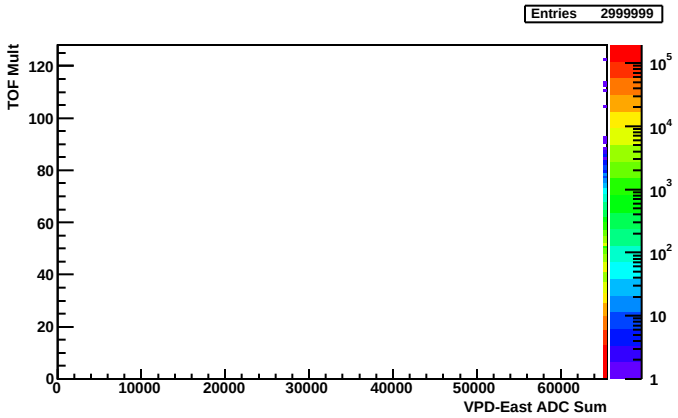

BBQ-VP004 (HI threshold)
Entries 4.799998e+07
TAC
QT Input Channel

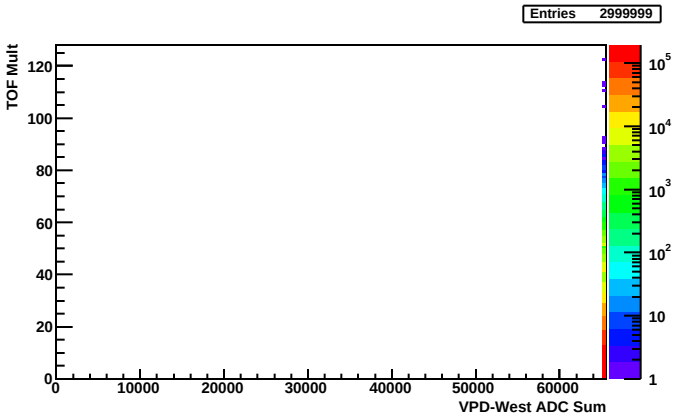

Entries 2999999
TOF Mult
BBC-L-East ADC Sum

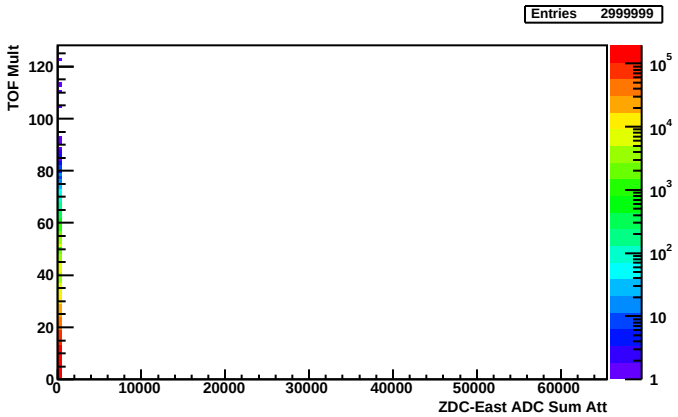

Entries 2999999
TOF Mult
BBC-L-West ADC Sum

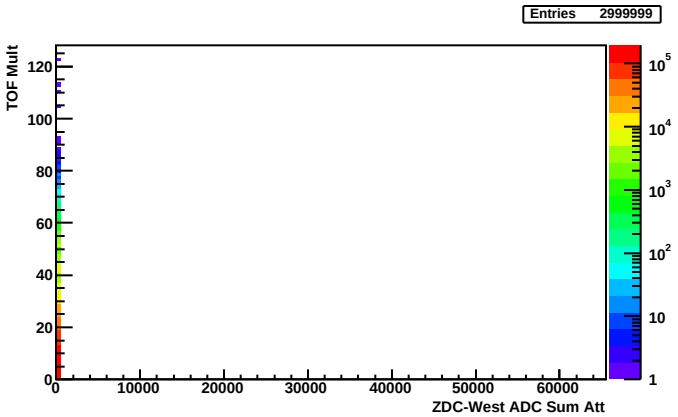

Entries 2999999
TOF Mult
BBC-S-East ADC Sum

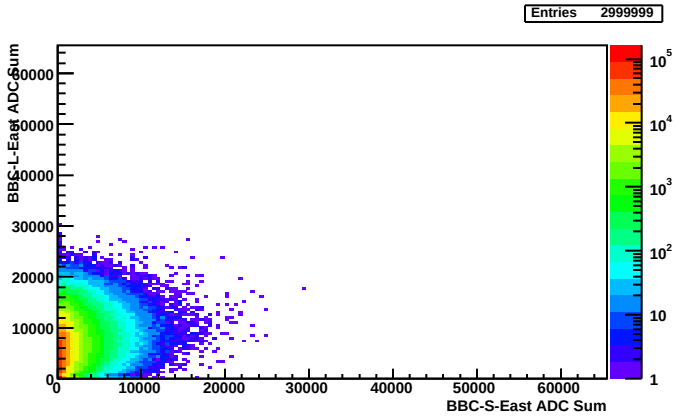

Entries 2999999
TOF Mult
BBC-S-West ADC Sum

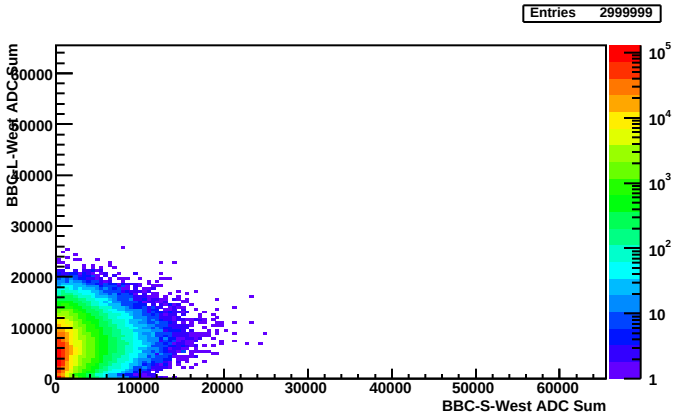

Entries 2999999
TOF Mult
VPD-East ADC Sum

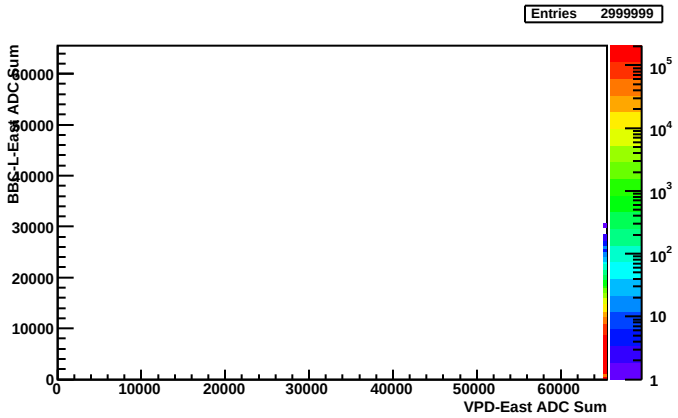

Entries 2999999
TOF Mult
VPD-West ADC Sum

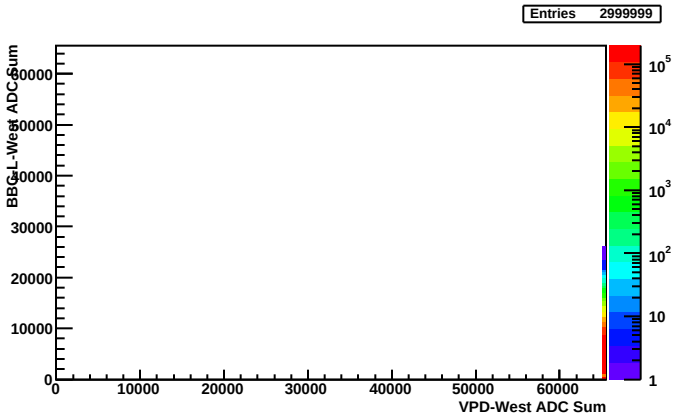

Entries 2999999
TOF Mult
ZDC-East ADC Sum Att

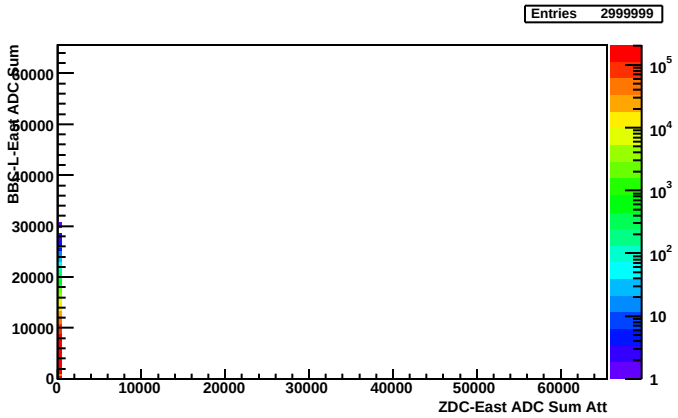

Entries 2999999
TOF Mult
ZDC-West ADC Sum Att

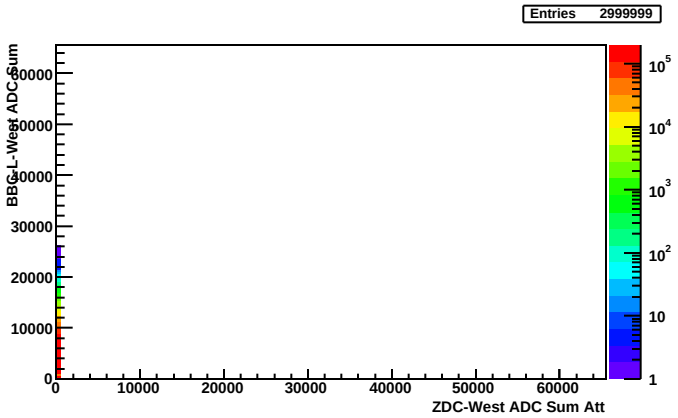

Entries 2999999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

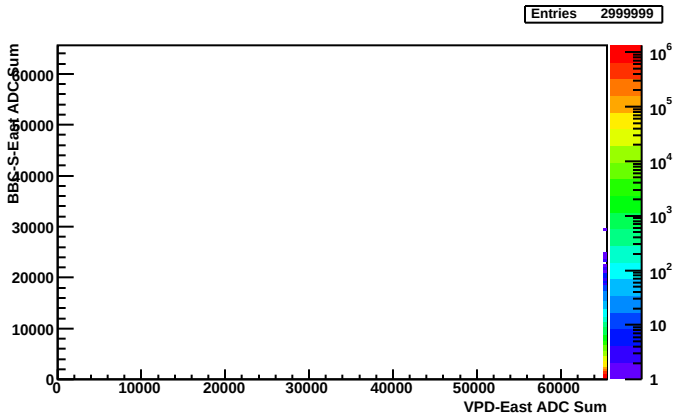

Entries 2999999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

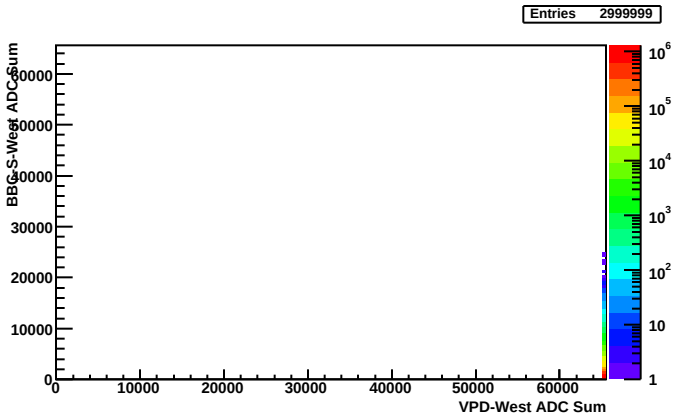

Entries 2999999
BBC-L-East ADC Sum
VPD-East ADC Sum

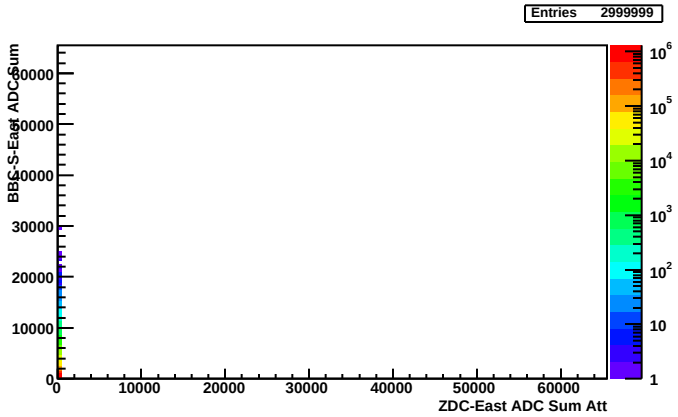

Entries 2999999
BBC-L-West ADC Sum
VPD-West ADC Sum

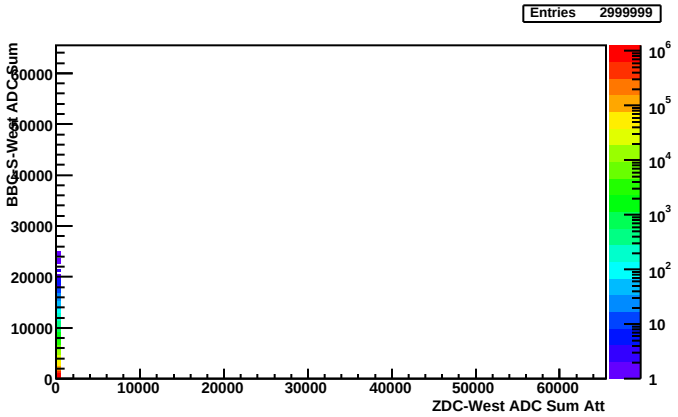

Entries 2999999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

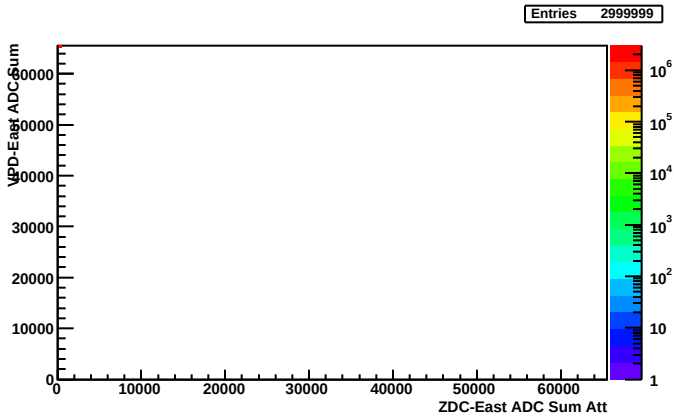

Entries 2999999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

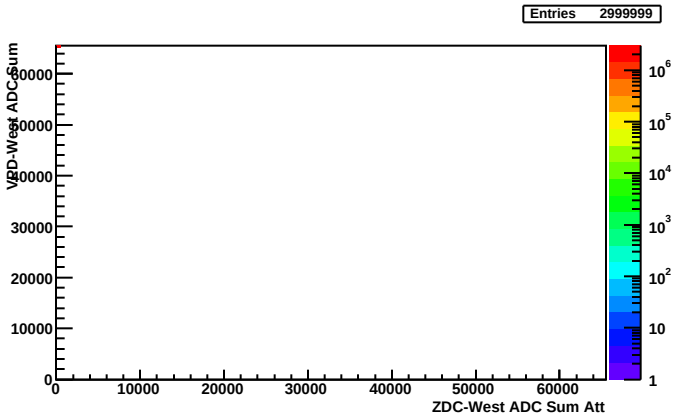

Entries 2999999
BBC-S-East ADC Sum
VPD-East ADC Sum

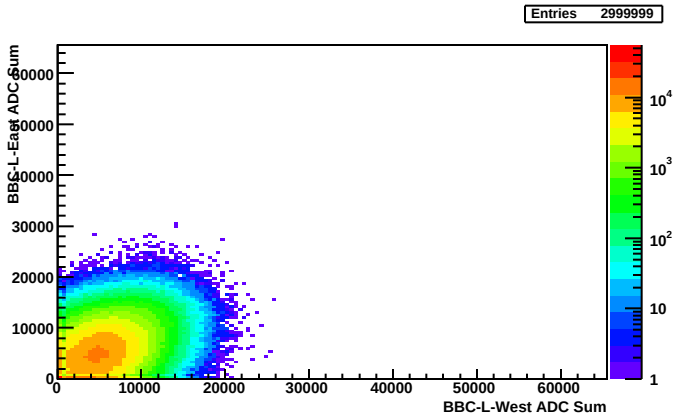

Entries 2999999
BBC-S-West ADC Sum
VPD-West ADC Sum

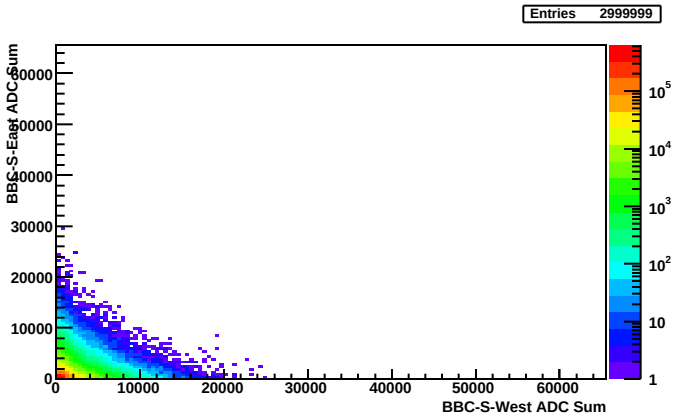

Entries 2999999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

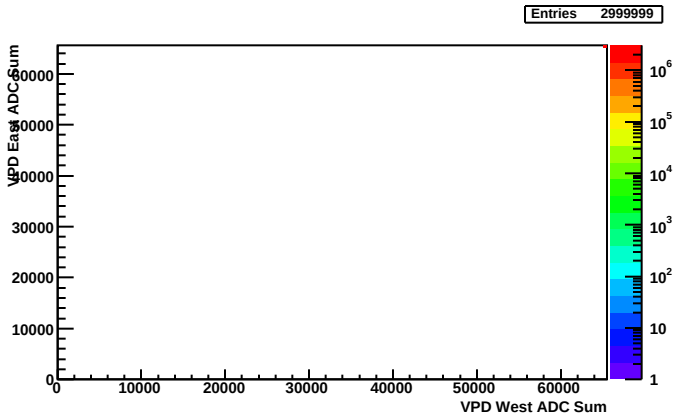

Entries 2999999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

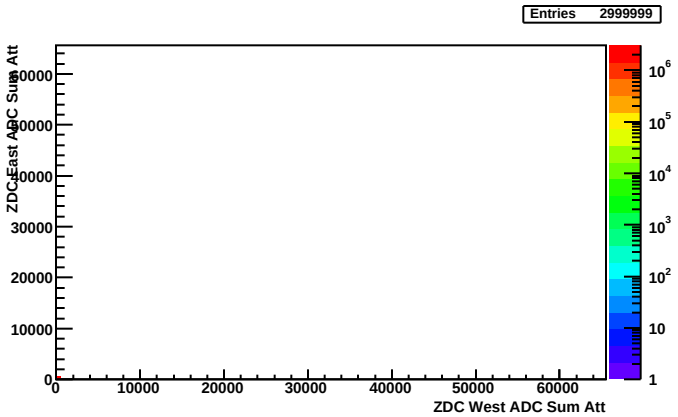

Entries 2999999
VPD-East ADC Sum
ZDC-East ADC Sum Att

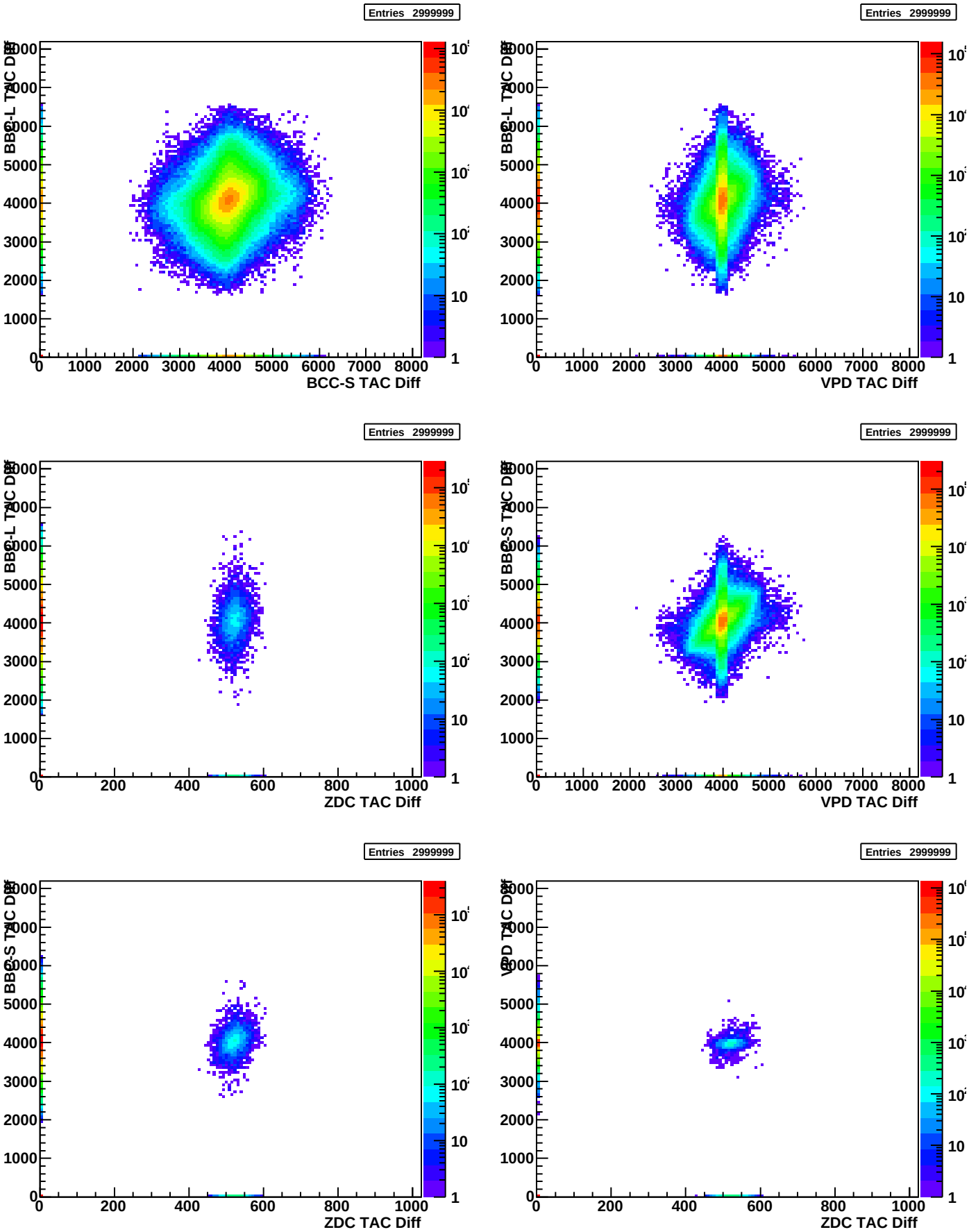

Entries 2999999
VPD-West ADC Sum
ZDC-West ADC Sum Att

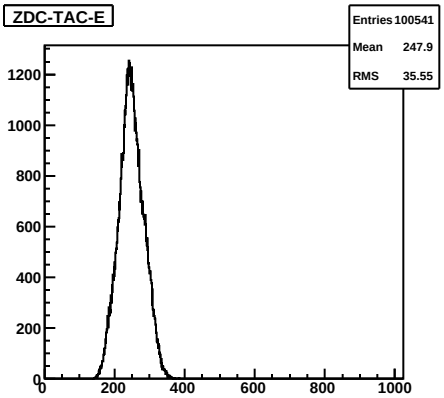

Entries 2999999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

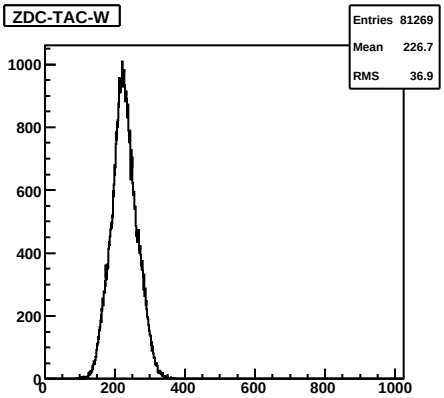

Entries 2999999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

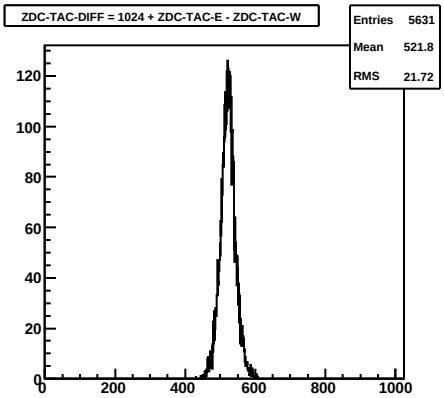

Entries 2999999
VPD East ADC Sum
VPD West ADC Sum

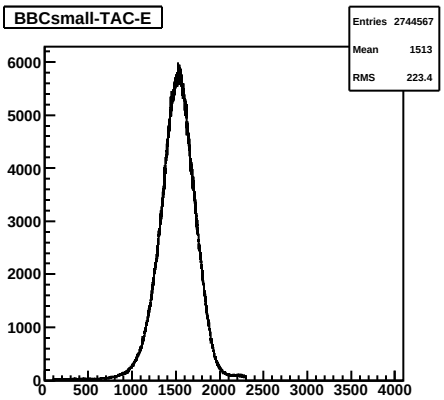

Entries 2999999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

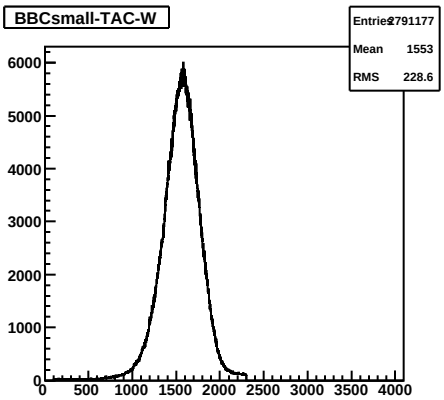

Entries 2999999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2999999
BBC-L TAC Diff
VPD TAC Diff
Entries 2999999
BBC-L TAC Diff
ZDC TAC Diff
Entries 2999999
BBC-S TAC Diff
VPD TAC Diff
Entries 2999999
BBC-S TAC Diff
ZDC TAC Diff
Entries 2999999
VPD TAC Diff
ZDC TAC Diff

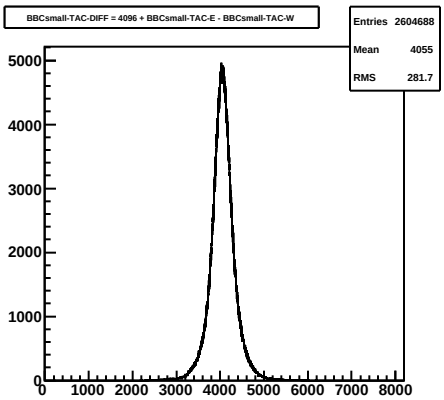

ZDC-TAC-E
Entries 100541
Mean 247.9
RMS 35.55

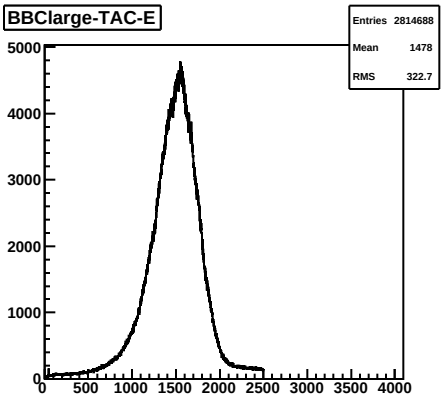

ZDC-TAC-W
Entries 81269
Mean 226.7
RMS 36.9

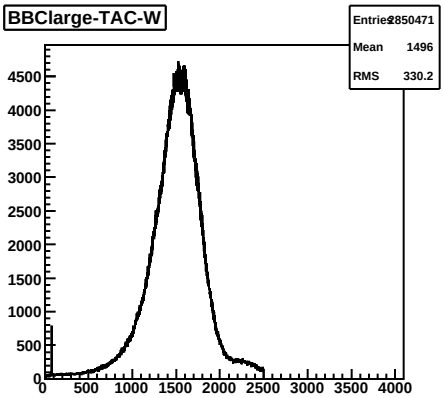

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5631
Mean 521.8
RMS 21.72

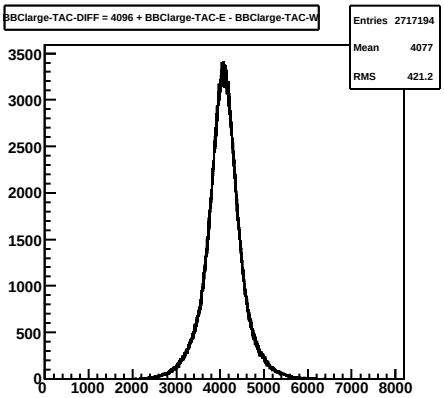

BBCsmall-TAC-E
Entries 2744567
Mean 1513
RMS 223.4

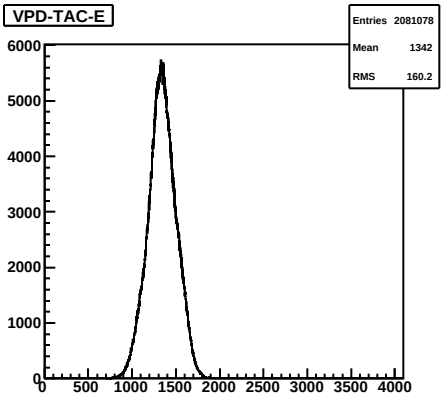

BBCsmall-TAC-W
Entries 2791177
Mean 1553
RMS 228.6

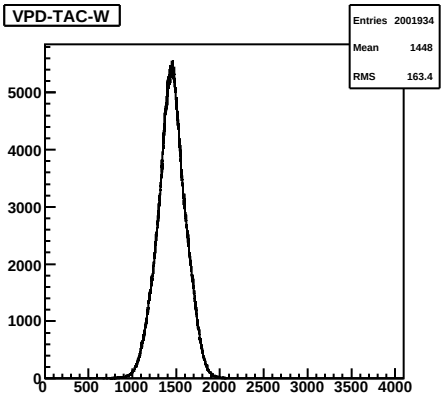

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2604688
Mean 4055
RMS 281.7

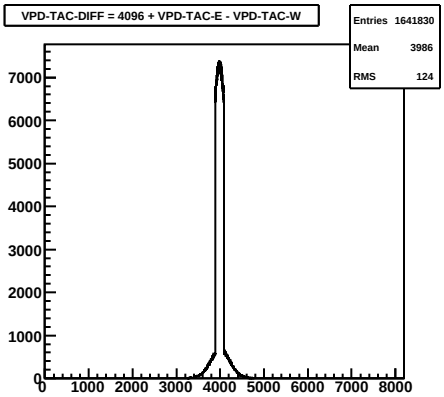

BBClarge-TAC-E
Entries 2814688
Mean 1478
RMS 322.7

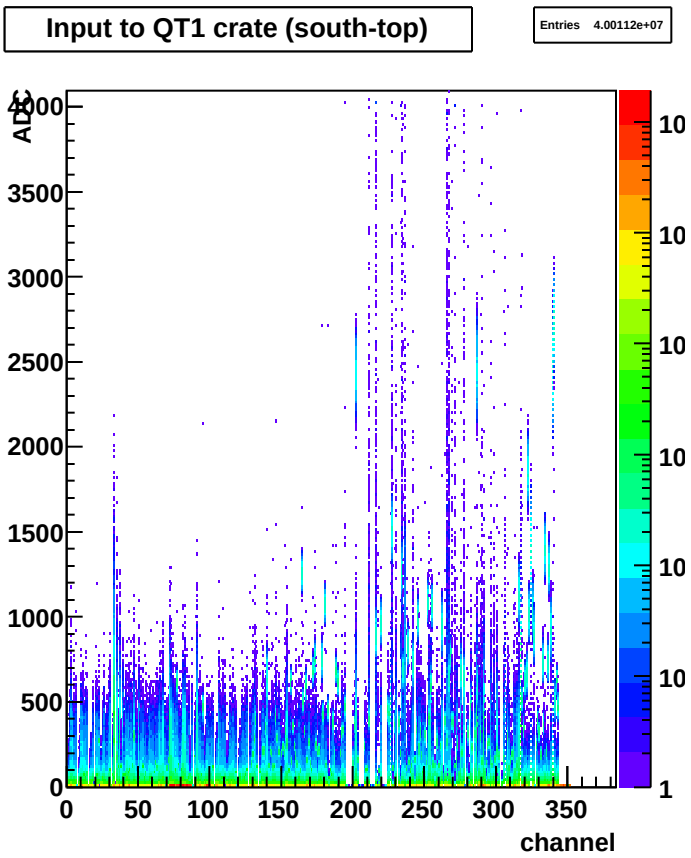

BBClarge-TAC-W
Entries 2850471
Mean 1496
RMS 330.2

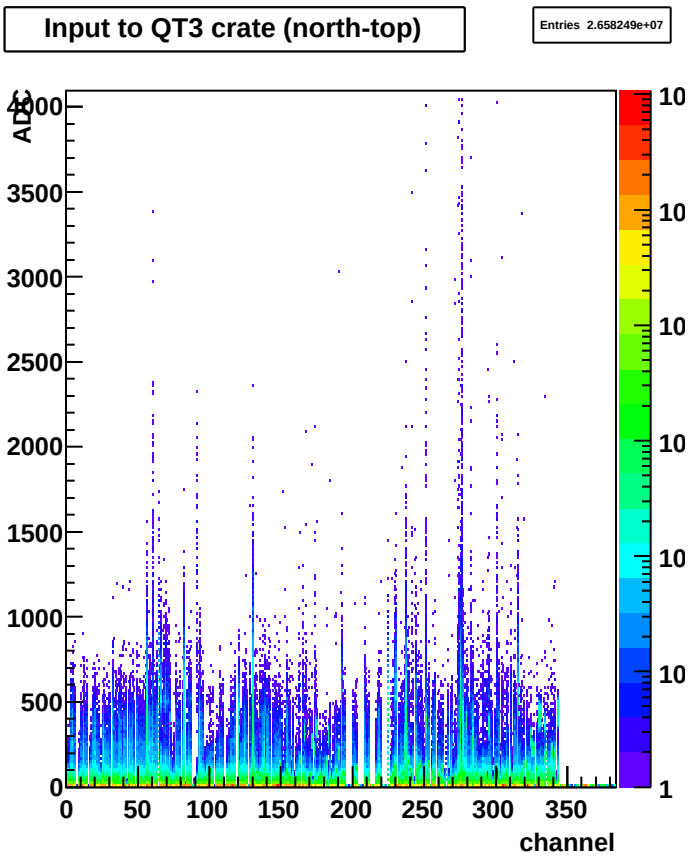

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2717194
Mean 4077
RMS 421.2

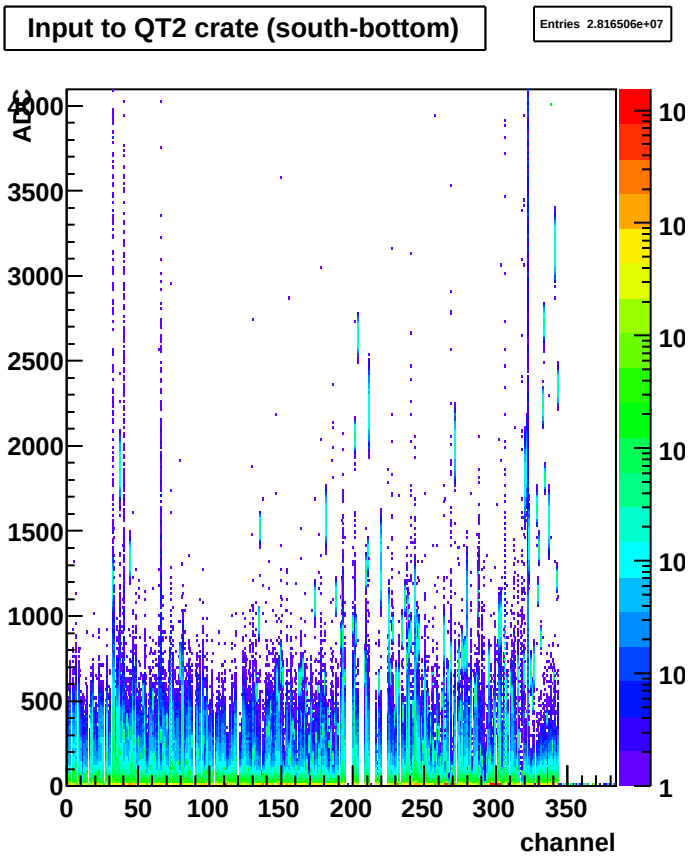

VPD-TAC-E
Entries 2081078
Mean 1342
RMS 160.2

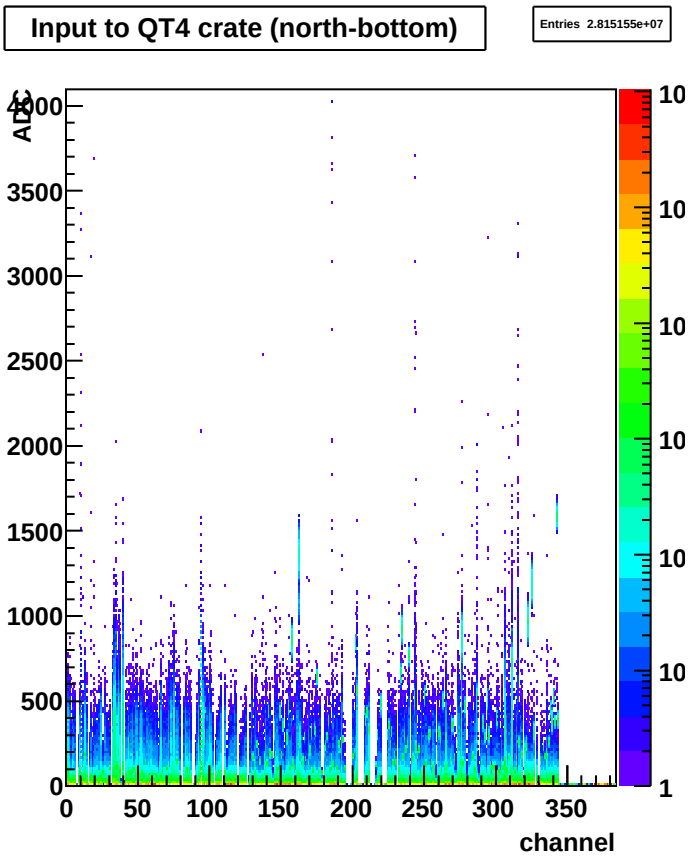

VPD-TAC-W
Entries 2001934
Mean 1448
RMS 163.4

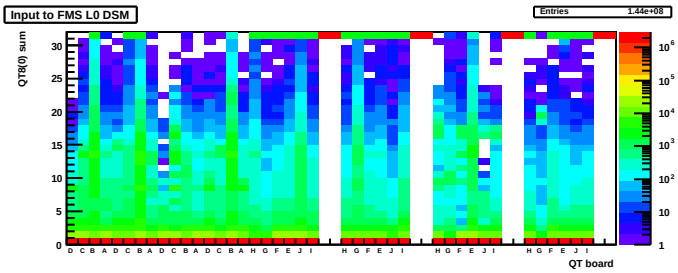

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1641830
Mean 3986
RMS 124

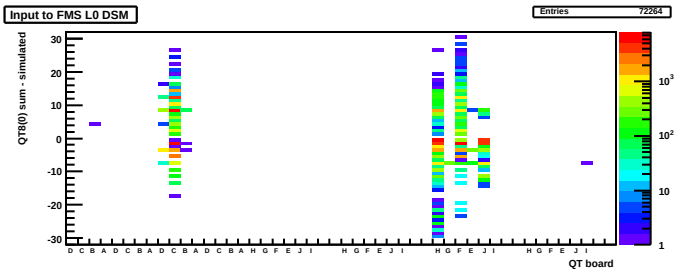

Input to QT1 crate (south-top)
Entries 4.00112e+07
ADC
channel

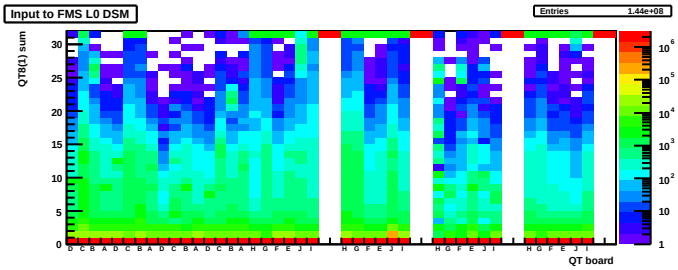

Input to QT3 crate (north-top)
Entries 2.658249e+07
ADC
channel

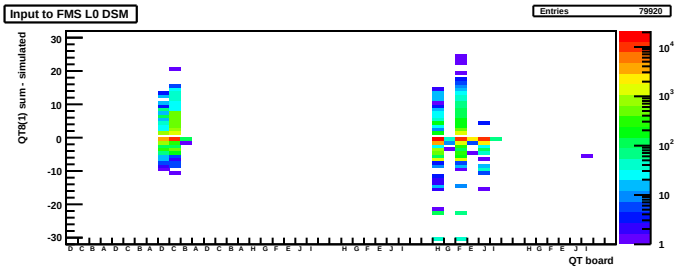

Input to QT2 crate (south-bottom)
Entries 2.816506e+07
ADC
channel

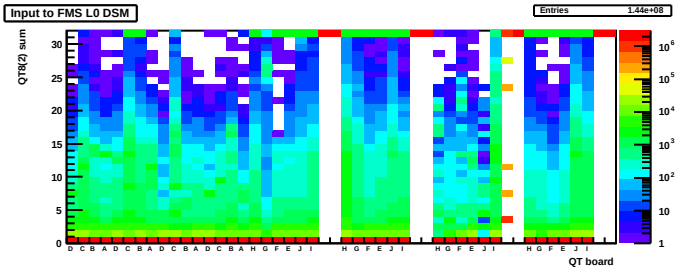

Input to QT4 crate (north-bottom)
Entries 2.815155e+07
ADC
channel

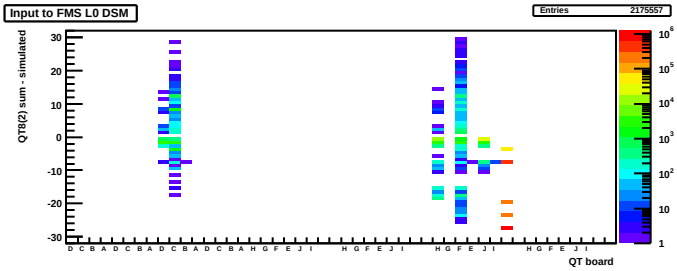

Input to FMS L0 DSM
Entries 1.44e+08
QT8(0) sum
QT board

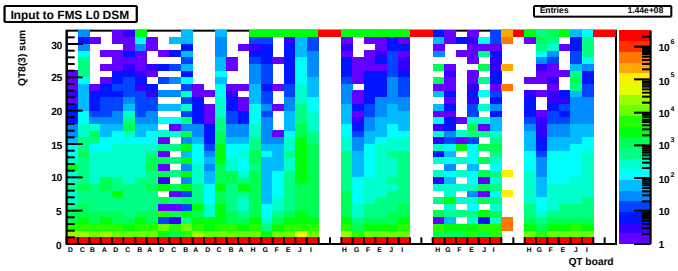

Input to FMS L0 DSM
Entries 72264
QT8(0) sum - simulated
QT board

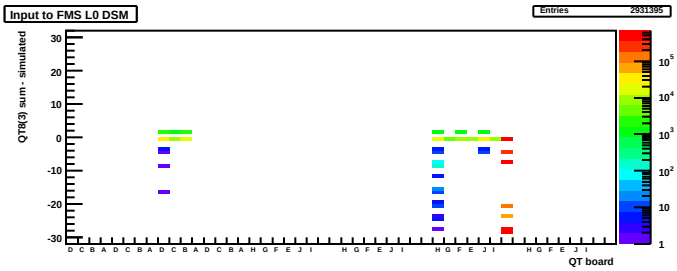

Input to FMS L0 DSM
Entries 1.44e+08
QT8(1) sum
QT board

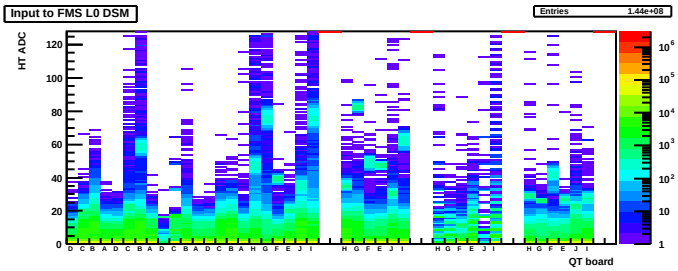

Input to FMS L0 DSM
Entries 79920
QT8(1) sum - simulated
QT board

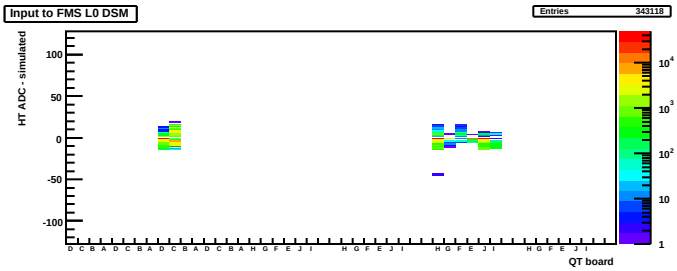

Input to FMS L0 DSM
Entries 1.44e+08
QT8(2) sum
QT board

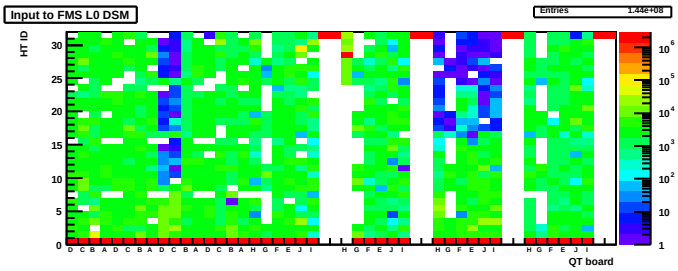

Input to FMS L0 DSM
Entries 2175557
QT8(2) sum - simulated
QT board

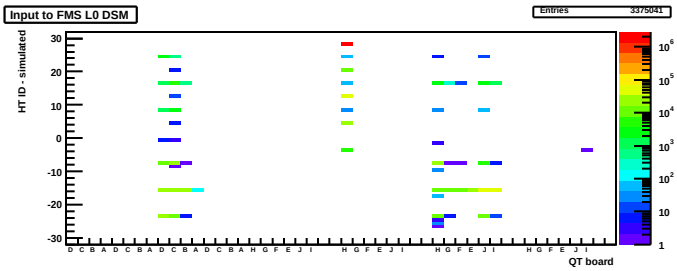

Input to FMS L0 DSM
Entries 1.44e+08
QT8(3) sum
QT board

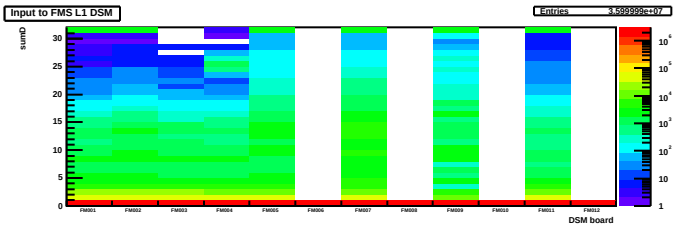

Input to FMS L0 DSM
Entries 2931395
QT8(3) sum - simulated
QT board

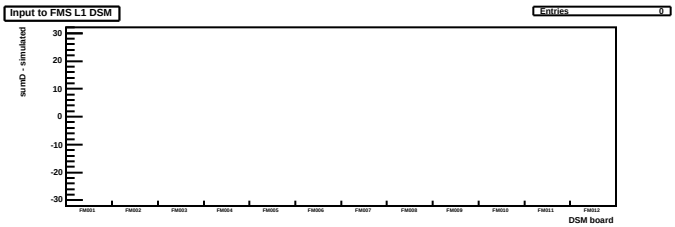

Input to FMS L0 DSM
Entries 1.44e+08
HT ADC
QT board

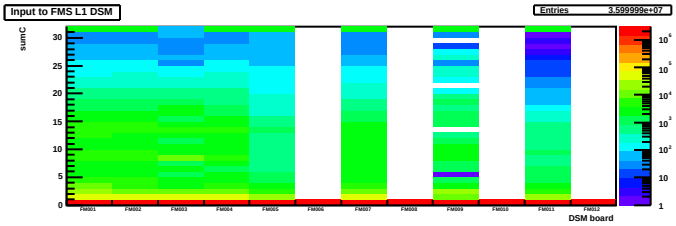

Input to FMS L0 DSM
Entries 343118
HT ADC - simulated
QT board

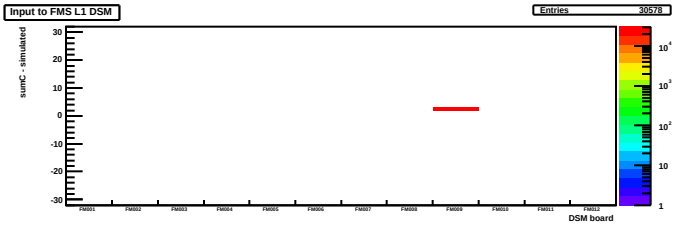

Input to FMS L0 DSM
Entries 1.44e+08
HT ID
QT board

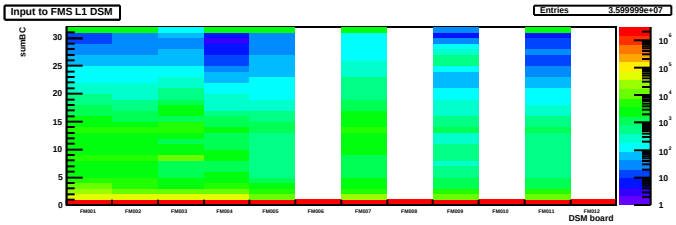

Input to FMS L0 DSM
Entries 3375041
HT ID - simulated
QT board

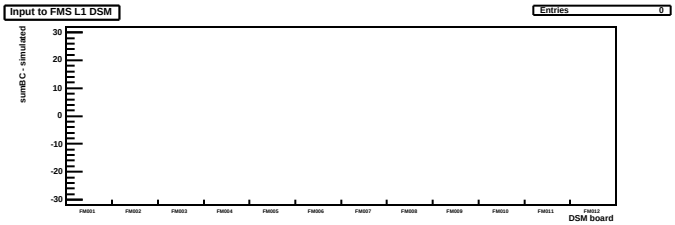

Input to FMS L1 DSM
Entries 3.599999e+07
sumD
DSM board

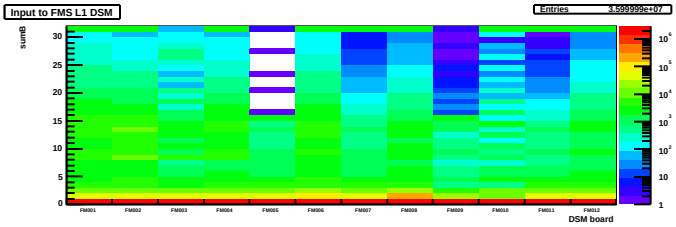

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

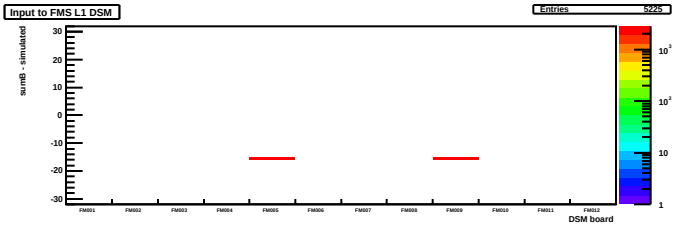

Input to FMS L1 DSM
Entries 3.599999e+07
sumC
DSM board

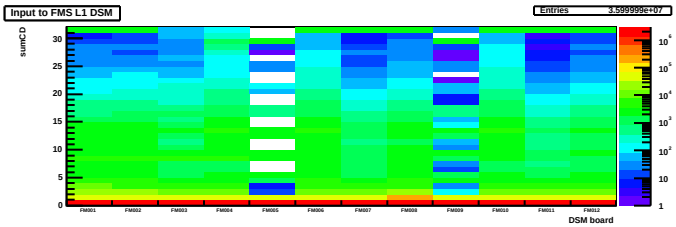

Input to FMS L1 DSM
Entries 30578
sumC - simulated
DSM board

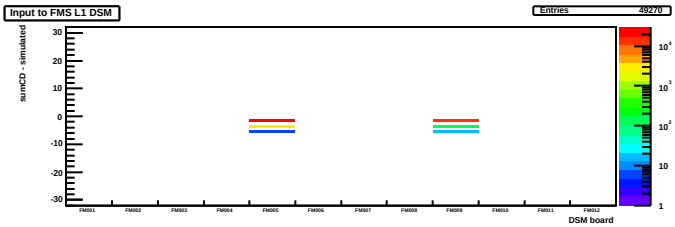

Input to FMS L1 DSM
Entries 3.599999e+07
sumBC
DSM board

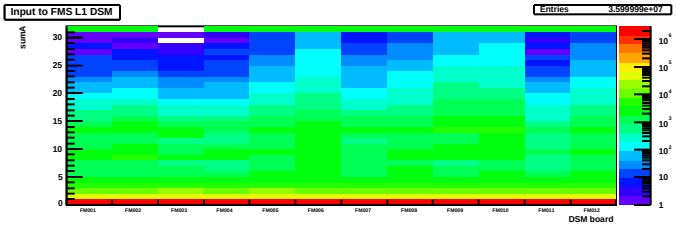

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

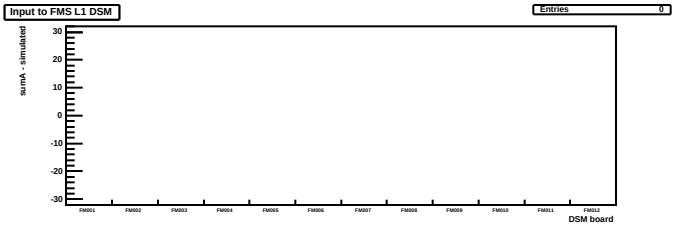

Input to FMS L1 DSM
Entries 3.599999e+07
sumB
DSM board

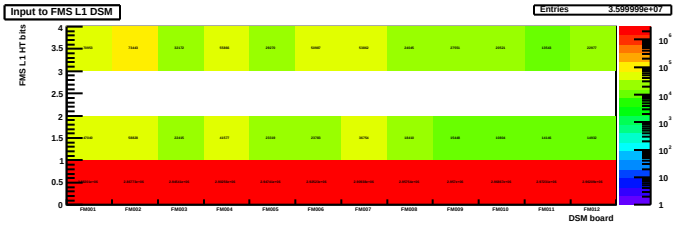

Input to FMS L1 DSM
Entries 5225
sumB - simulated
DSM board

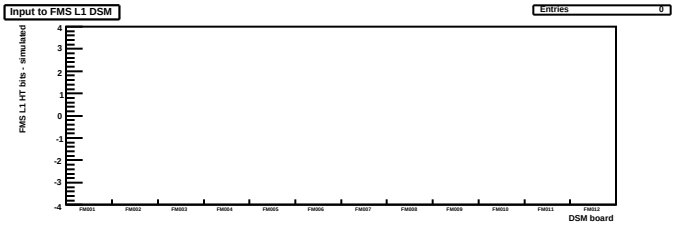

Input to FMS L1 DSM
Entries 3.599999e+07
sumCD
DSM board

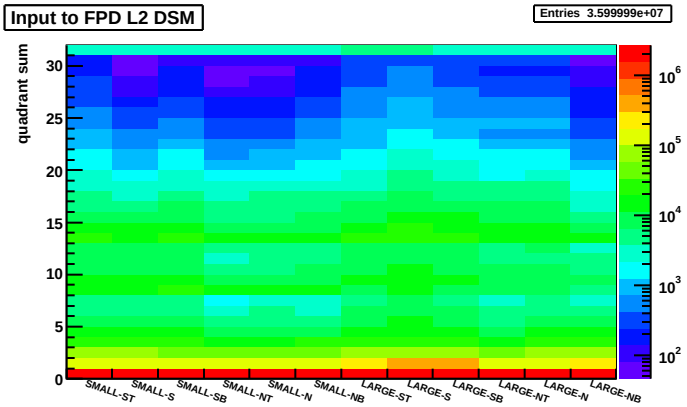

Input to FMS L1 DSM
Entries 49270
sumCD - simulated
DSM board

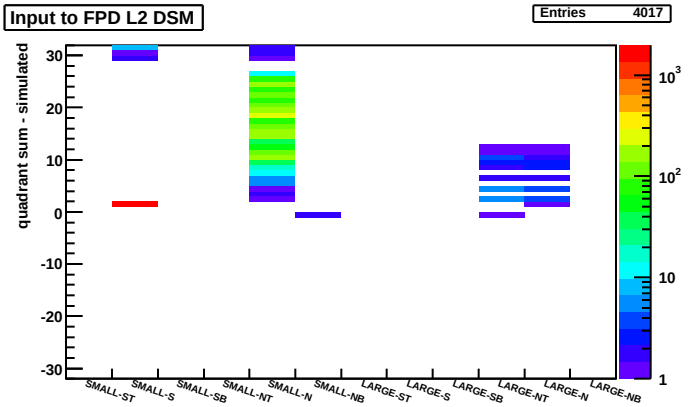

Input to FMS L1 DSM
Entries 3.599999e+07
sumA
DSM board

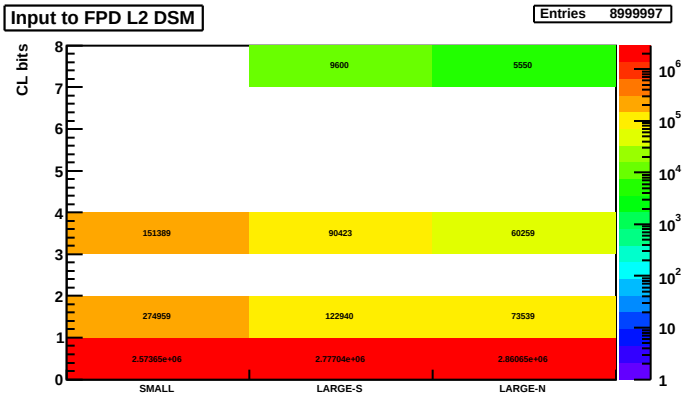

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

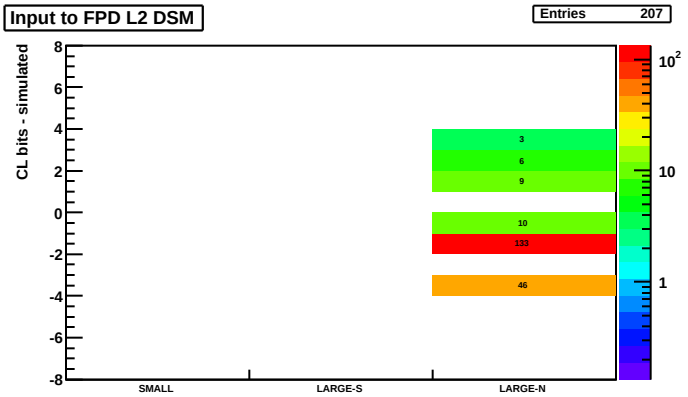

Input to FMS L1 DSM
Entries 3.599999e+07
FMS L1 HT bits
DSM board

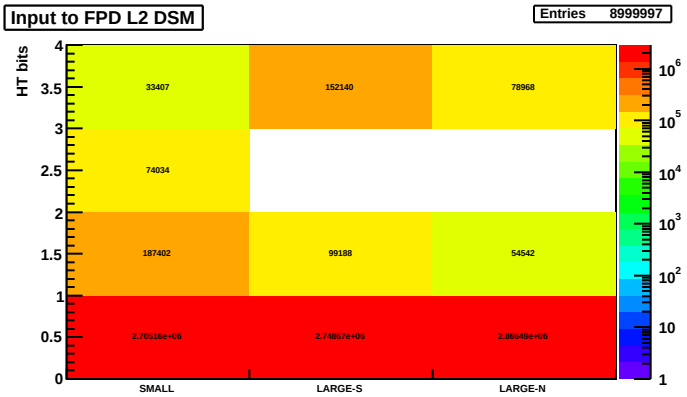

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

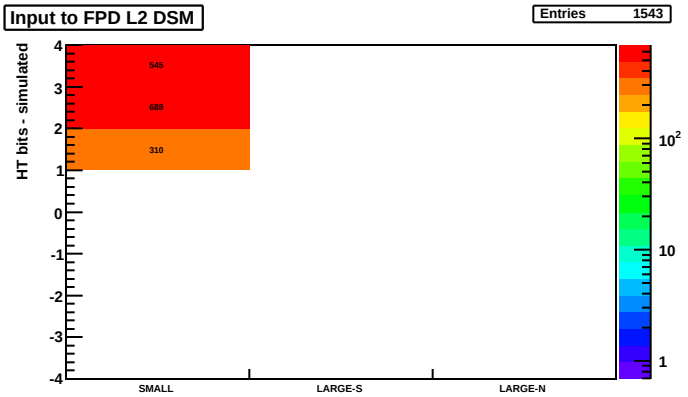

Input to FPD L2 DSM
Entries 3.599999e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

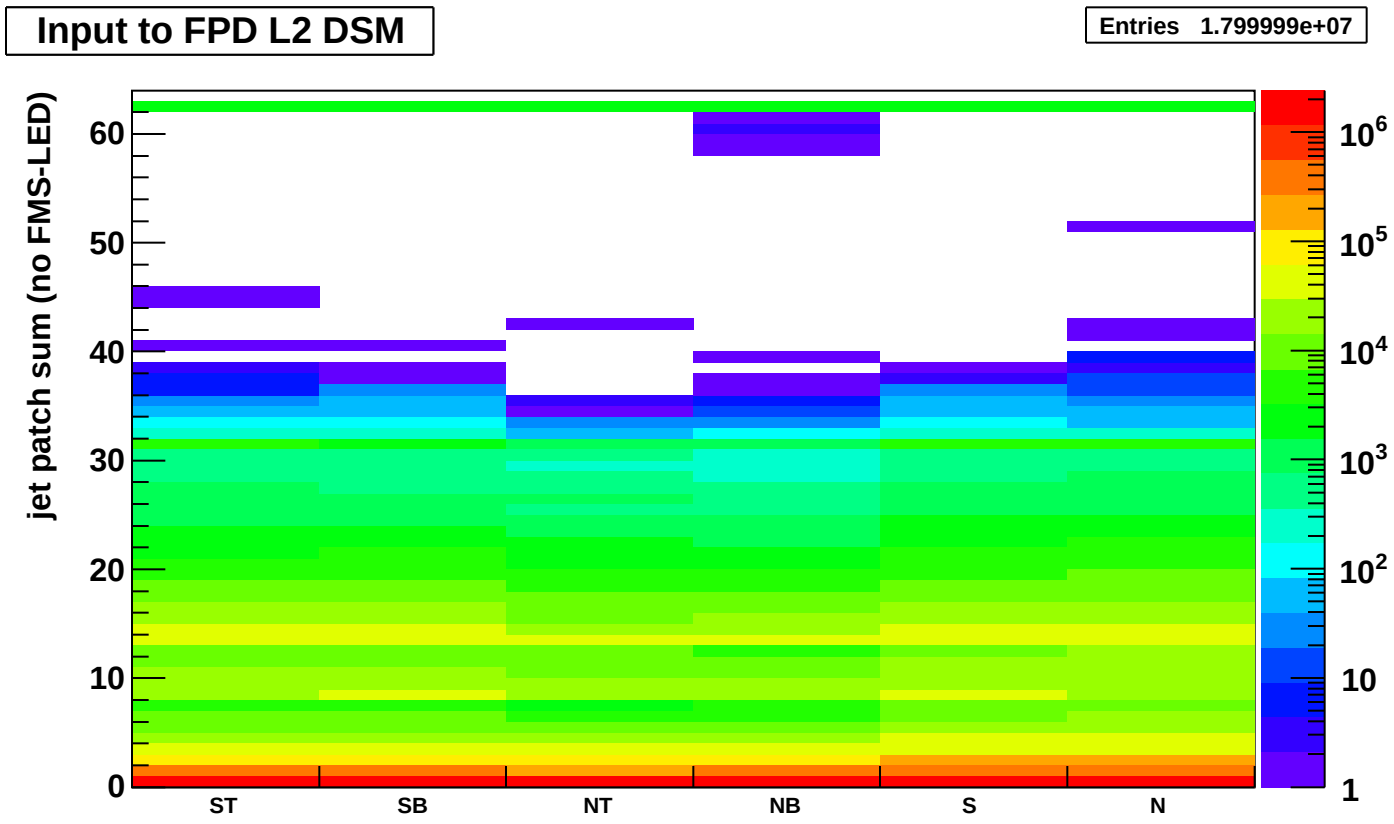

Input to FPD L2 DSM
Entries 4017
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

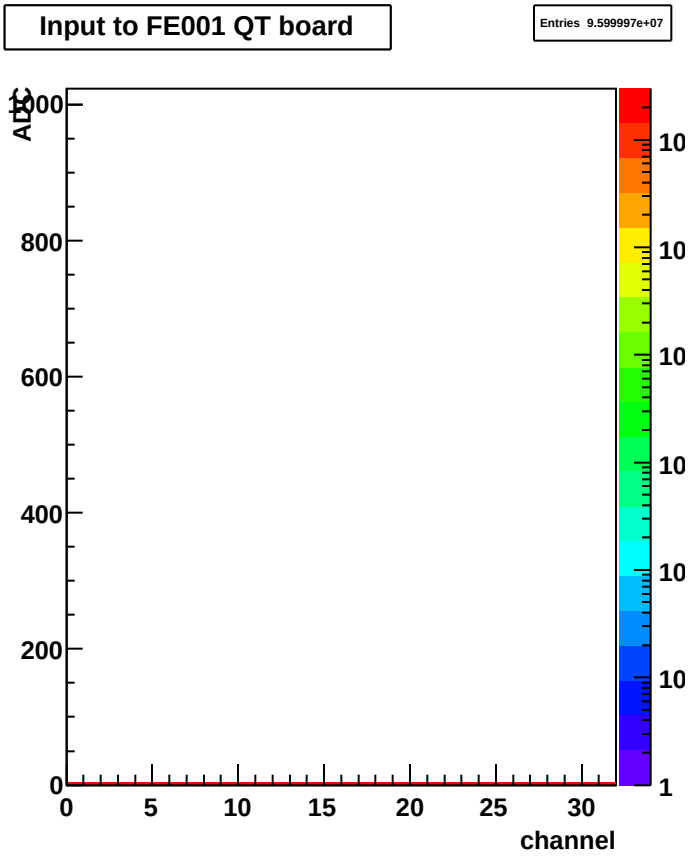

Input to FPD L2 DSM
Entries 8999997
CL bits
9600
5550
151389
90423
60259
274959
122940
73539
2.57365e+06
2.77704e+06
2.86065e+06
SMALL
LARGE-S
LARGE-N

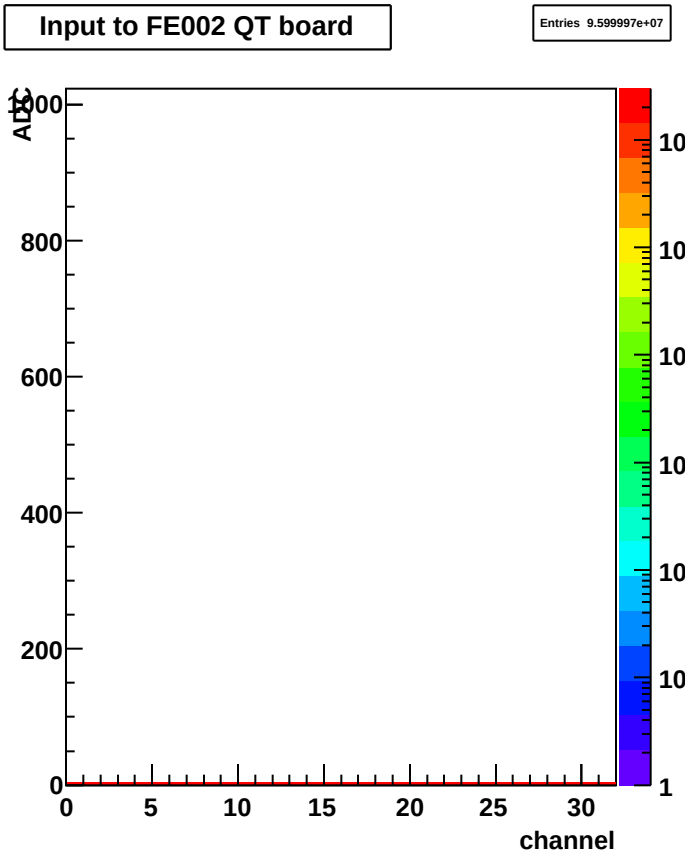

Input to FPD L2 DSM
Entries 207
CL bits - simulated
3
6
9
10
133
46
SMALL
LARGE-S
LARGE-N

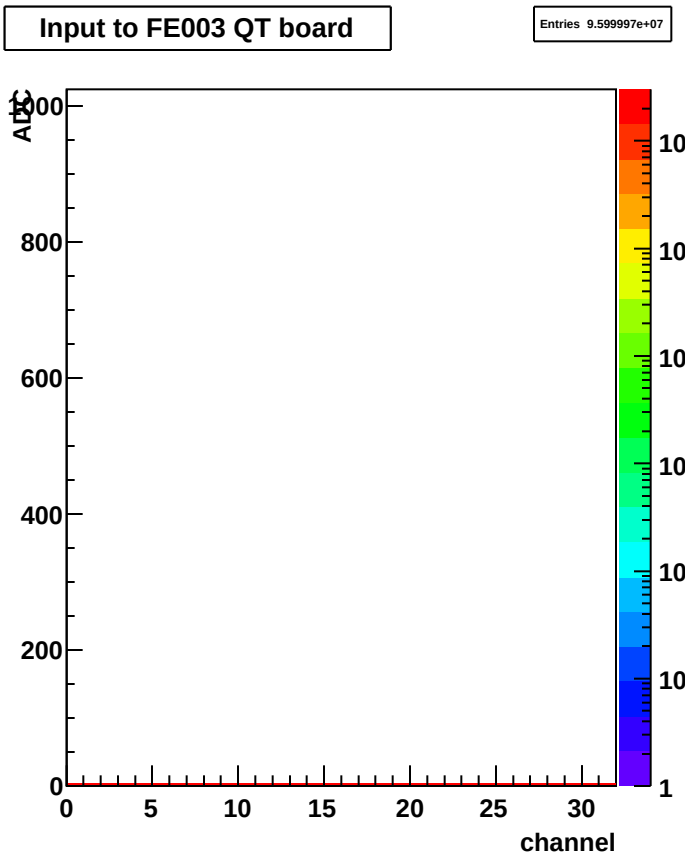

Input to FPD L2 DSM
Entries 8999997
HT bits
33407
152140
78968
74034
187402
99188
54542
2.70516e+06
2.74867e+06
2.86649e+06
SMALL
LARGE-S
LARGE-N

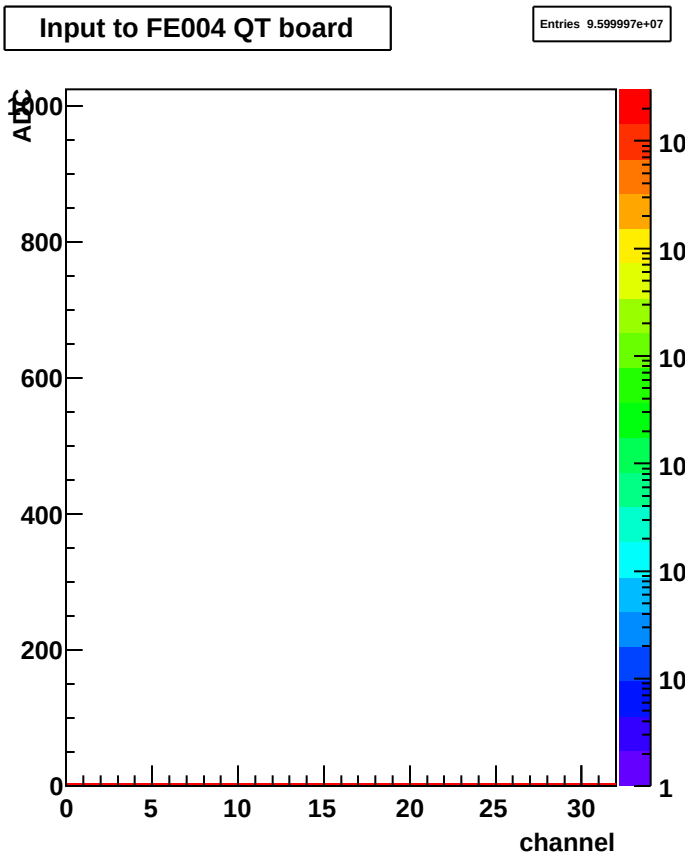

Input to FPD L2 DSM
Entries 1543
HT bits - simulated
545
688
310
SMALL
LARGE-S
LARGE-N

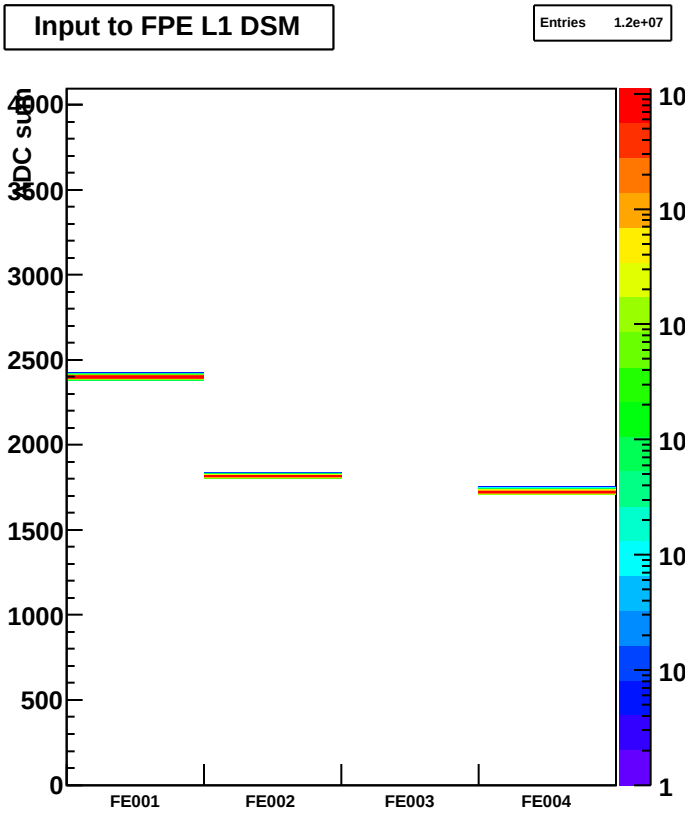

Input to FPD L2 DSM
Entries 1.799999e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

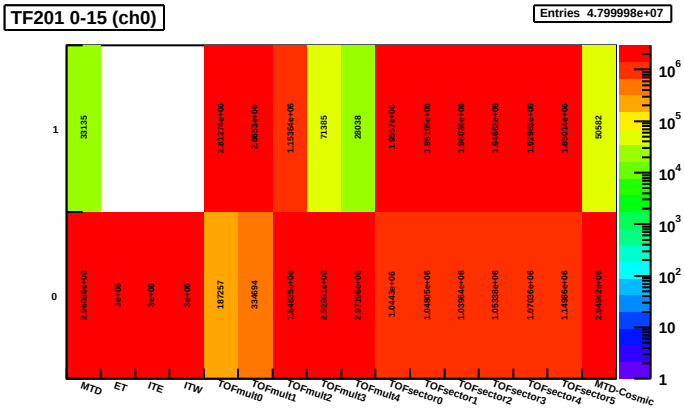

Input to FE001 QT board
Entries 9.599997e+07
ADC
channel

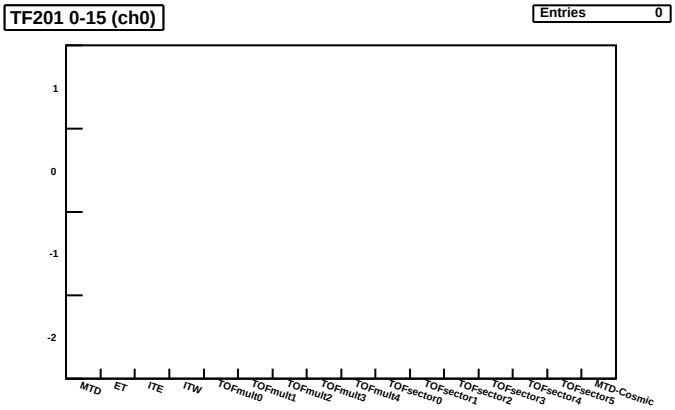

Input to FE002 QT board
Entries 9.599997e+07
ADC
channel

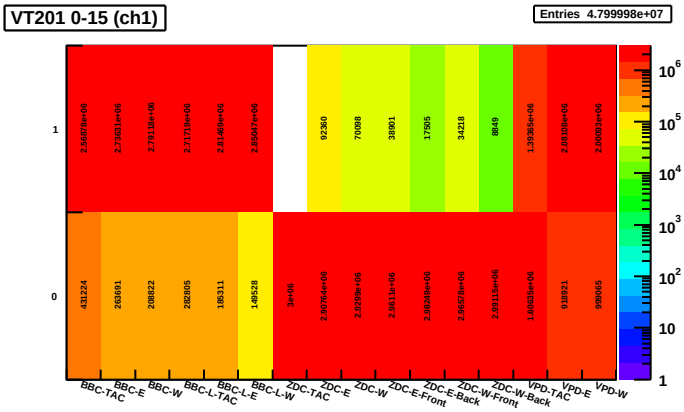

Input to FE003 QT board
Entries 9.599997e+07
ADC
channel


Input to FE004 QT board
Entries 9.599997e+07
ADC
channel


Input to FPE L1 DSM
Entries 1.2e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 4.799998e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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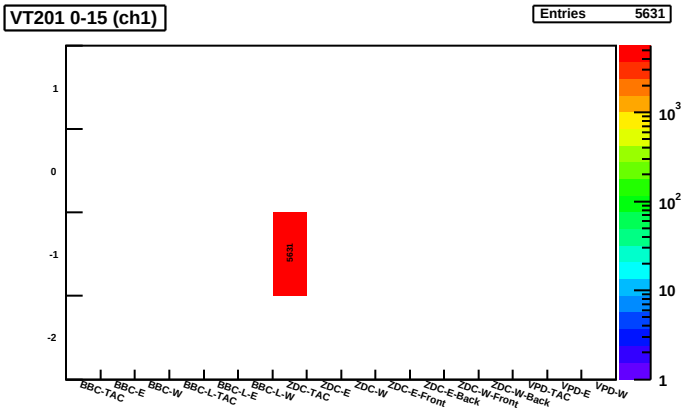
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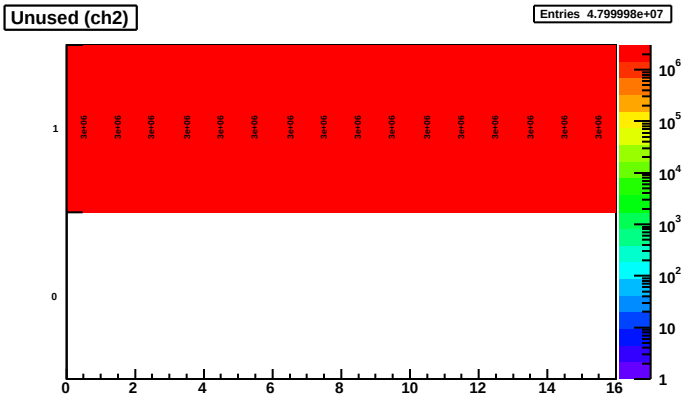
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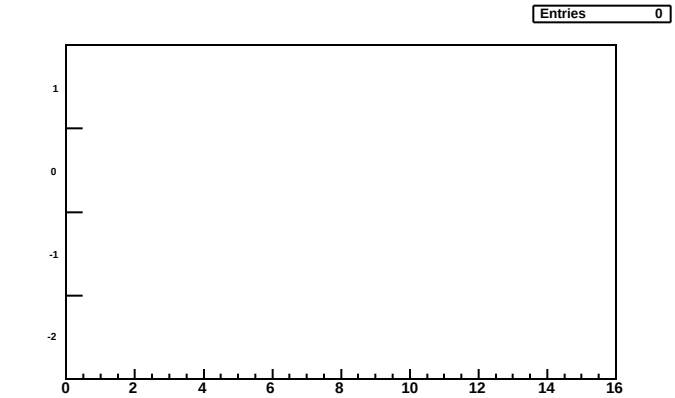
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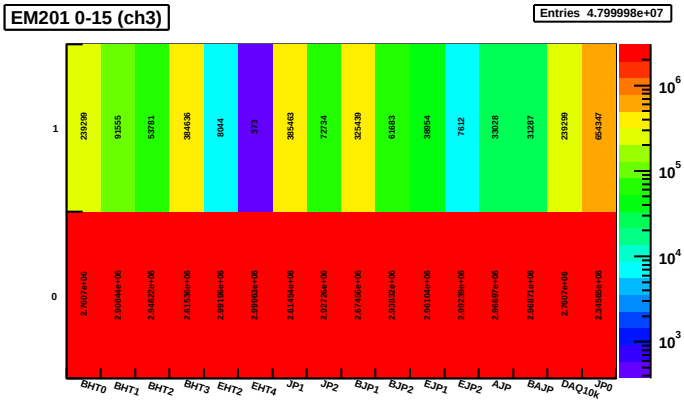
EM201 0-15 (ch3)
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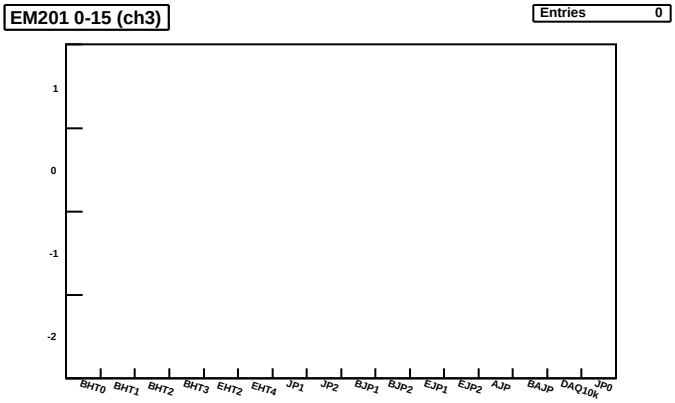
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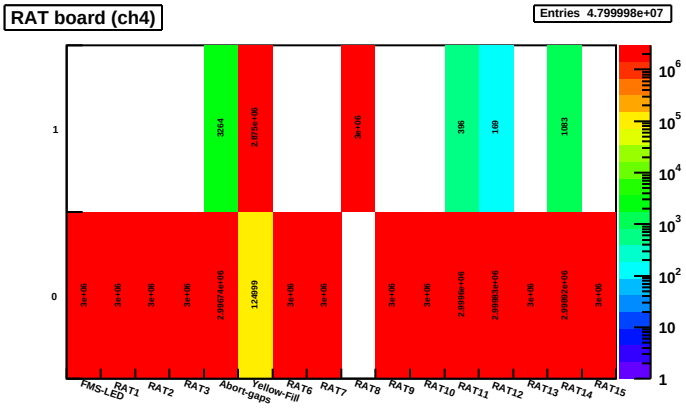
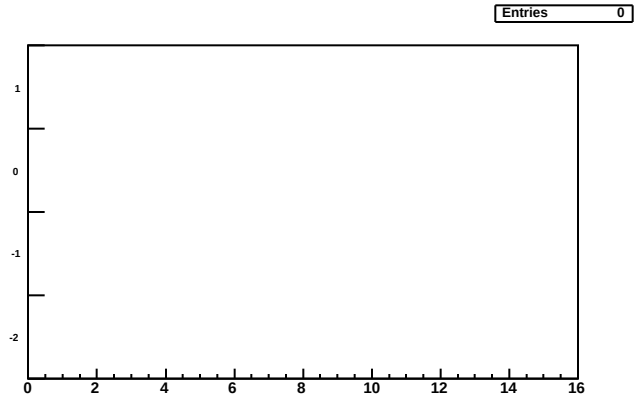
RAT board (ch4)





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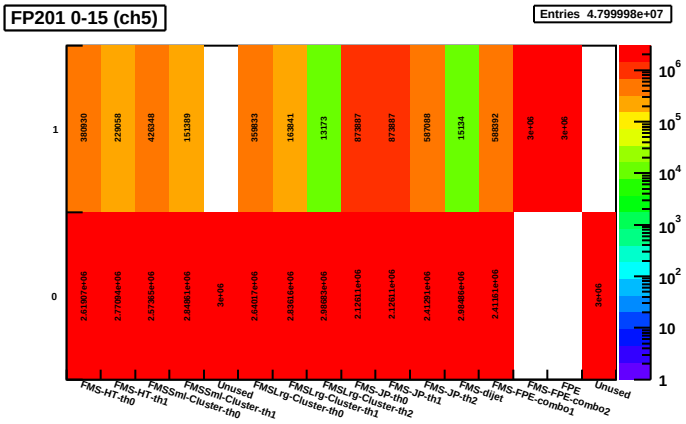

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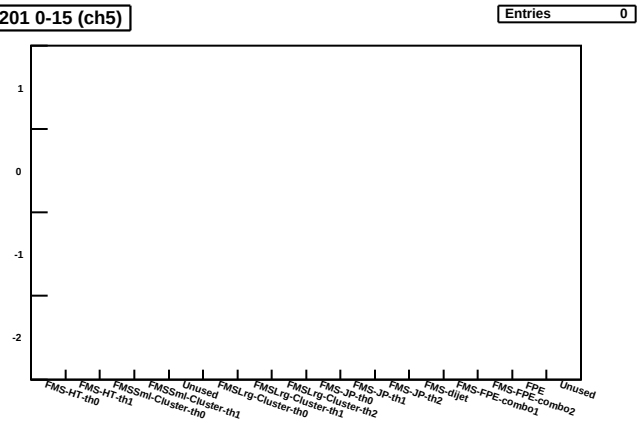
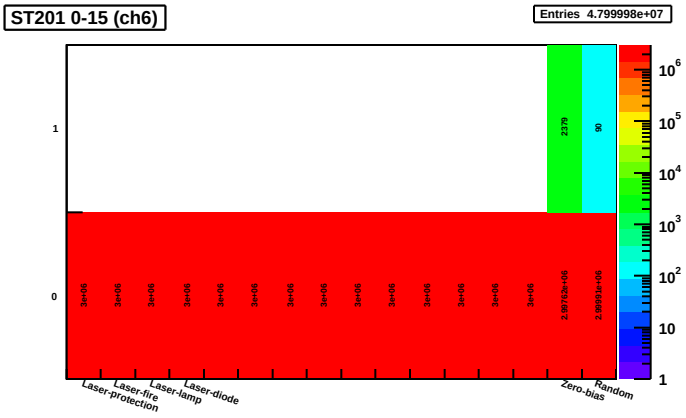
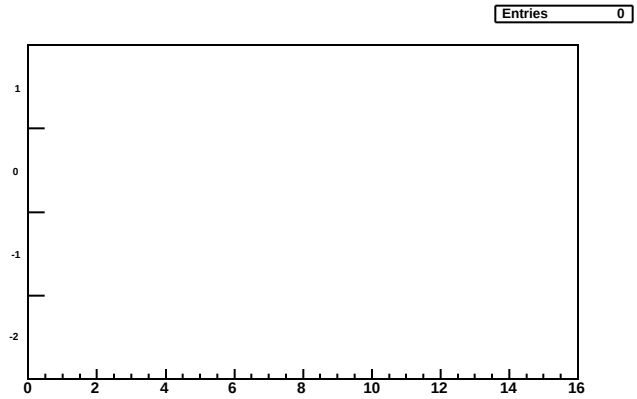
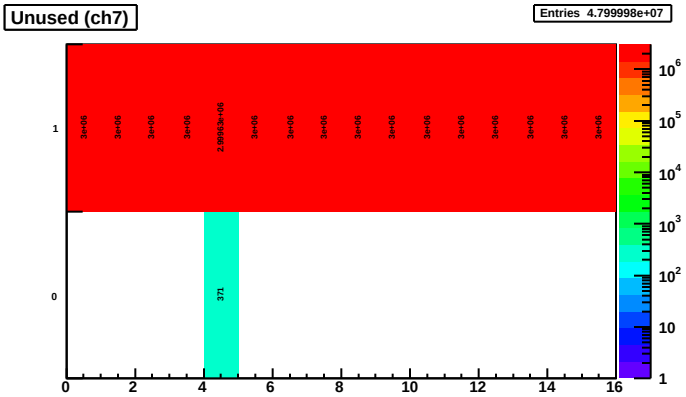
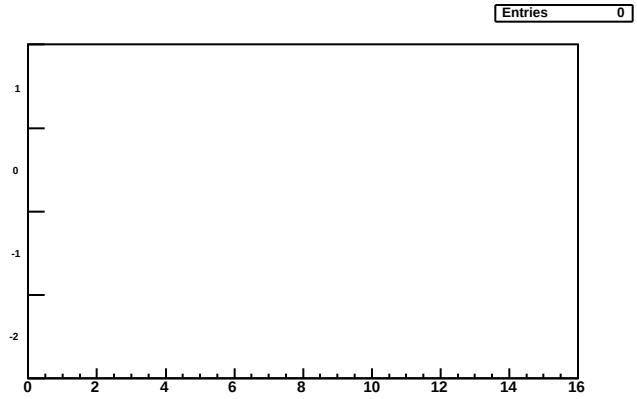


ST201 0-15 (ch6)





Unused (ch7)